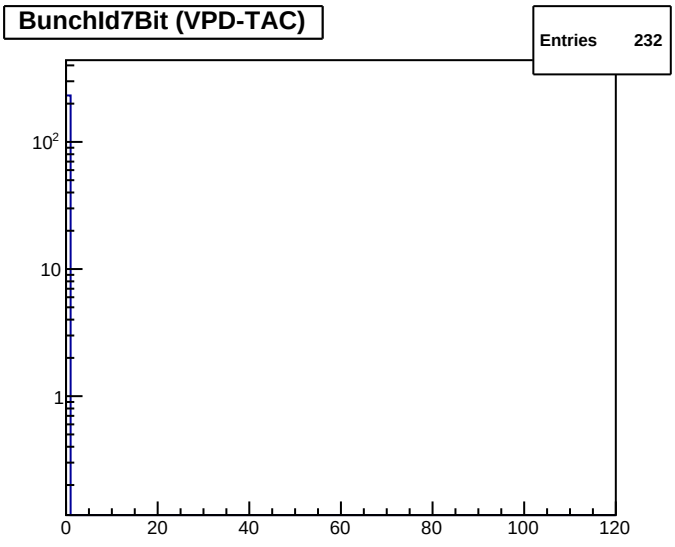
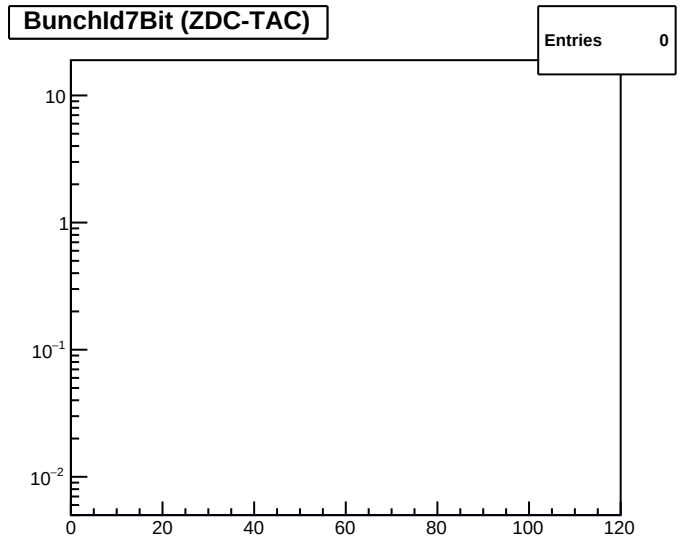
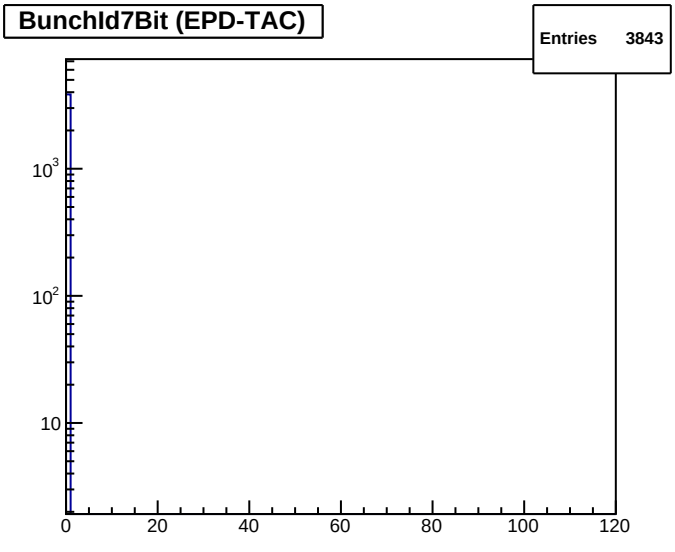
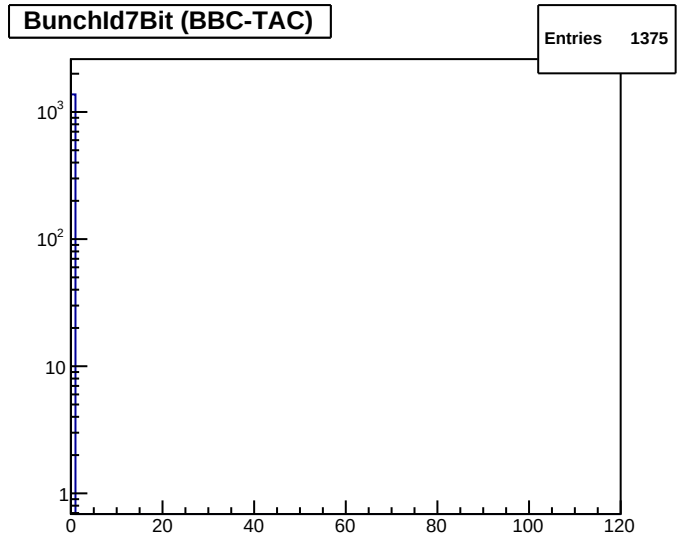
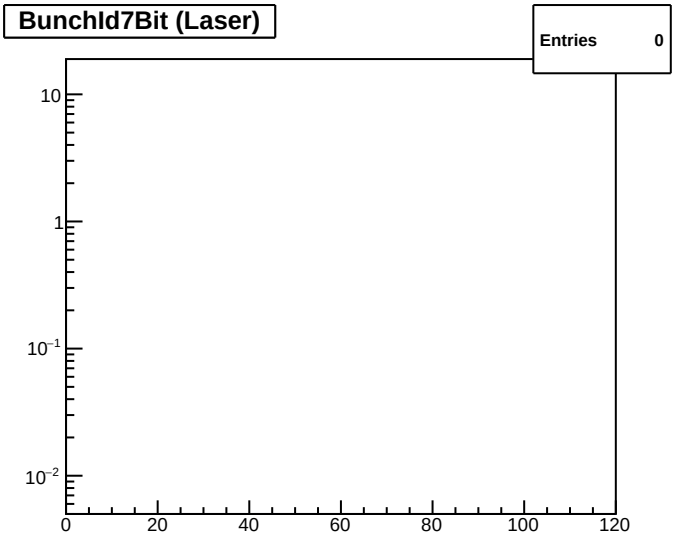
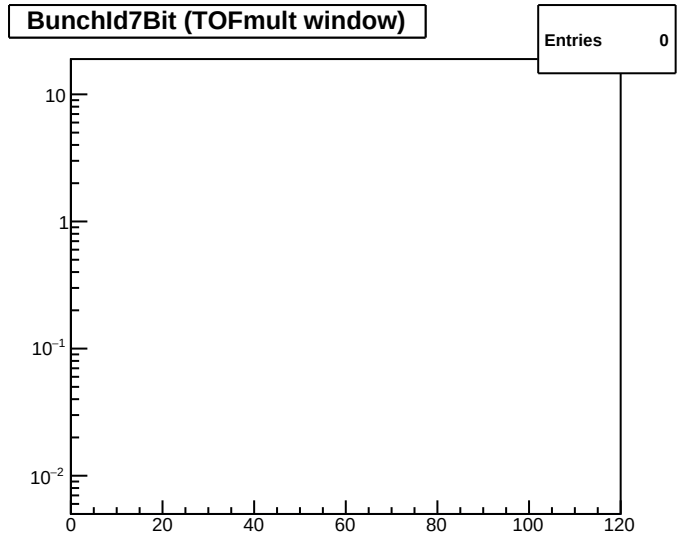

BunchId7Bit (VPD-TAC)
Entries 232
10^2
10
1
0
20
40
60
80
100
120

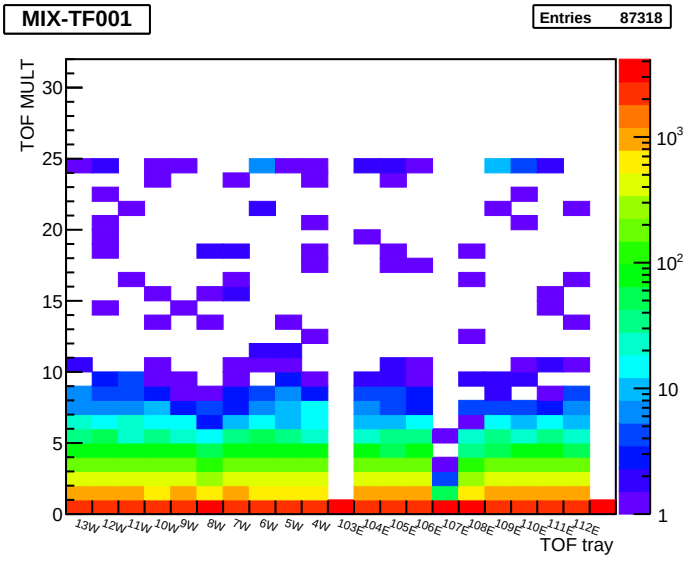

BunchId7Bit (ZDC-TAC)
Entries 0
10
1
10^{-1}
10^{-2}
0
20
40
60
80
100
120

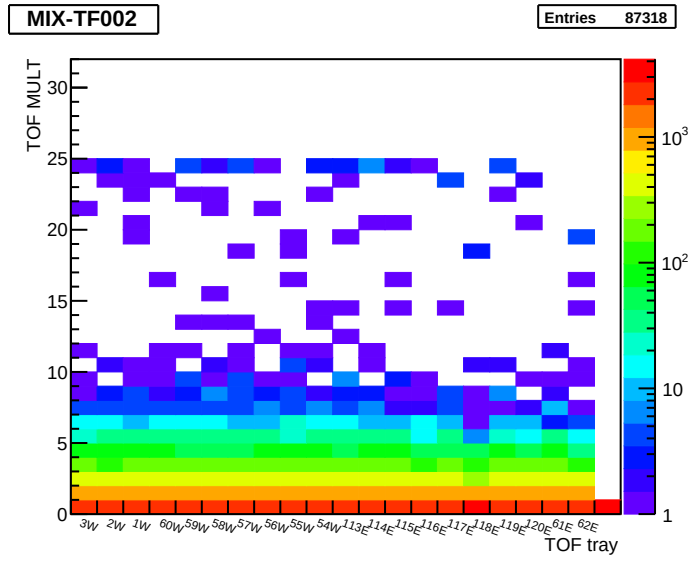

BunchId7Bit (EPD-TAC)
Entries 3843
10^3
10^2
10
0
20
40
60
80
100
120

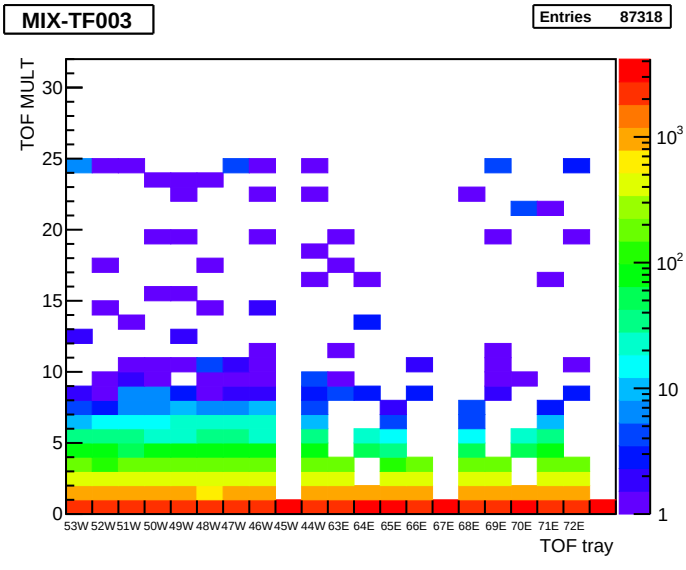

BunchId7Bit (BBC-TAC)
Entries 1375
10^3
10^2
10
1
0
20
40
60
80
100
120

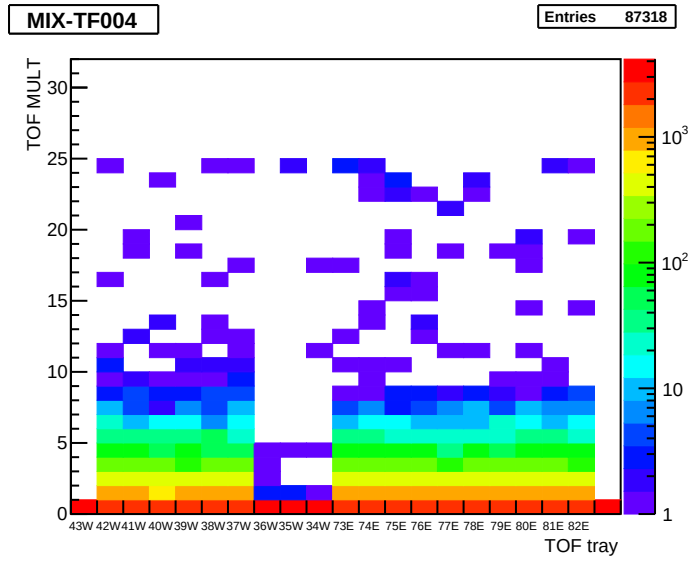

BunchId7Bit (Laser)
Entries 0
10
1
10^{-1}
10^{-2}
0
20
40
60
80
100
120

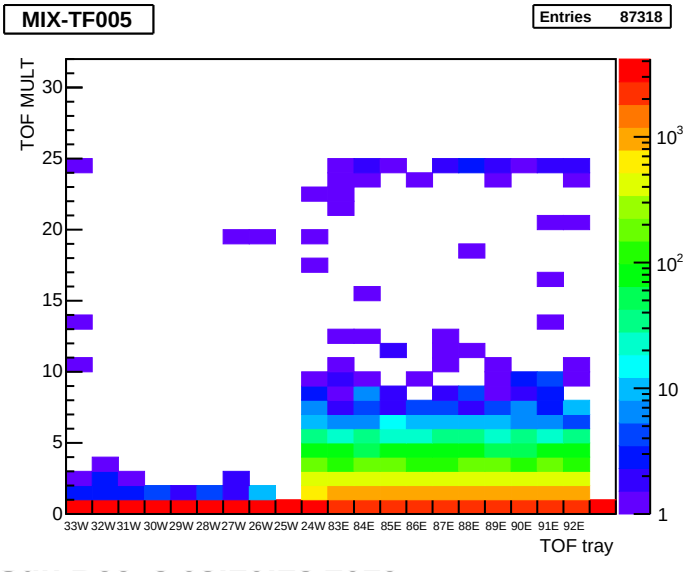

BunchId7Bit (TOFmult window)
Entries 0
10
1
10^{-1}
10^{-2}
0
20
40
60
80
100
120

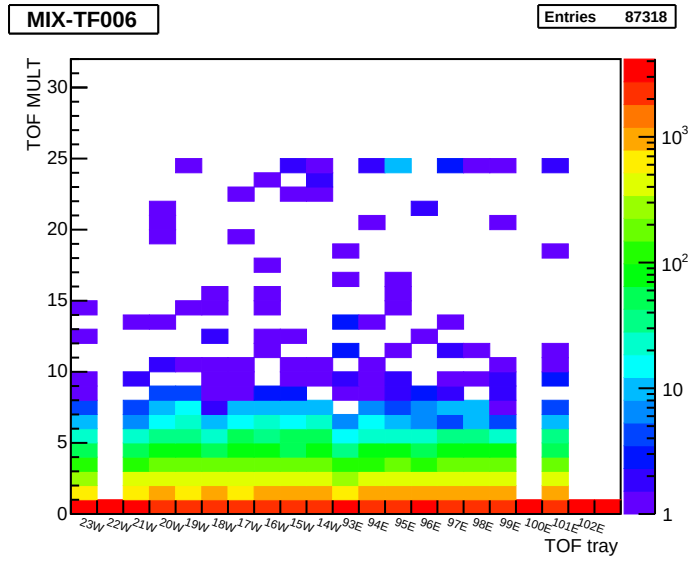

MIX-TF001
Entries 87318
TOF MULT
TOF tray

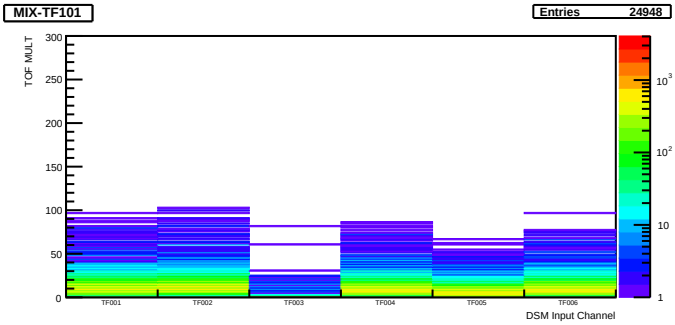

MIX-TF002
Entries 87318
TOF MULT
TOF tray

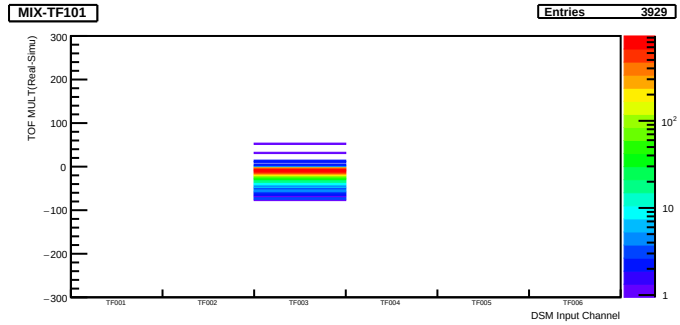

MIX-TF003
Entries 87318
TOF MULT
TOF tray

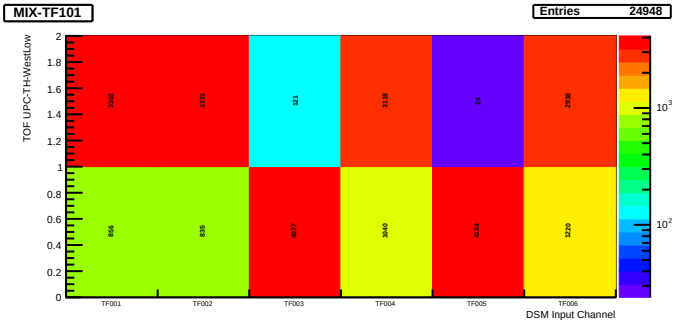

MIX-TF004
Entries 87318
TOF MULT
TOF tray

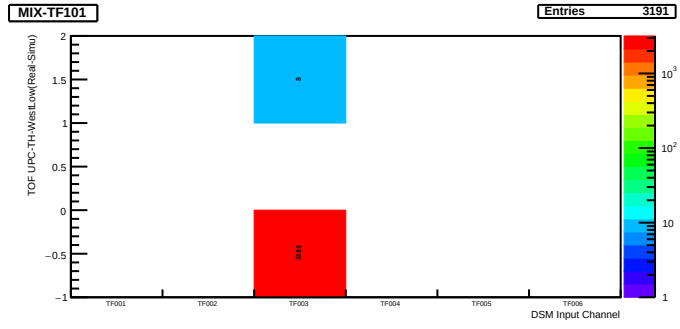

MIX-TF005
Entries 87318
TOF MULT
TOF tray

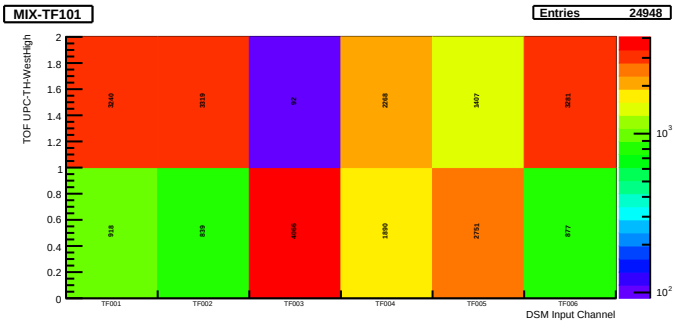

MIX-TF006
Entries 87318
TOF MULT
TOF tray

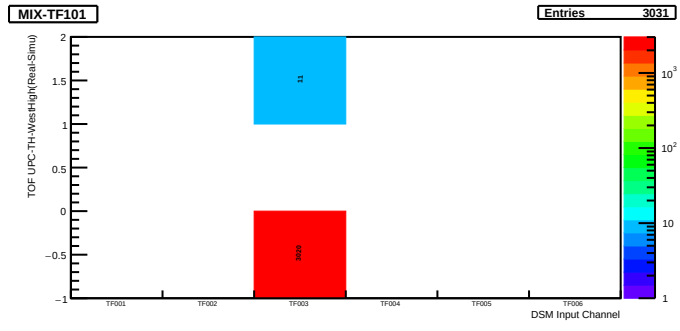

MIX-TF101
Entries 24948
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

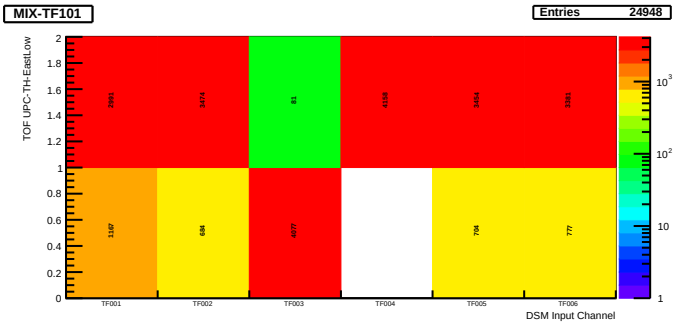

MIX-TF101
Entries 3929
TOF MULT(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

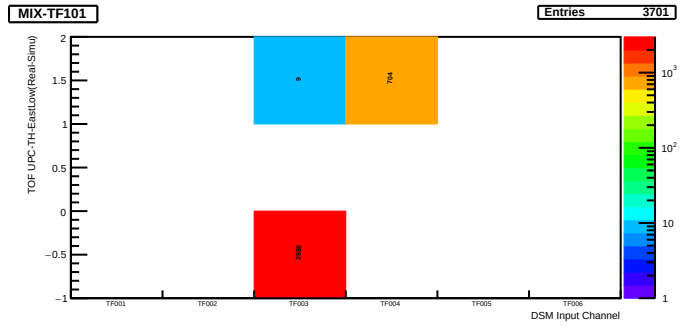

MIX-TF101
Entries 24948
TOF UPC-TH-WestLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

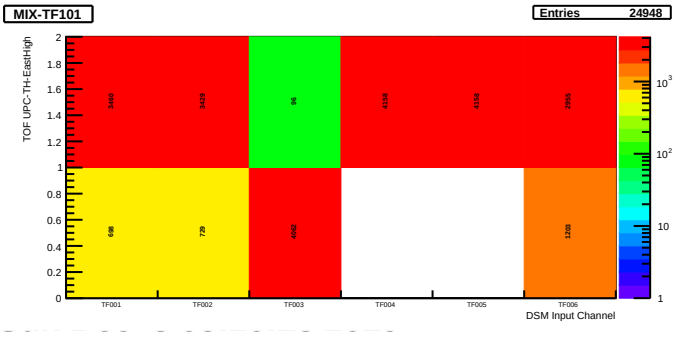

MIX-TF101
Entries 3191
TOF UPC-TH-WestLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

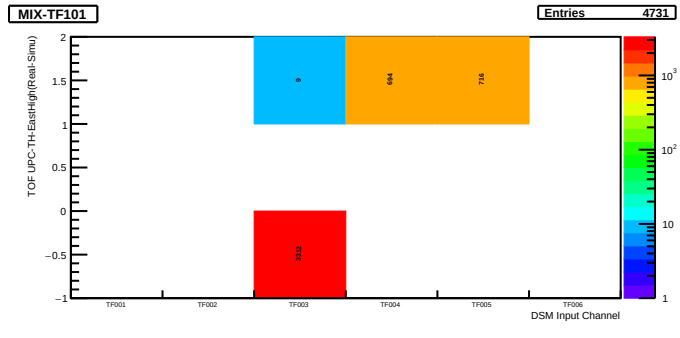

MIX-TF101
Entries 24948
TOF UPC-TH-WestHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

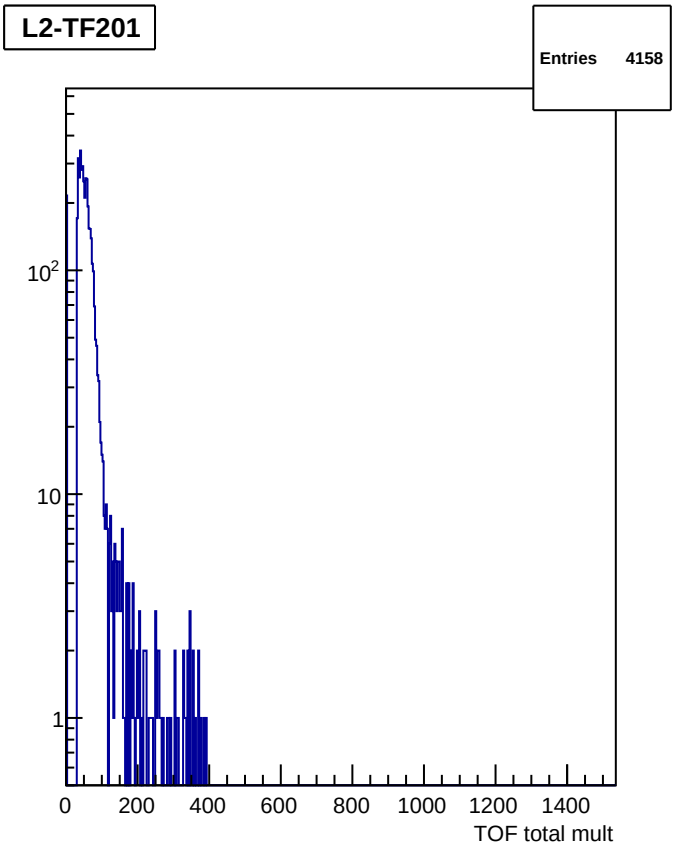

MIX-TF101
Entries 3031
TOF UPC-TH-WestHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

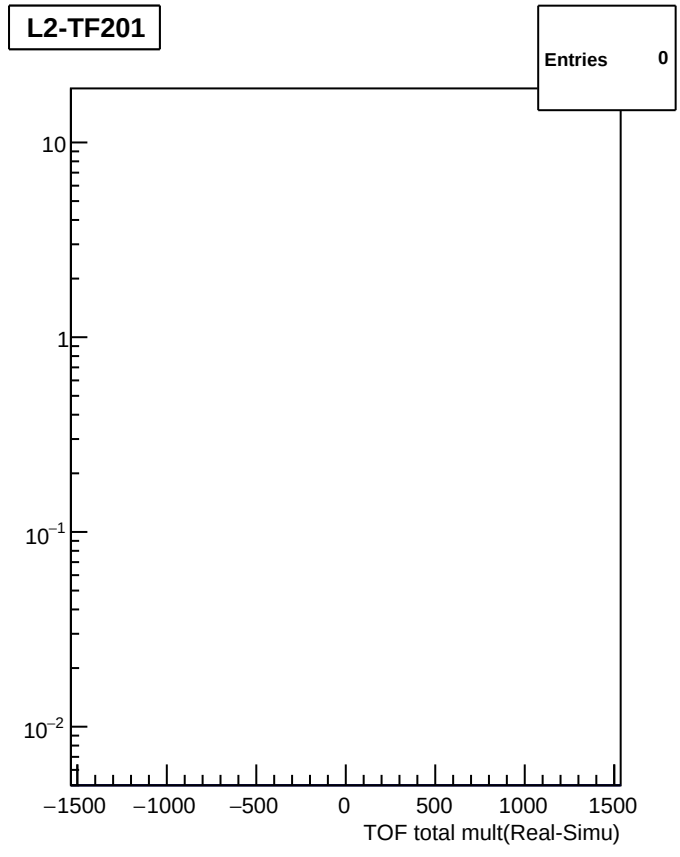

MIX-TF101
Entries 24948
TOF UPC-TH-EastLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

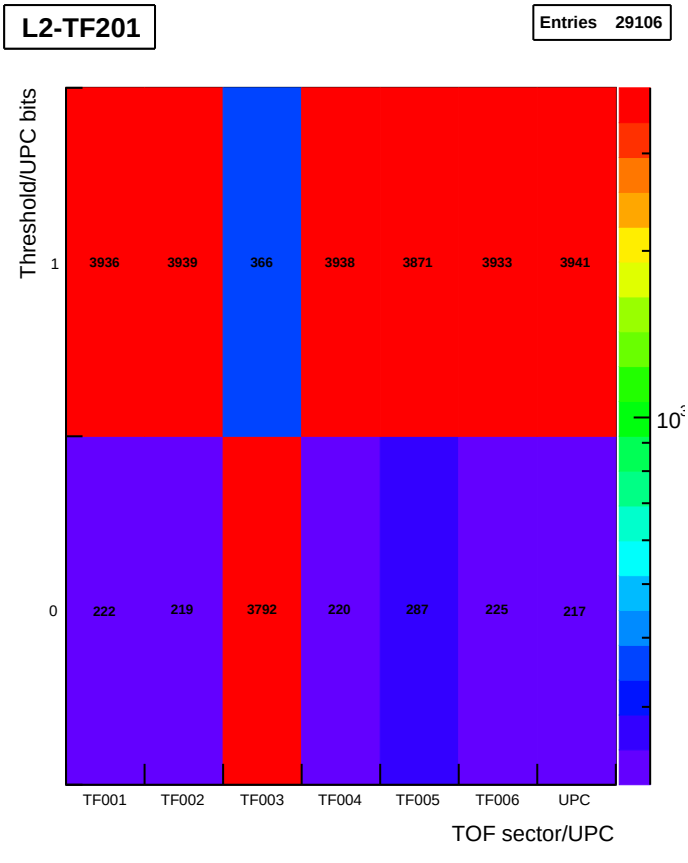

MIX-TF101
Entries 3701
TOF UPC-TH-EastLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

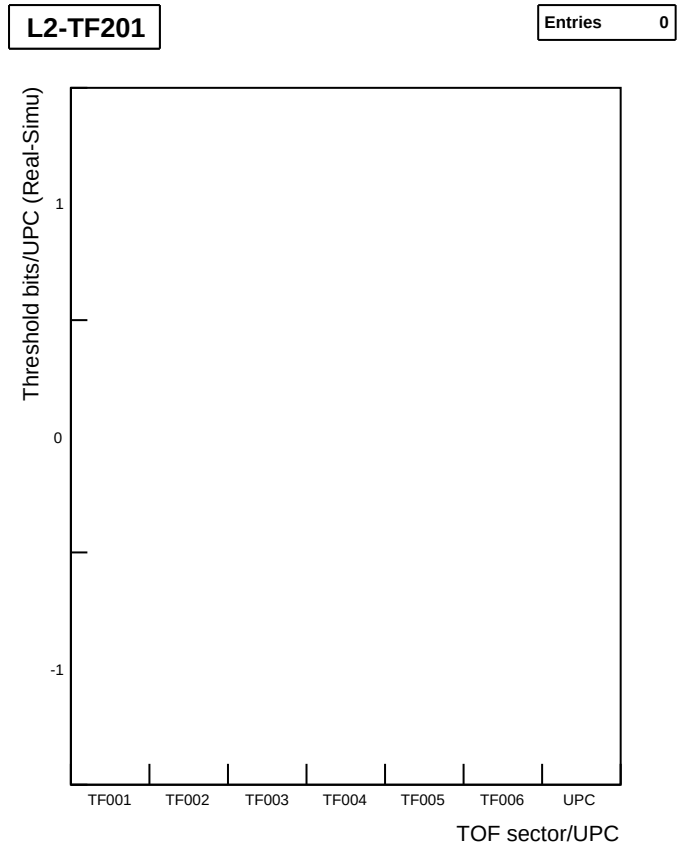

MIX-TF101
Entries 24948
TOF UPC-TH-EastHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

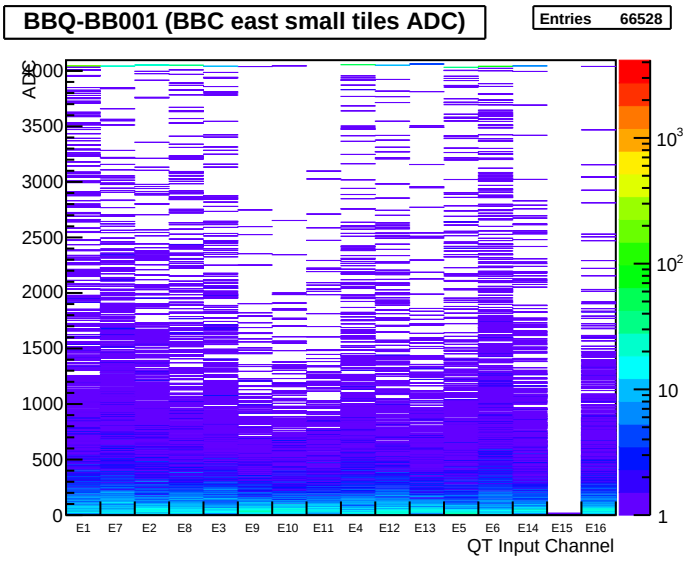

MIX-TF101
Entries 4731
TOF UPC-TH-EastHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

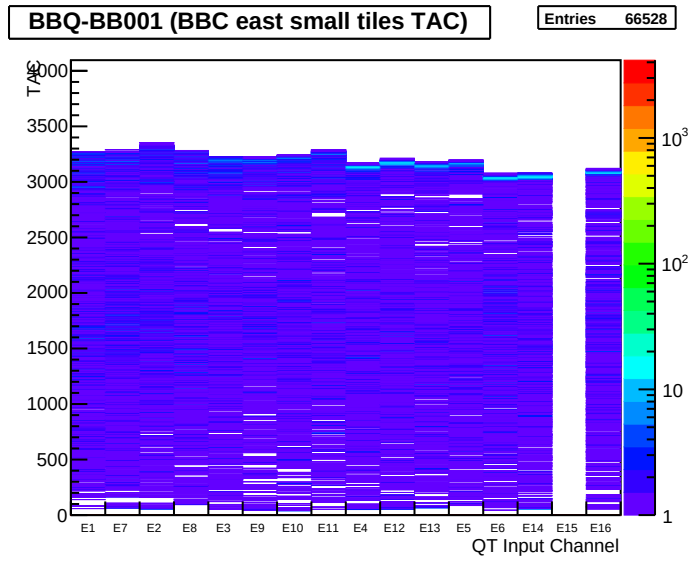

L2-TF201
Entries 4158
10^2
10
1
0
200
400
600
800
1000
1200
1400
TOF total mult

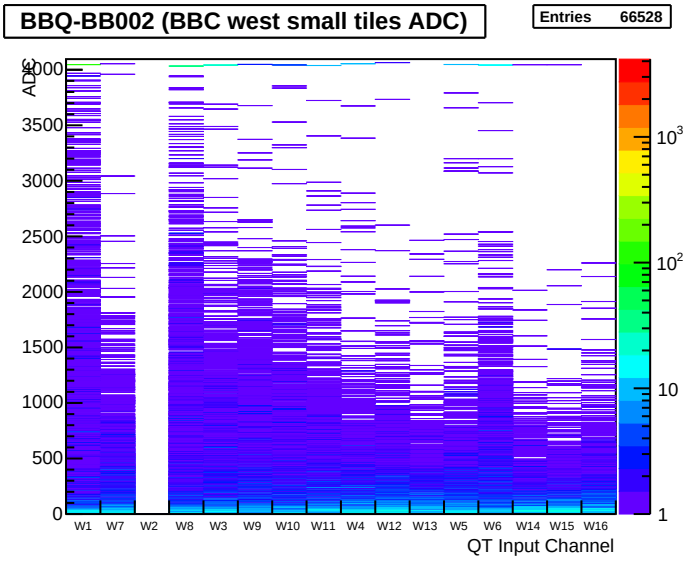

L2-TF201
Entries 0
10
1
10^{-1}
10^{-2}
−1500
−1000
−500
0
500
1000
1500
TOF total mult(Real-Simu)

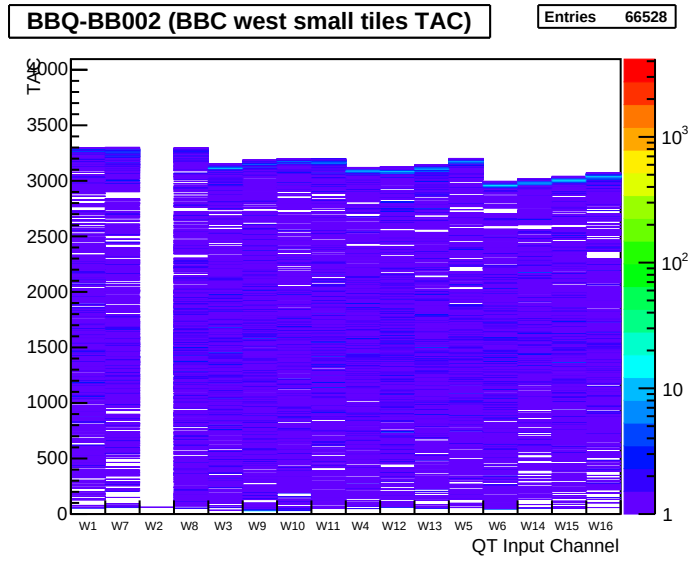

L2-TF201
Entries 29106
Threshold/UPC bits
1
0
3936
3939
366
3938
3871
3933
3941
222
219
3792
220
287
225
217
TF001
TF002
TF003
TF004
TF005
TF006
UPC
TOF sector/UPC

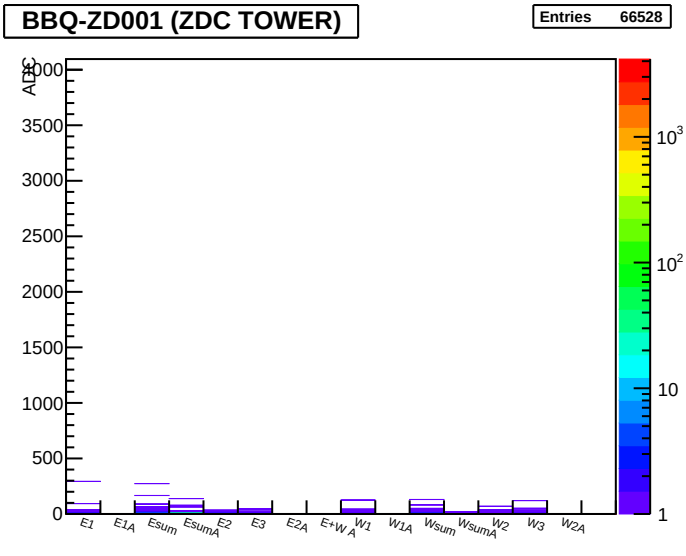

L2-TF201
Entries 0
Threshold bits/UPC (Real-Simu)
1
0
-1
TF001
TF002
TF003
TF004
TF005
TF006
UPC
TOF sector/UPC

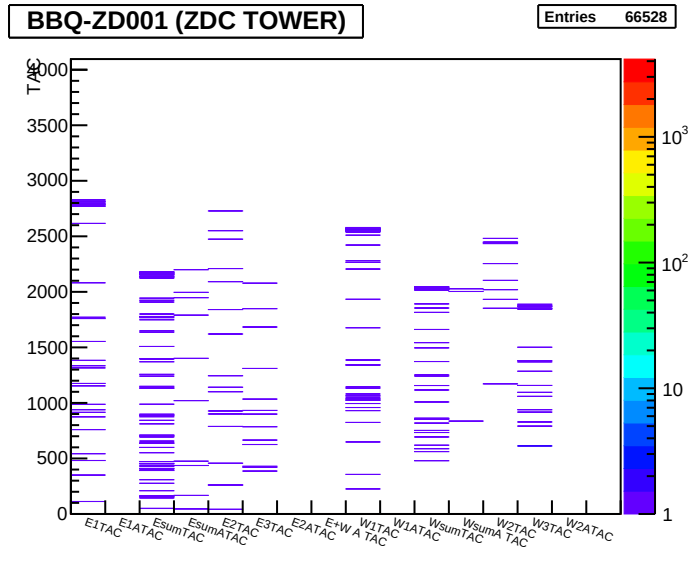

BBQ-BB001 (BBC east small tiles ADC)
Entries 66528
ADC
QT Input Channel
E1 E7 E2 E8 E3 E9 E10 E11 E4 E12 E13 E5 E6 E14 E15 E16

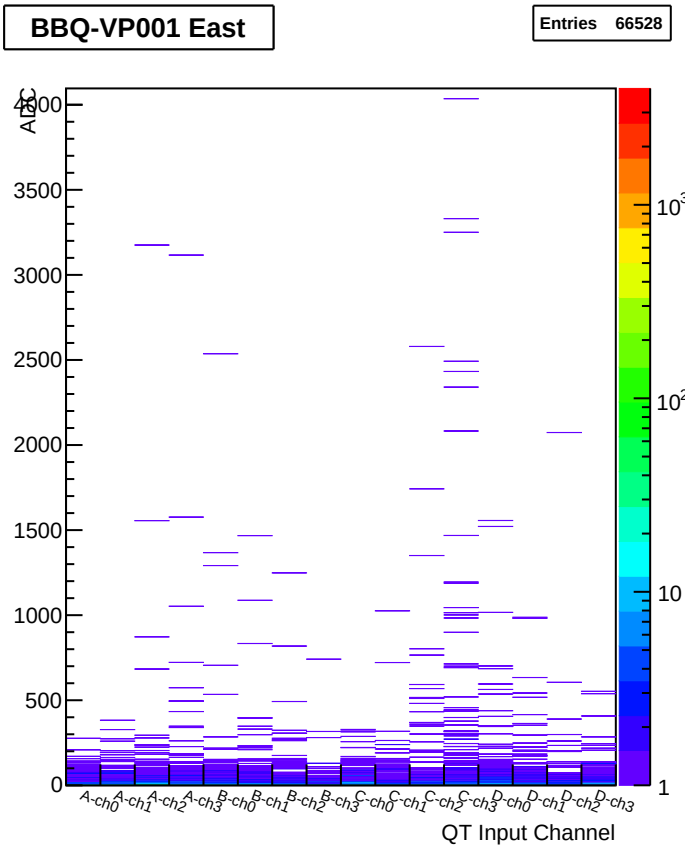

BBQ-BB001 (BBC east small tiles TAC)
Entries 66528
TAC
QT Input Channel
E1 E7 E2 E8 E3 E9 E10 E11 E4 E12 E13 E5 E6 E14 E15 E16

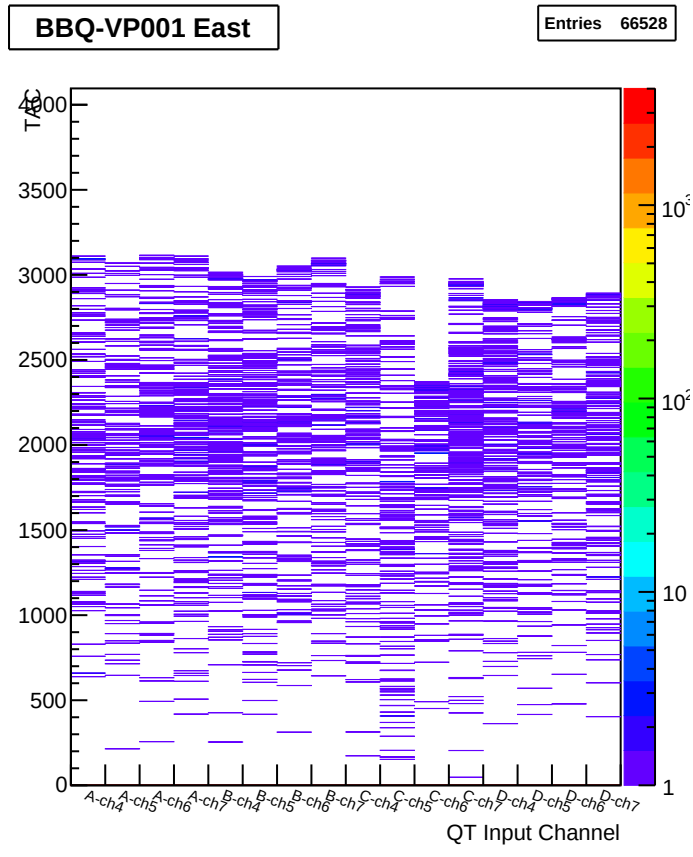

BBQ-BB002 (BBC west small tiles ADC)
Entries 66528
ADC
QT Input Channel
W1 W7 W2 W8 W3 W9 W10 W11 W4 W12 W13 W5 W6 W14 W15 W16

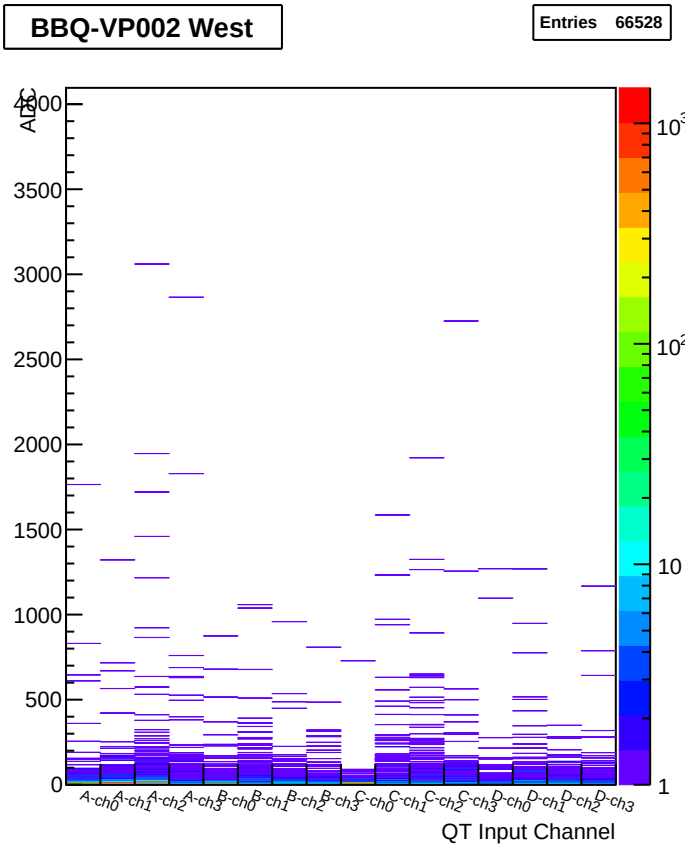

BBQ-BB002 (BBC west small tiles TAC)
Entries 66528
TAC
QT Input Channel
W1 W7 W2 W8 W3 W9 W10 W11 W4 W12 W13 W5 W6 W14 W15 W16

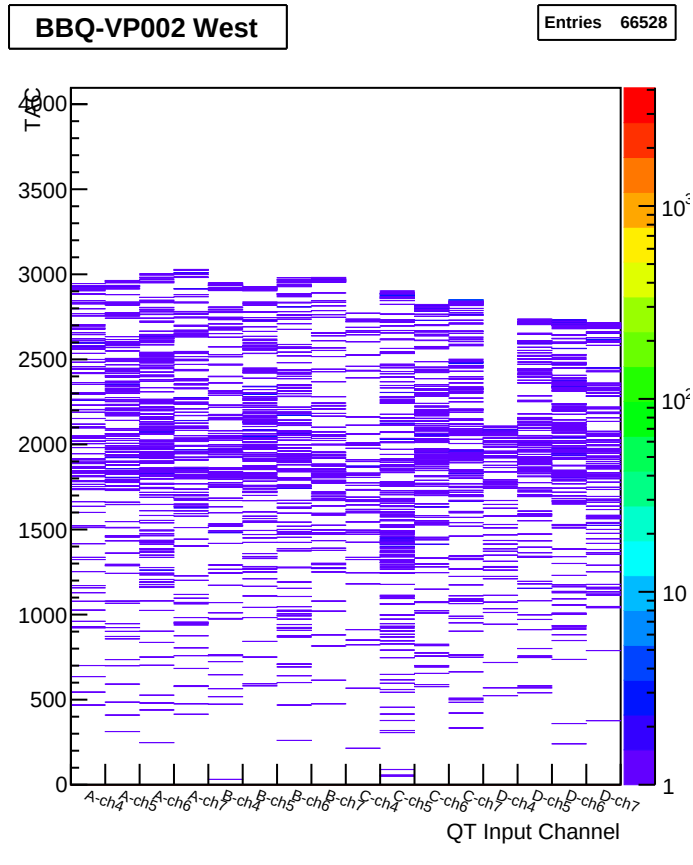

BBQ-ZD001 (ZDC TOWER)
Entries 66528
ADC
E1 E1A Esum EsumA E2 E3 E2A E+W A W1 W1A Wsum WsumA W2 W3 W2A

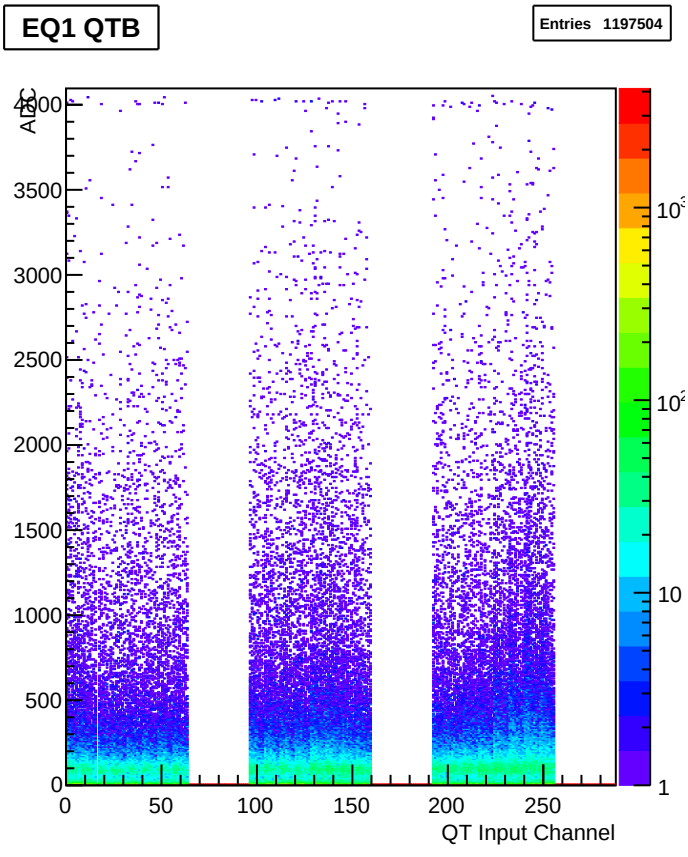

BBQ-ZD001 (ZDC TOWER)
Entries 66528
TAC
E1TAC E1ATAC EsumTAC EsumATAC E2TAC E3TAC E2ATAC E+W A TAC W1TAC W1ATAC WsumTAC WsumA TAC W2TAC W3TAC W2ATAC

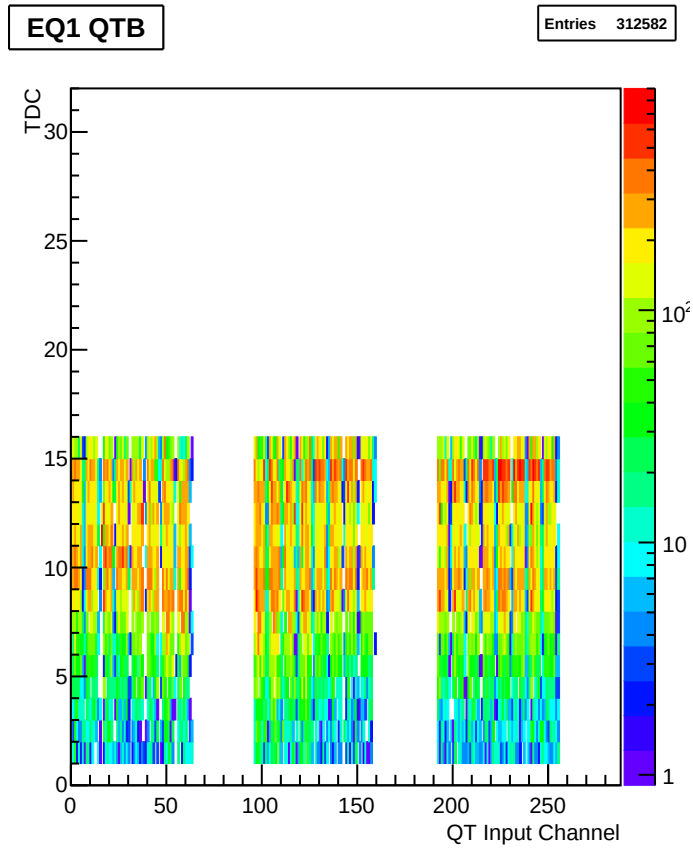

BBQ-VP001 East
Entries 66528
ADC
QT Input Channel

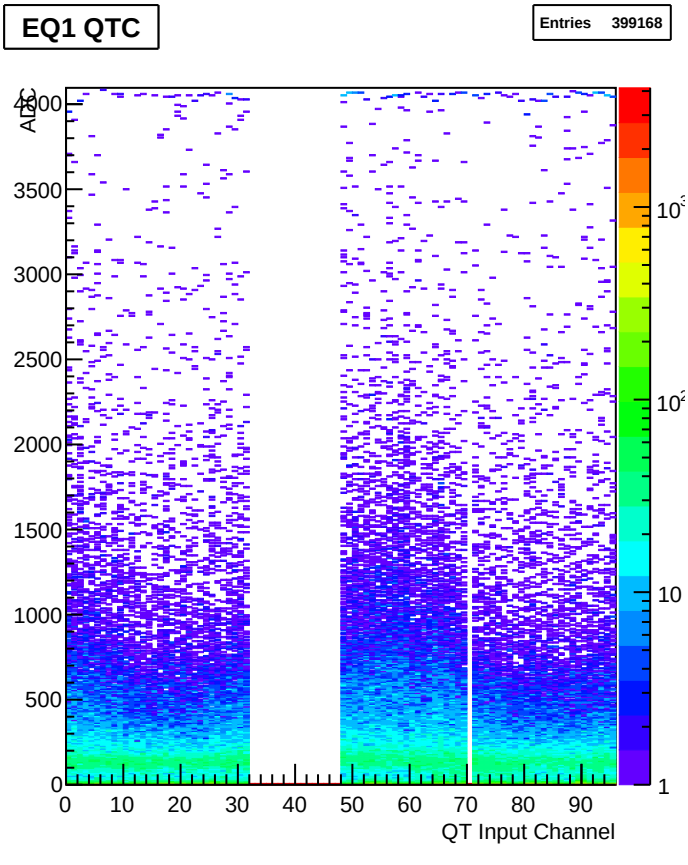

BBQ-VP001 East
Entries 66528
TADC
QT Input Channel

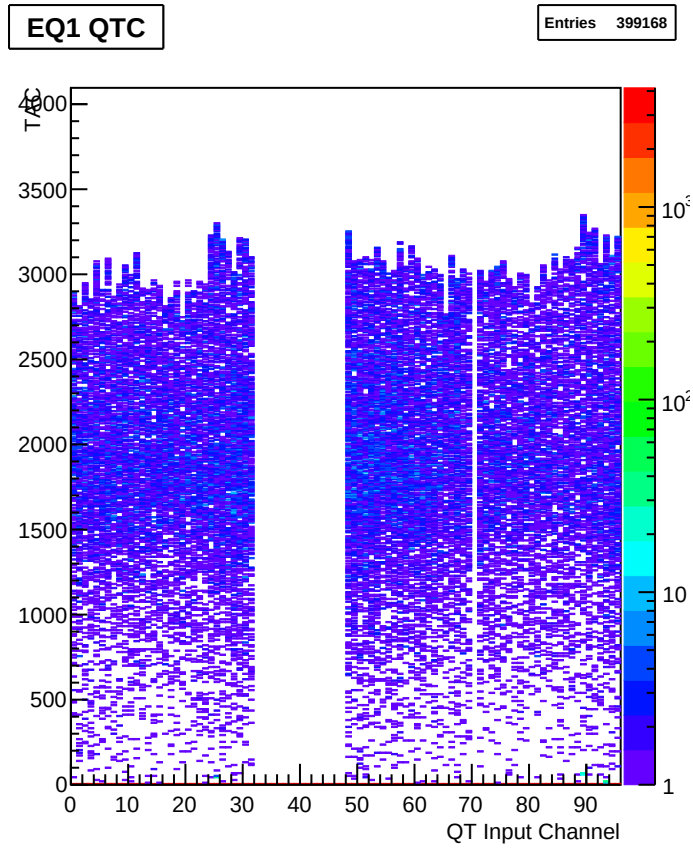

BBQ-VP002 West
Entries 66528
ADC
QT Input Channel

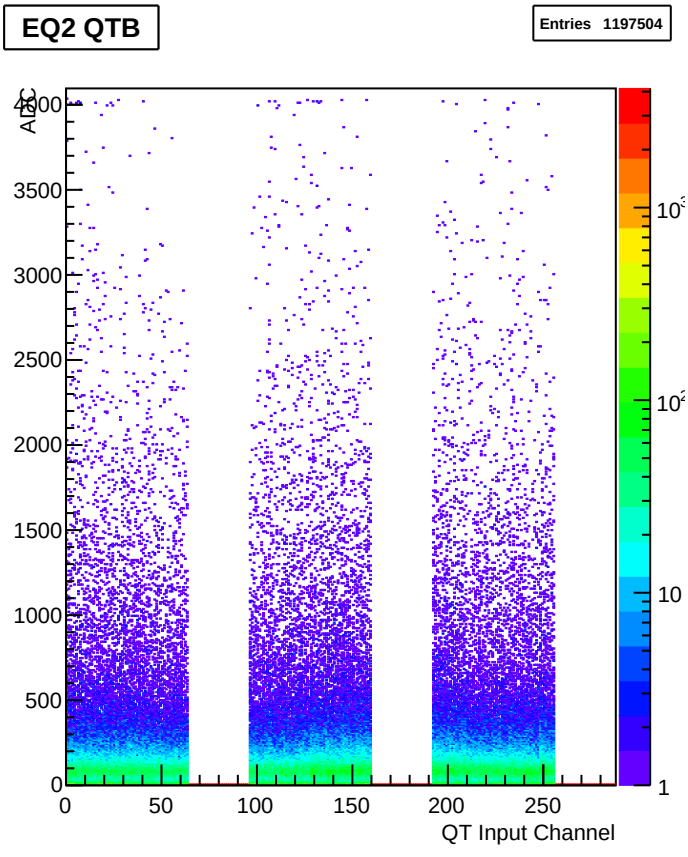

BBQ-VP002 West
Entries 66528
TADC
QT Input Channel

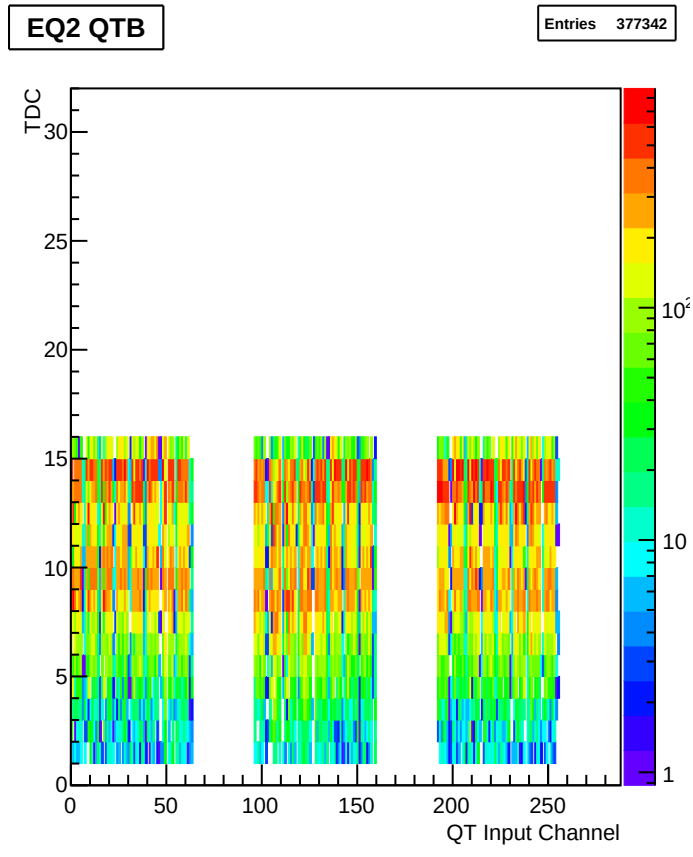

EQ1 QTB
Entries 1197504
ADC
QT Input Channel

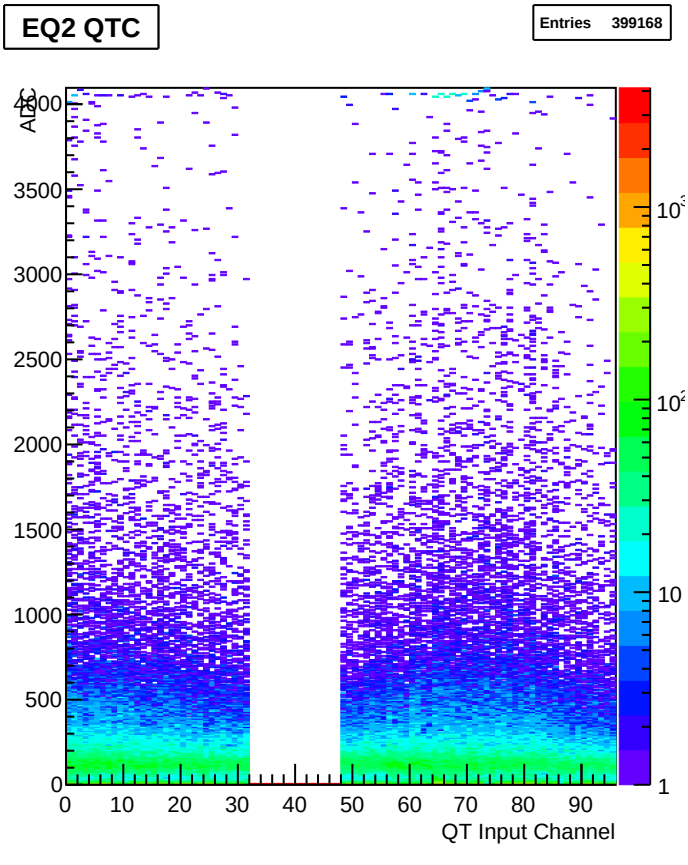

EQ1 QTB
Entries 312582
TDC
QT Input Channel

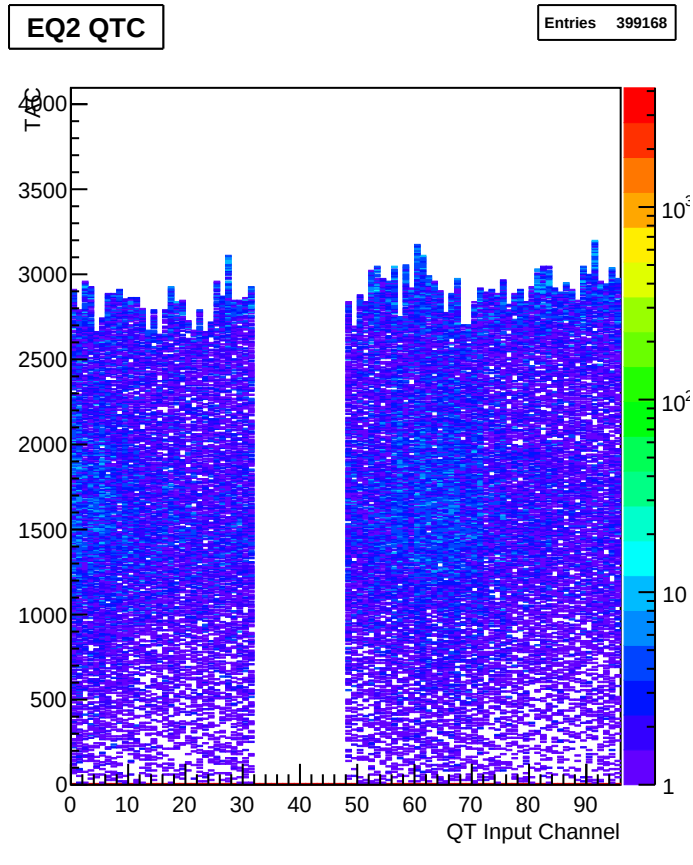

EQ1 QTC
Entries 399168
ADC
QT Input Channel

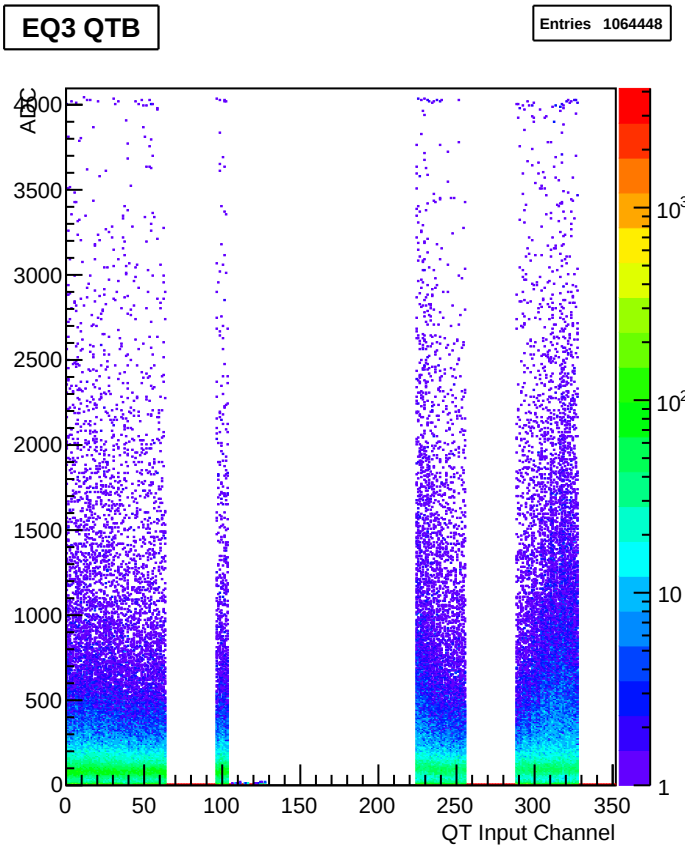

EQ1 QTC
Entries 399168
TADC
QT Input Channel

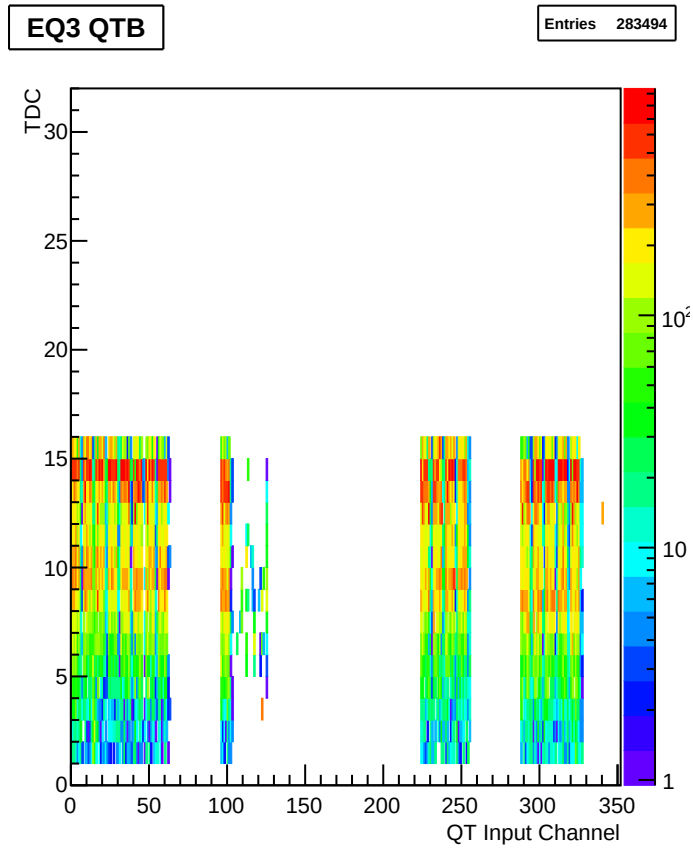

EQ2 QTB
Entries 1197504
ADC
QT Input Channel

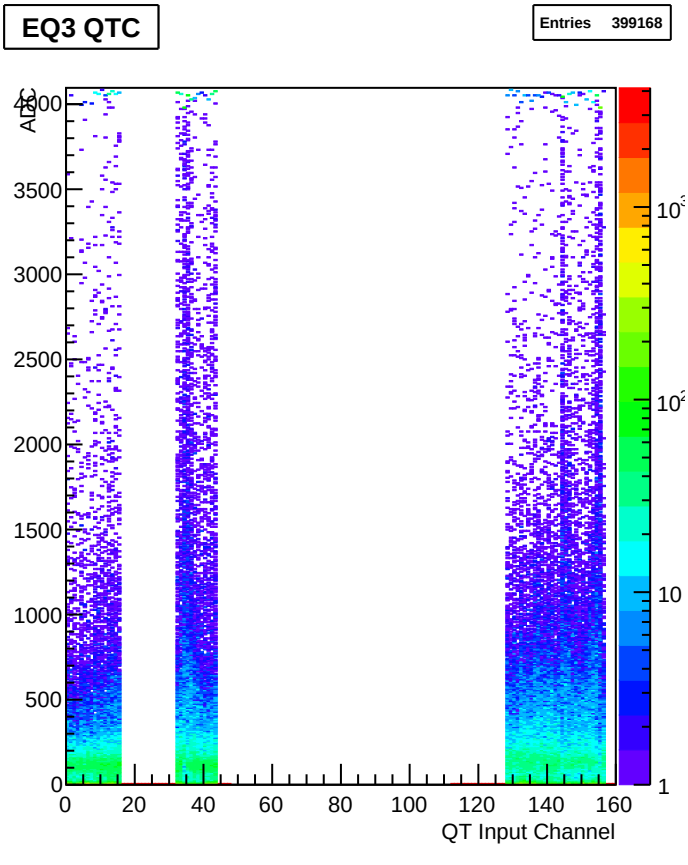

EQ2 QTB
Entries 377342
TDC
QT Input Channel

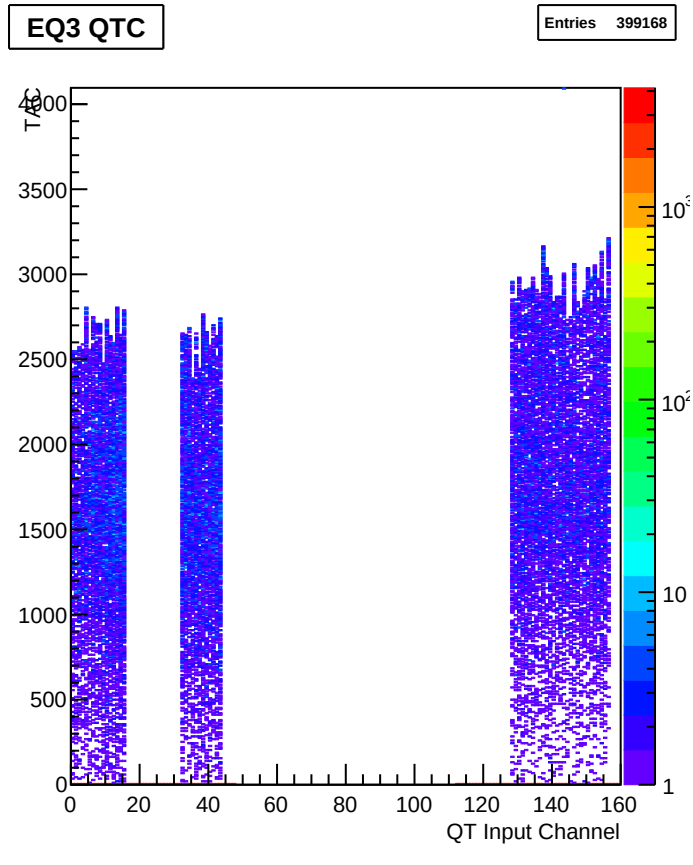

EQ2 QTC
Entries 399168
ADC
QT Input Channel

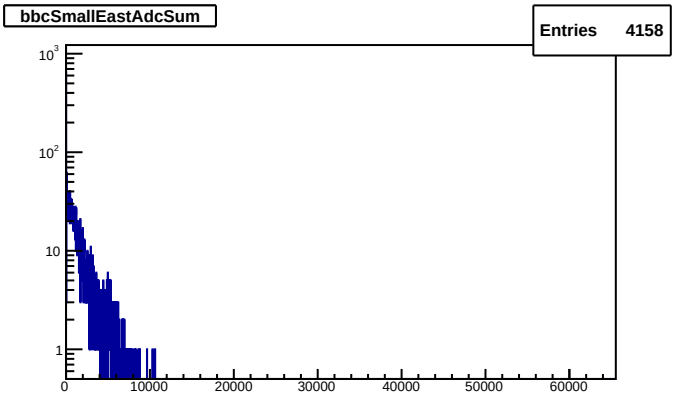

EQ2 QTC
Entries 399168
TADC
QT Input Channel

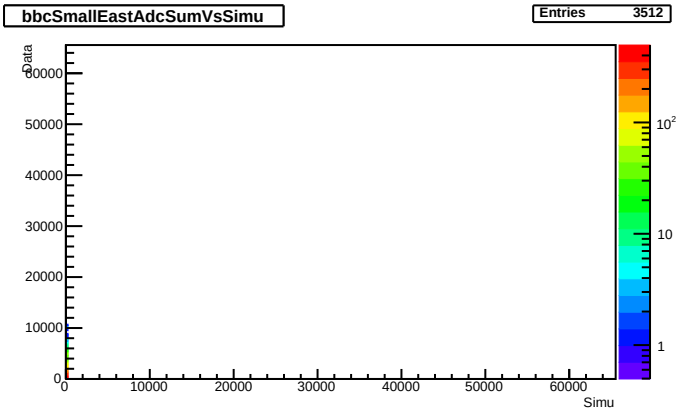

EQ3 QTB
Entries 1064448
ADC
QT Input Channel

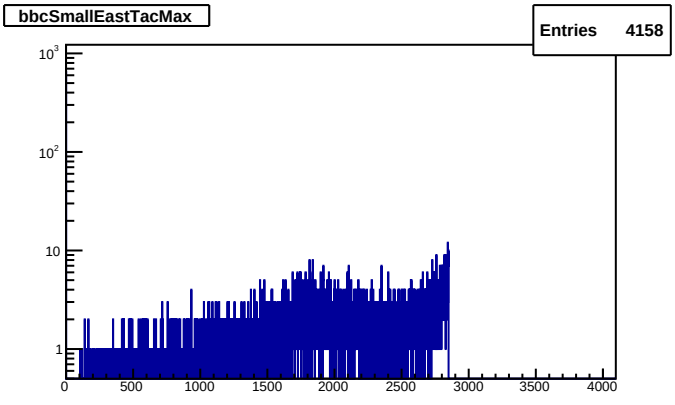

EQ3 QTB
Entries 283494
TDC
QT Input Channel

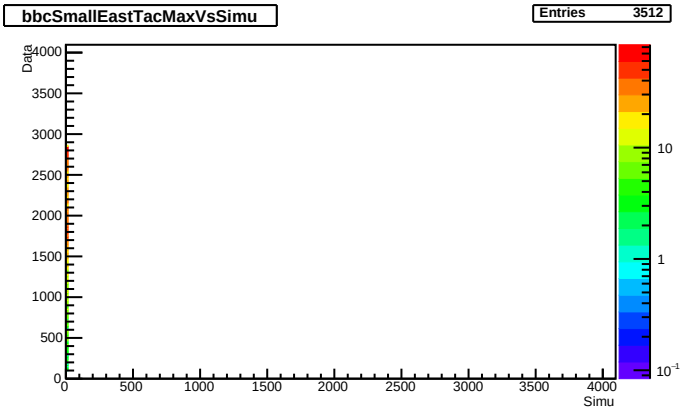

EQ3 QTC
Entries 399168
ADC
QT Input Channel

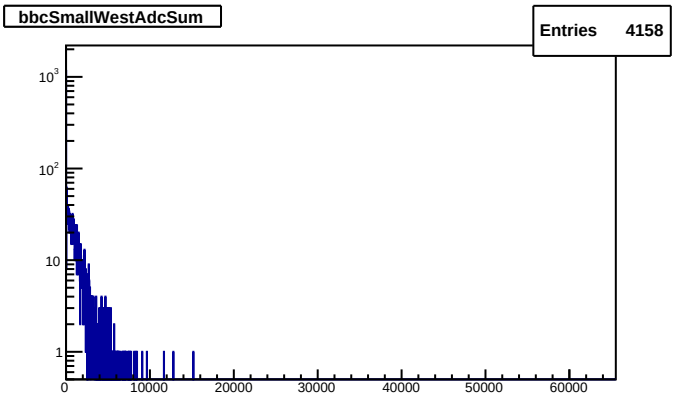

EQ3 QTC
Entries 399168
TADC
QT Input Channel

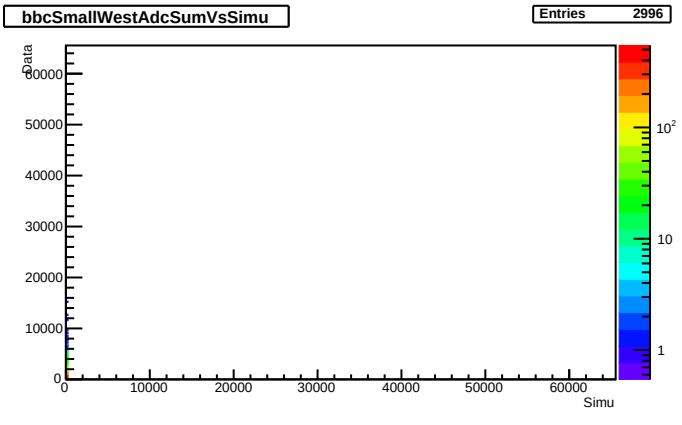

bbcSmallEastAdcSum
Entries 4158

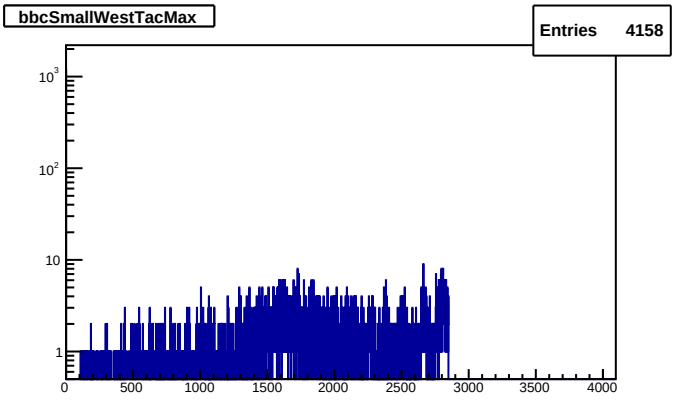

bbcSmallEastAdcSumVsSimu
Entries 3512
Data
Simu

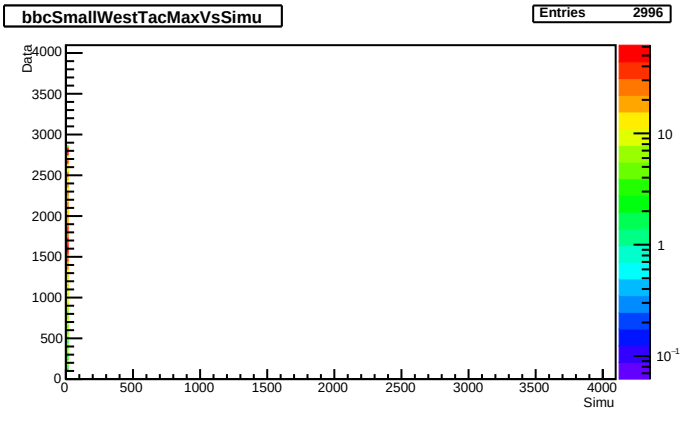

bbcSmallEastTacMax
Entries 4158

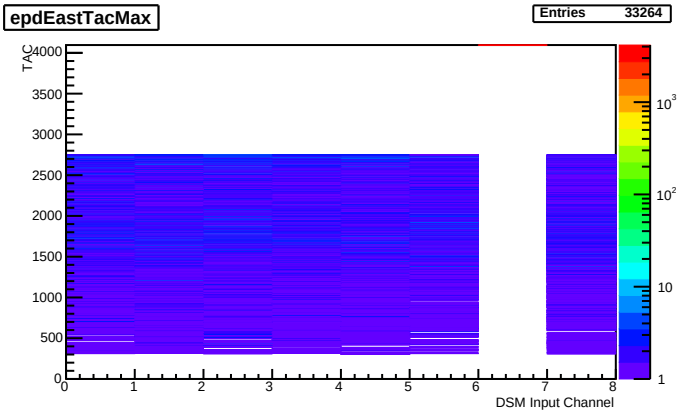

bbcSmallEastTacMaxVsSimu
Entries 3512
Data
Simu

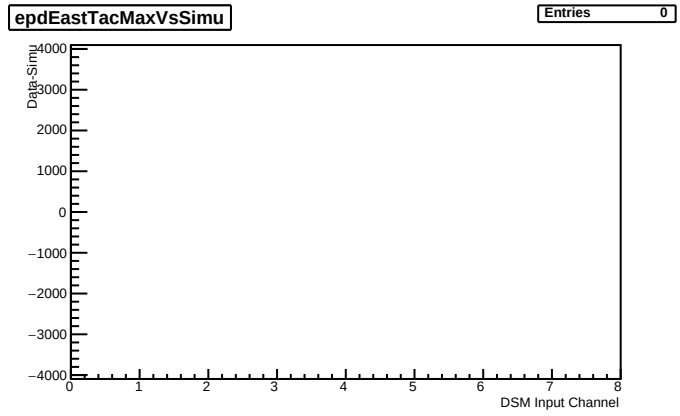

bbcSmallWestAdcSum
Entries 4158

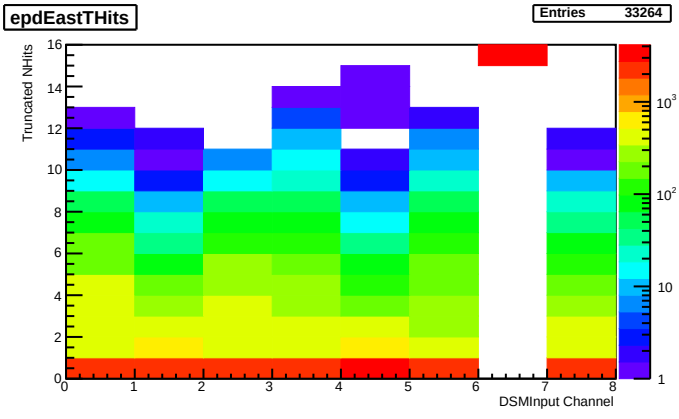

bbcSmallWestAdcSumVsSimu
Entries 2996
Data
Simu

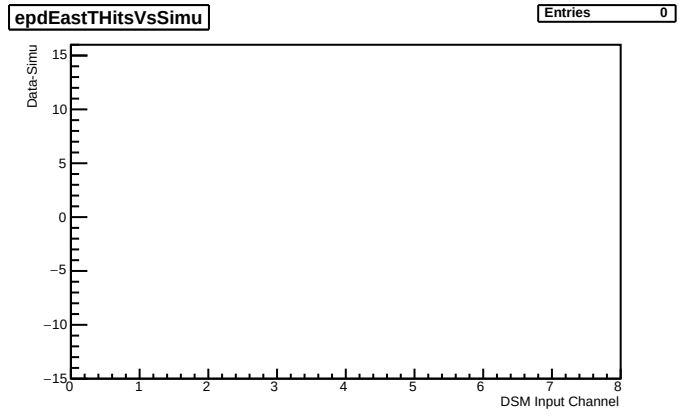

bbcSmallWestTacMax
Entries 4158

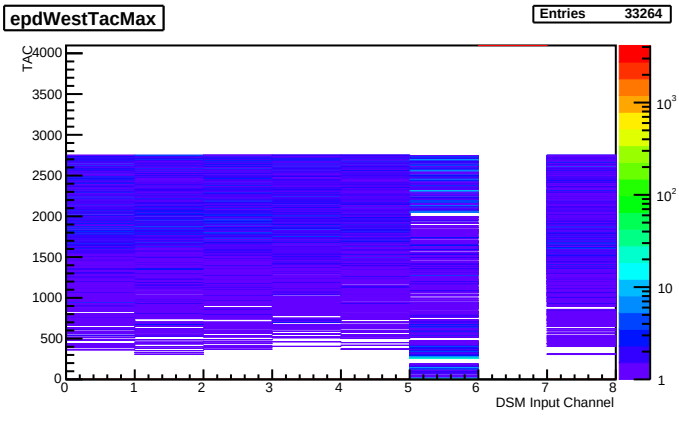

bbcSmallWestTacMaxVsSimu
Entries 2996
Data
Simu

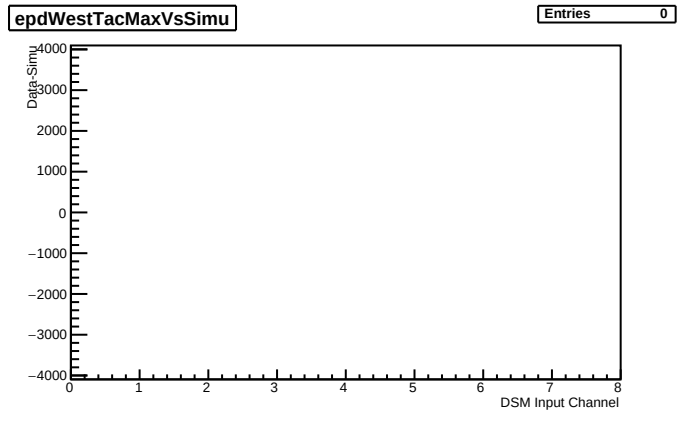

epdEastTacMax
Entries 33264
TAC
DSM Input Channel

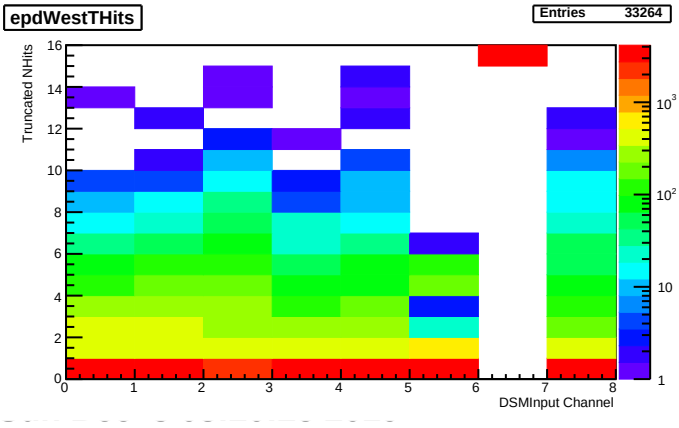

epdEastTacMaxVsSimu
Entries 0
Data-Simu
DSM Input Channel

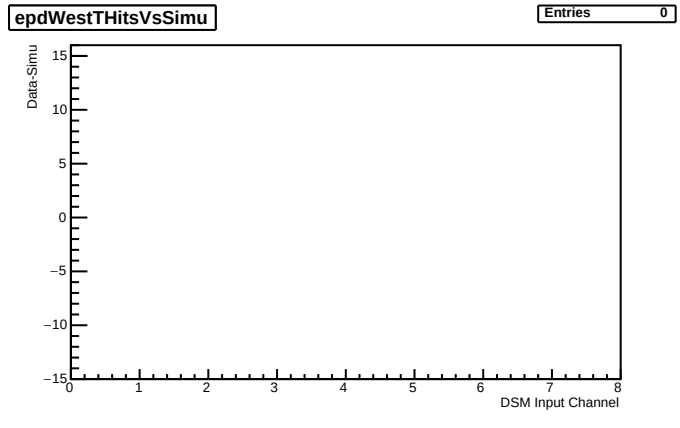

epdEastTHits
Entries 33264
Truncated NHits
DSMInput Channel

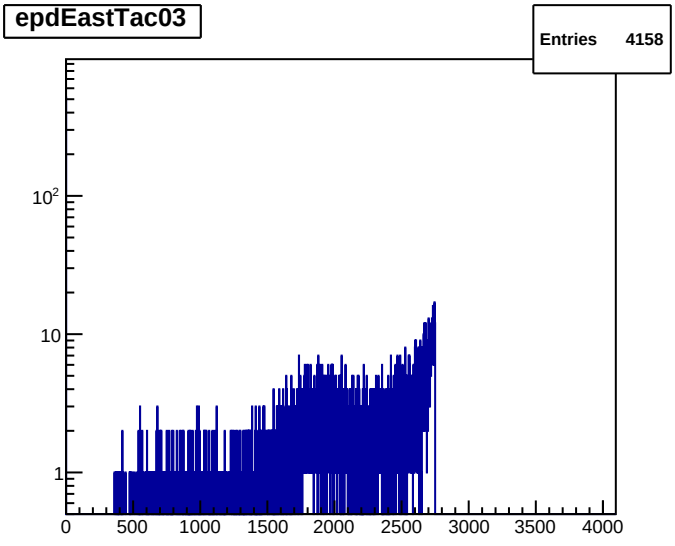

epdEastTHitsVsSimu
Entries 0
Data-Simu
DSM Input Channel

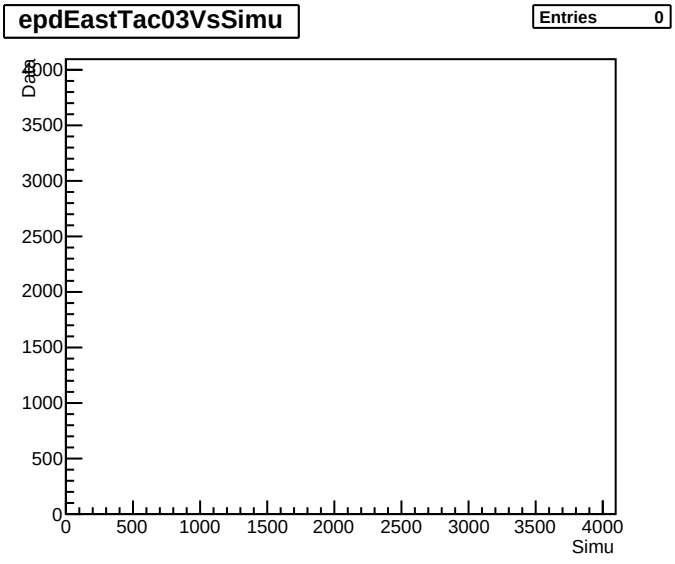

epdWestTacMax
Entries 33264
TAC
DSM Input Channel

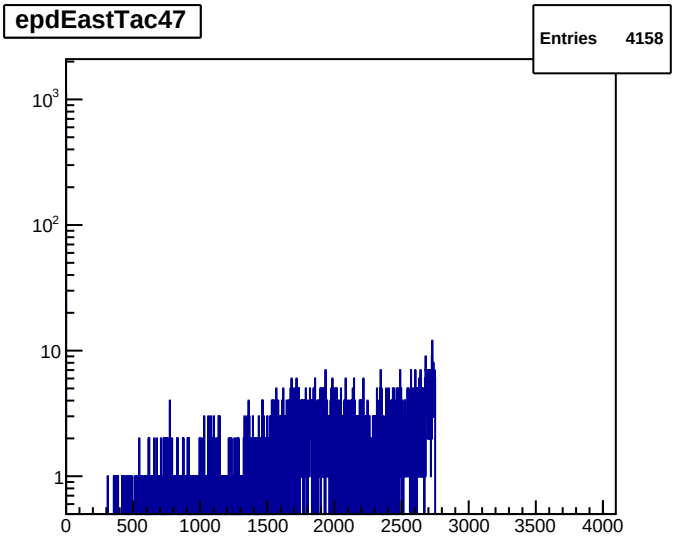

epdWestTacMaxVsSimu
Entries 0
Data-Simu
DSM Input Channel

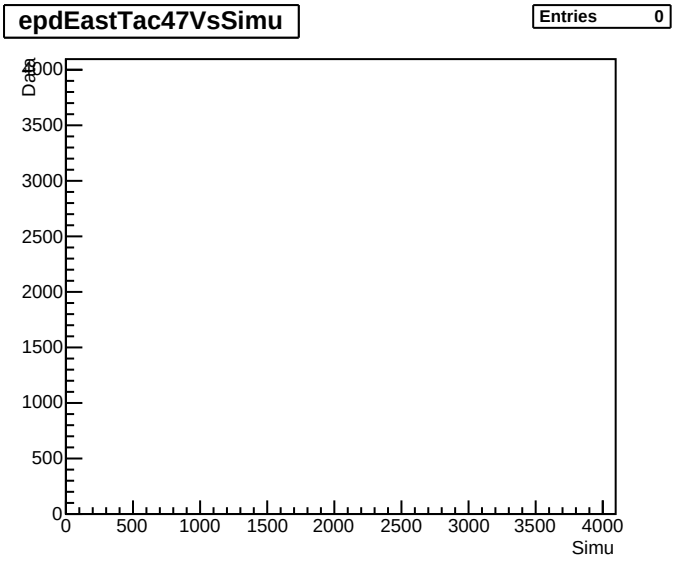

epdWestTHits
Entries 33264
Truncated NHits
DSMInput Channel

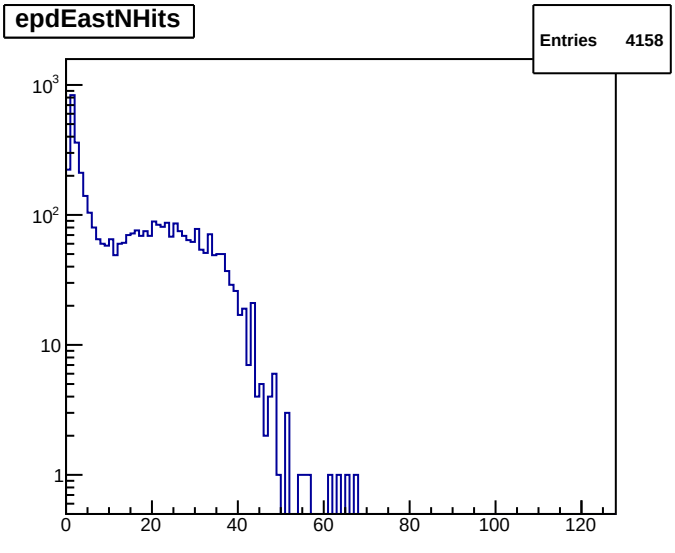

epdWestTHitsVsSimu
Entries 0
Data-Simu
DSM Input Channel

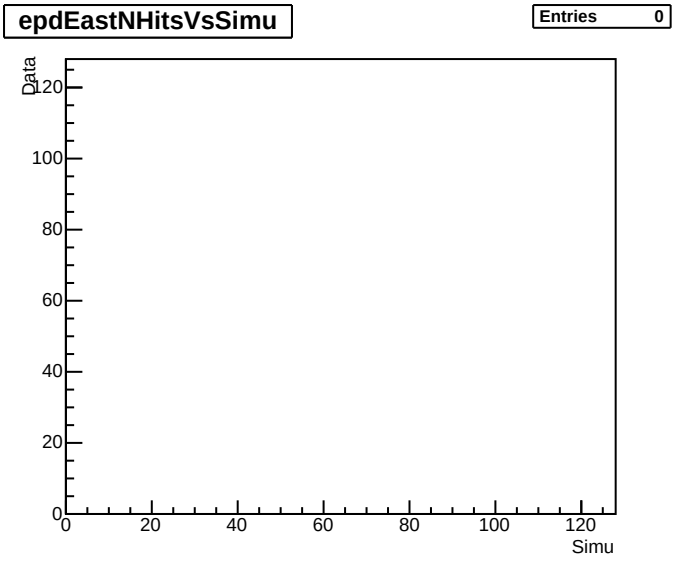

epdEastTac03
Entries 4158
10^2
10
1
0 500 1000 1500 2000 2500 3000 3500 4000

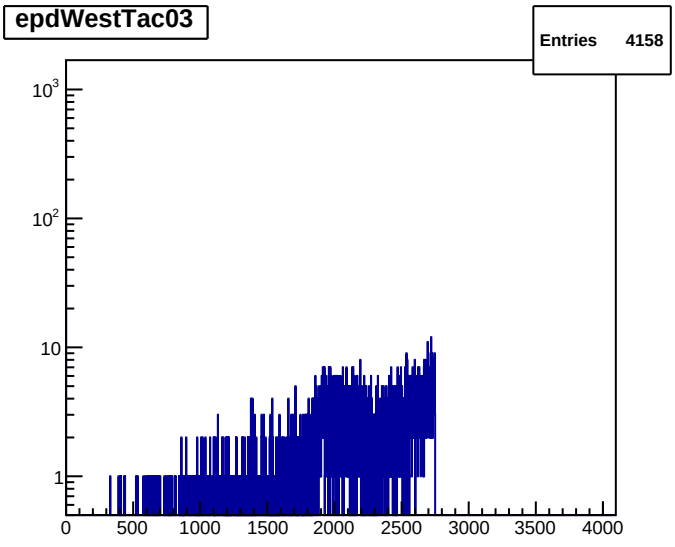

epdEastTac03VsSimu
Entries 0
Data
4000 3500 3000 2500 2000 1500 1000 500 0
0 500 1000 1500 2000 2500 3000 3500 4000
Simu

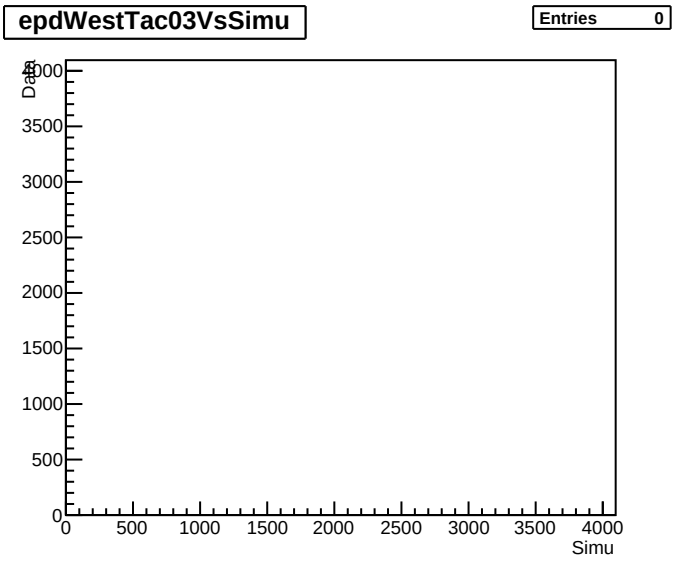

epdEastTac47
Entries 4158
10^3
10^2
10
1
0 500 1000 1500 2000 2500 3000 3500 4000


epdEastTac47VsSimu
Entries 0
Data
4000 3500 3000 2500 2000 1500 1000 500 0
0 500 1000 1500 2000 2500 3000 3500 4000
Simu


epdEastNHits
Entries 4158
10^3
10^2
10
1
0 20 40 60 80 100 120


epdEastNHitsVsSimu
Entries 0
Data
120 100 80 60 40 20 0
0 20 40 60 80 100 120
Simu


epdWestTac03
Entries 4158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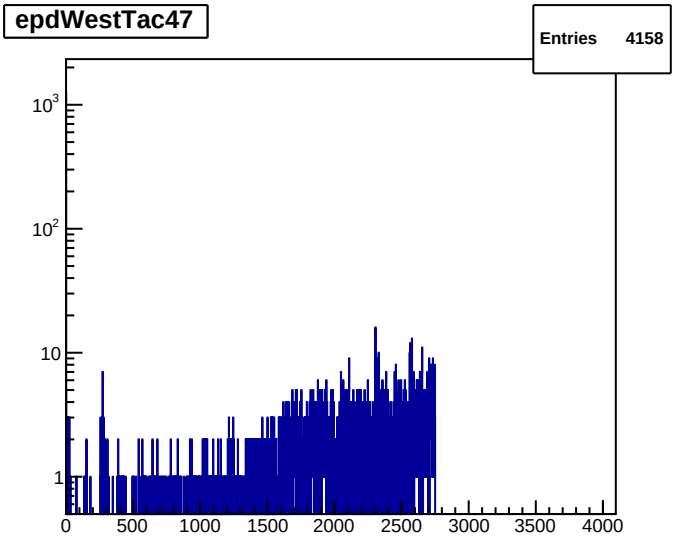
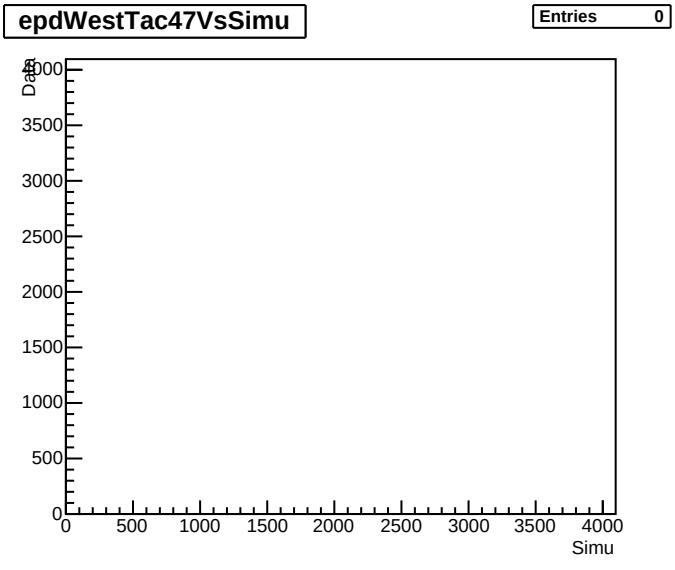
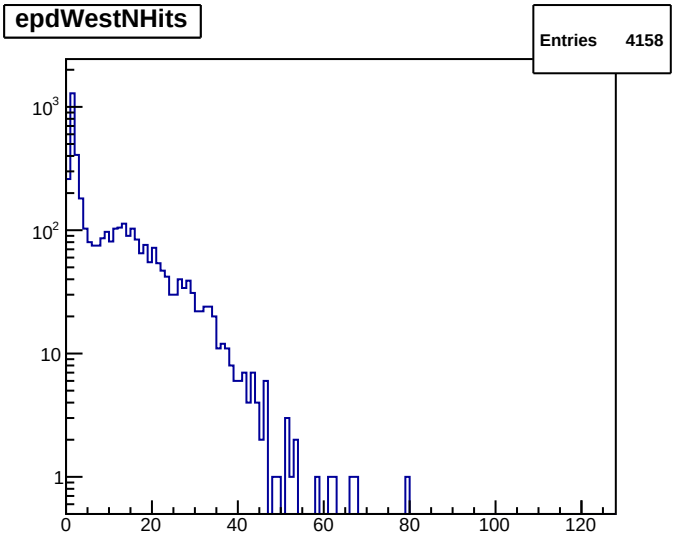
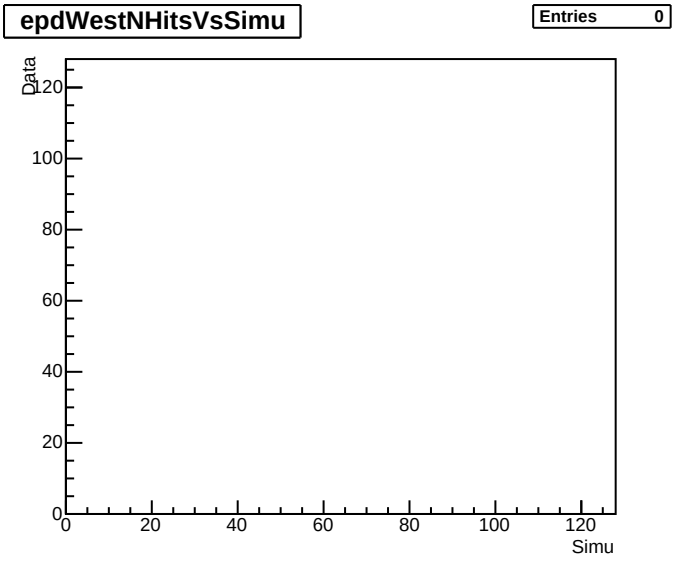
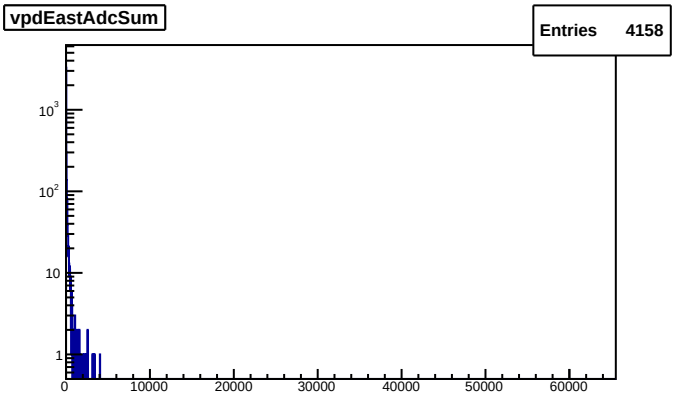
Entries 0
Data
Simu

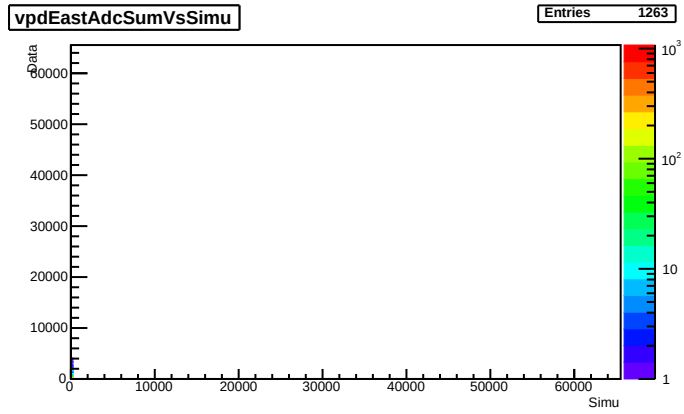

epdWestTac47
Entries 4158

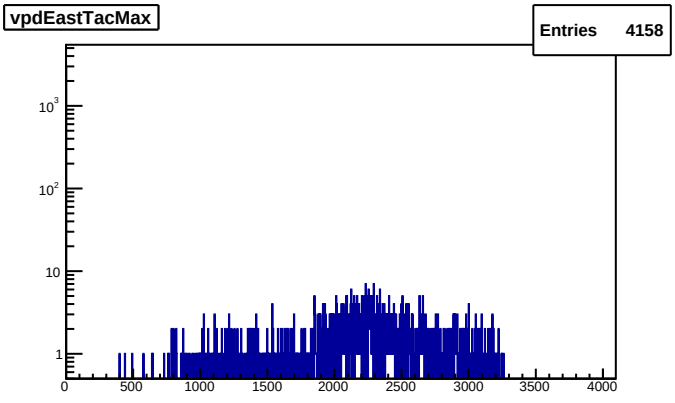

epdWestTac47VsSimu
Entries 0
Data
Simu

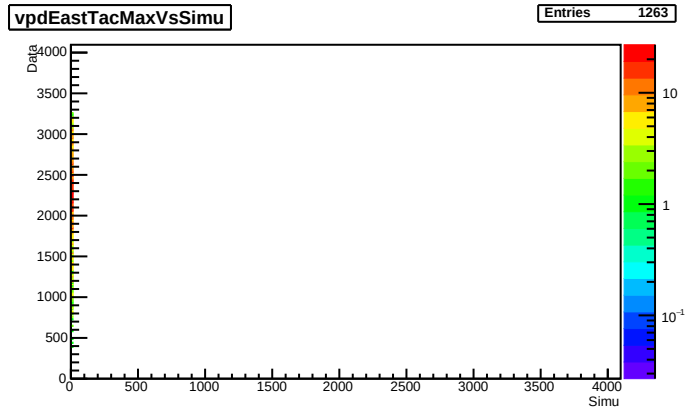

epdWestNHits
Entries 4158

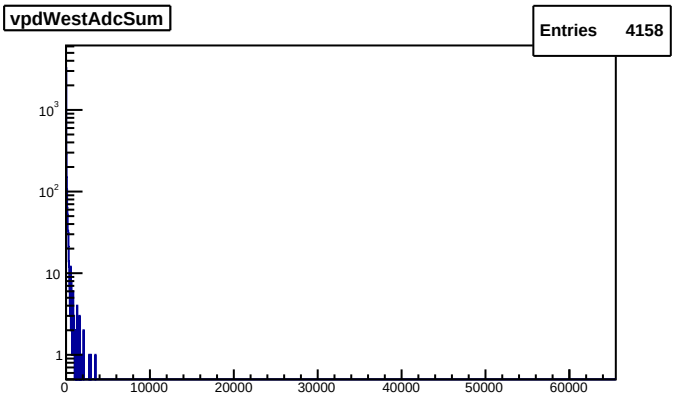

epdWestNHitsVsSimu
Entries 0
Data
Simu

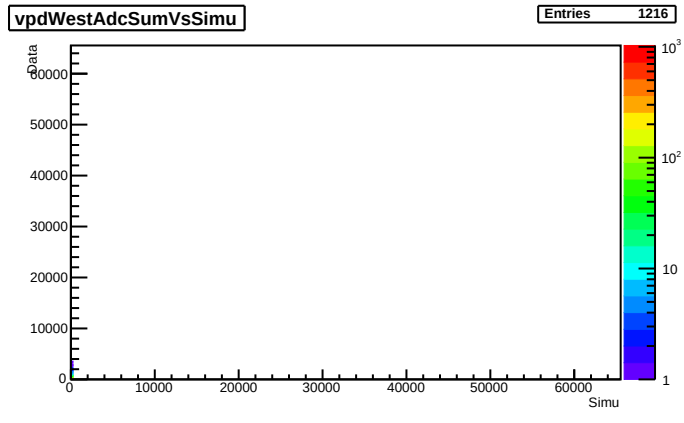

vpdEastAdcSum
Entries 4158

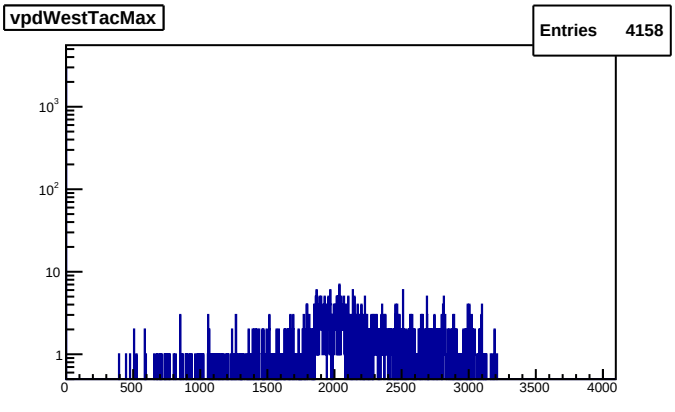

vpdEastAdcSumVsSimu
Entries 1263
Data
Simu

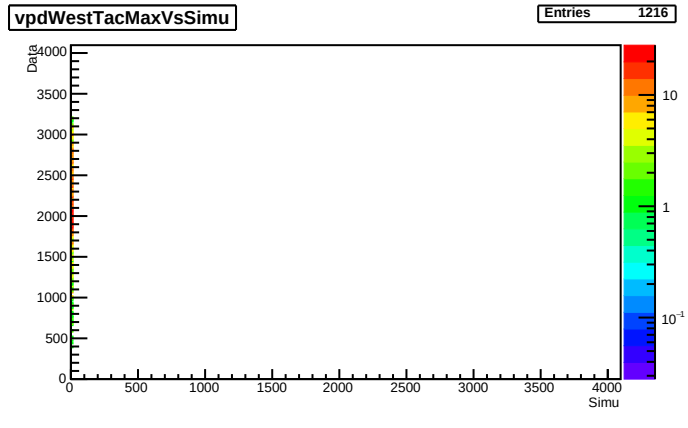

vpdEastTacMax
Entries 4158

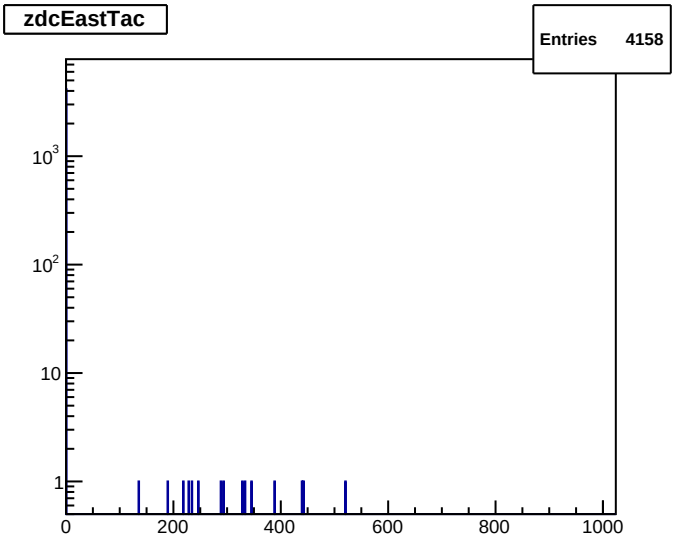

vpdEastTacMaxVsSimu
Entries 1263
Data
Simu

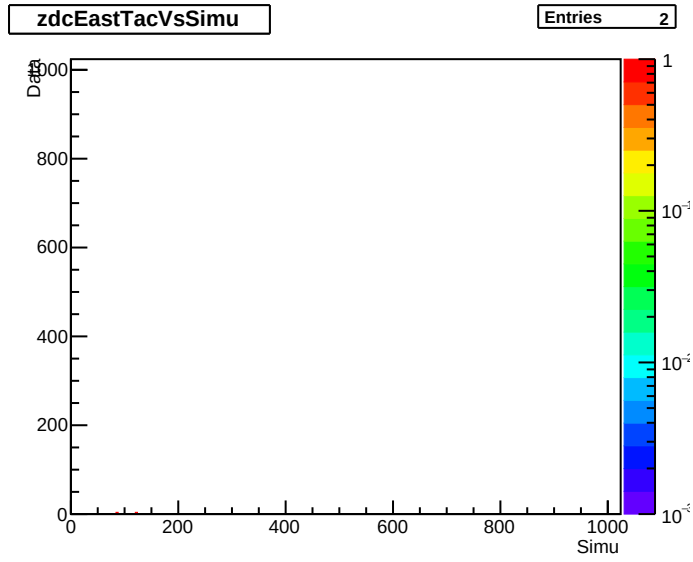

vpdWestAdcSum
Entries 4158

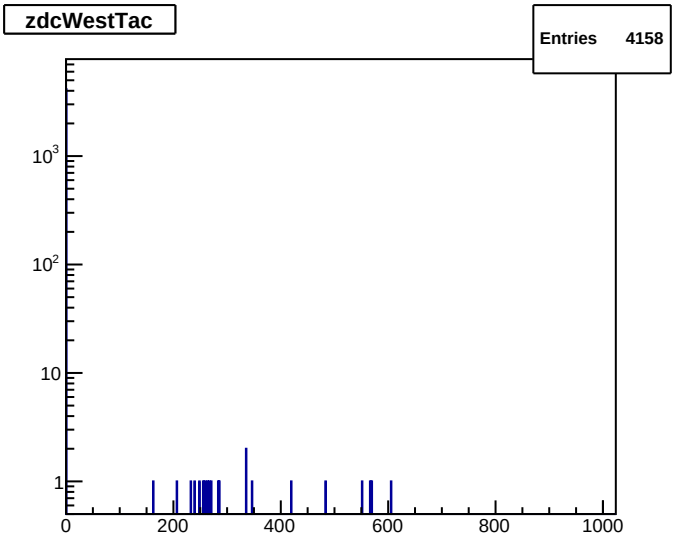

vpdWestAdcSumVsSimu
Entries 1216
Data
Simu

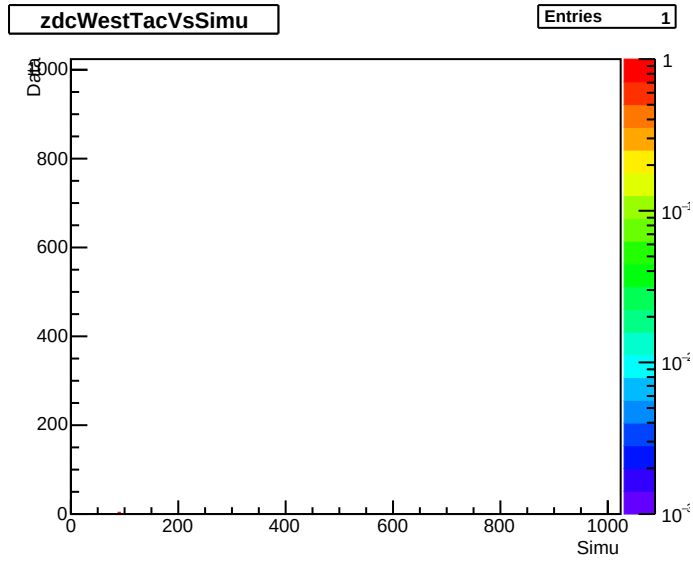

vpdWestTacMax
Entries 4158

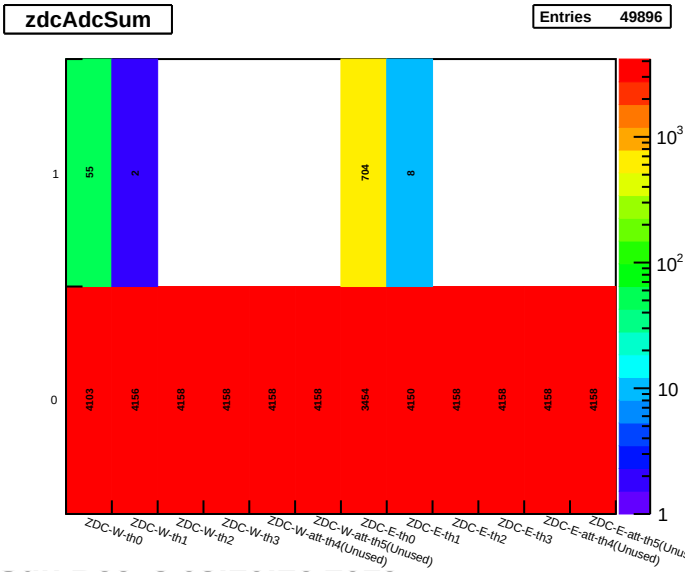

vpdWestTacMaxVsSimu
Entries 1216
Data
Simu

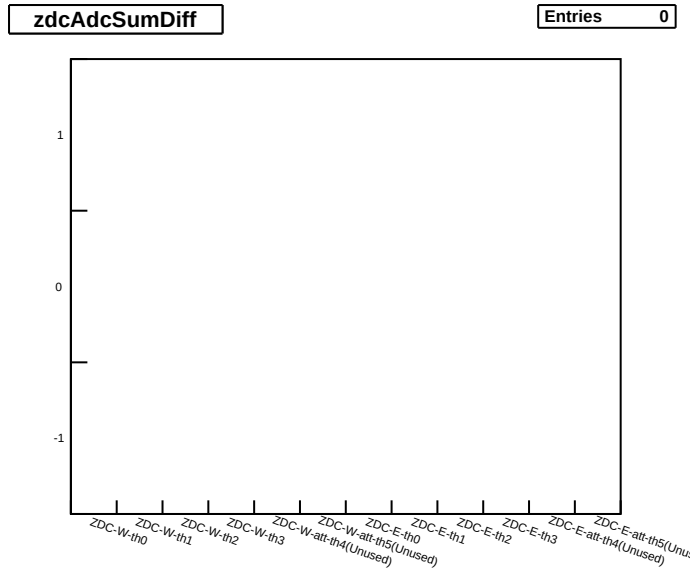

zdcEastTac
Entries 4158
10^3
10^2
10
1
0
200
400
600
800
1000

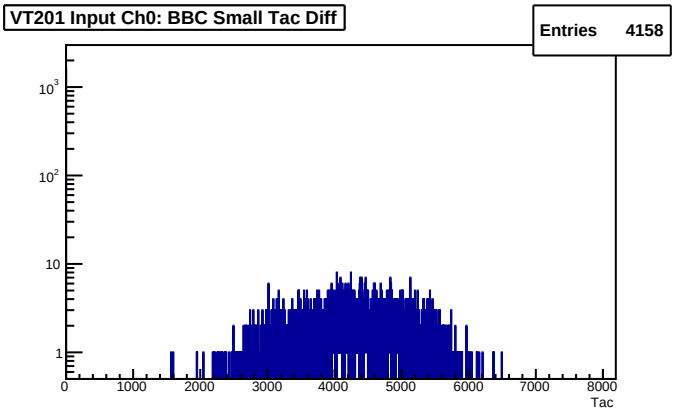

zdcEastTacVsSimu
Entries 2
Data
1000
800
600
400
200
0
0
200
400
600
800
1000
Simu
1

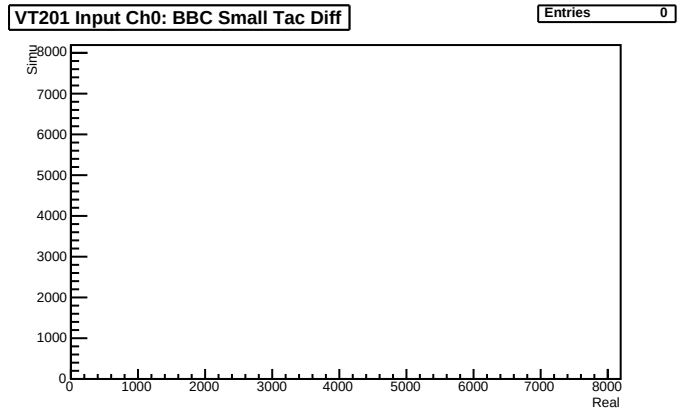

zdcWestTac
Entries 4158
10^3
10^2
10
1
0
200
400
600
800
1000

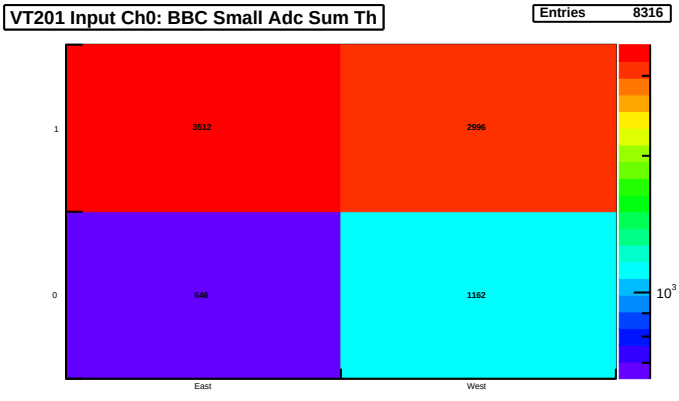

zdcWestTacVsSimu
Entries 1
Data
1000
800
600
400
200
0
0
200
400
600
800
1000
Simu
1

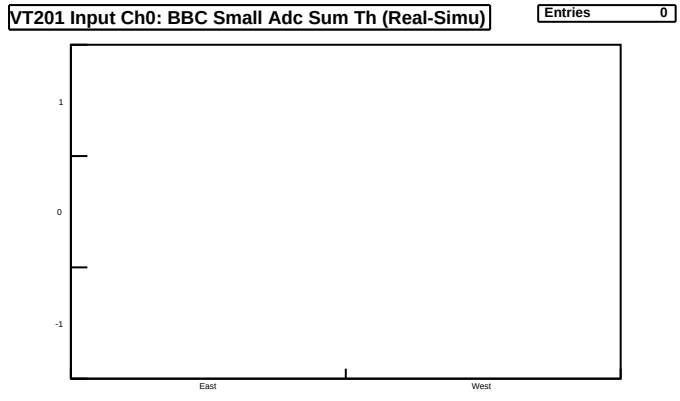

zdcAdcSum
Entries 49896
1
0
55
2
704
8
4103
4156
4158
4158
4158
4158
3454
4150
4158
4158
4158
4158
10^3
10^2
10
1
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)

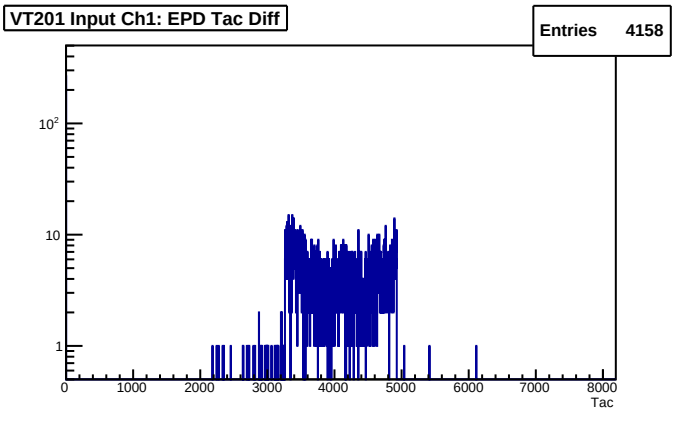

zdcAdcSumDiff
Entries 0
1
0
-1
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)

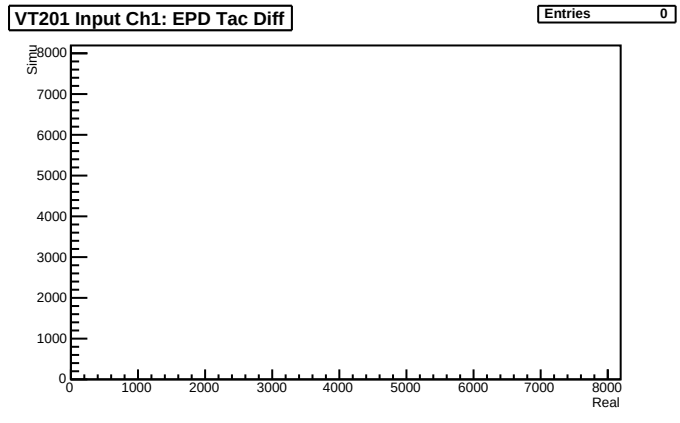

VT201 Input Ch0: BBC Small Tac Diff
Entries 4158
Tac

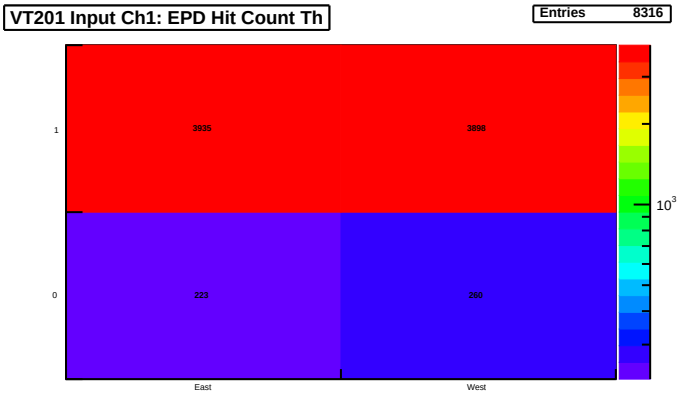

VT201 Input Ch0: BBC Small Tac Diff
Entries 0
Simu
Real

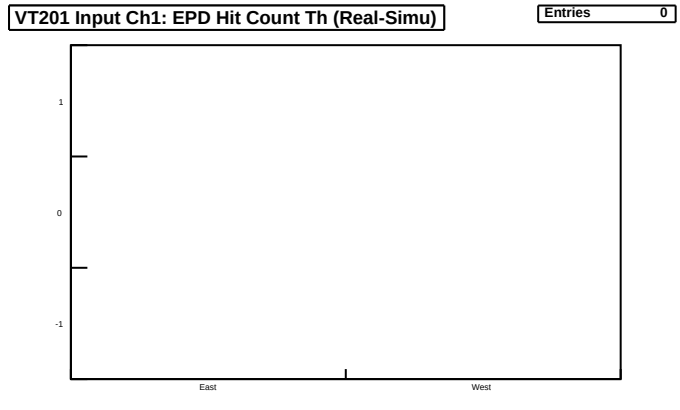

VT201 Input Ch0: BBC Small Adc Sum Th
Entries 8316
3512
2996
646
1162
East
West

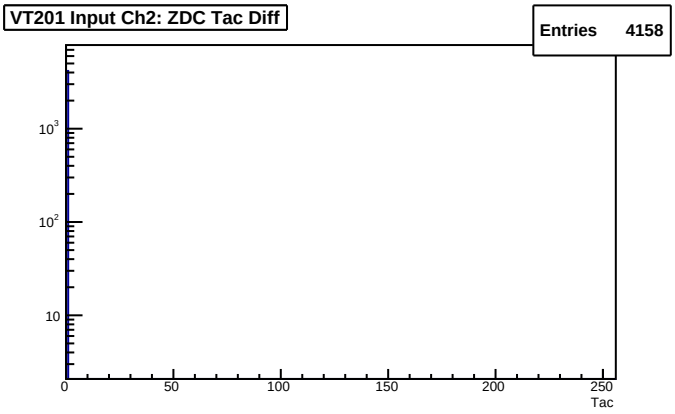

VT201 Input Ch0: BBC Small Adc Sum Th (Real-Simu)
Entries 0
East
West

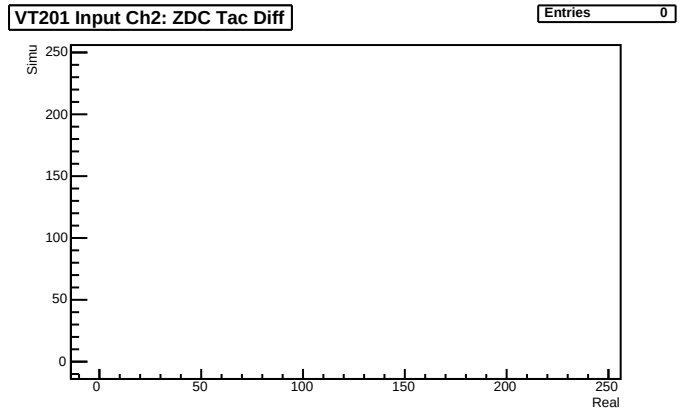

VT201 Input Ch1: EPD Tac Diff
Entries 4158
Tac

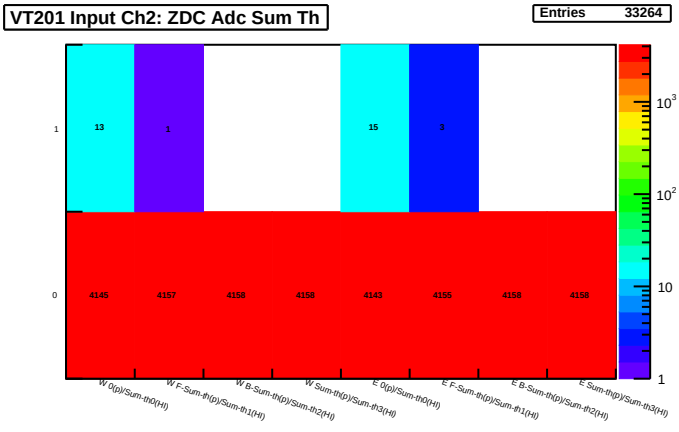

VT201 Input Ch1: EPD Tac Diff
Entries 0
Simu
Real

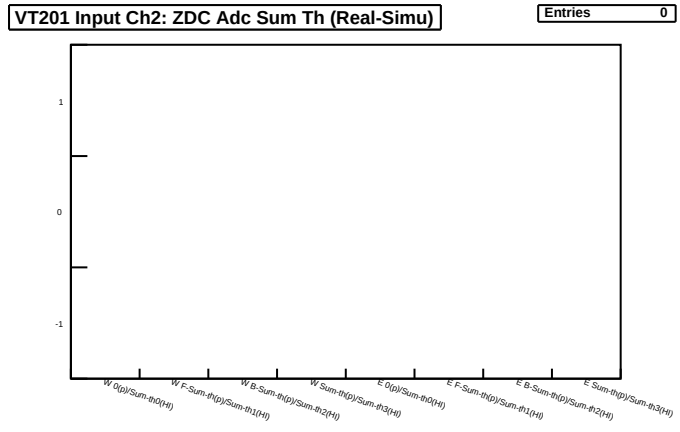

VT201 Input Ch1: EPD Hit Count Th
Entries 8316
3935
3898
223
260
East
West

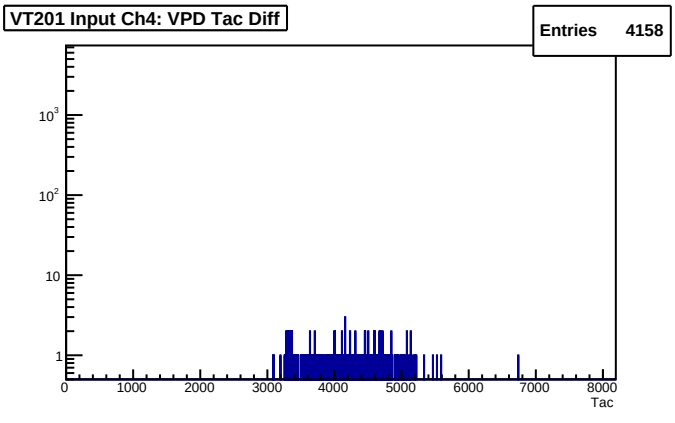

VT201 Input Ch1: EPD Hit Count Th (Real-Simu)
Entries 0
East
West

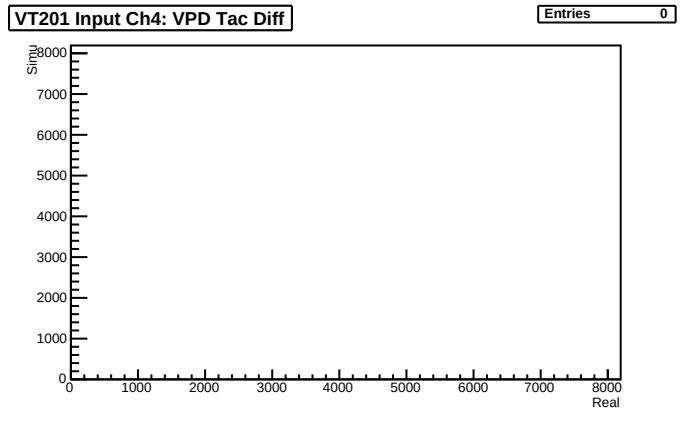

VT201 Input Ch2: ZDC Tac Diff
Entries 4158
Tac

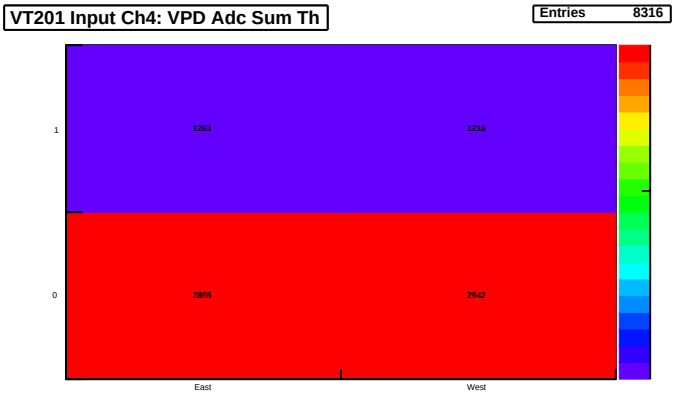

VT201 Input Ch2: ZDC Tac Diff
Entries 0
Simu
Real

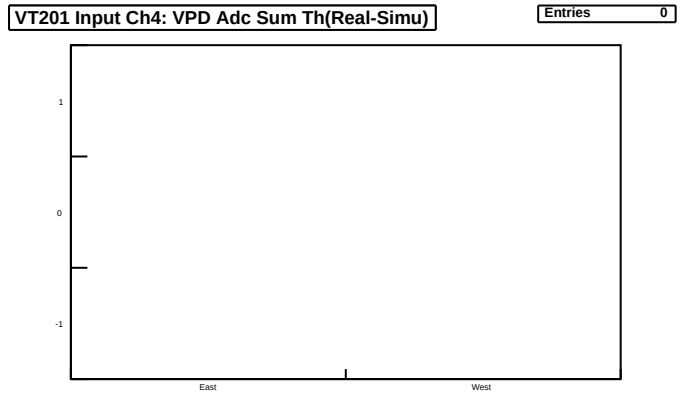

VT201 Input Ch2: ZDC Adc Sum Th
Entries 33264
13
1
15
3
4145
4157
4158
4158
4143
4155
4158
4158
W 0(p)/Sum-th0(H)
W F-Sum-th(p)/Sum-th1(H)
W B-Sum-th(p)/Sum-th2(H)
W Sum-th(p)/Sum-th3(H)
E 0(p)/Sum-th0(H)
E F-Sum-th(p)/Sum-th1(H)
E B-Sum-th(p)/Sum-th2(H)
E Sum-th(p)/Sum-th3(H)

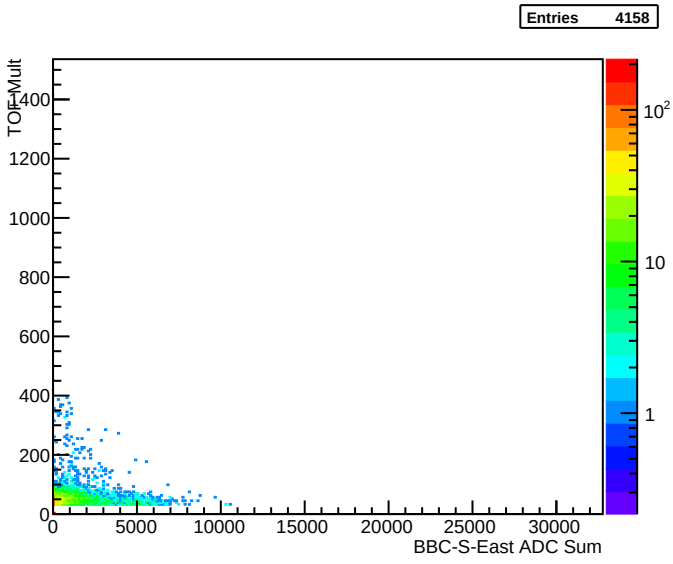

VT201 Input Ch2: ZDC Adc Sum Th (Real-Simu)
Entries 0
W 0(p)/Sum-th0(H)
W F-Sum-th(p)/Sum-th1(H)
W B-Sum-th(p)/Sum-th2(H)
W Sum-th(p)/Sum-th3(H)
E 0(p)/Sum-th0(H)
E F-Sum-th(p)/Sum-th1(H)
E B-Sum-th(p)/Sum-th2(H)
E Sum-th(p)/Sum-th3(H)

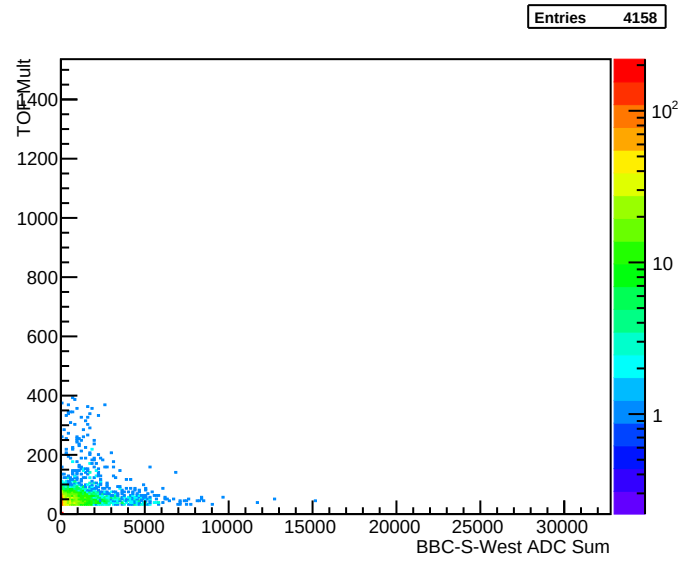

VT201 Input Ch4: VPD Tac Diff
Entries 4158
Tac

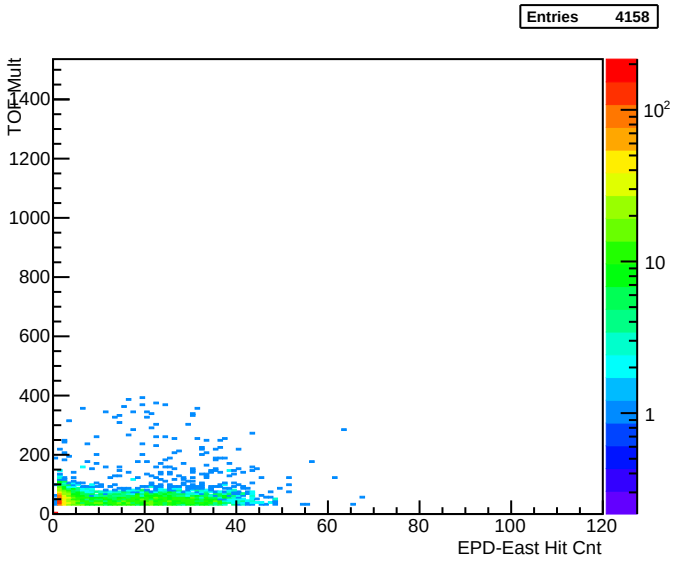

VT201 Input Ch4: VPD Tac Diff
Entries 0
Simu
Real

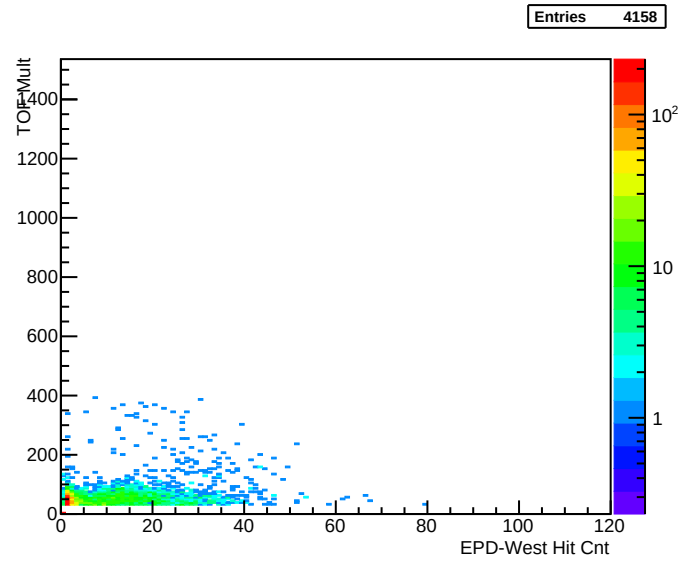

VT201 Input Ch4: VPD Adc Sum Th
Entries 8316
1283
1216
2895
2942
East
West

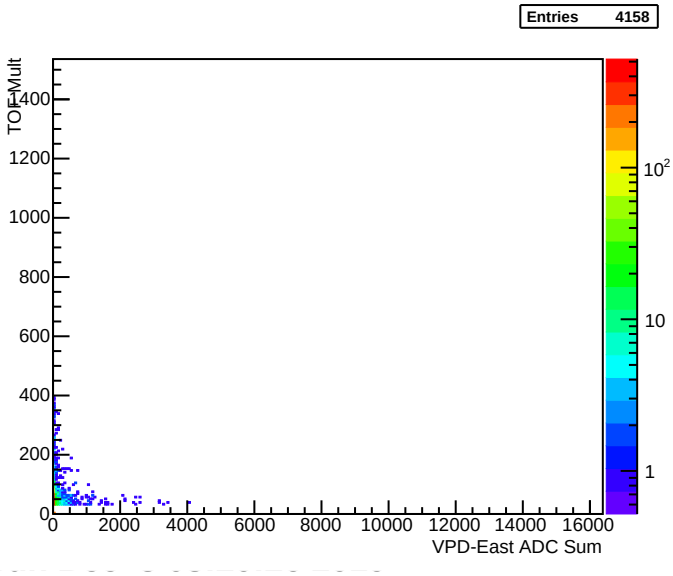

VT201 Input Ch4: VPD Adc Sum Th(Real-Simu)
Entries 0
East
West

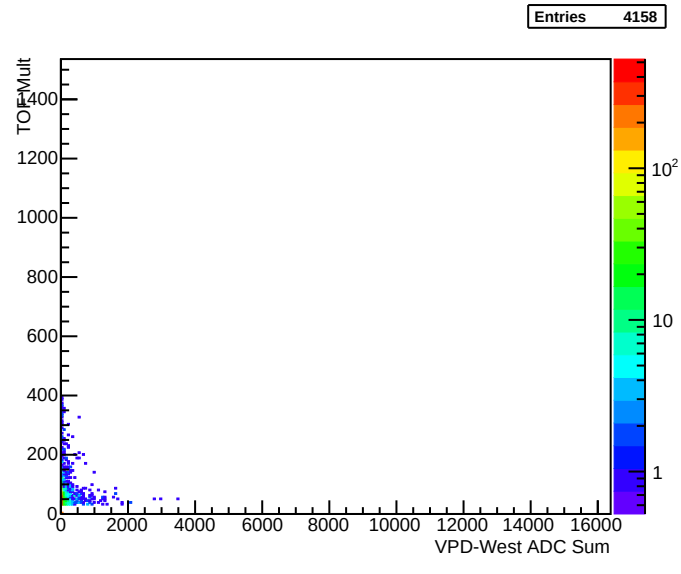

Entries 4158
TOF-Mult
BBC-S-East ADC Sum

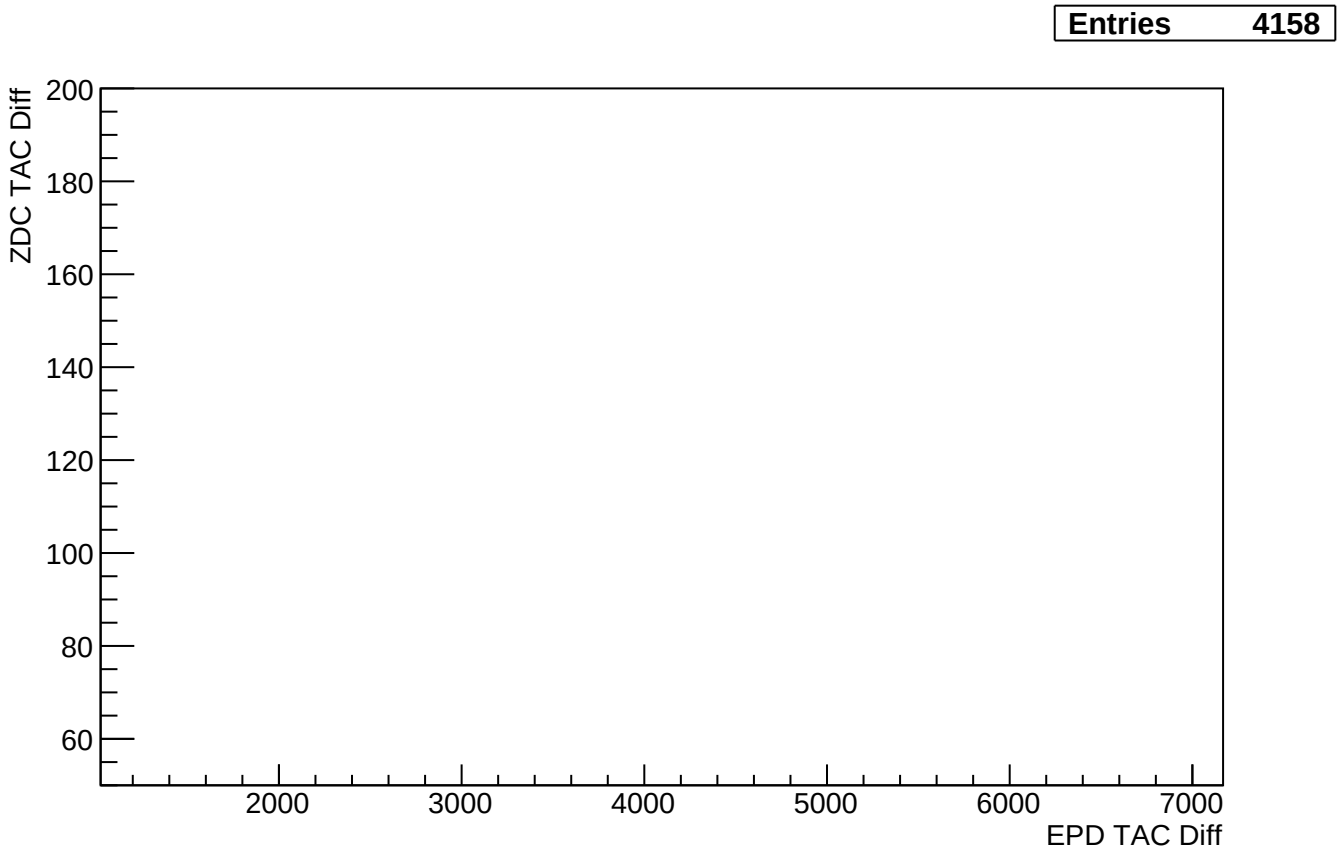

Entries 4158
TOF-Mult
BBC-S-West ADC Sum

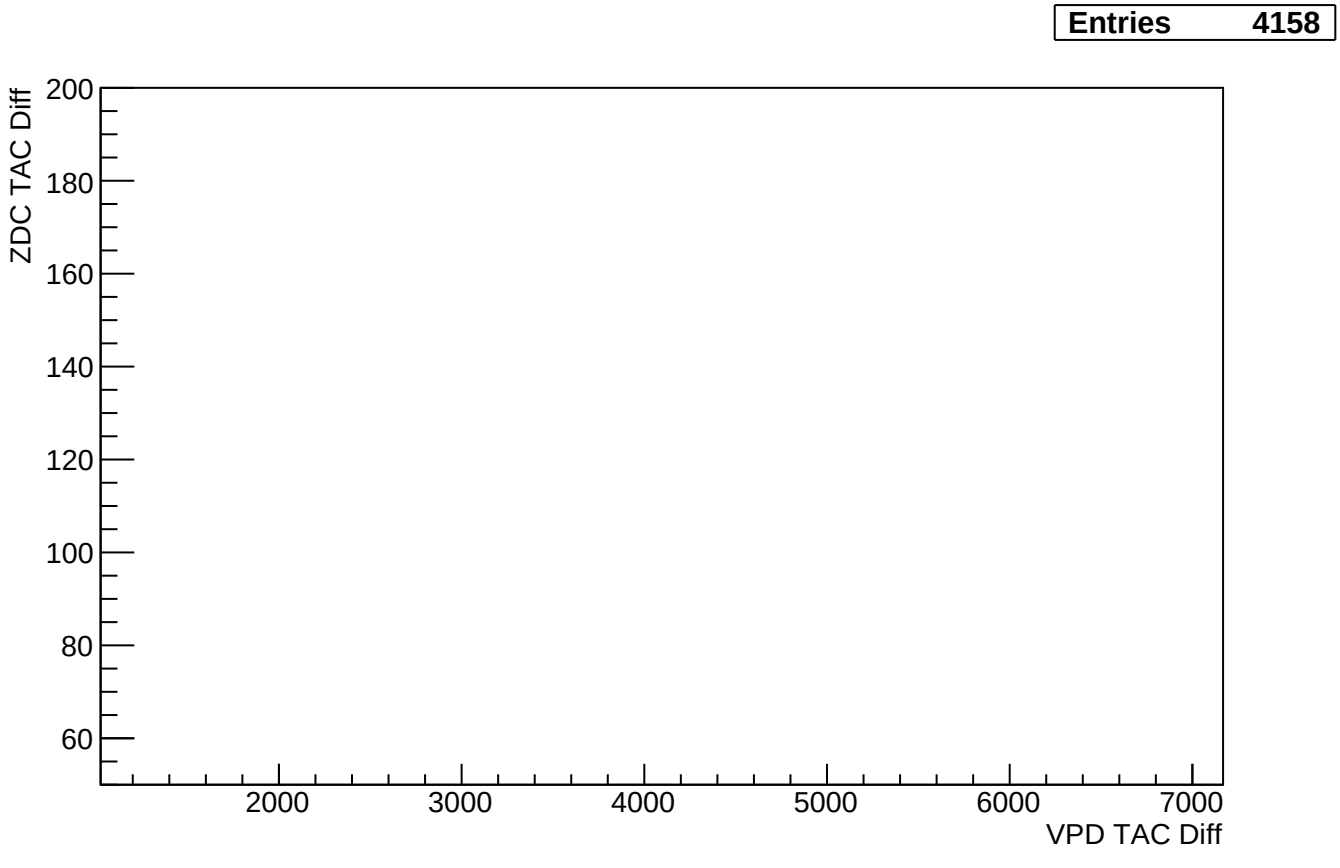

Entries 4158
TOF-Mult
EPD-East Hit Cnt

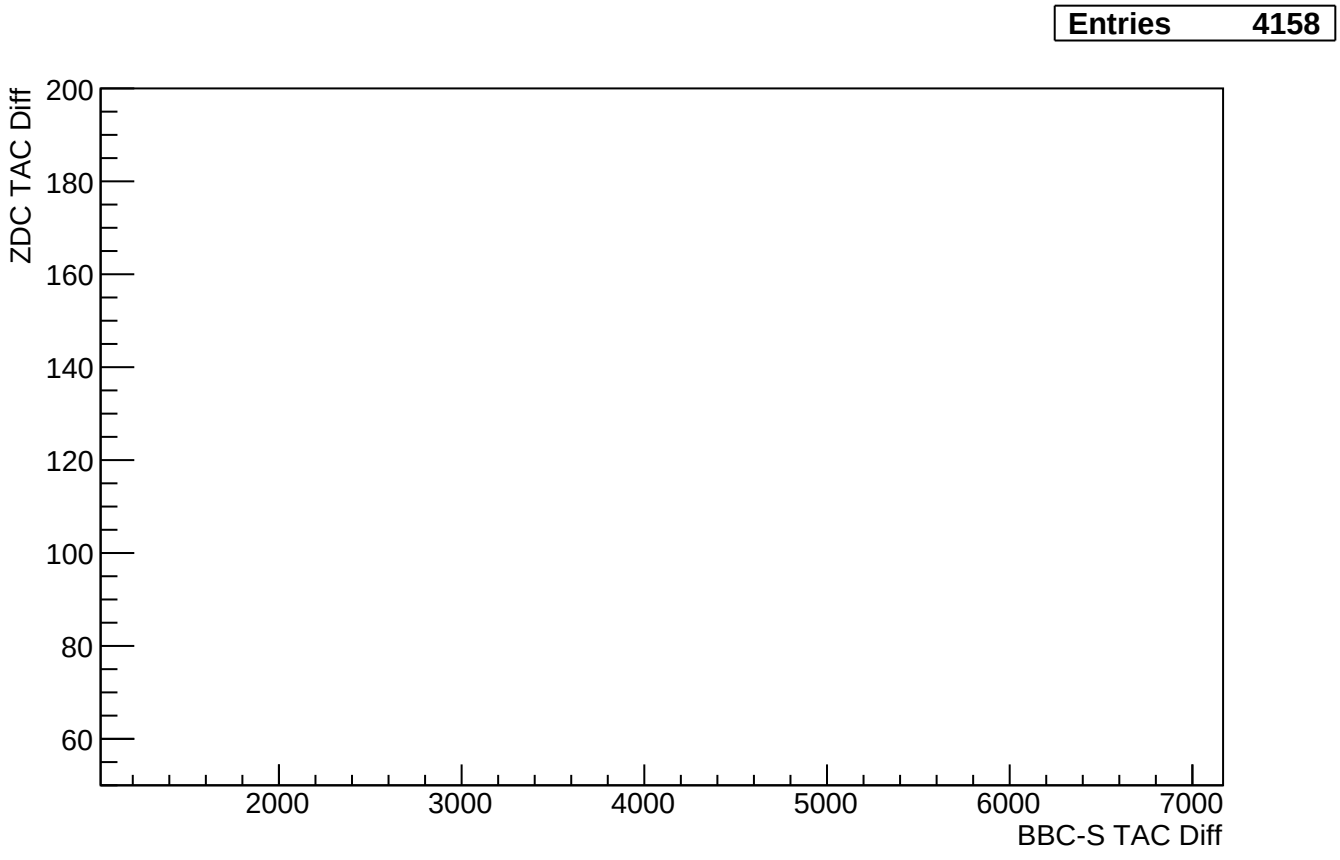

Entries 4158
TOF-Mult
EPD-West Hit Cnt

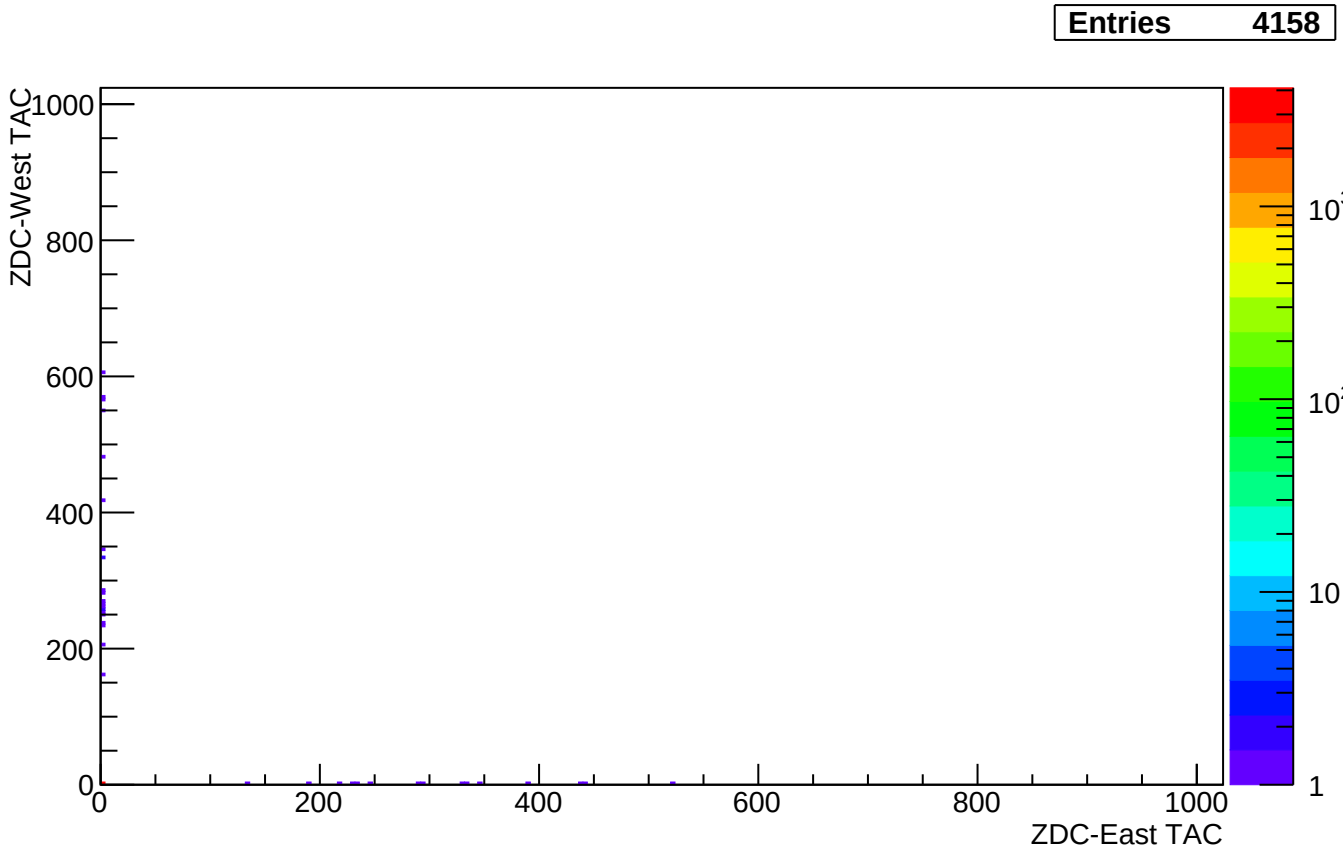

Entries 4158
TOF-Mult
VPD-East ADC Sum

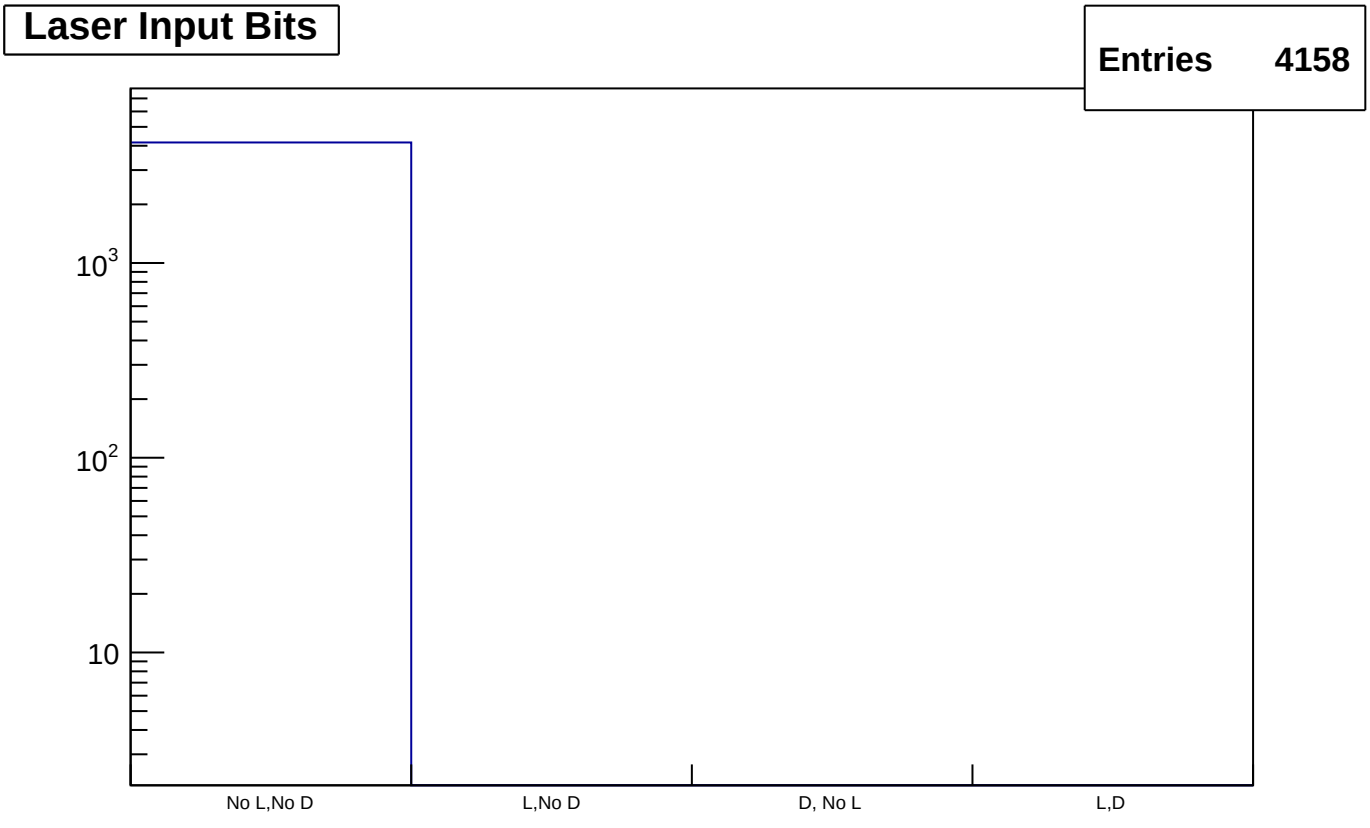

Entries 4158
TOF-Mult
VPD-West ADC Sum

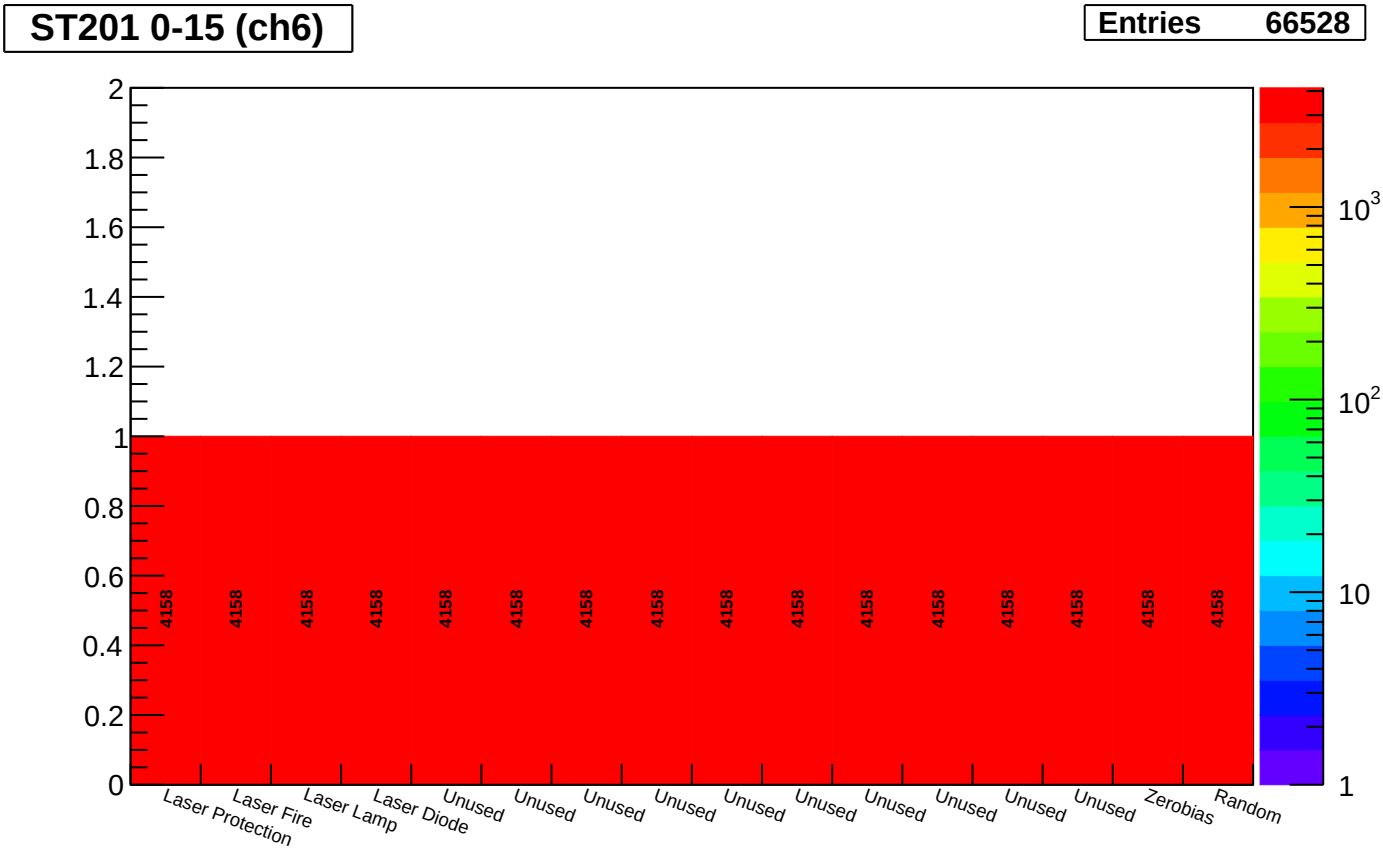

Entries 4158
ZDC TAC Diff
200
180
160
140
120
100
80
60
2000
3000
4000
5000
6000
7000
EPD TAC Diff

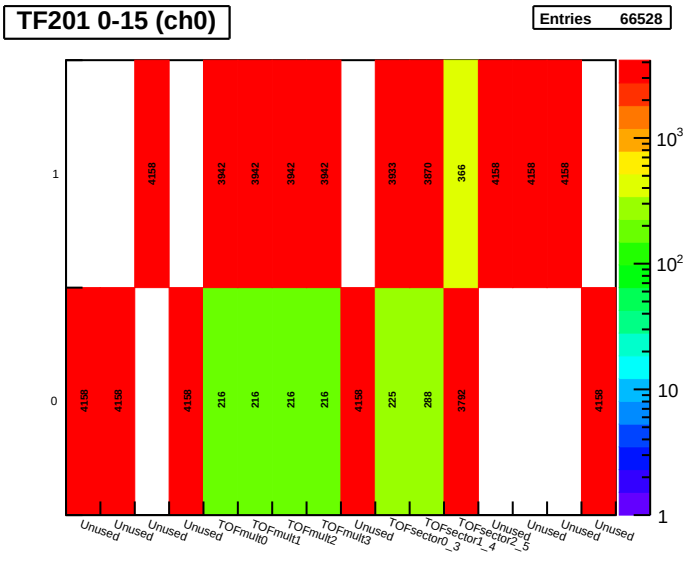

Entries 4158
ZDC TAC Diff
200
180
160
140
120
100
80
60
2000
3000
4000
5000
6000
7000
VPD TAC Diff

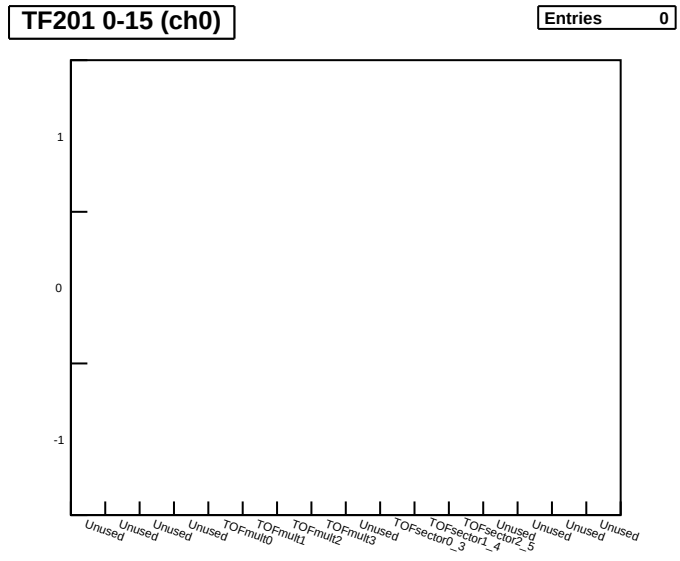

Entries 4158
ZDC TAC Diff
200
180
160
140
120
100
80
60
2000
3000
4000
5000
6000
7000
BBC-S TAC Diff

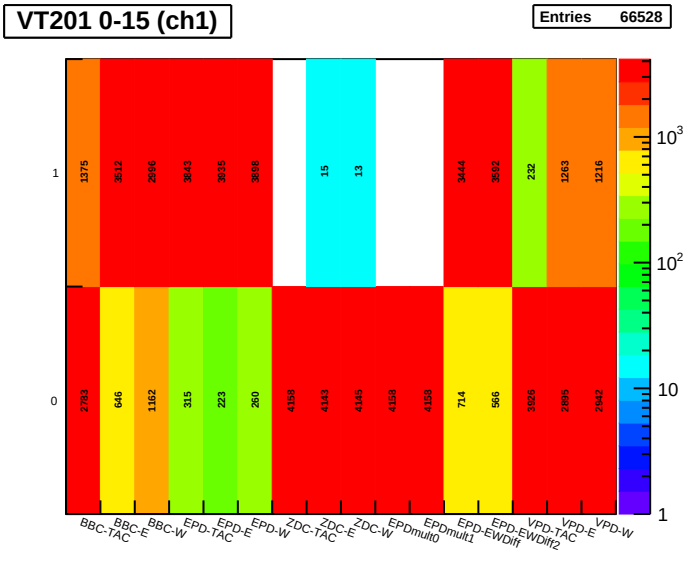

Entries 4158
ZDC-West TAC
1000
800
600
400
200
0
0
200
400
600
800
1000
ZDC-East TAC
10^3
10^2
10
1

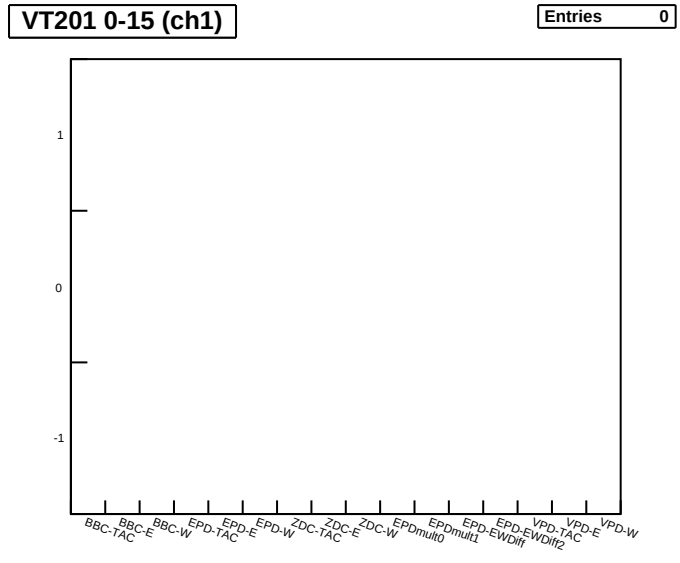

Laser Input Bits
Entries 4158
10^3
10^2
10
No L,No D
L,No D
D, No L
L,D

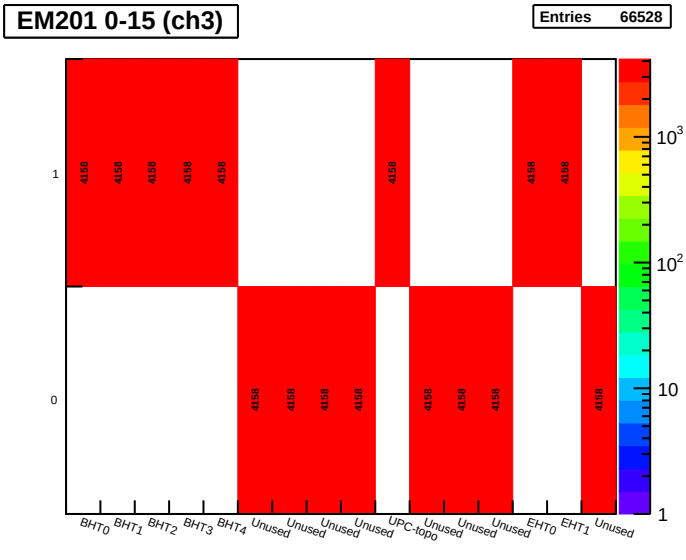

ST201 0-15 (ch6)
Entries 66528
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
4158
Laser Protection
Laser Fire
Laser Lamp
Laser Diode
Unused
Zerobias
Random
10^3
10^2
10
1

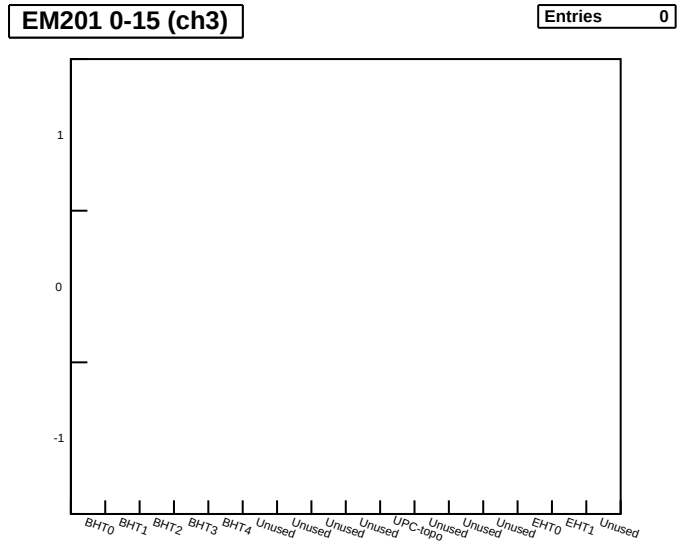

TF201 0-15 (ch0)
Entries 66528
1
0
4158 3942 3942 3942 3942 3933 3870 366 4158 4158 4158
4158 4158 4158 216 216 216 216 4158 225 288 3792 4158
Unused Unused Unused Unused TOFmult0 TOFmult1 TOFmult2 TOFmult3 Unused TOFsector0_3 TOFsector1_4 TOFsector2_5 Unused Unused Unused
10^3
10^2
10
1


TF201 0-15 (ch0)
Entries 0
1
0
-1
Unused Unused Unused Unused TOFmult0 TOFmult1 TOFmult2 TOFmult3 Unused TOFsector0_3 TOFsector1_4 TOFsector2_5 Unused Unused Unused


VT201 0-15 (ch1)
Entries 66528
1
0
1375 3512 2996 3843 3935 3898 15 13 3444 3592 232 1263 1216
2783 646 1162 315 223 260 4158 4143 4145 4158 4158 714 566 3926 2895 2942
BBC-TAC BBC-E BBC-W EPD-TAC EPD-E EPD-W ZDC-TAC ZDC-E ZDC-W EPDmult0 EPDmult1 EPD-EWDiff EPD-EWDiff2 VPD-TAC VPD-E VPD-W
10^3
10^2
10
1


VT201 0-15 (ch1)
Entries 0
1
0
-1
BBC-TAC BBC-E BBC-W EPD-TAC EPD-E EPD-W ZDC-TAC ZDC-E ZDC-W EPDmult0 EPDmult1 EPD-EWDiff EPD-EWDiff2 VPD-TAC VPD-E VPD-W


EM201 0-15 (ch3)
Entries 66528
1
0
4158 4158 4158 4158 4158 4158 4158 4158
4158 4158 4158 4158 4158 4158 4158 4158
BHT0 BHT1 BHT2 BHT3 BHT4 Unused Unused Unused Unused UPC-topo Unused Unused Unused EHT0 EHT1 Unused
10^3
10^2
10
1


EM201 0-15 (ch3)
Entries 0
1
0
-1
BHT0 BHT1 BHT2 BHT3 BHT4 Unused Unused Unused Unused UPC-topo Unused Unused Unused EHT0 EHT1 Unused