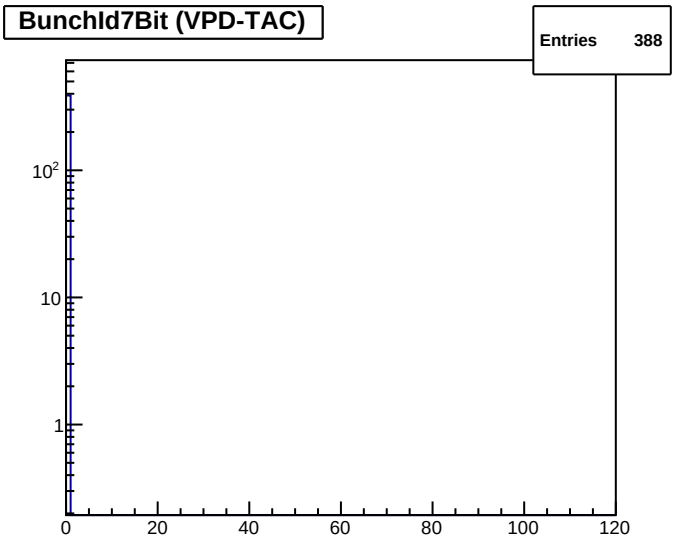
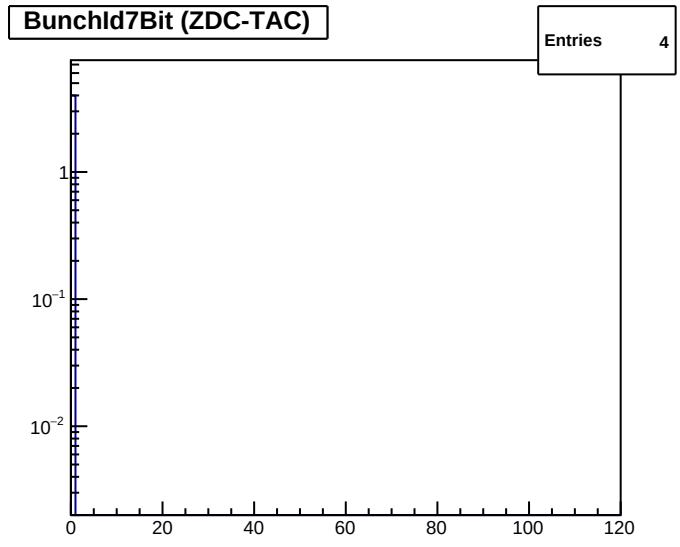
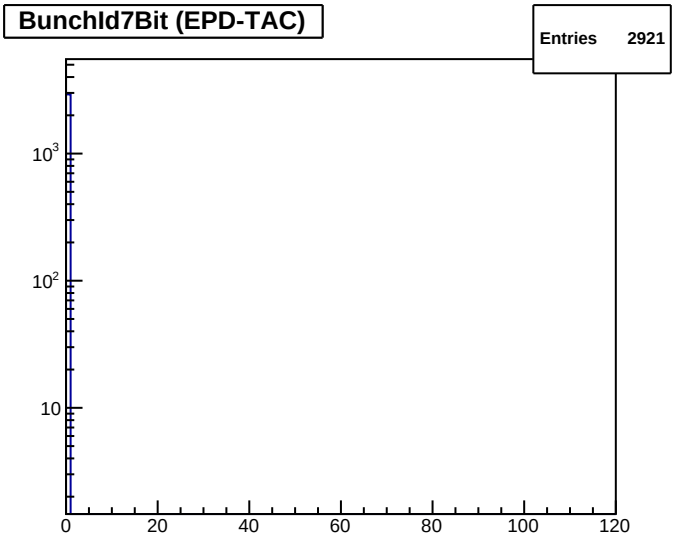
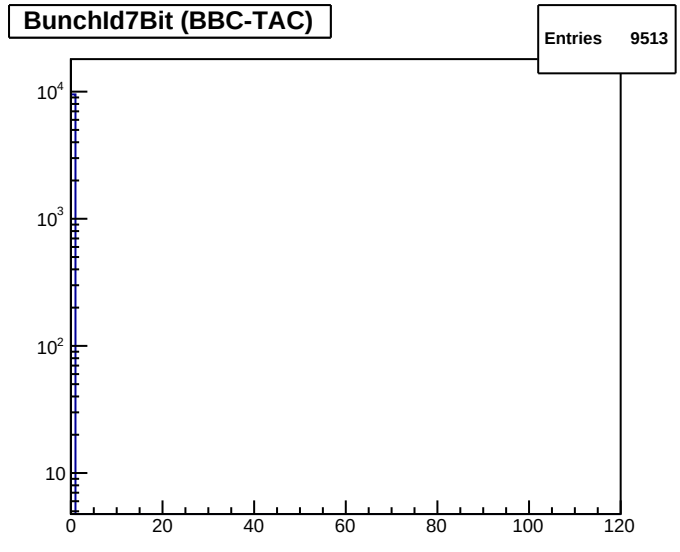
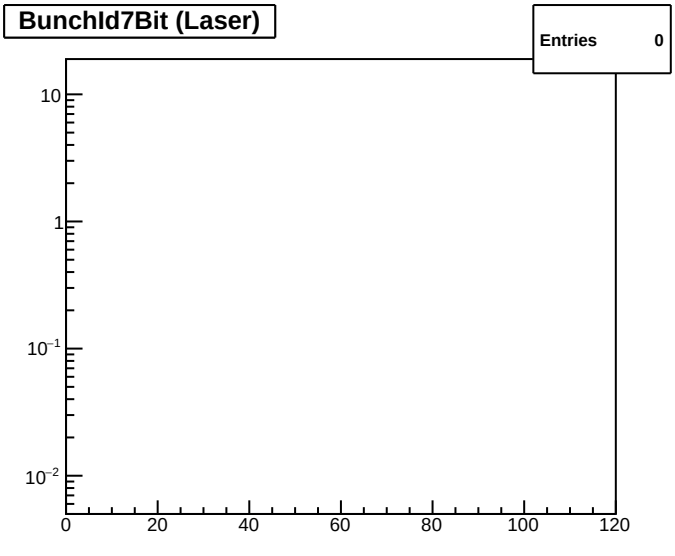
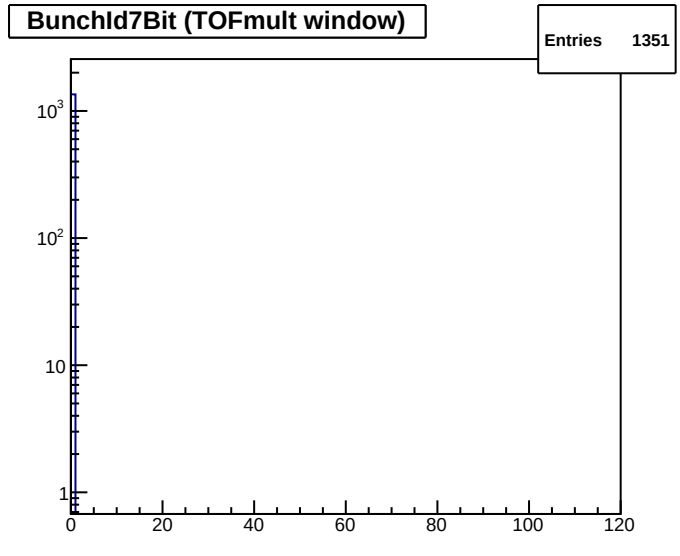

BunchId7Bit (VPD-TAC)
Entries 388
10^2
10
1
0 20 40 60 80 100 120

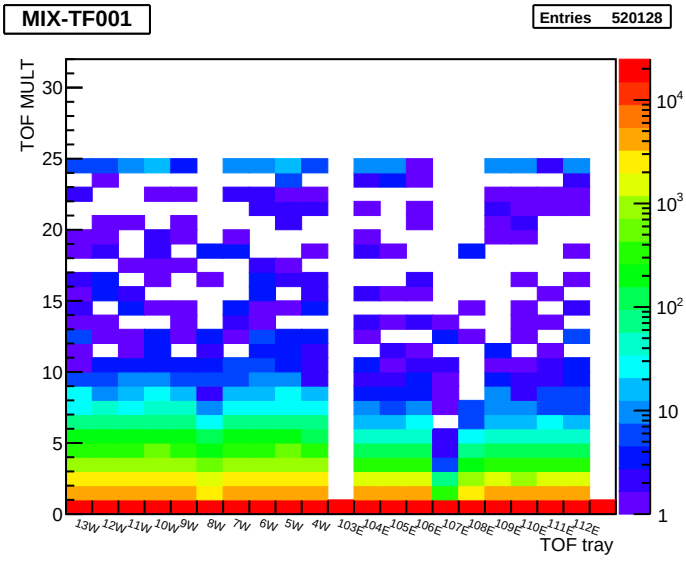

BunchId7Bit (ZDC-TAC)
Entries 4
1
10^{-1}
10^{-2}
0 20 40 60 80 100 120

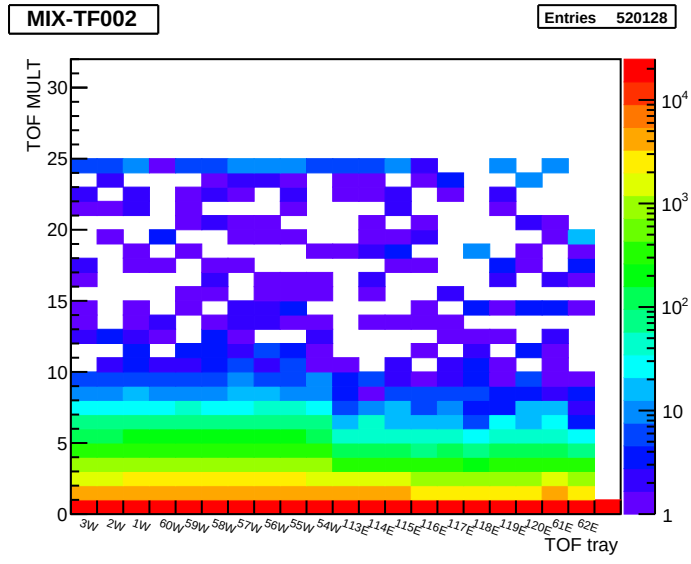

BunchId7Bit (EPD-TAC)
Entries 2921
10^3
10^2
10
0 20 40 60 80 100 120

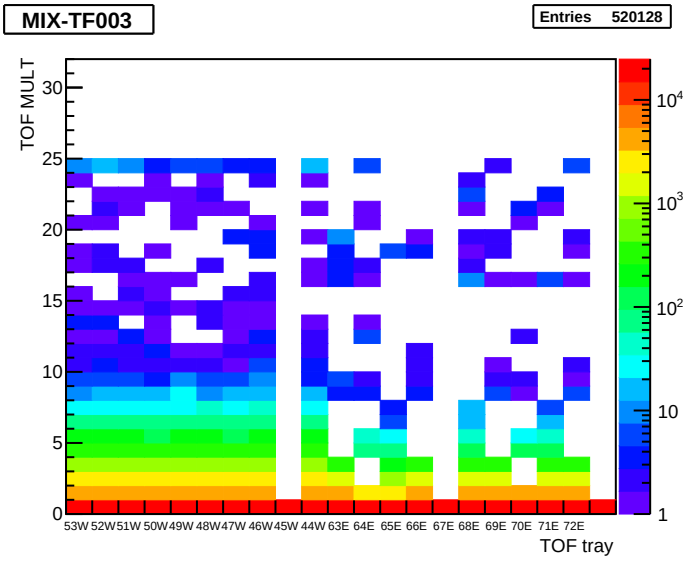

BunchId7Bit (BBC-TAC)
Entries 9513
10^4
10^3
10^2
10
0 20 40 60 80 100 120

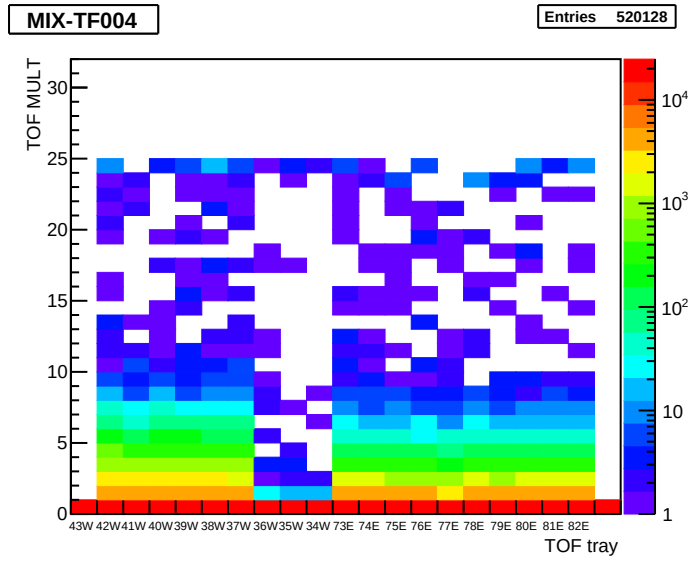

BunchId7Bit (Laser)
Entries 0
10
1
10^{-1}
10^{-2}
0 20 40 60 80 100 120

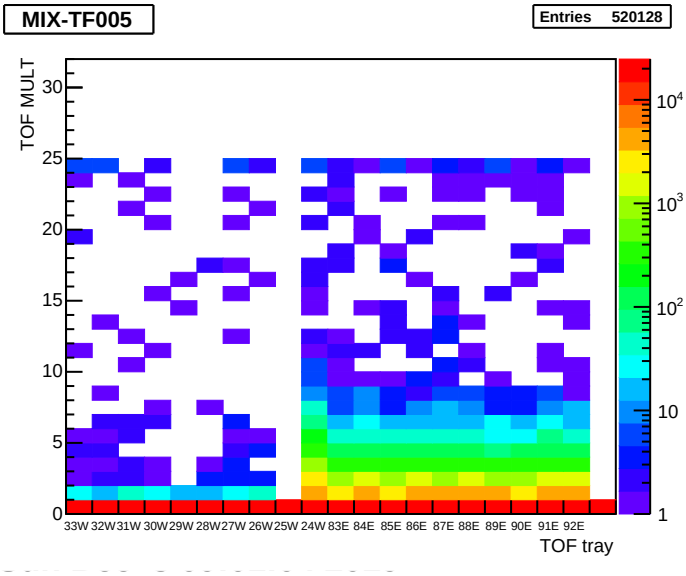

BunchId7Bit (TOFmult window)
Entries 1351
10^3
10^2
10
1
0 20 40 60 80 100 120

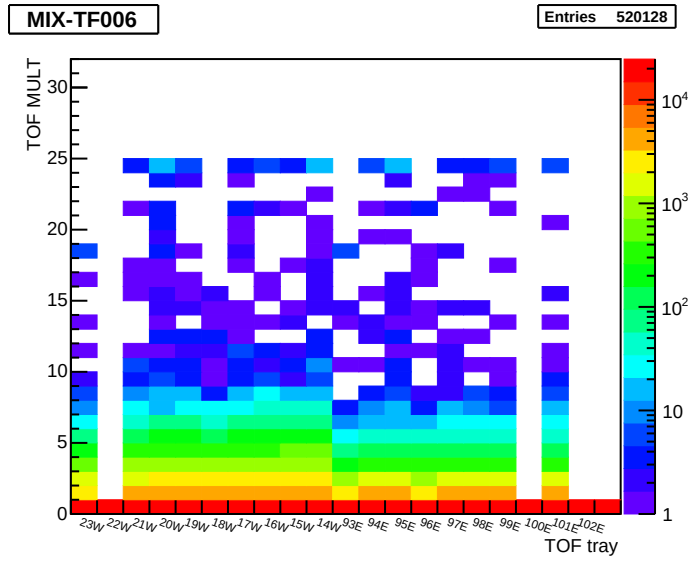

MIX-TF001
Entries 520128
TOF MULT
TOF tray

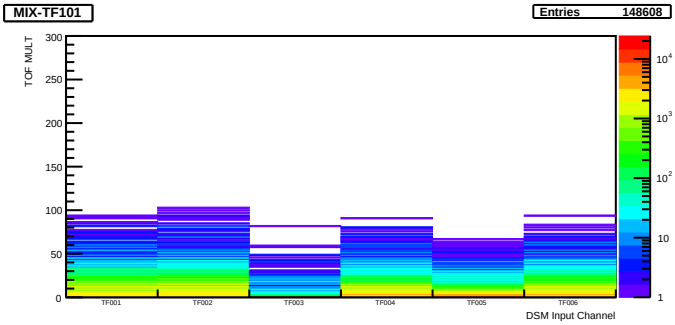

MIX-TF002
Entries 520128
TOF MULT
TOF tray

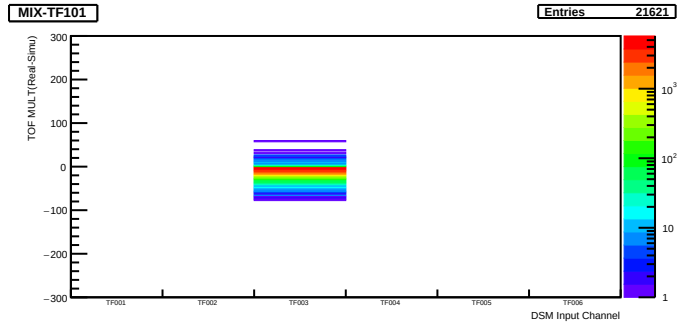

MIX-TF003
Entries 520128
TOF MULT
TOF tray

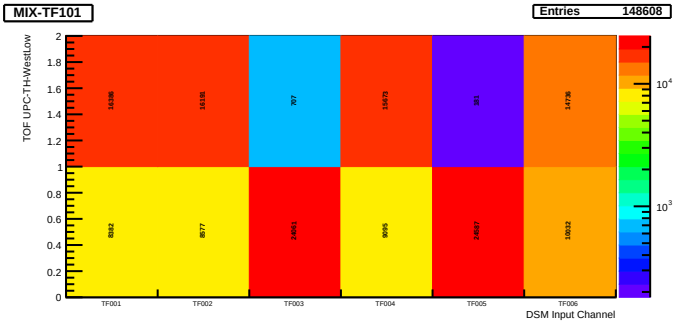

MIX-TF004
Entries 520128
TOF MULT
TOF tray

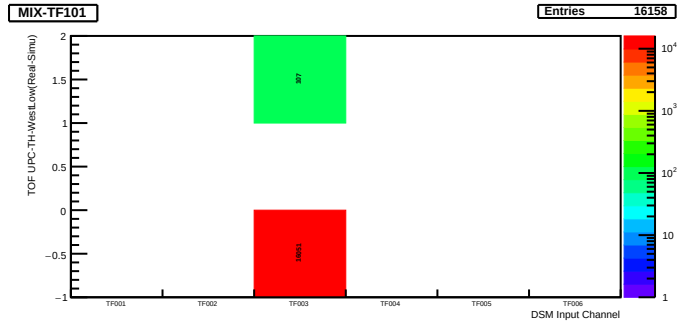

MIX-TF005
Entries 520128
TOF MULT
TOF tray

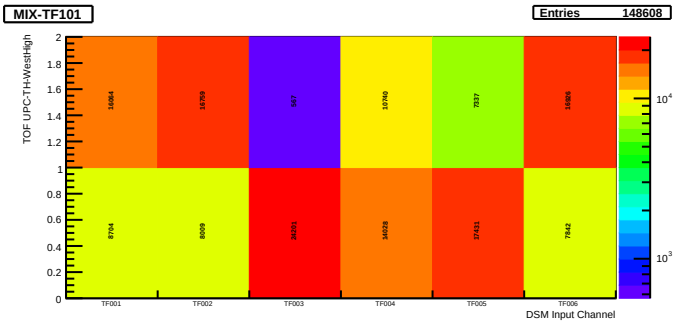

MIX-TF006
Entries 520128
TOF MULT
TOF tray

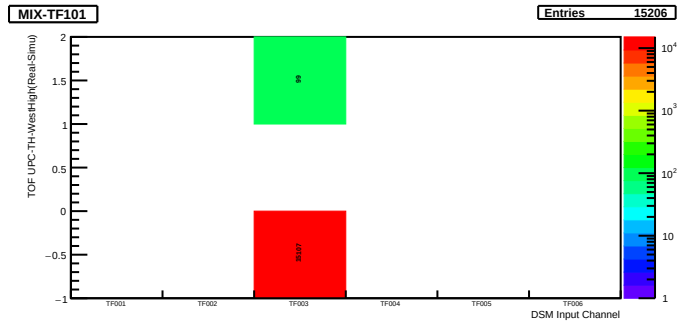

MIX-TF101
Entries 148608
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

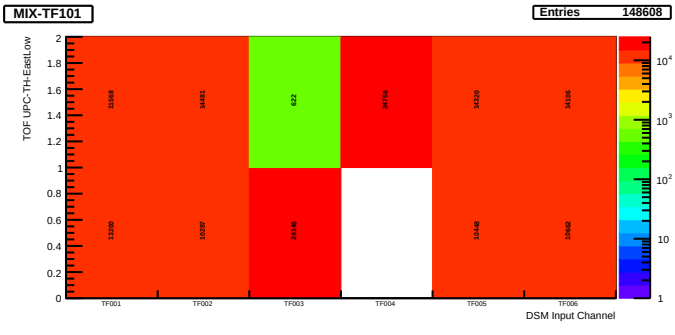

MIX-TF101
Entries 21621
TOF MULT(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

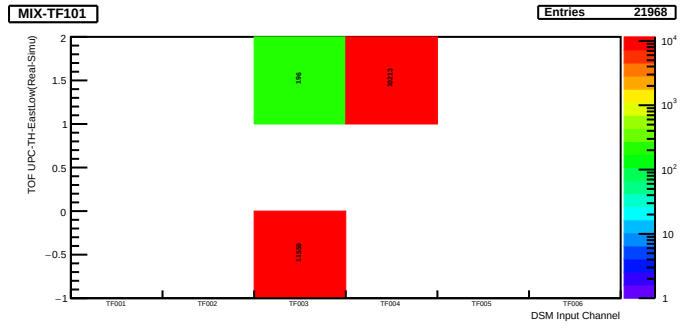

MIX-TF101
Entries 148608
TOF UPC-TH-WestLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

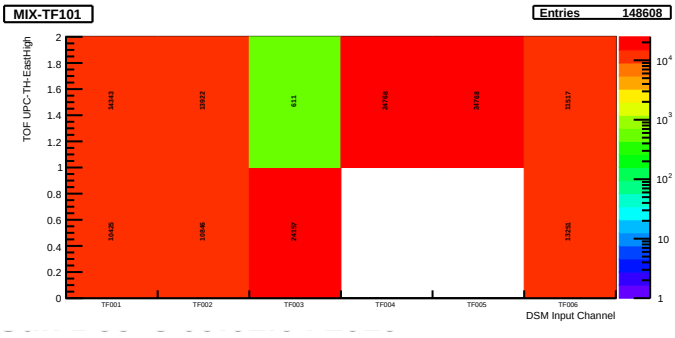

MIX-TF101
Entries 16158
TOF UPC-TH-WestLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

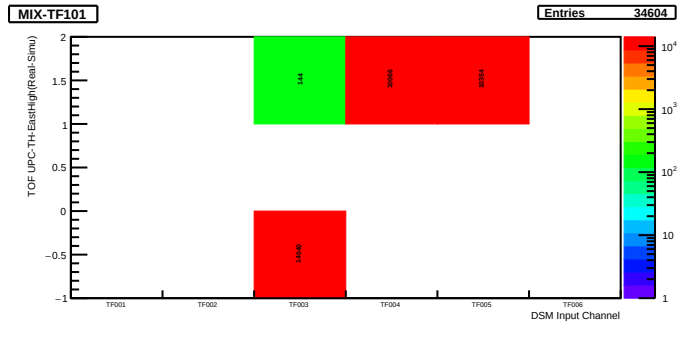

MIX-TF101
Entries 148608
TOF UPC-TH-WestHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

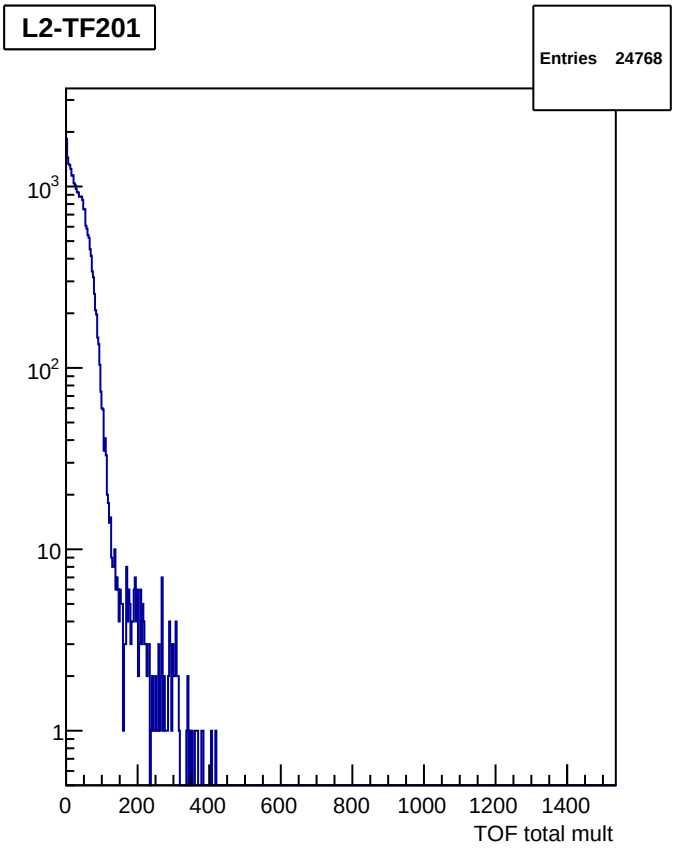

MIX-TF101
Entries 15206
TOF UPC-TH-WestHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

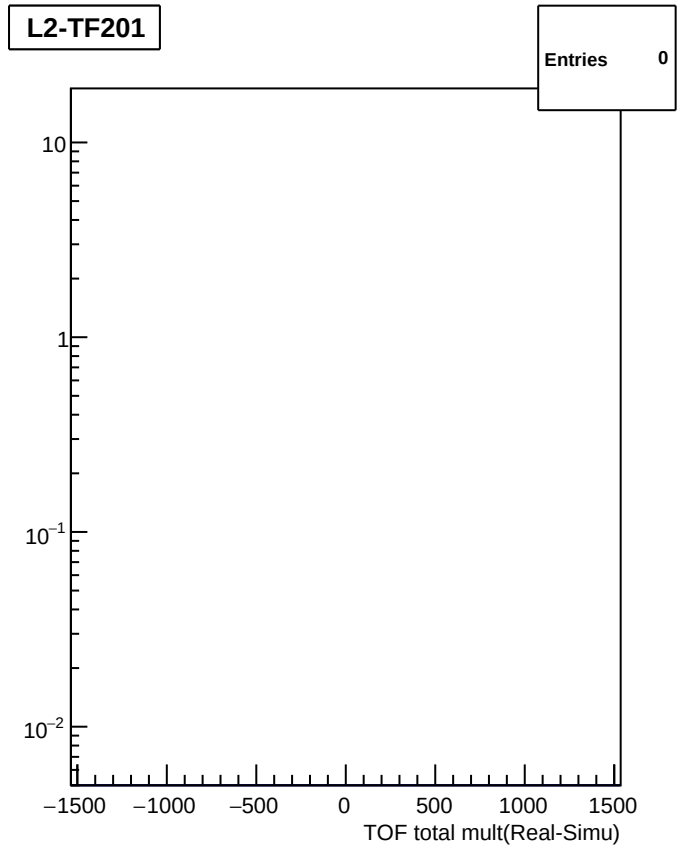

MIX-TF101
Entries 148608
TOF UPC-TH-EastLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

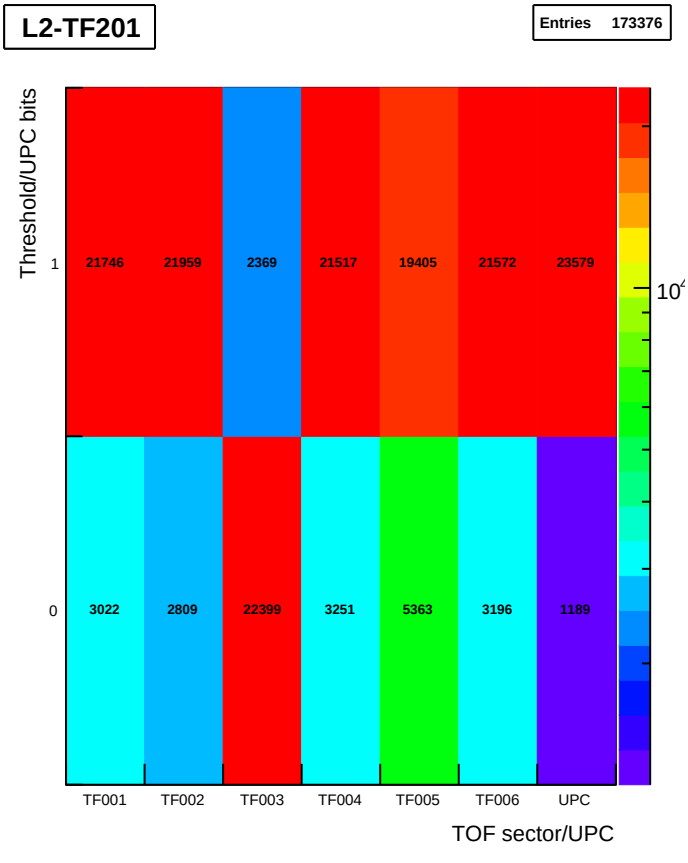

MIX-TF101
Entries 21968
TOF UPC-TH-EastLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

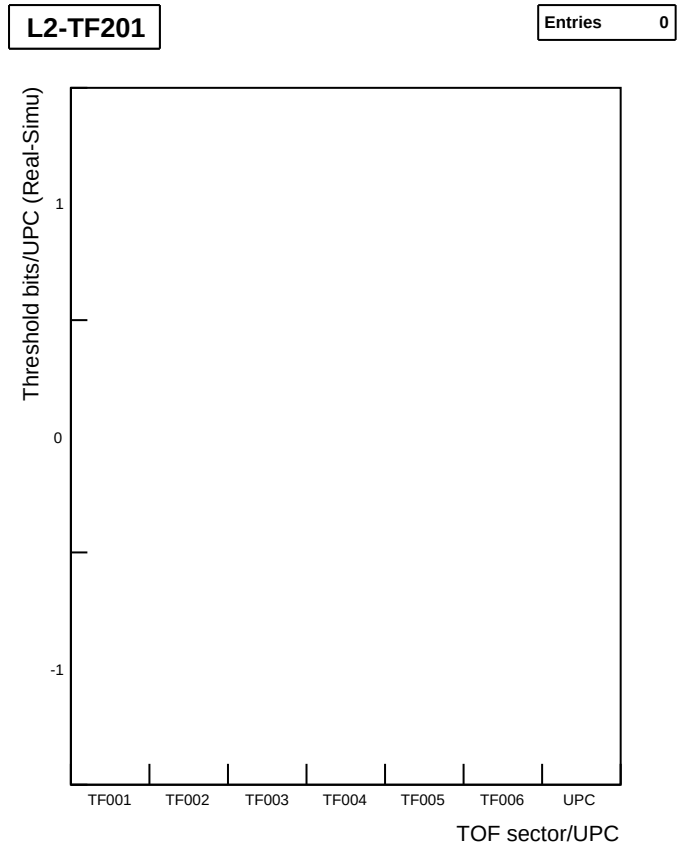

MIX-TF101
Entries 148608
TOF UPC-TH-EastHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

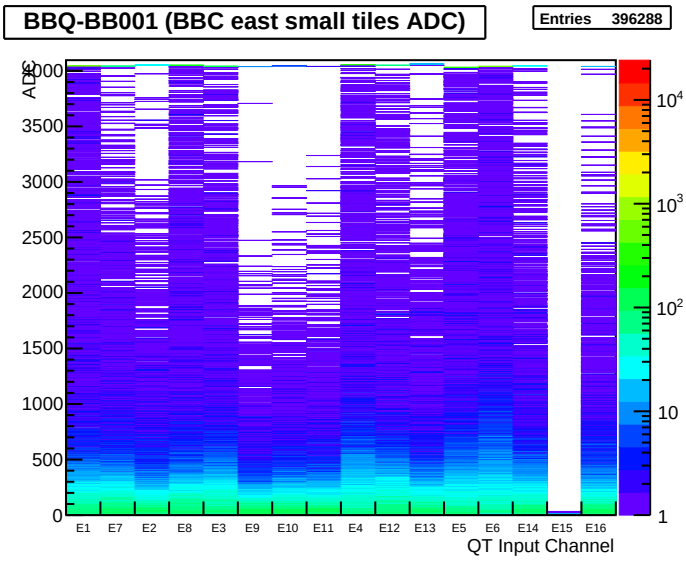

MIX-TF101
Entries 34604
TOF UPC-TH-EastHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

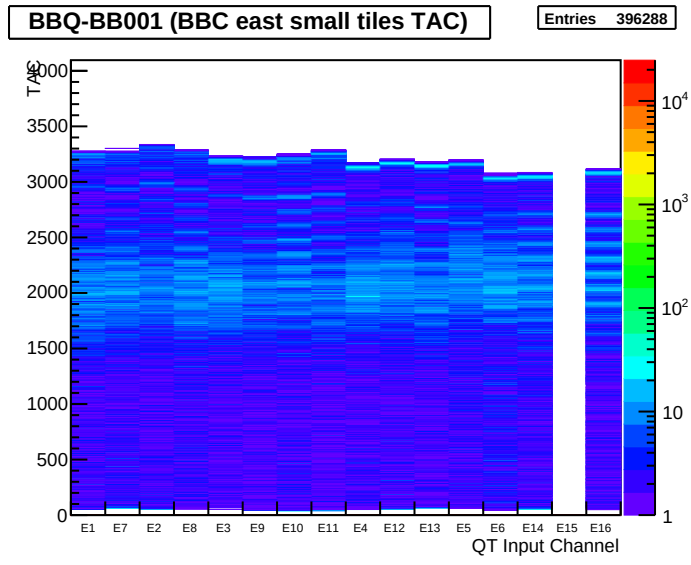

L2-TF201
Entries 24768
10^3
10^2
10
1
0
200
400
600
800
1000
1200
1400
TOF total mult

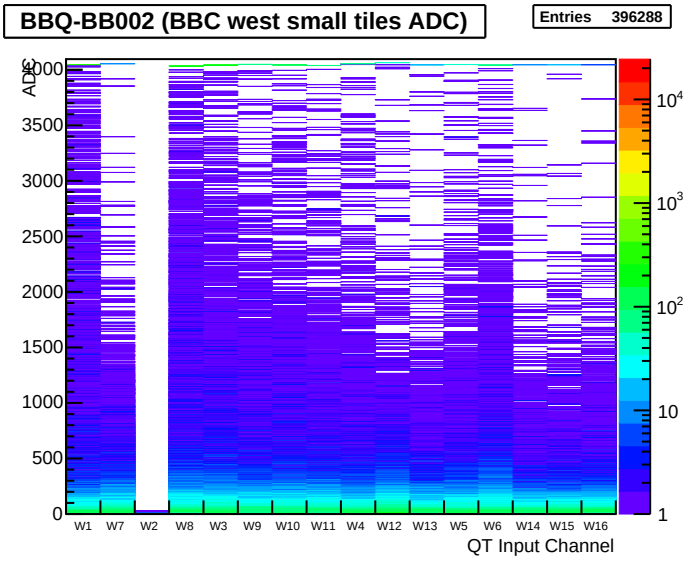

L2-TF201
Entries 0
10
1
10^{-1}
10^{-2}
−1500
−1000
−500
0
500
1000
1500
TOF total mult(Real-Simu)

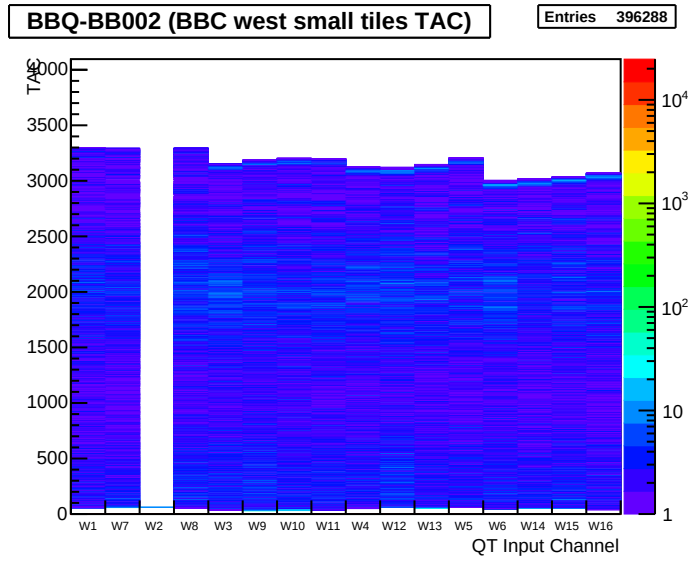

L2-TF201
Entries 173376
Threshold/UPC bits
1
0
21746
21959
2369
21517
19405
21572
23579
3022
2809
22399
3251
5363
3196
1189
TF001
TF002
TF003
TF004
TF005
TF006
UPC
TOF sector/UPC

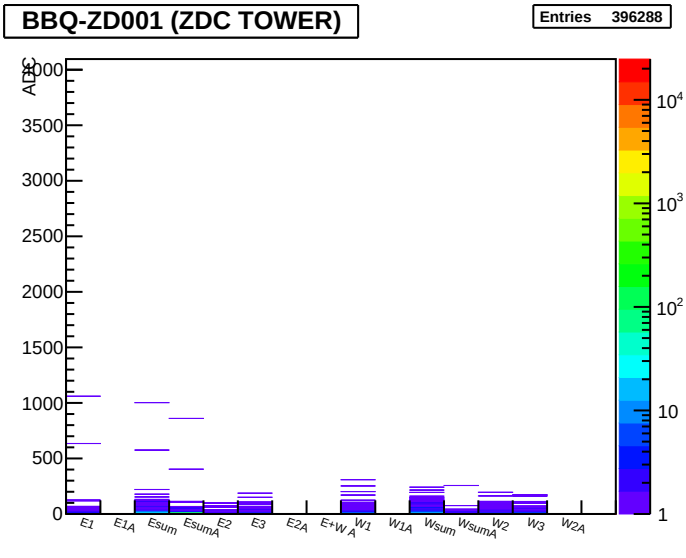

L2-TF201
Entries 0
Threshold bits/UPC (Real-Simu)
1
0
-1
TF001
TF002
TF003
TF004
TF005
TF006
UPC
TOF sector/UPC

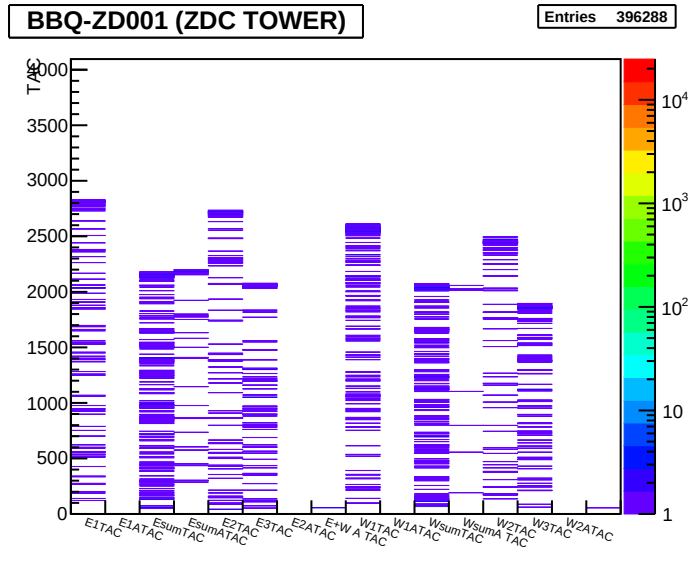

BBQ-BB001 (BBC east small tiles ADC)
Entries 396288
ADC
QT Input Channel
E1 E7 E2 E8 E3 E9 E10 E11 E4 E12 E13 E5 E6 E14 E15 E16

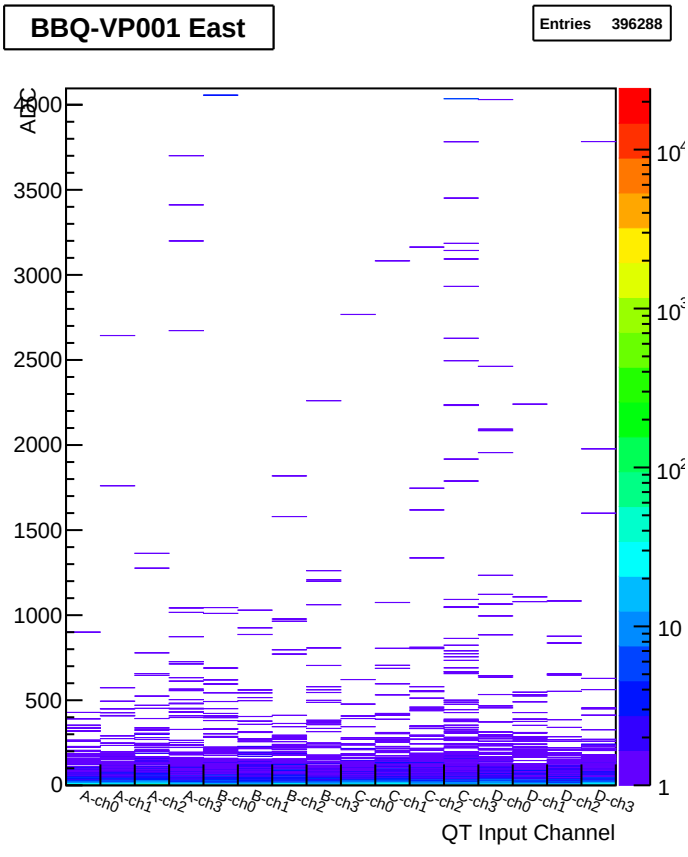

BBQ-BB001 (BBC east small tiles TAC)
Entries 396288
TAC
QT Input Channel
E1 E7 E2 E8 E3 E9 E10 E11 E4 E12 E13 E5 E6 E14 E15 E16

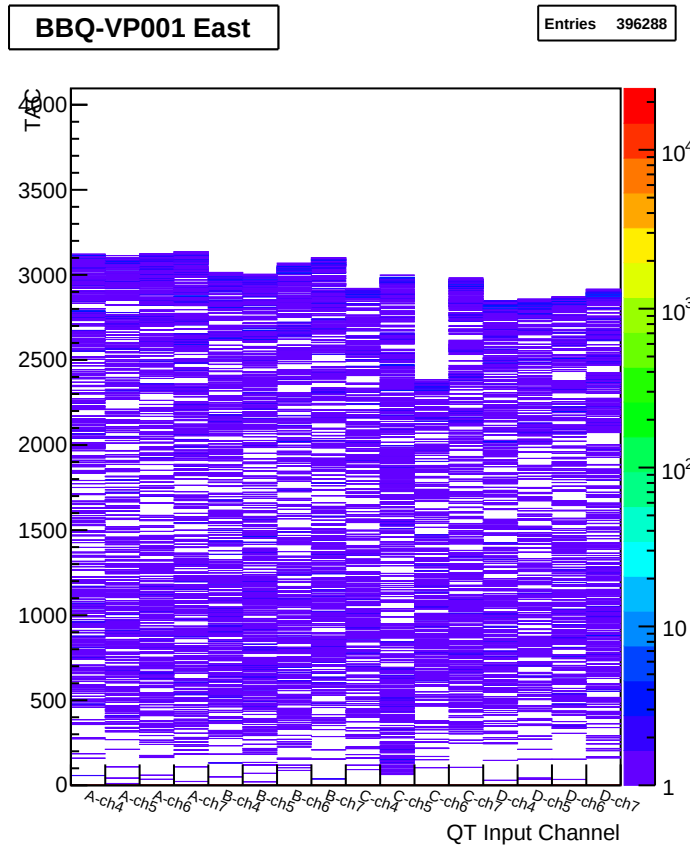

BBQ-BB002 (BBC west small tiles ADC)
Entries 396288
ADC
QT Input Channel
W1 W7 W2 W8 W3 W9 W10 W11 W4 W12 W13 W5 W6 W14 W15 W16

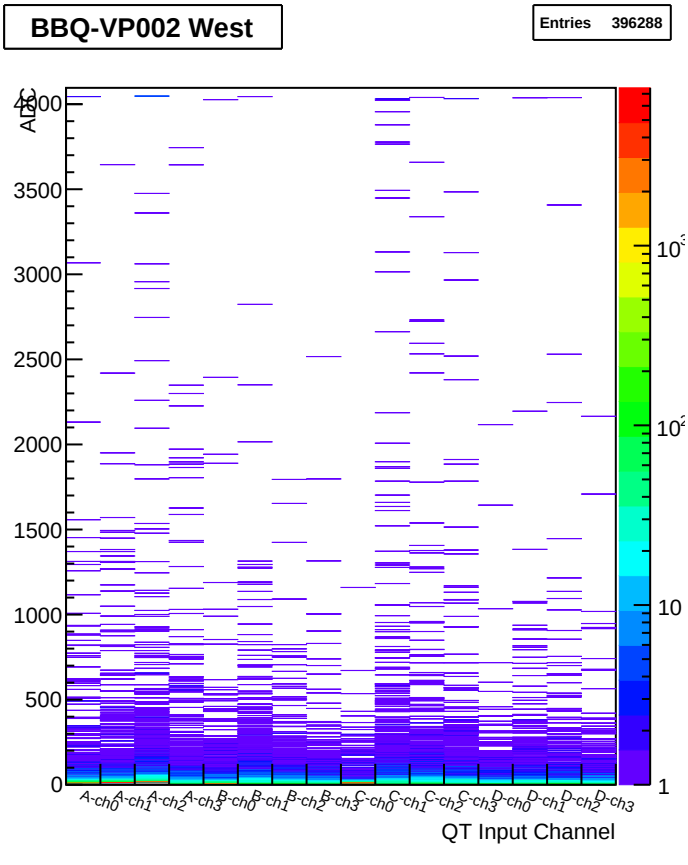

BBQ-BB002 (BBC west small tiles TAC)
Entries 396288
TAC
QT Input Channel
W1 W7 W2 W8 W3 W9 W10 W11 W4 W12 W13 W5 W6 W14 W15 W16

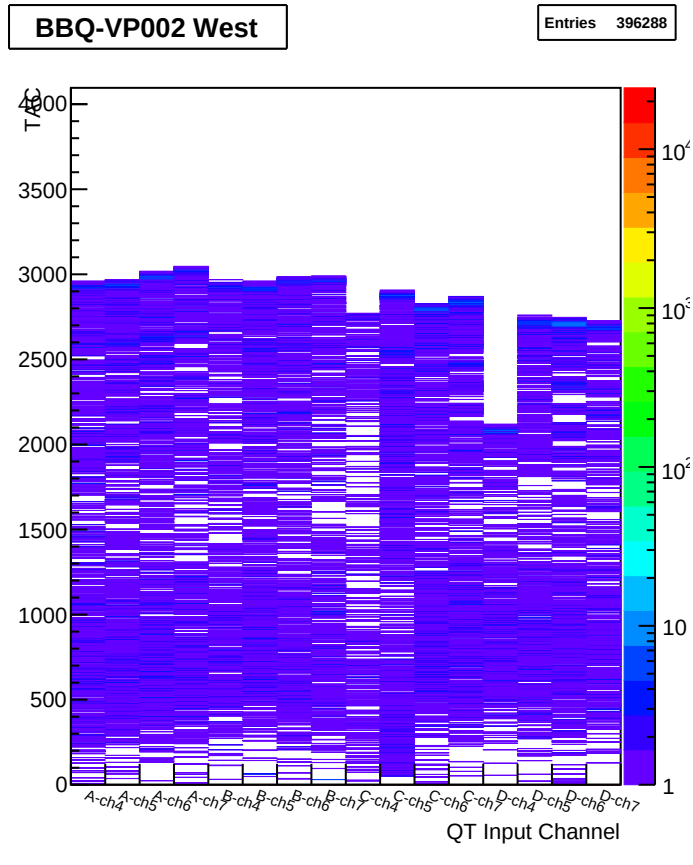

BBQ-ZD001 (ZDC TOWER)
Entries 396288
ADC
E1 E1A Esum EsumA E2 E3 E2A E+W A W1 W1A Wsum WsumA W2 W3 W2A

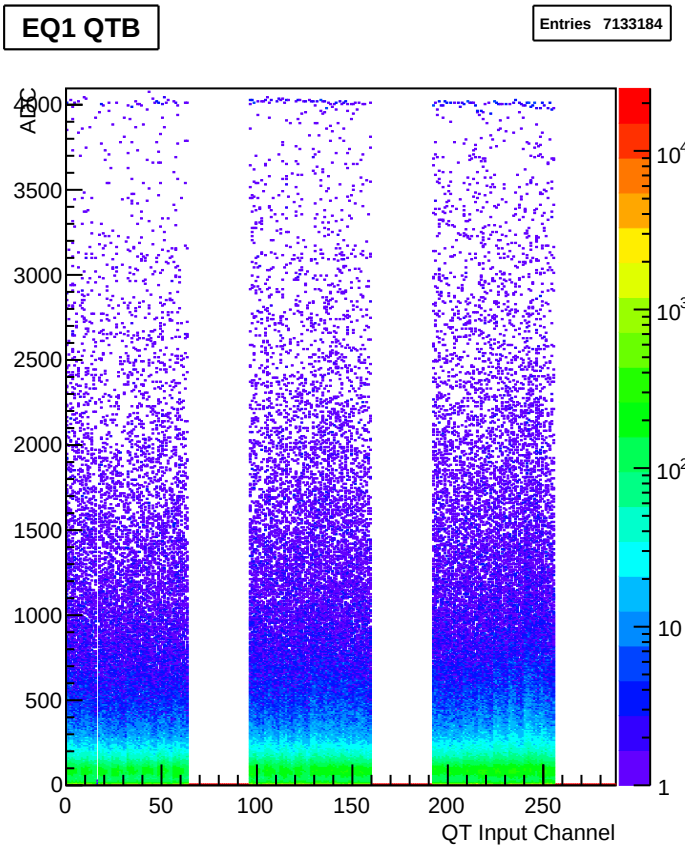

BBQ-ZD001 (ZDC TOWER)
Entries 396288
TAC
E1TAC E1ATAC EsumTAC EsumATAC E2TAC E3TAC E2ATAC E+W A TAC W1TAC W1ATAC WsumTAC WsumA TAC W2TAC W3TAC W2ATAC

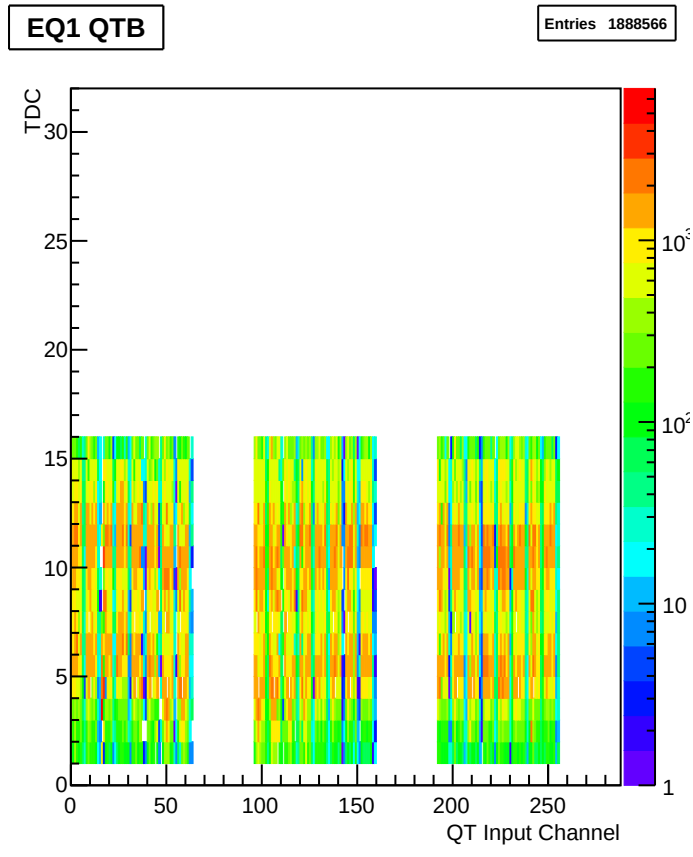

BBQ-VP001 East
Entries 396288
ADC
QT Input Channel

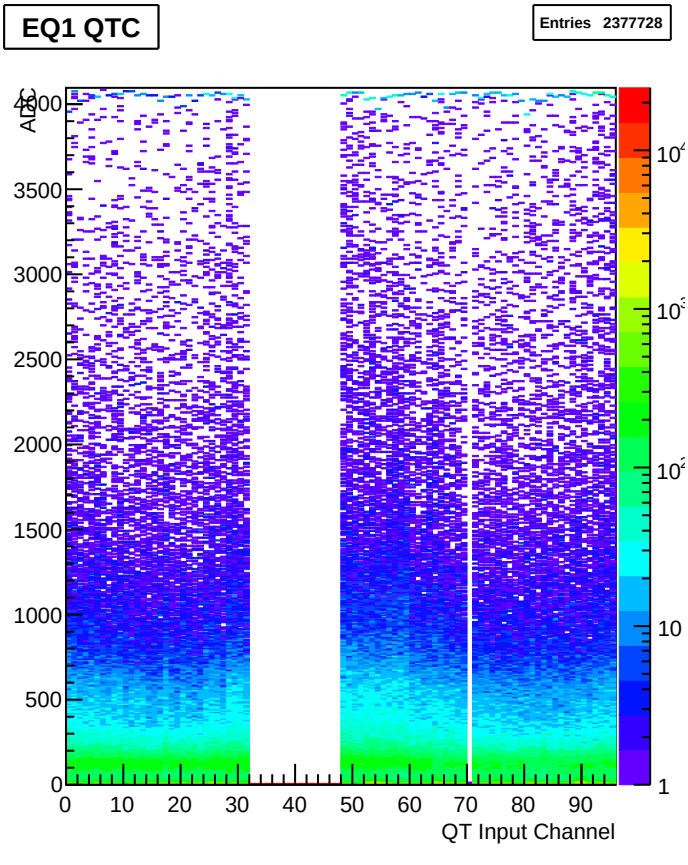

BBQ-VP001 East
Entries 396288
TADC
QT Input Channel

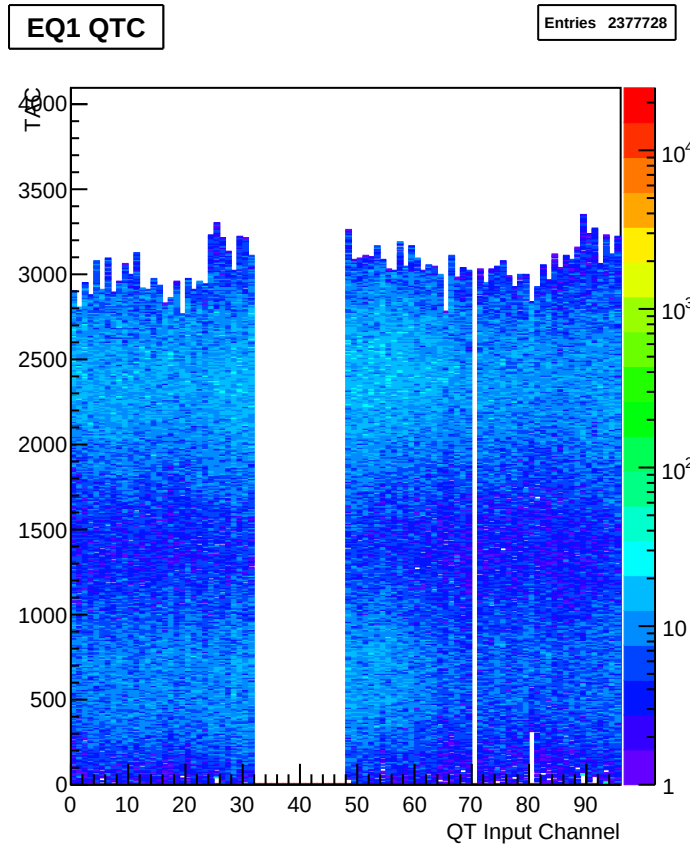

BBQ-VP002 West
Entries 396288
ADC
QT Input Channel

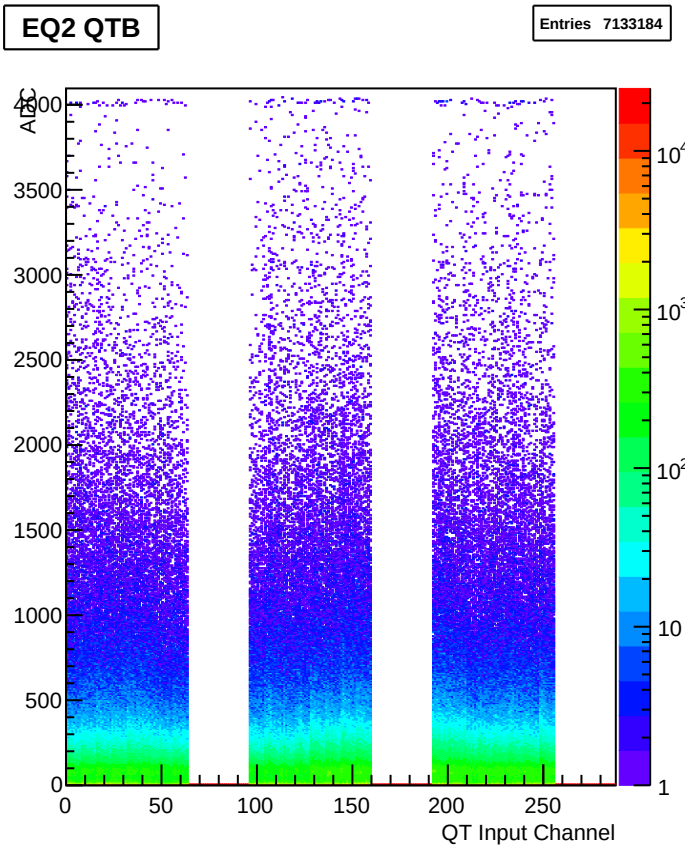

BBQ-VP002 West
Entries 396288
TADC
QT Input Channel

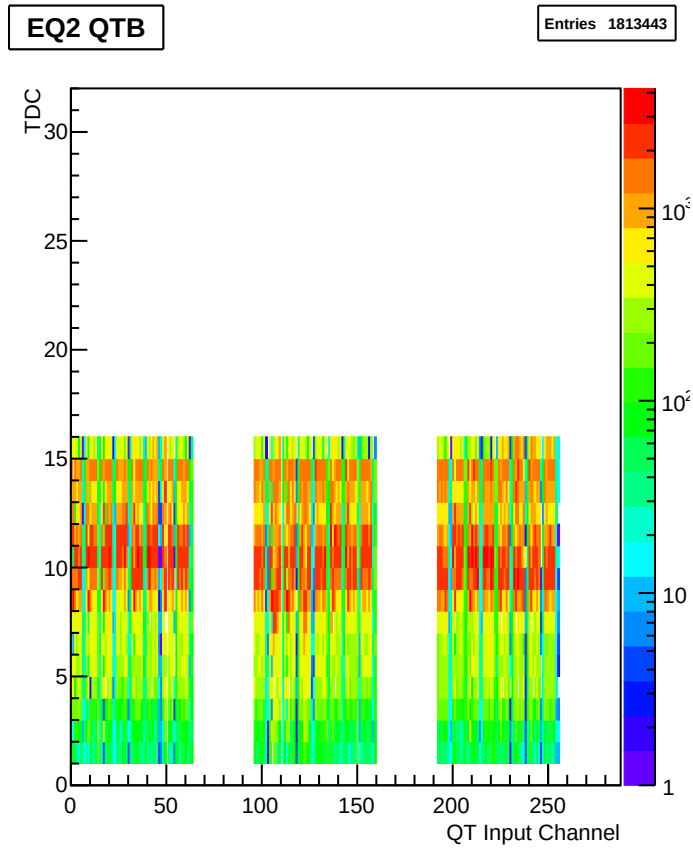

EQ1 QTB
Entries 7133184
ADC
QT Input Channel

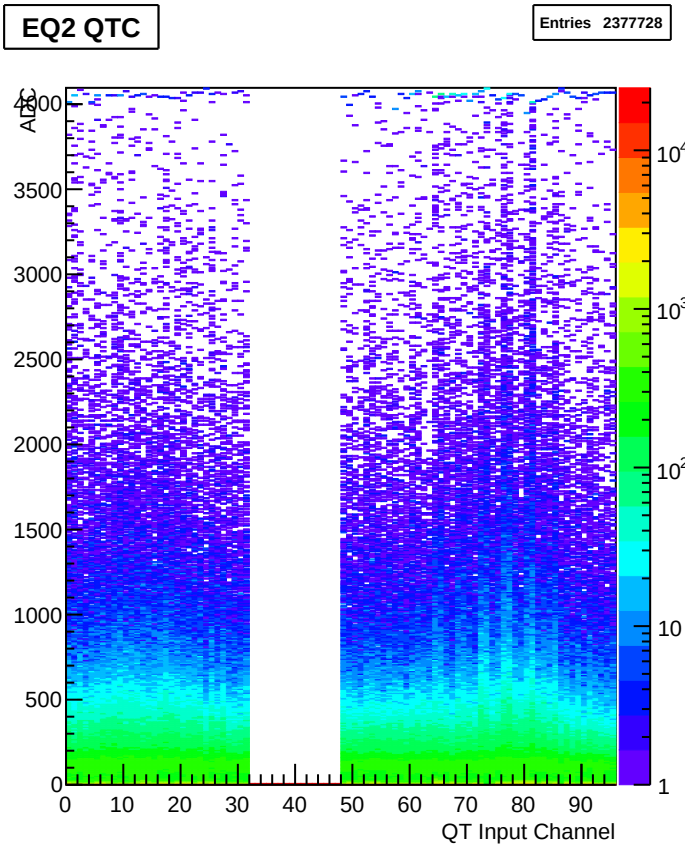

EQ1 QTB
Entries 1888566
TDC
QT Input Channel

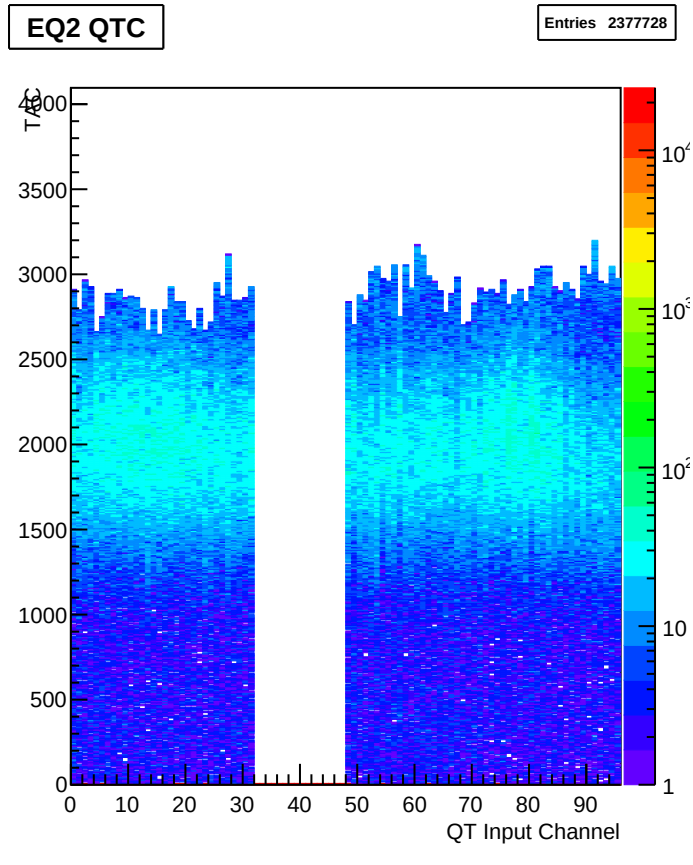

EQ1 QTC
Entries 2377728
ADC
QT Input Channel

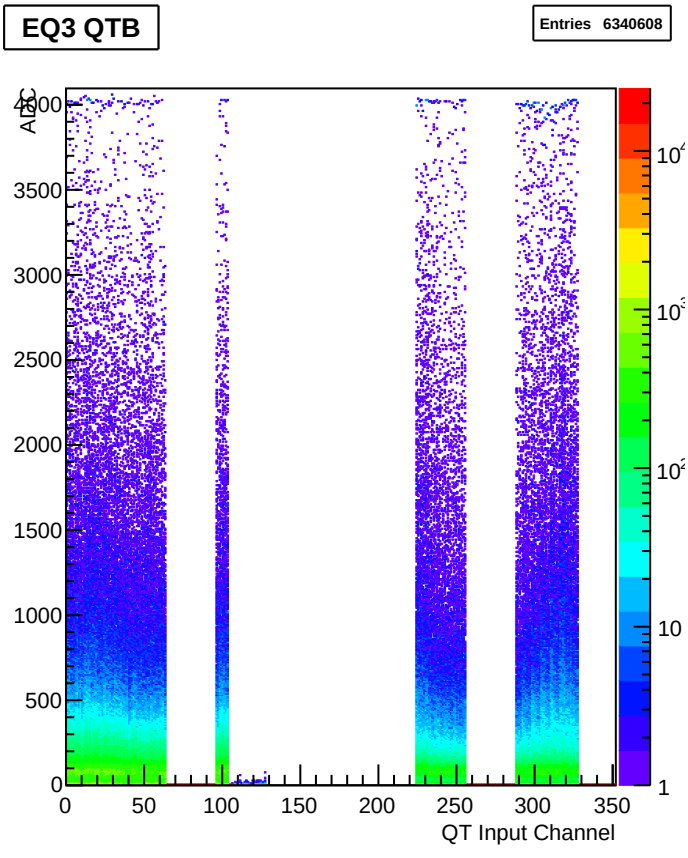

EQ1 QTC
Entries 2377728
TADC
QT Input Channel

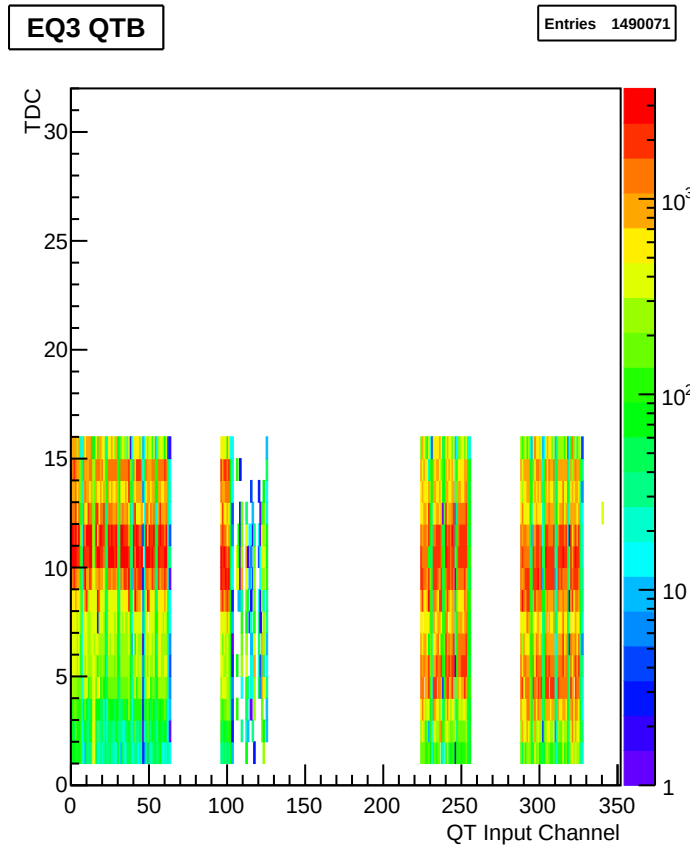

EQ2 QTB
Entries 7133184
ADC
QT Input Channel

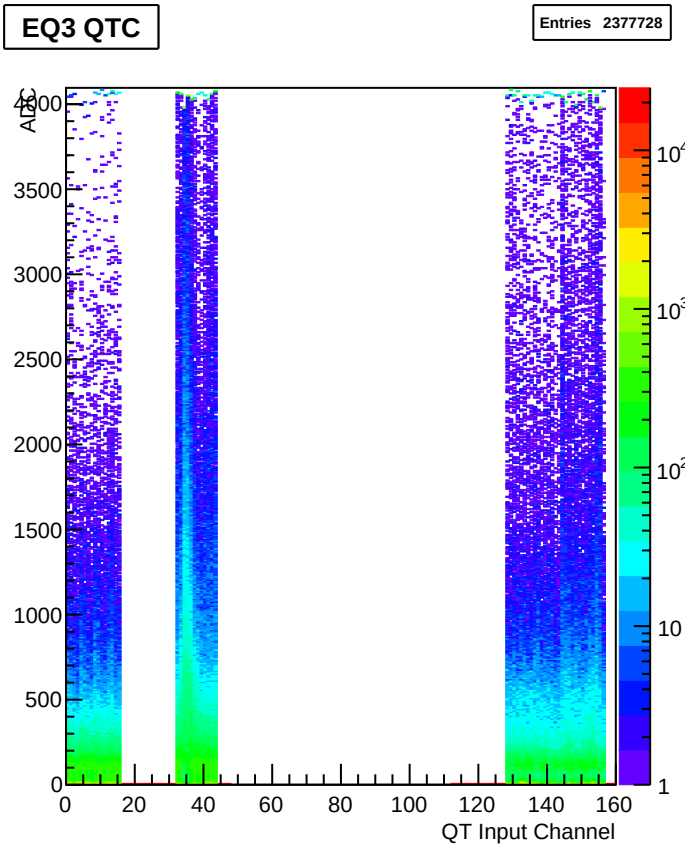

EQ2 QTB
Entries 1813443
TDC
QT Input Channel

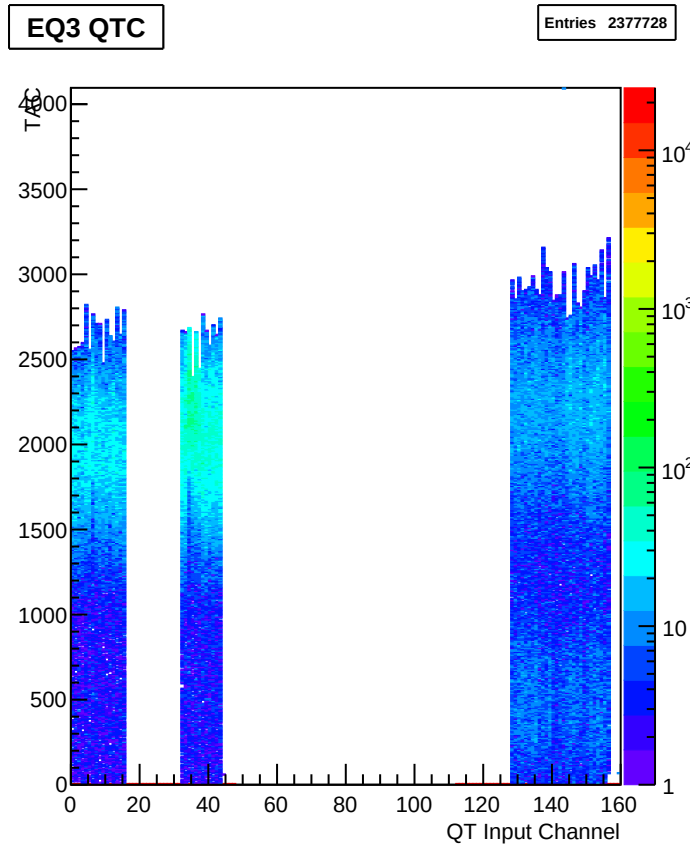

EQ2 QTC
Entries 2377728
ADC
QT Input Channel

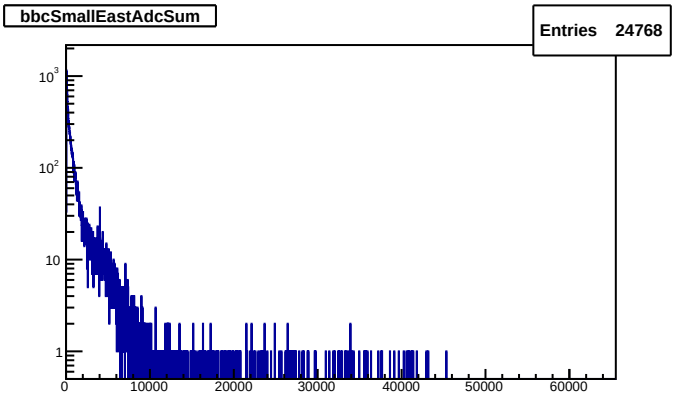

EQ2 QTC
Entries 2377728
TADC
QT Input Channel

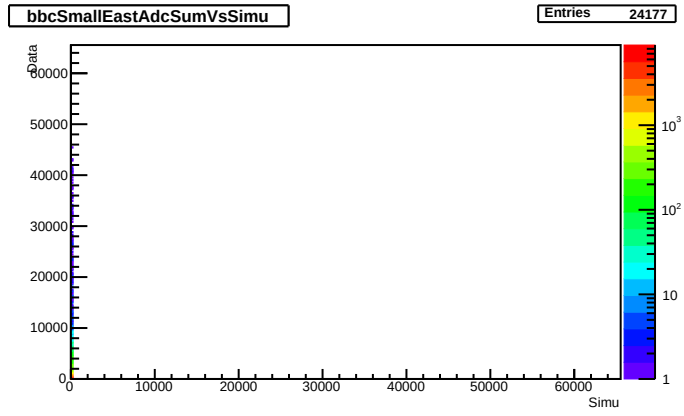

EQ3 QTB
Entries 6340608
ADC
QT Input Channel

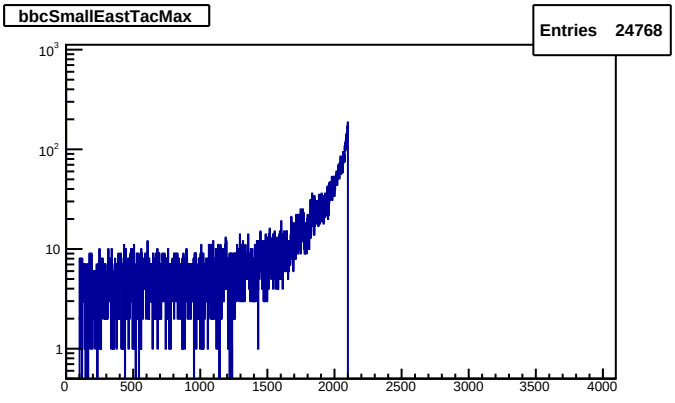

EQ3 QTB
Entries 1490071
TDC
QT Input Channel

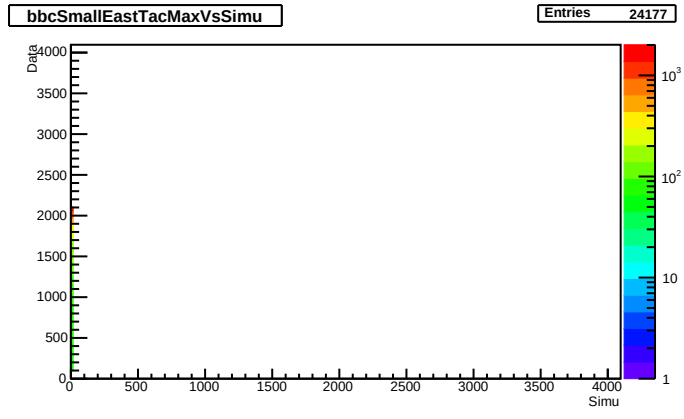

EQ3 QTC
Entries 2377728
ADC
QT Input Channel


EQ3 QTC
Entries 2377728
TADC
QT Input Channel


bbcSmallEastAdcSum
Entries 24768


bbcSmallEastAdcSumVsSimu
Entries 24177
Data
Simu


bbcSmallEastTacMax
Entries 24768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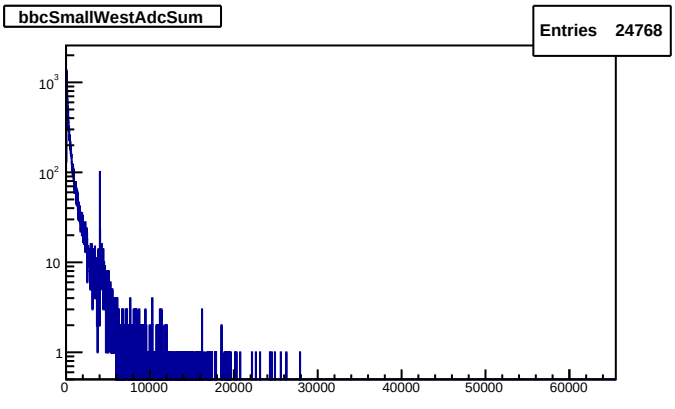
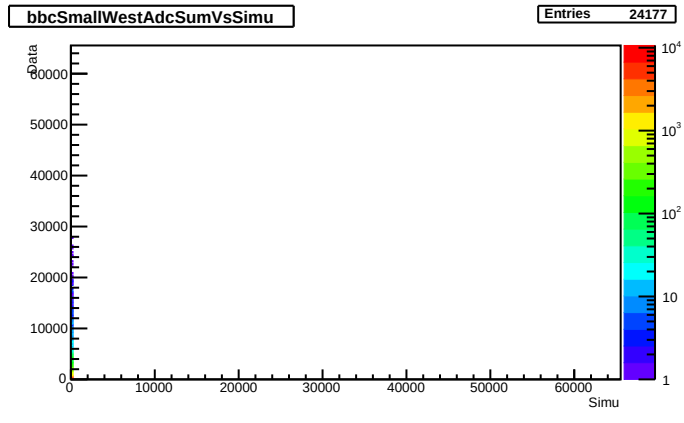
Entries 24177
Data
Simu


bbcSmallWestAdcSum
Entries 24768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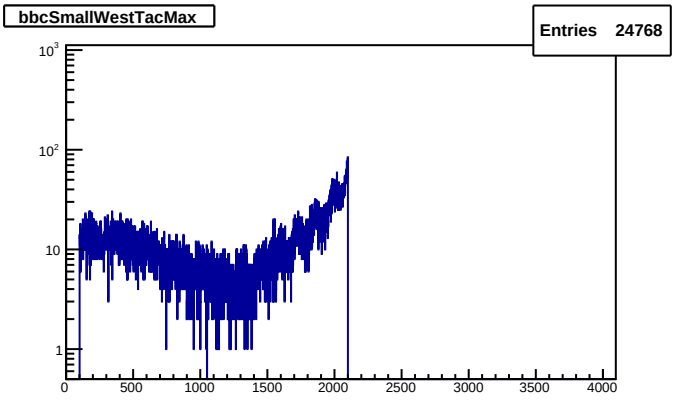
Entries 24177
Data
Simu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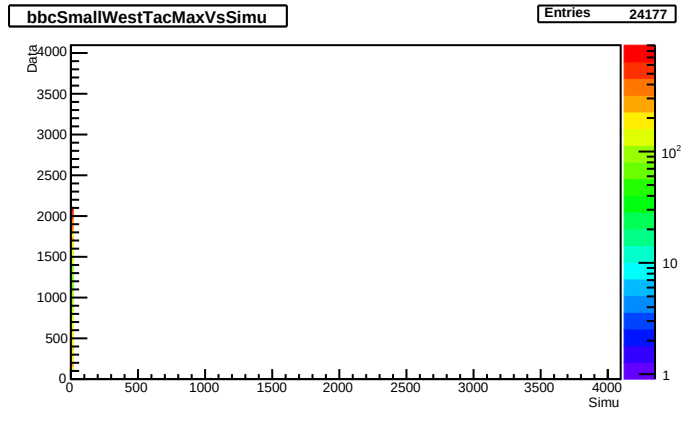
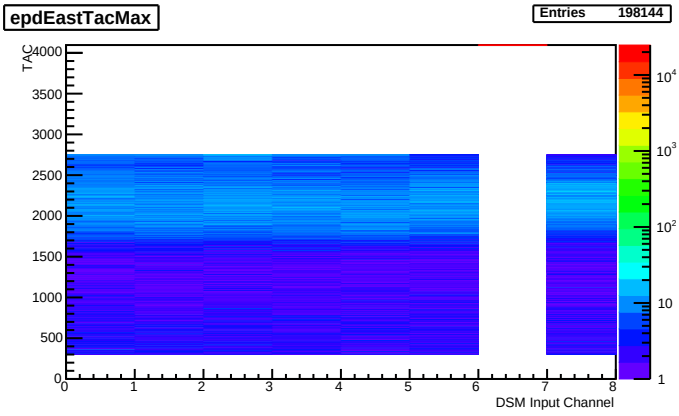
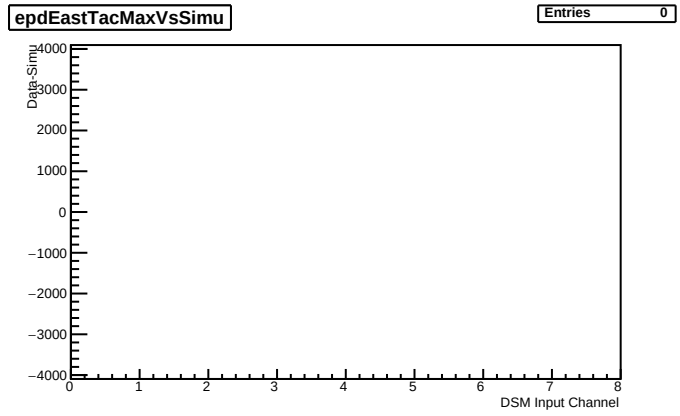
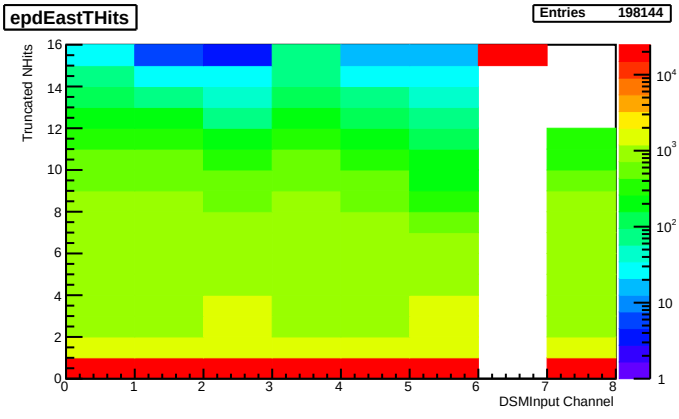
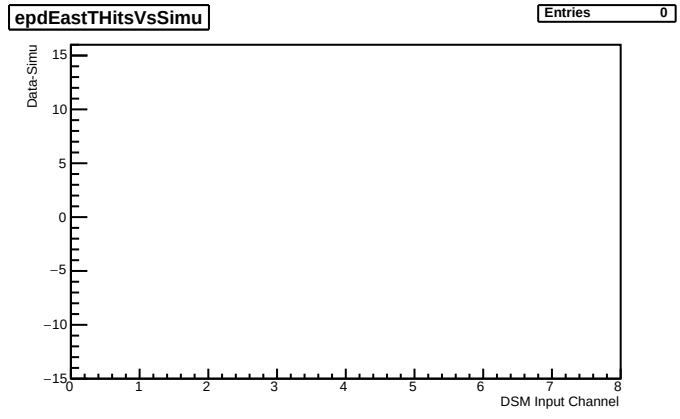
Entries 24768


bbcSmallWestTacMaxVsSimu
Entries 24177
Data
Simu


epdEastTacMax
Entries 198144
TAC
DSM Input Channel


epdEastTacMaxVsSimu
Entries 0
Data-Simu
DSM Input Channel


epdEastTHits
Entries 198144
Truncated NHits
DSMInput Channel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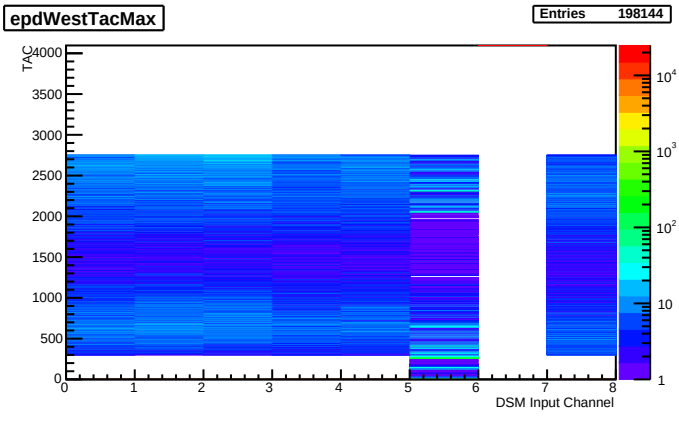
Data-Simu
DSM Input Channel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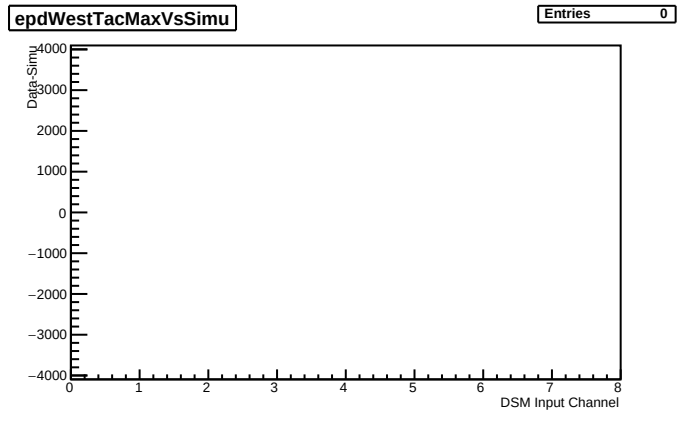
Entries 198144
TAC
DSM Input Channel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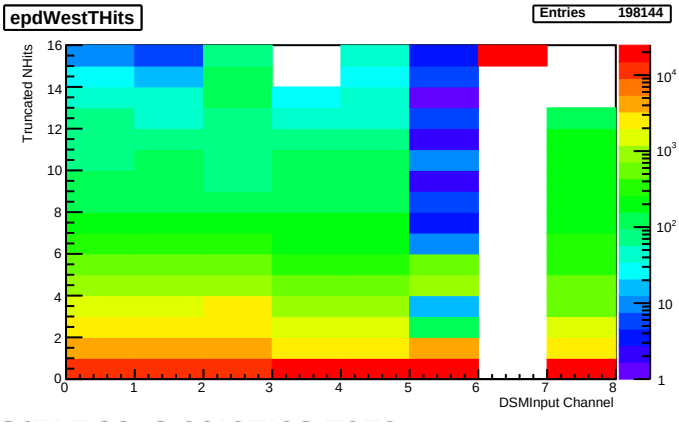
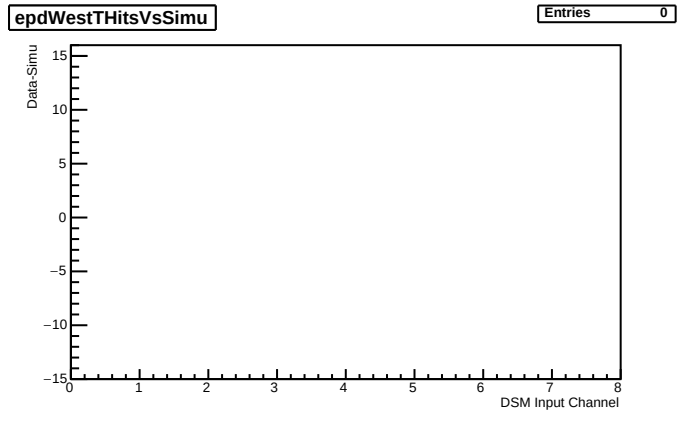
Entries 0
Data-Simu
DSM Input Channel


epdWestTHits
Entries 198144
Truncated NHits
DSMInput Channel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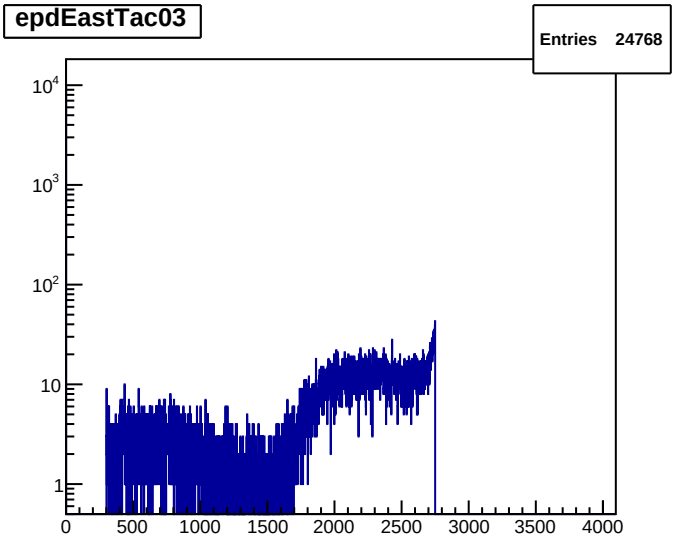
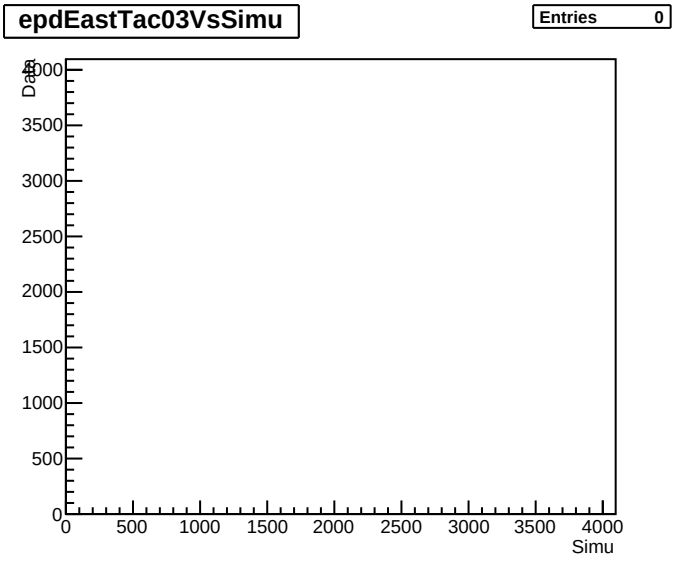
Entries 0
Data-Simu
DSM Input Channel


epdEastTac03
Entries 24768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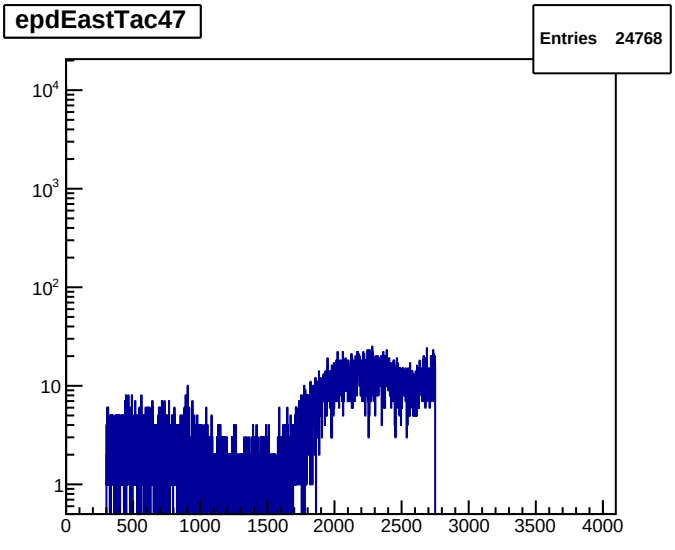
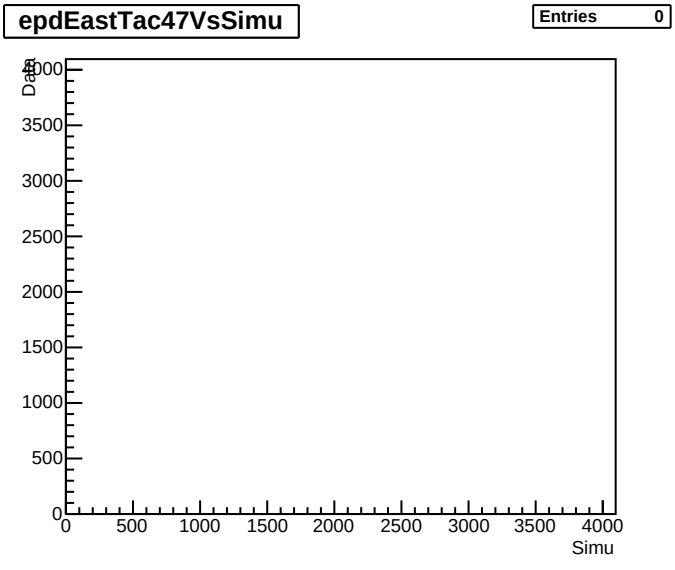
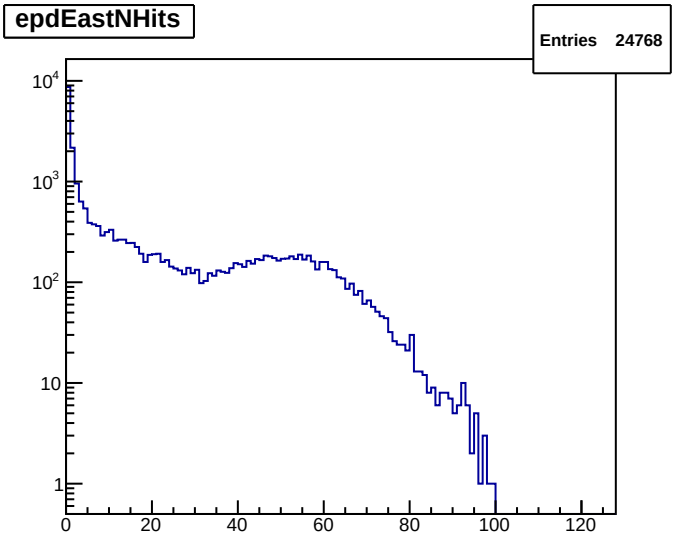
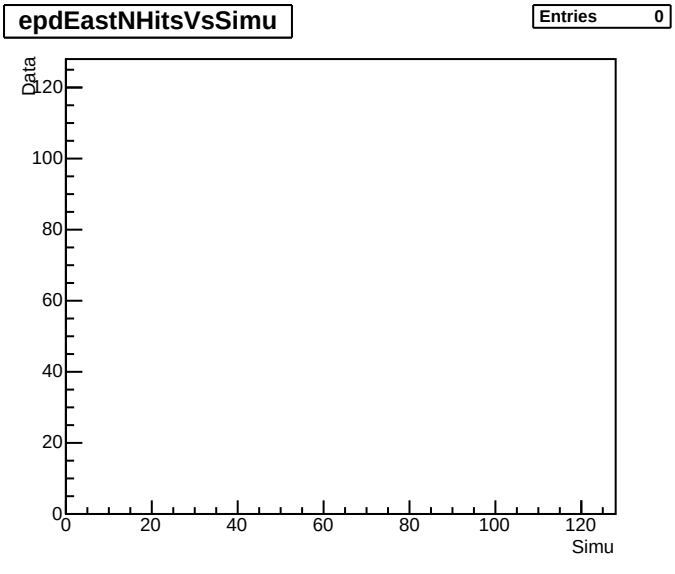
Entries 0
Data
Simu


epdEastTac47
Entries 24768


epdEastTac47VsSimu
Entries 0
Data
Simu


epdEastNHits
Entries 24768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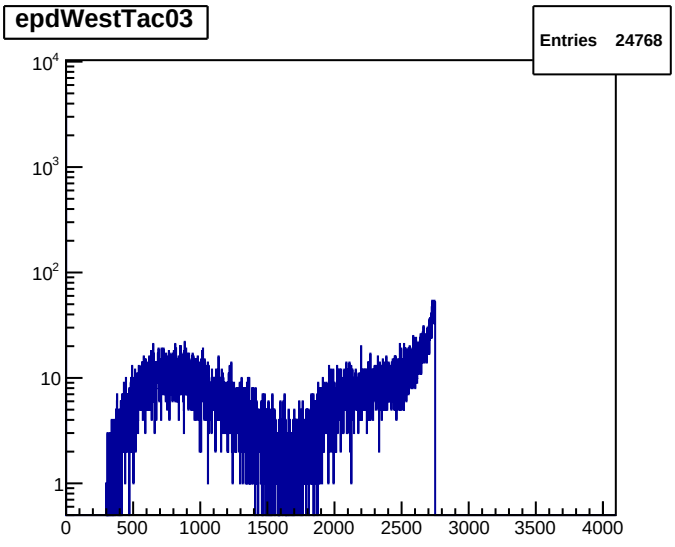
Entries 0
Data
Simu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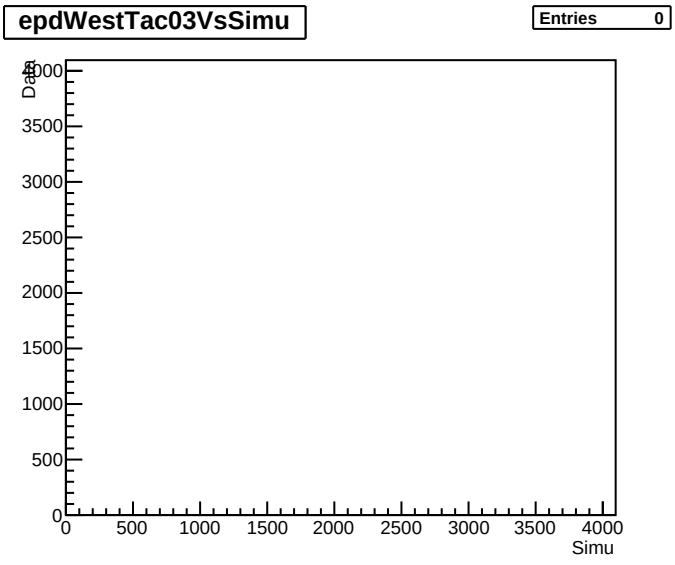
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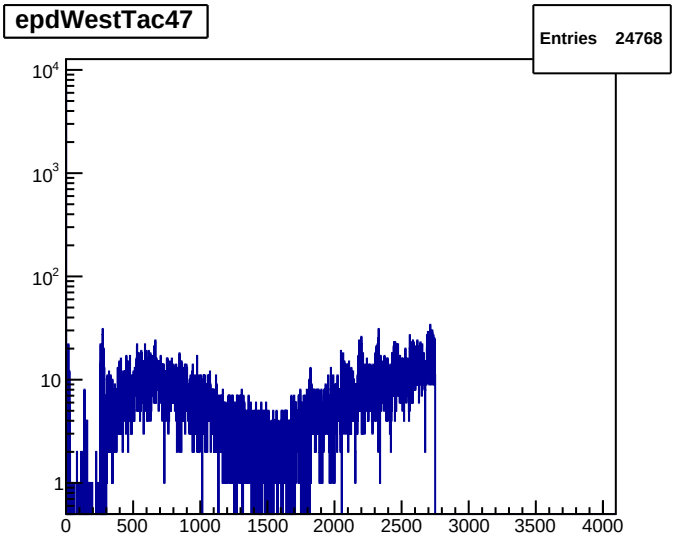
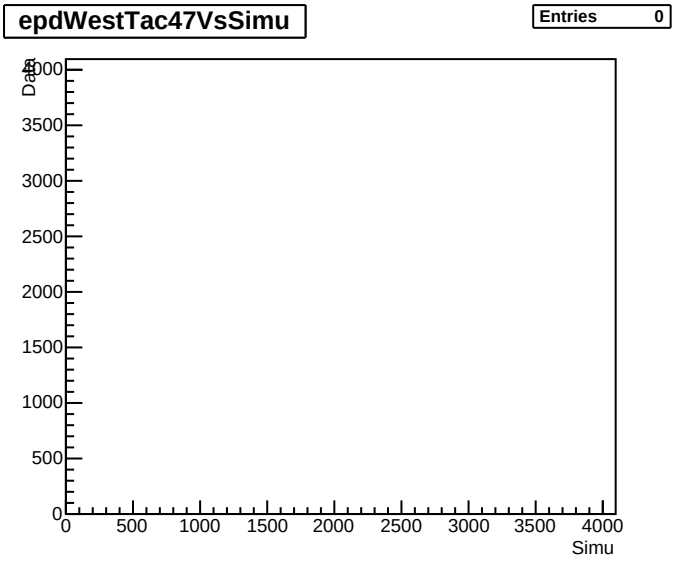
Entries 0
Data
Simu


epdWestTac47
Entries 24768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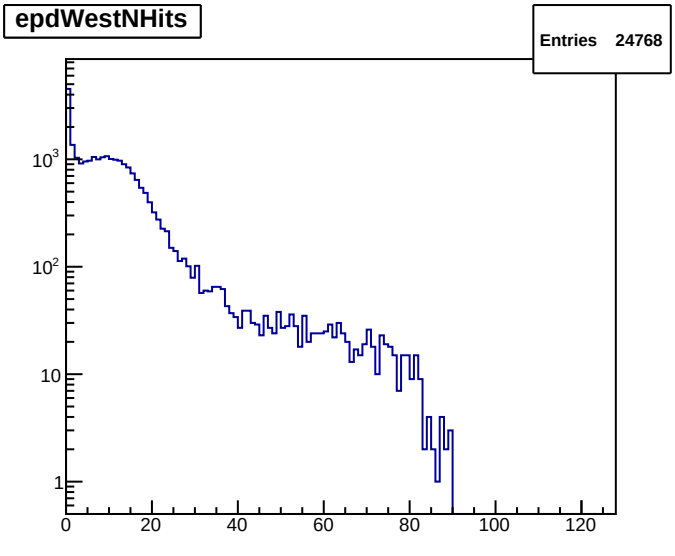
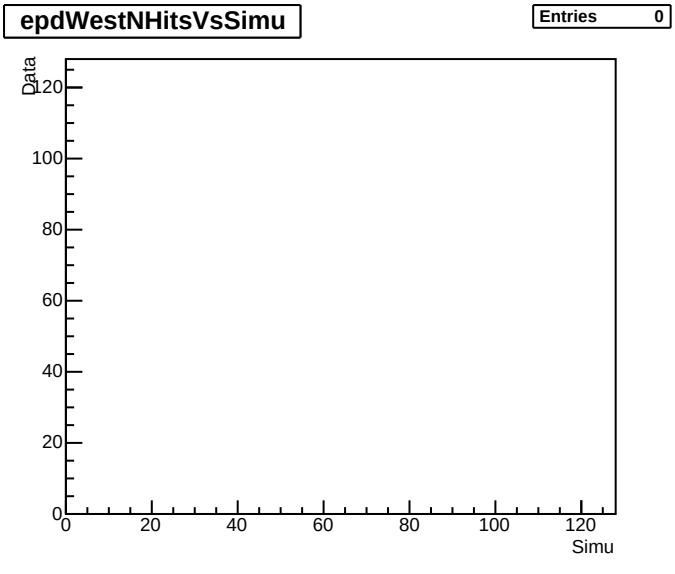
Entries 0
Data
Simu


epdWestNHits
Entries 24768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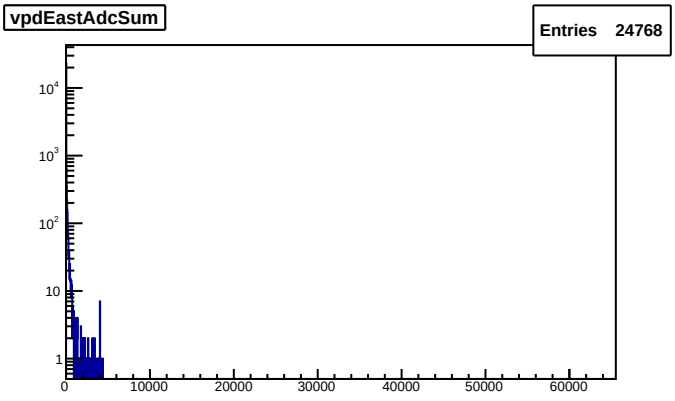
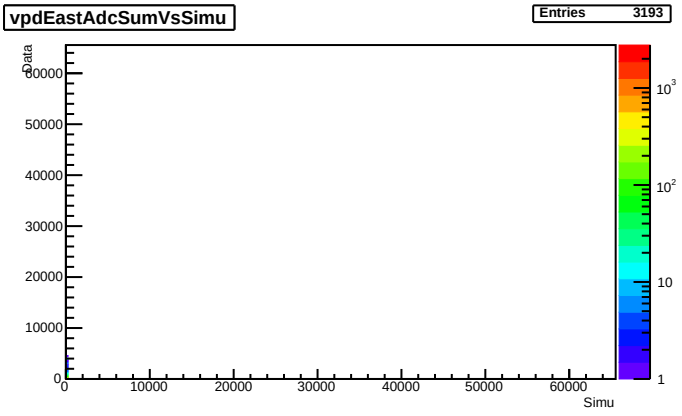
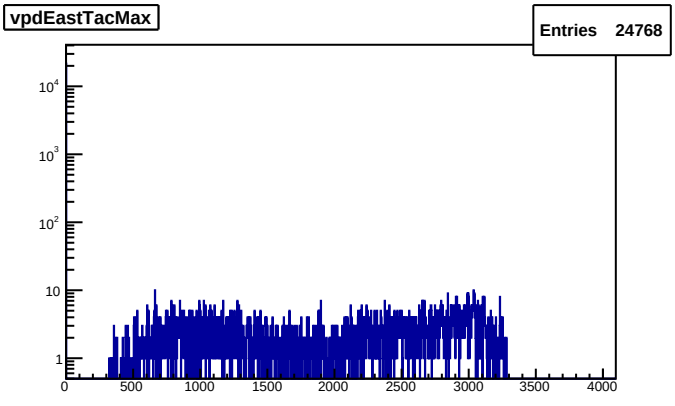
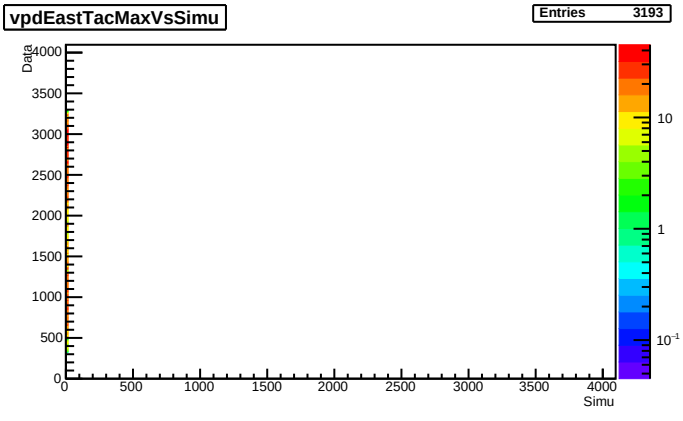
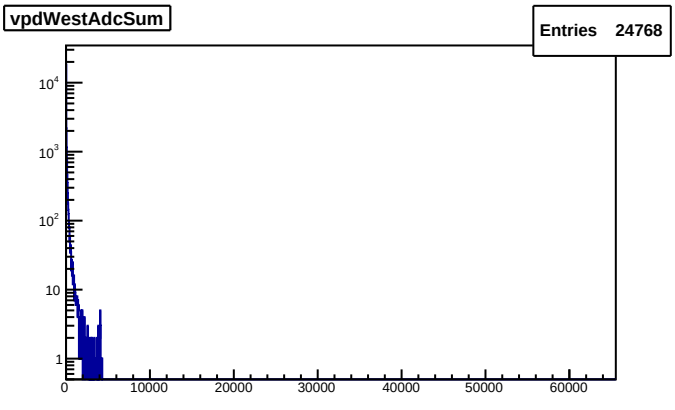
Entries 0
Data
Simu

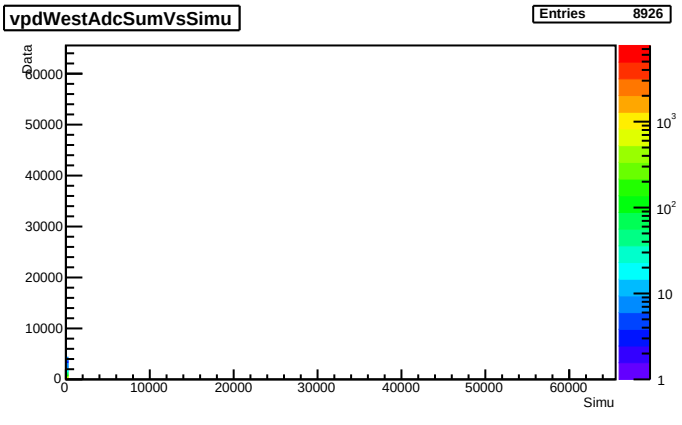

vpdEastAdcSum
Entries 24768

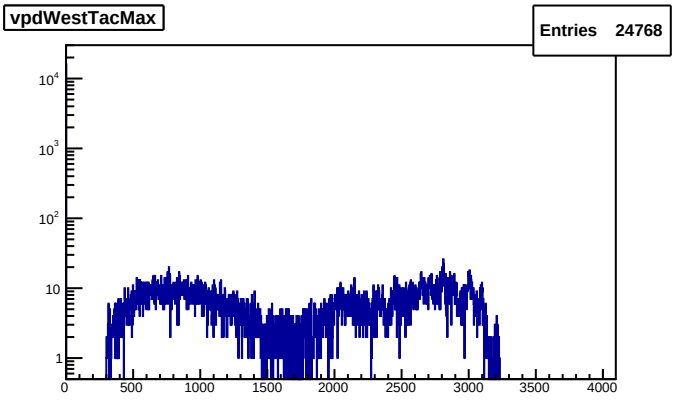

vpdEastAdcSumVsSimu
Entries 3193
Data
Simu

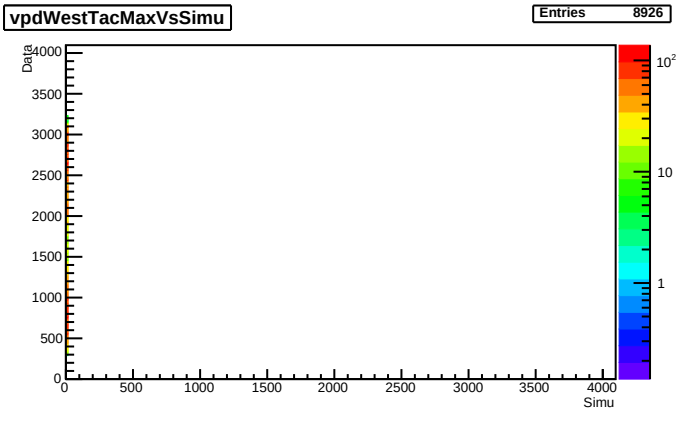

vpdEastTacMax
Entries 24768

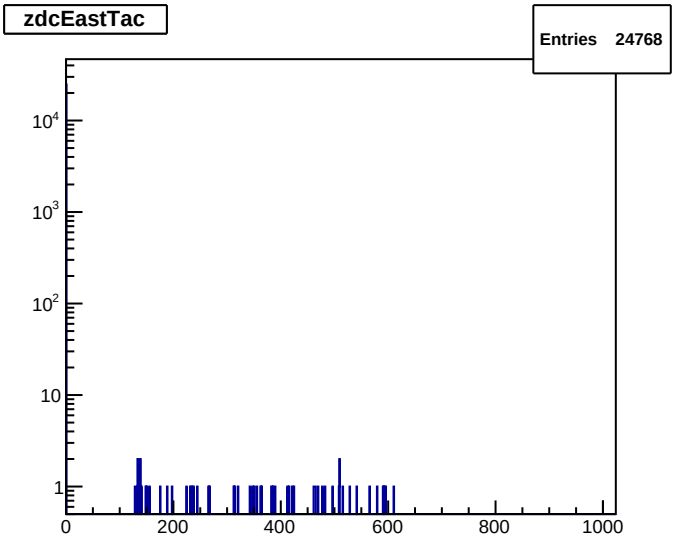

vpdEastTacMaxVsSimu
Entries 3193
Data
Simu

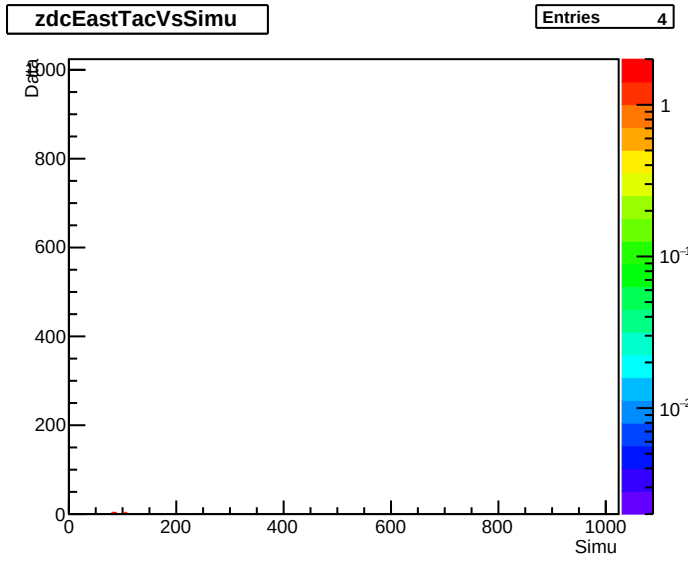

vpdWestAdcSum
Entries 24768

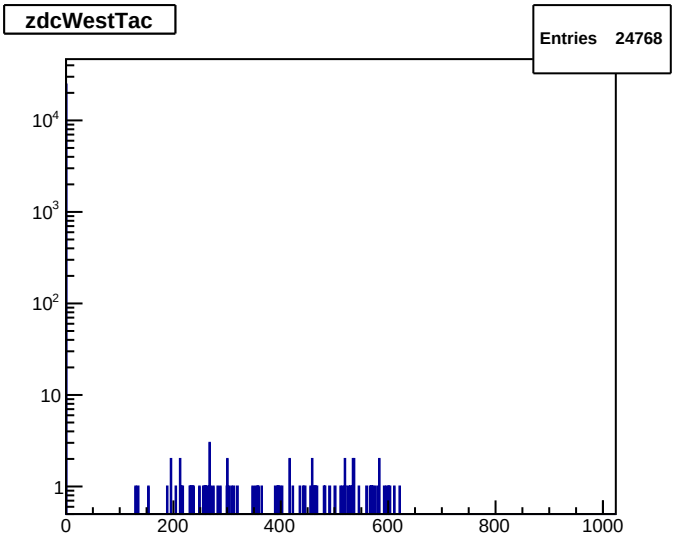

vpdWestAdcSumVsSimu
Entries 8926
Data
Simu

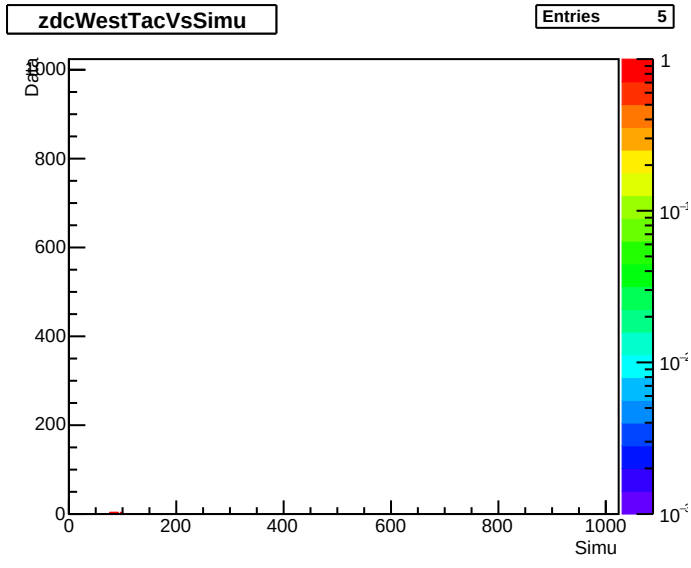

vpdWestTacMax
Entries 24768

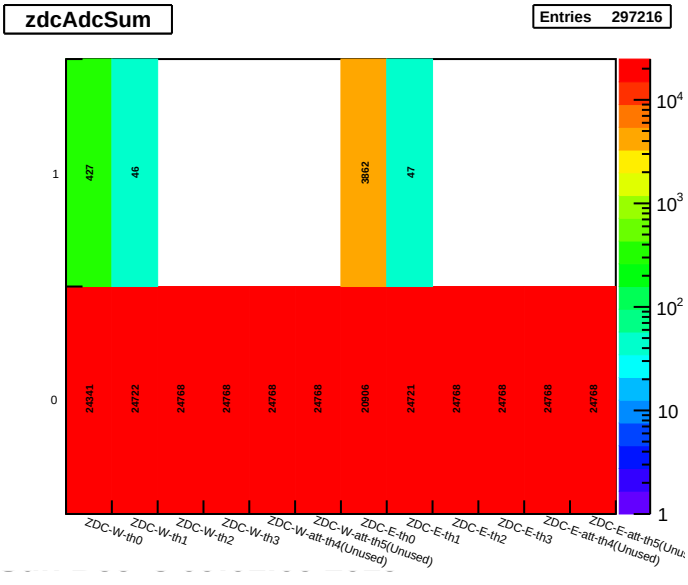

vpdWestTacMaxVsSimu
Entries 8926
Data
Simu

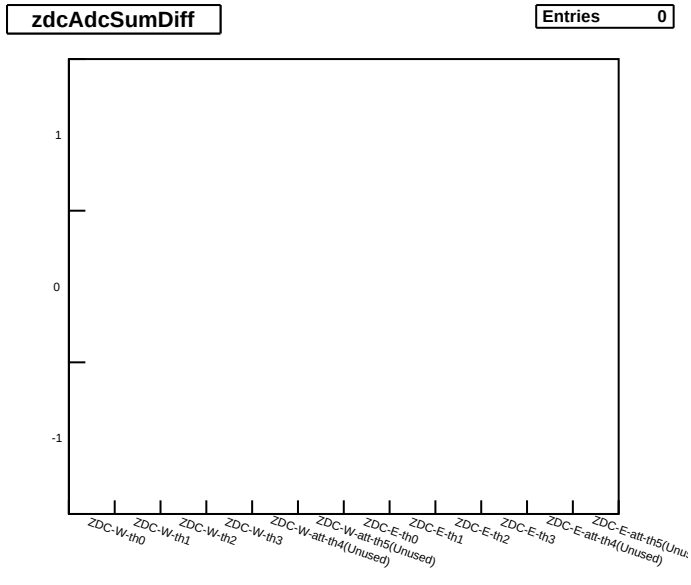

zdcEastTac
Entries 24768

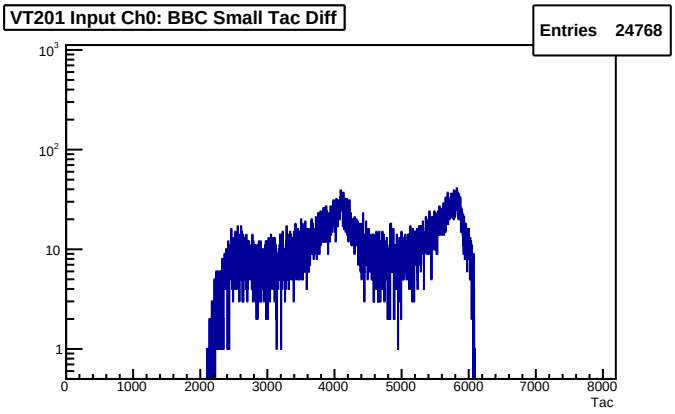

zdcEastTacVsSimu
Entries 4
Data
Simu

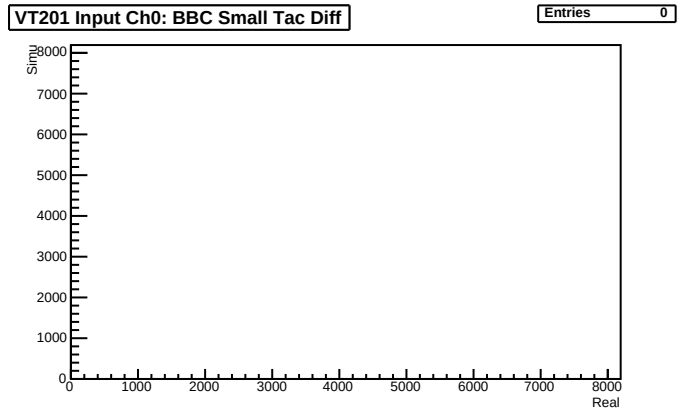

zdcWestTac
Entries 24768

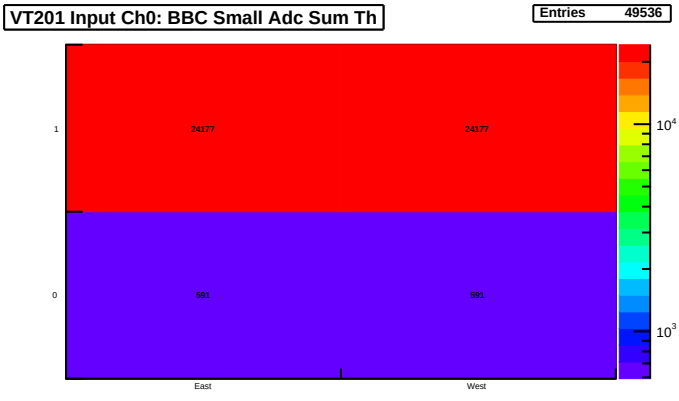

zdcWestTacVsSimu
Entries 5
Data
Simu

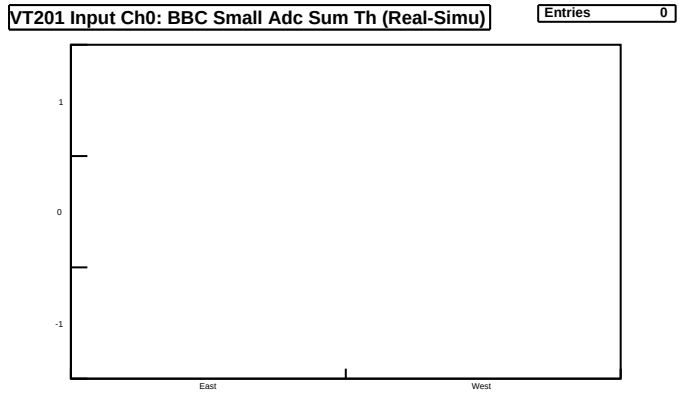

zdcAdcSum
Entries 297216
427
46
3862
47
24341
24722
24768
24768
24768
24768
20906
24721
24768
24768
24768
24768
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)

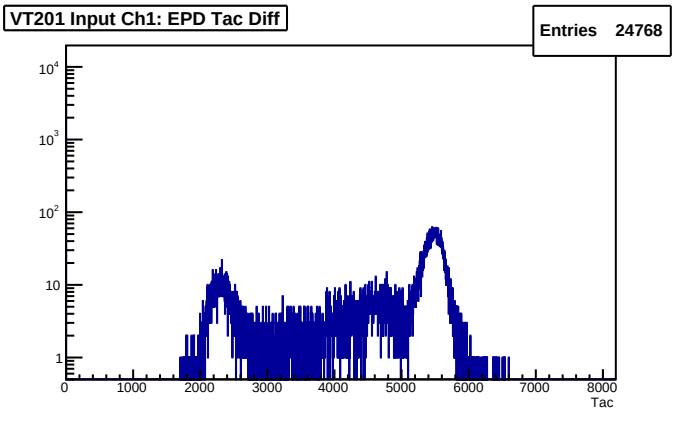

zdcAdcSumDiff
Entries 0
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)

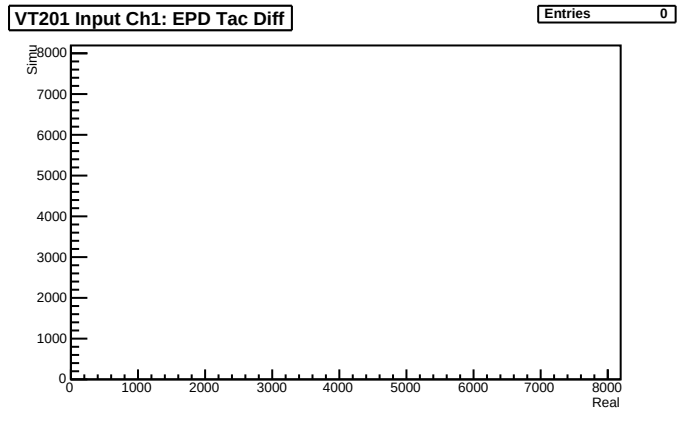

VT201 Input Ch0: BBC Small Tac Diff
Entries 24768
Tac

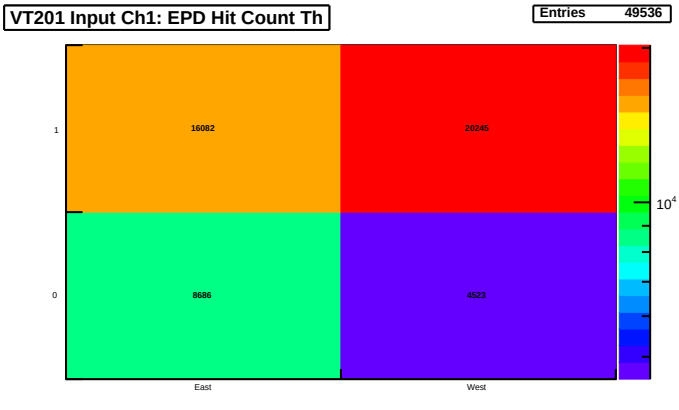

VT201 Input Ch0: BBC Small Tac Diff
Entries 0
Simu
Real

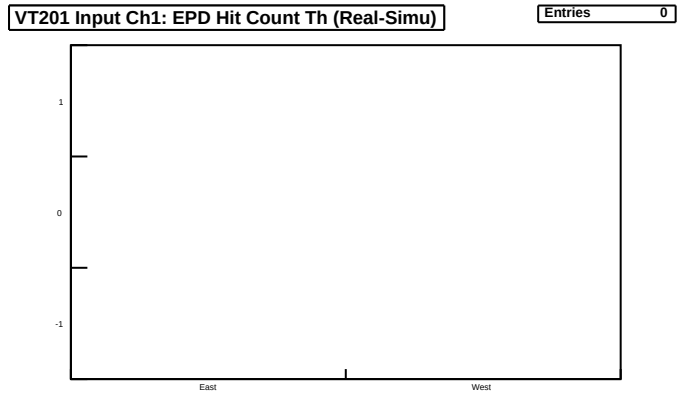

VT201 Input Ch0: BBC Small Adc Sum Th
Entries 49536
24177
24177
591
591
East
West

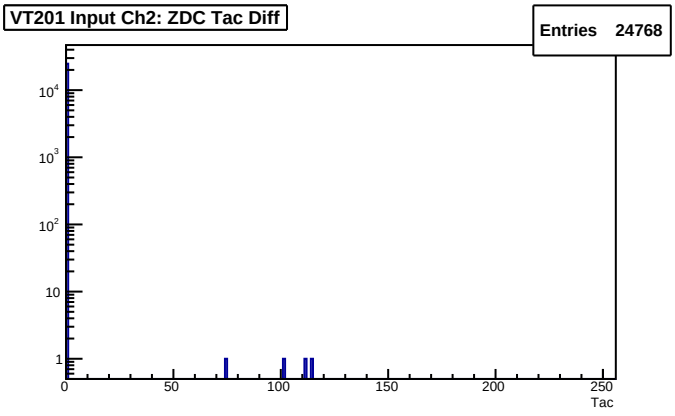

VT201 Input Ch0: BBC Small Adc Sum Th (Real-Simu)
Entries 0
East
West

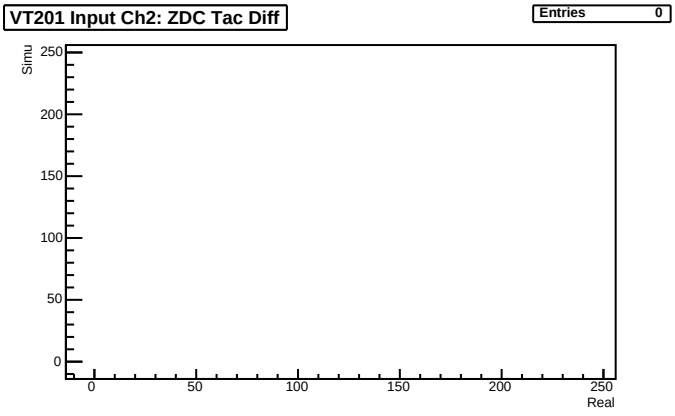

VT201 Input Ch1: EPD Tac Diff
Entries 24768
Tac

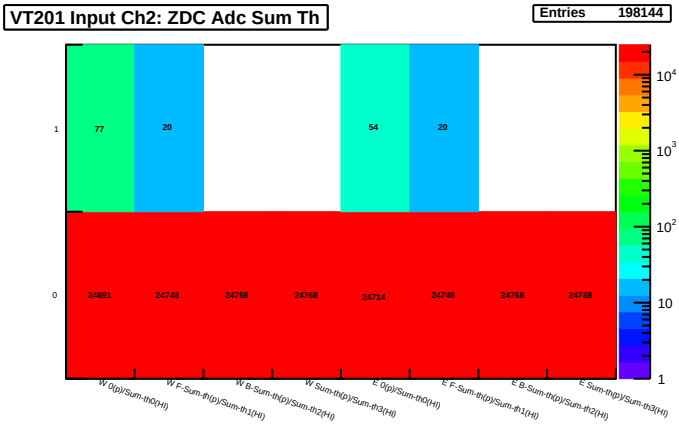

VT201 Input Ch1: EPD Tac Diff
Entries 0
Simu
Real

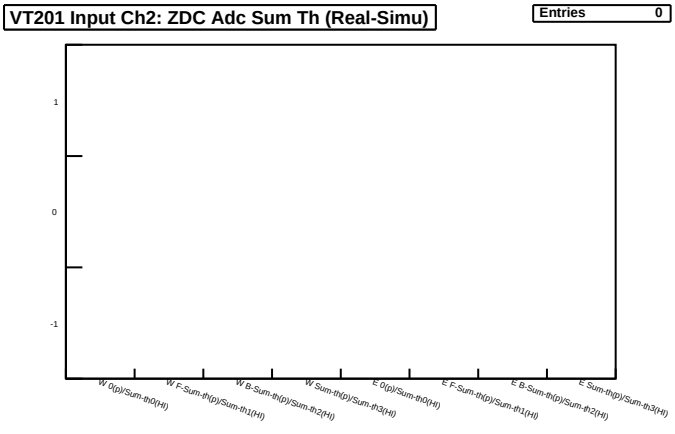

VT201 Input Ch1: EPD Hit Count Th
Entries 49536
16082
20245
8686
4523
East
West

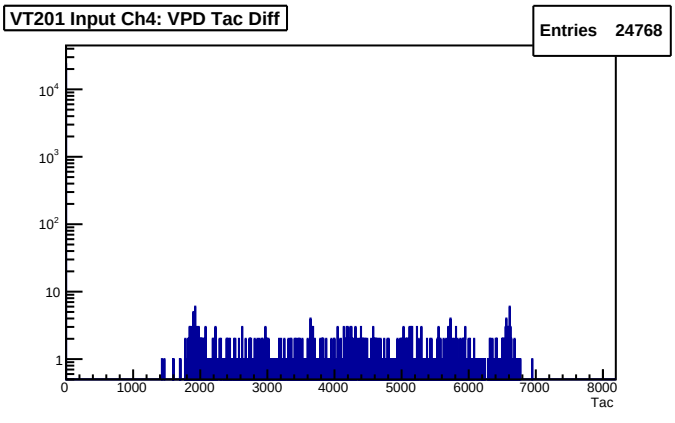

VT201 Input Ch1: EPD Hit Count Th (Real-Simu)
Entries 0
East
West

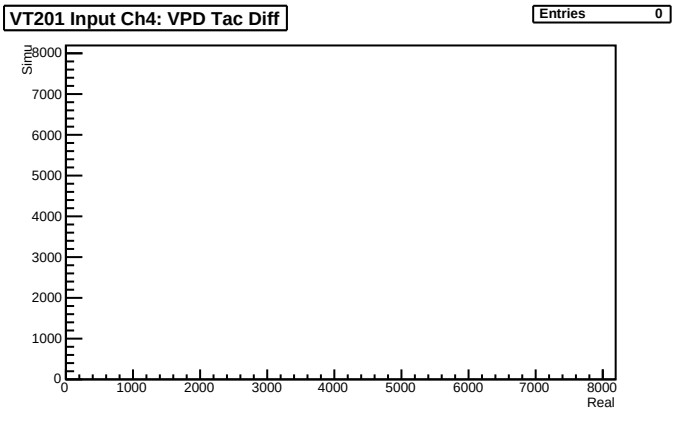

VT201 Input Ch2: ZDC Tac Diff
Entries 24768
Tac

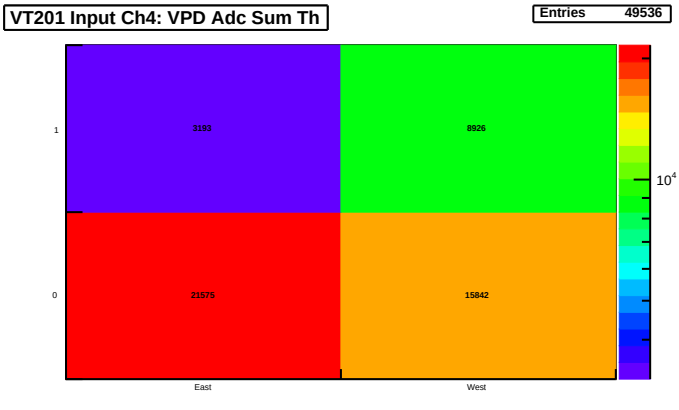

VT201 Input Ch2: ZDC Tac Diff
Entries 0
Simu
Real

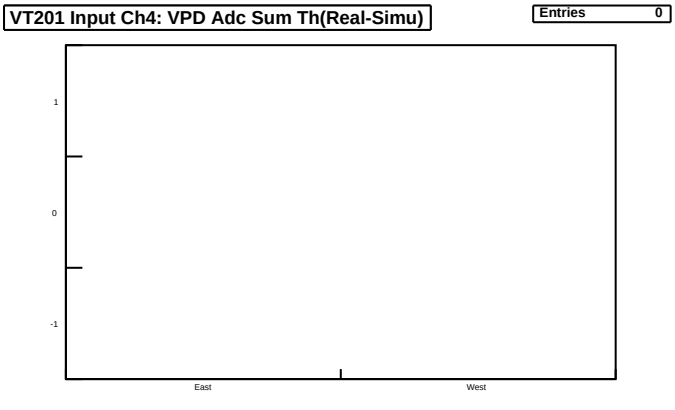

VT201 Input Ch2: ZDC Adc Sum Th
Entries 198144
77
20
54
20
24691
24748
24768
24768
24714
24748
24768
24768
W 0(p)/Sum-th0(H)
W F-Sum-th(p)/Sum-th1(H)
W B-Sum-th(p)/Sum-th2(H)
W Sum-th(p)/Sum-th3(H)
E 0(p)/Sum-th0(H)
E F-Sum-th(p)/Sum-th1(H)
E B-Sum-th(p)/Sum-th2(H)
E Sum-th(p)/Sum-th3(H)

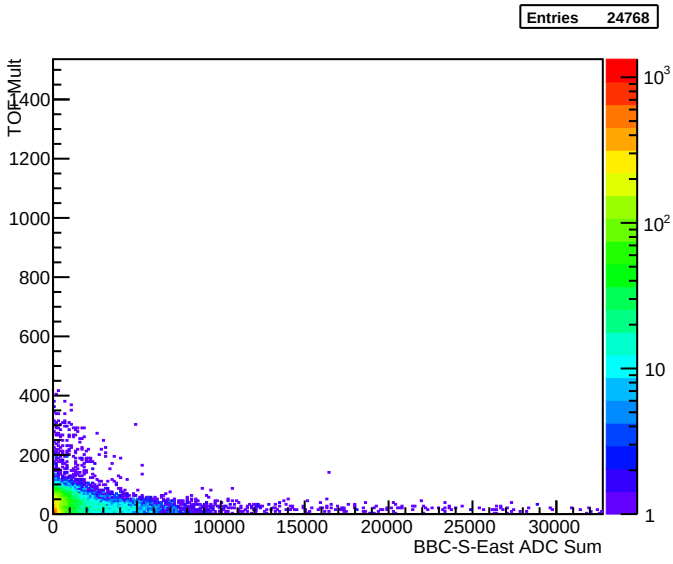

VT201 Input Ch2: ZDC Adc Sum Th (Real-Simu)
Entries 0
W 0(p)/Sum-th0(H)
W F-Sum-th(p)/Sum-th1(H)
W B-Sum-th(p)/Sum-th2(H)
W Sum-th(p)/Sum-th3(H)
E 0(p)/Sum-th0(H)
E F-Sum-th(p)/Sum-th1(H)
E B-Sum-th(p)/Sum-th2(H)
E Sum-th(p)/Sum-th3(H)

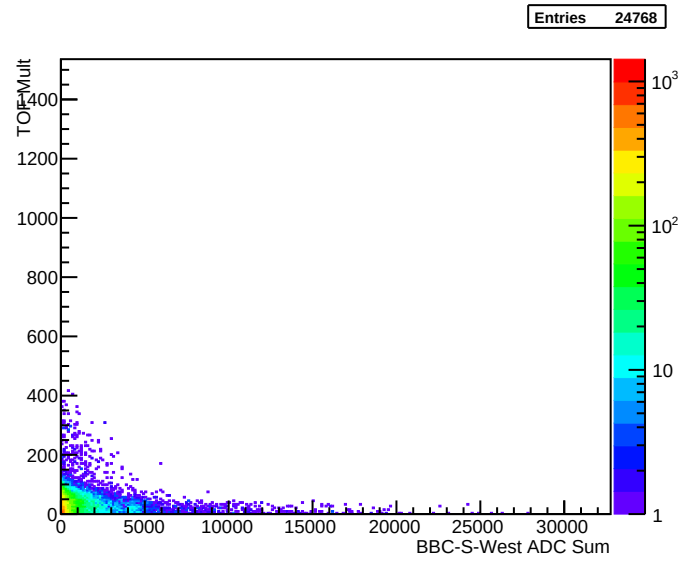

VT201 Input Ch4: VPD Tac Diff
Entries 24768
Tac

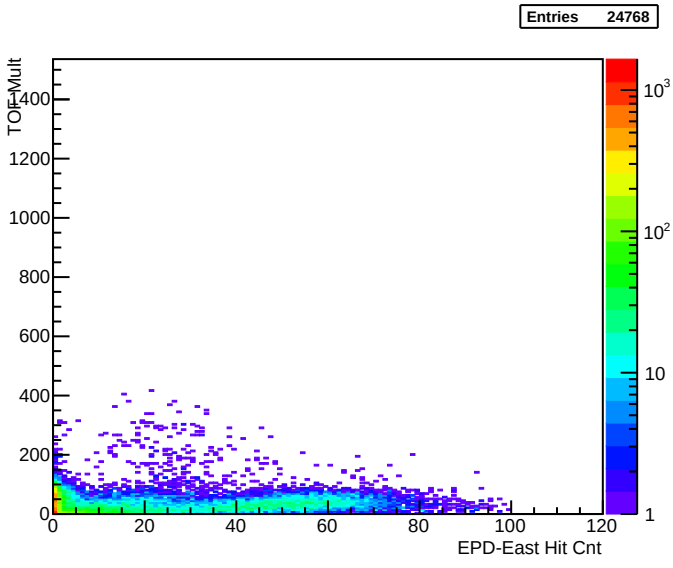

VT201 Input Ch4: VPD Tac Diff
Entries 0
Simu
Real

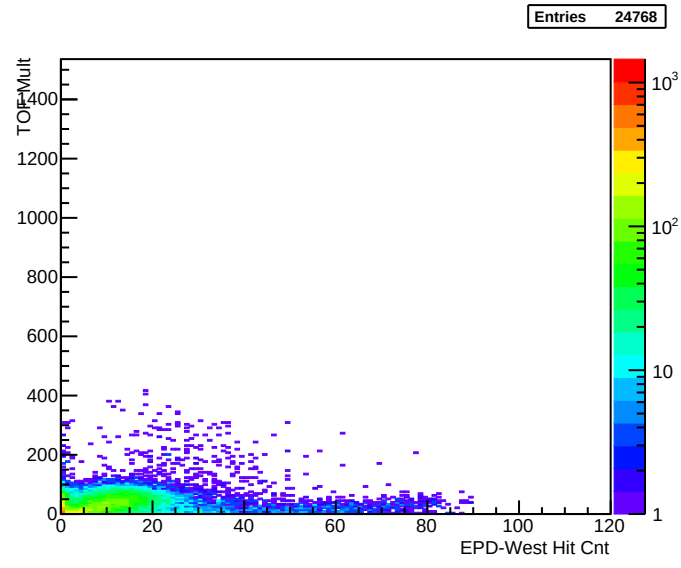

VT201 Input Ch4: VPD Adc Sum Th
Entries 49536
3193
8926
21575
15842
East
West

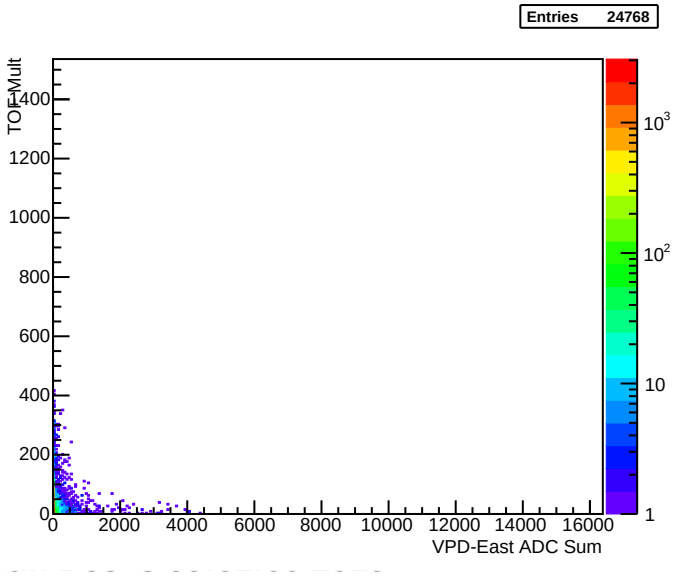

VT201 Input Ch4: VPD Adc Sum Th(Real-Simu)
Entries 0
East
West

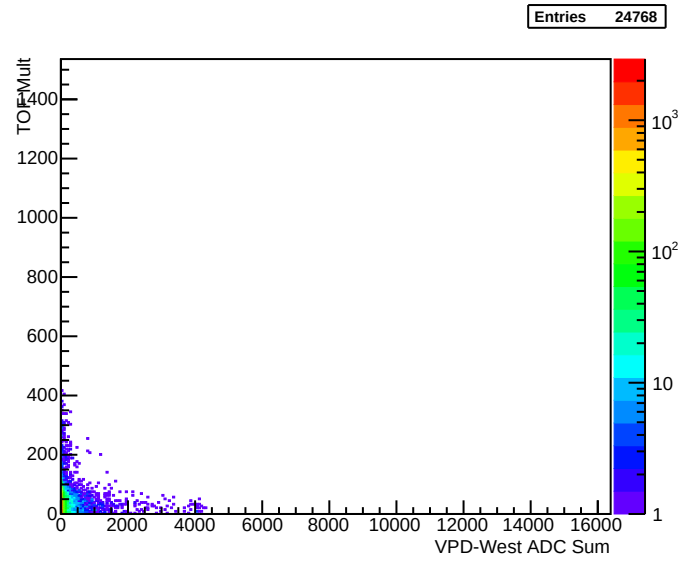

Entries 24768
TOF-Mult
BBC-S-East ADC Sum

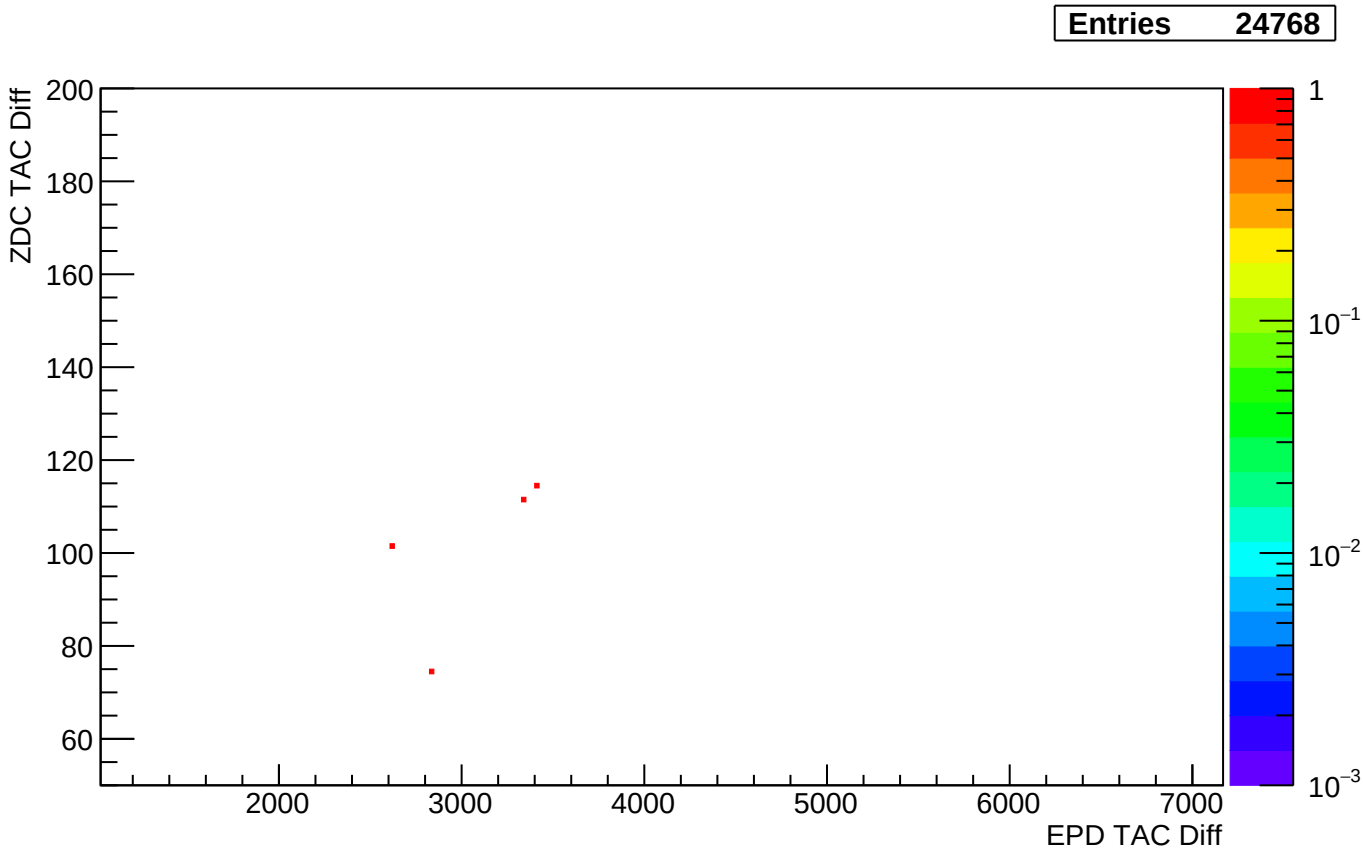

Entries 24768
TOF-Mult
BBC-S-West ADC Sum

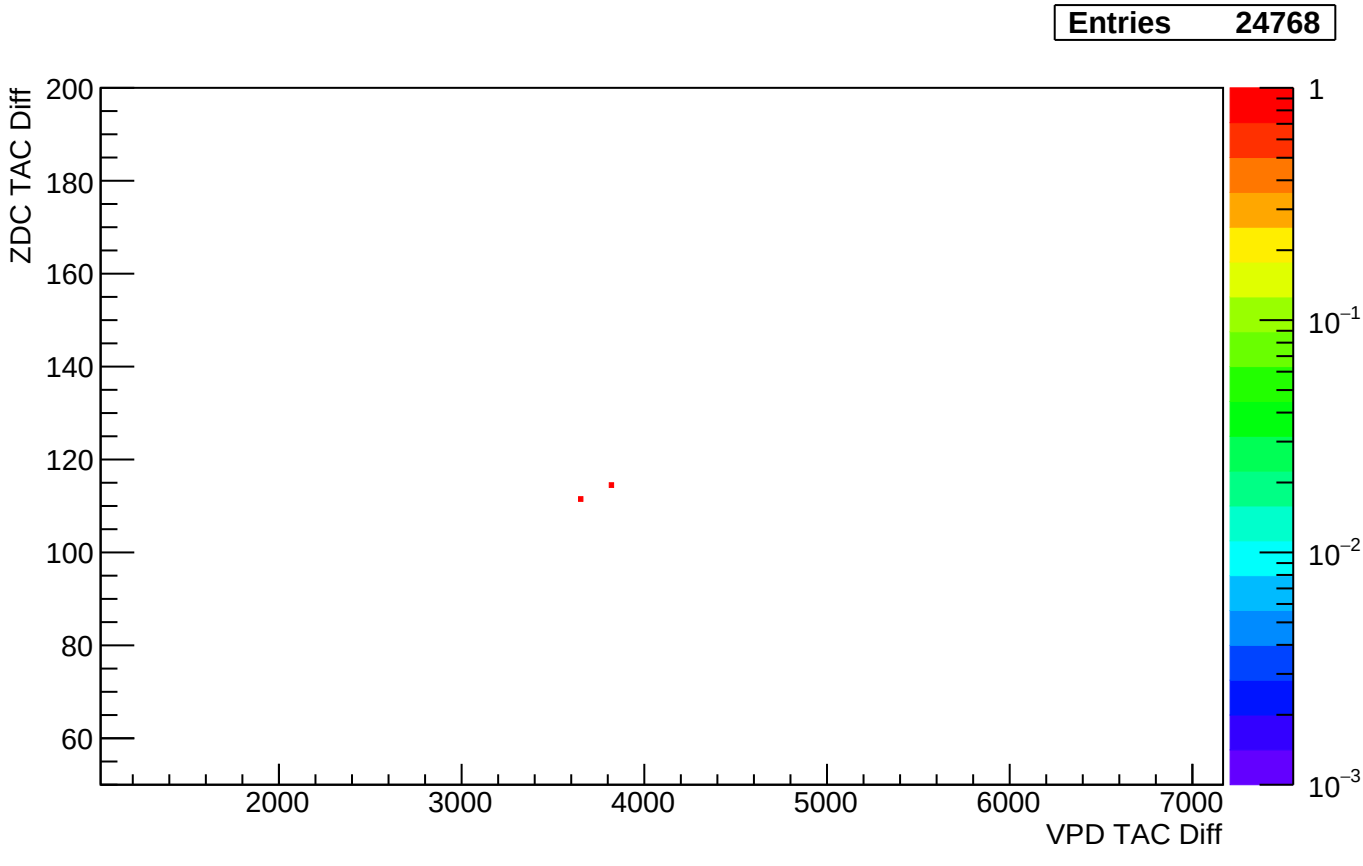

Entries 24768
TOF-Mult
EPD-East Hit Cnt

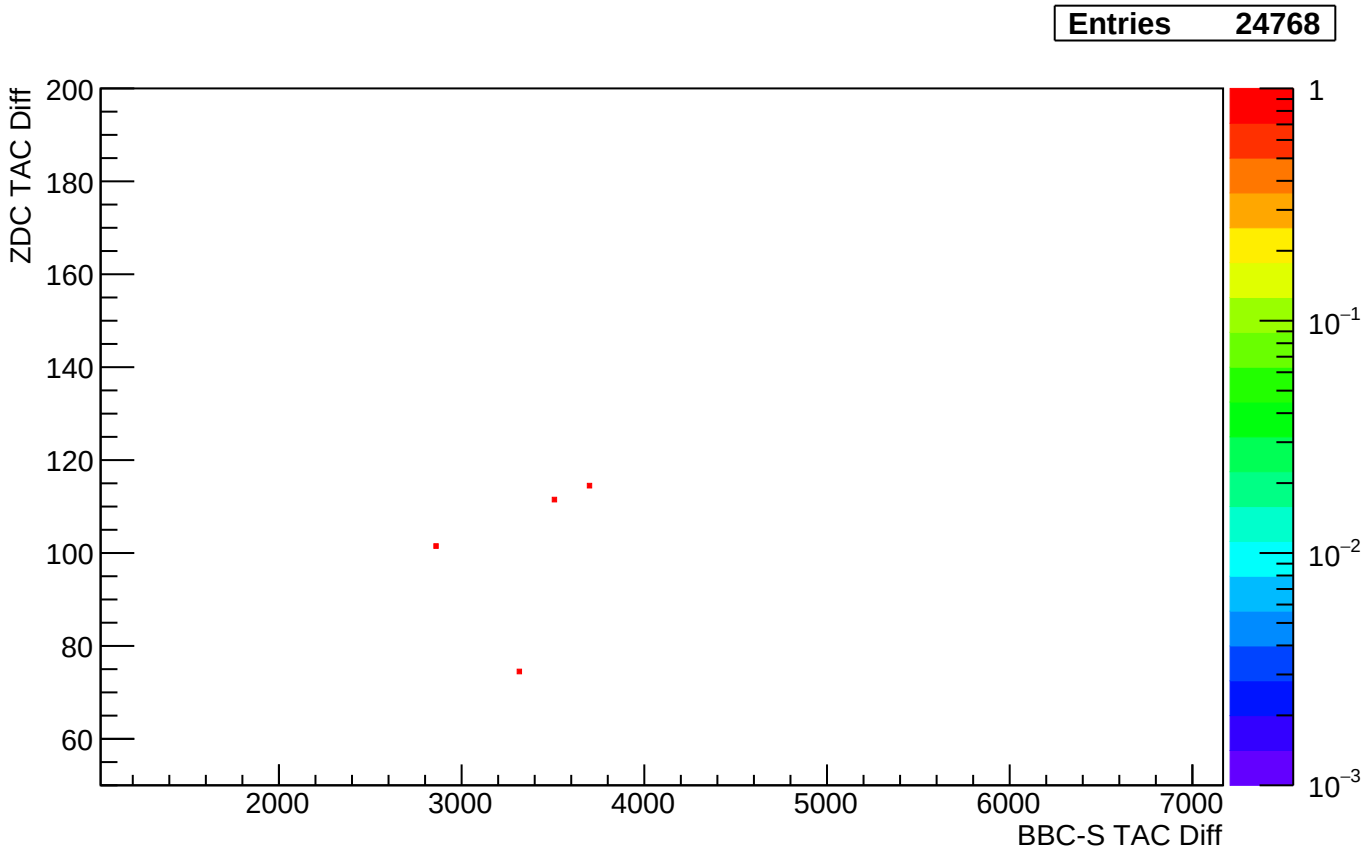

Entries 24768
TOF-Mult
EPD-West Hit Cnt

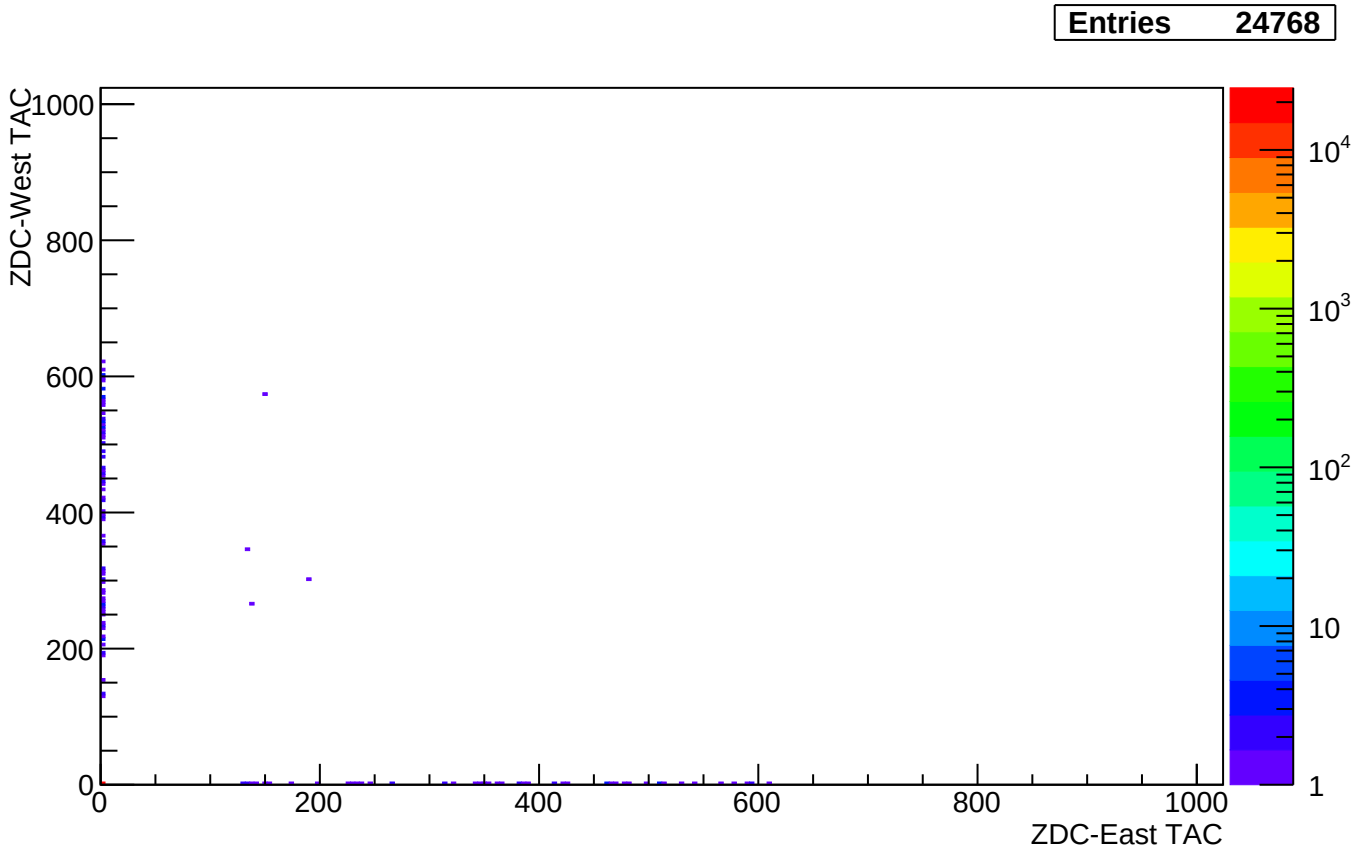

Entries 24768
TOF-Mult
VPD-East ADC Sum

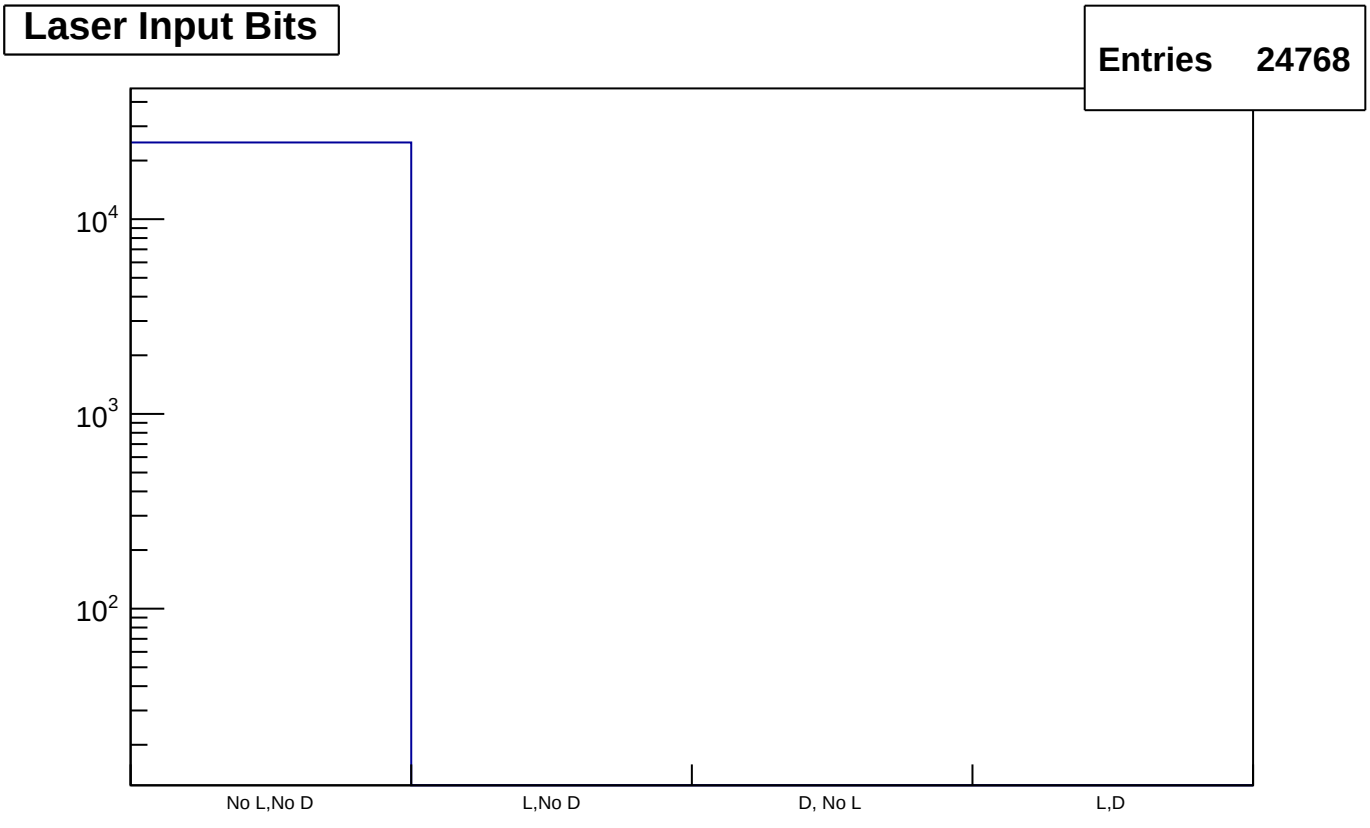

Entries 24768
TOF-Mult
VPD-West ADC Sum

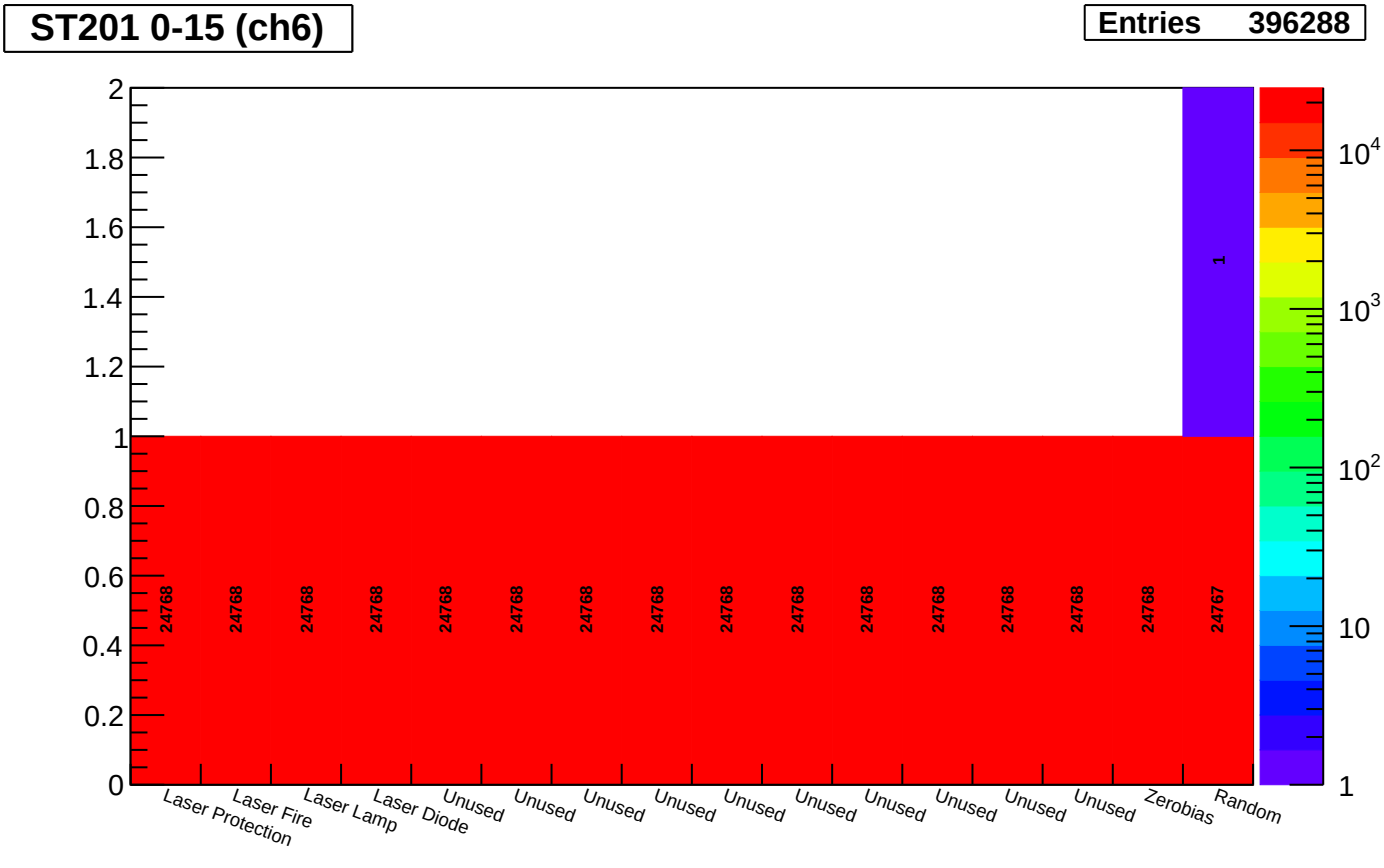

Entries 24768
ZDC TAC Diff
200
180
160
140
120
100
80
60
2000
3000
4000
5000
6000
7000
EPD TAC Diff
1
10^{-1}
10^{-2}
10^{-3}

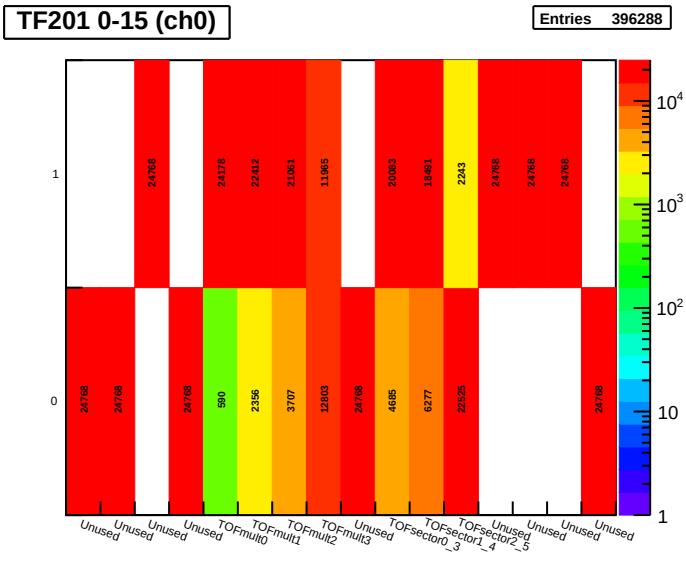

Entries 24768
ZDC TAC Diff
200
180
160
140
120
100
80
60
2000
3000
4000
5000
6000
7000
VPD TAC Diff
1
10^{-1}
10^{-2}
10^{-3}

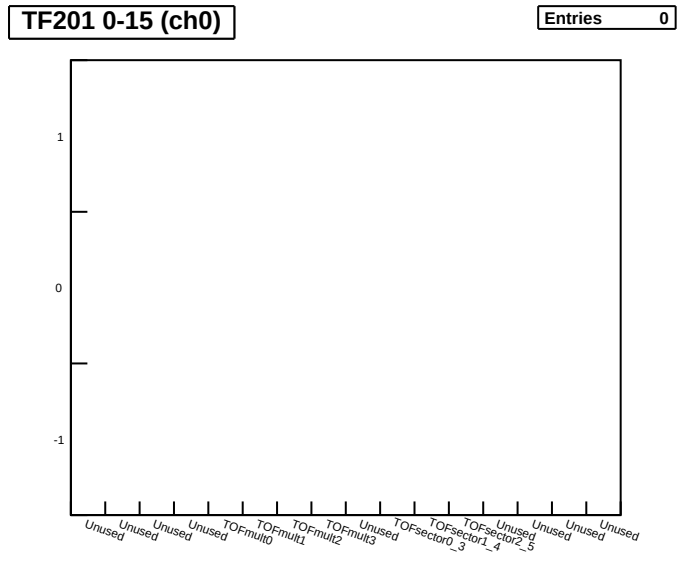

Entries 24768
ZDC TAC Diff
BBC-S TAC Diff
200
180
160
140
120
100
80
60
2000
3000
4000
5000
6000
7000
1
10^{-1}
10^{-2}
10^{-3}

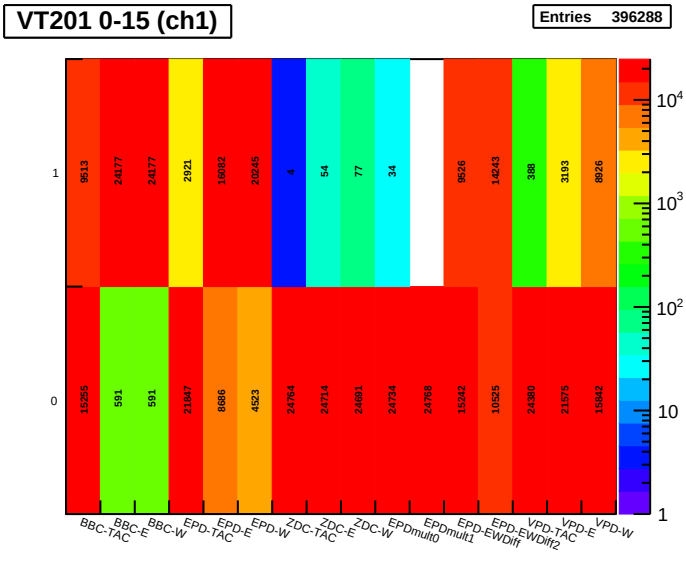

Entries 24768
ZDC-West TAC
ZDC-East TAC
1000
800
600
400
200
0
0
200
400
600
800
1000
10^4
10^3
10^2
10
1

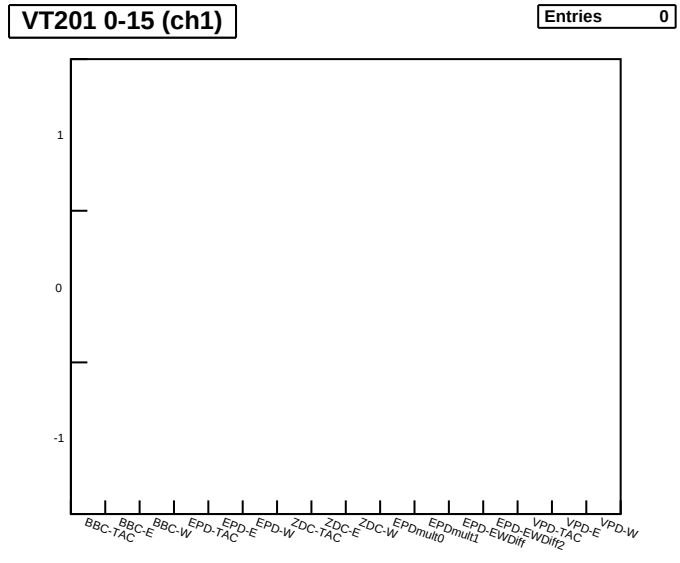

Laser Input Bits
Entries 24768
10^4
10^3
10^2
No L,No D
L,No D
D, No L
L,D

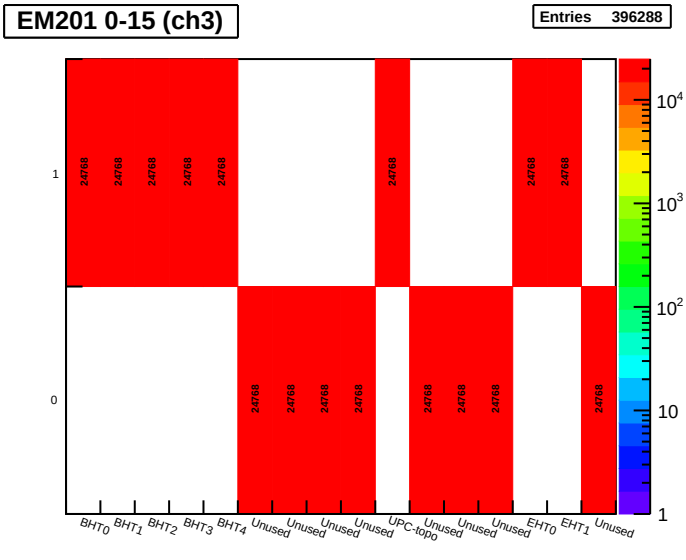

ST201 0-15 (ch6)
Entries 396288
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
24768
24767
1
10^4
10^3
10^2
10
1
Laser Protection
Laser Fire
Laser Lamp
Laser Diode
Unused
Zerobias
Random

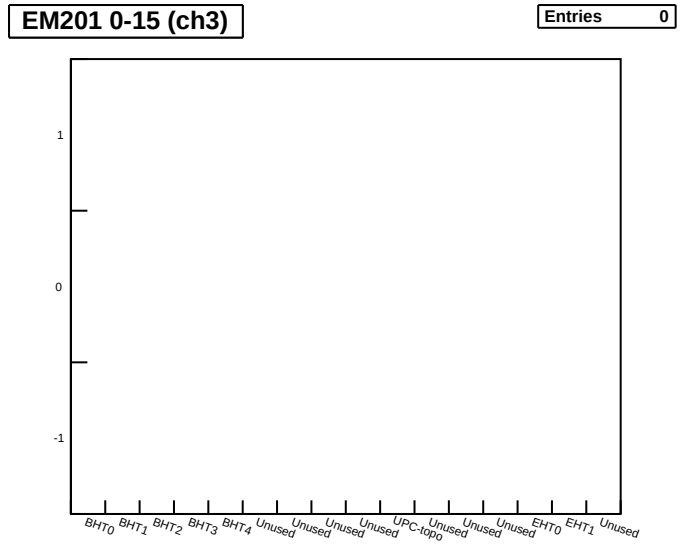

TF201 0-15 (ch0)
Entries 396288
1
0
24768 24178 22412 21061 11965 20083 18491 2243 24768 24768 24768
24768 24768 24768 590 2356 3707 12803 24768 4685 6277 22525 24768
Unused Unused Unused Unused TOFmult0 TOFmult1 TOFmult2 TOFmult3 Unused TOFsector0_3 TOFsector1_4 TOFsector2_5 Unused Unused Unused Unused
10^4 10^3 10^2 10 1


TF201 0-15 (ch0)
Entries 0
1
0
-1
Unused Unused Unused Unused TOFmult0 TOFmult1 TOFmult2 TOFmult3 Unused TOFsector0_3 TOFsector1_4 TOFsector2_5 Unused Unused Unused Unused


VT201 0-15 (ch1)
Entries 396288
1
0
9513 24177 24177 2921 16082 20245 4 54 77 34 9526 14243 388 3193 8926
15255 591 591 21847 8686 4523 24764 24714 24691 24734 24768 15242 10525 24380 21575 15842
BBC-TAC BBC-E BBC-W EPD-TAC EPD-E EPD-W ZDC-TAC ZDC-E ZDC-W EPDmult0 EPDmult1 EPD-EWDiff EPD-EWDiff2 VPD-TAC VPD-E VPD-W
10^4 10^3 10^2 10 1


VT201 0-15 (ch1)
Entries 0
1
0
-1
BBC-TAC BBC-E BBC-W EPD-TAC EPD-E EPD-W ZDC-TAC ZDC-E ZDC-W EPDmult0 EPDmult1 EPD-EWDiff EPD-EWDiff2 VPD-TAC VPD-E VPD-W


EM201 0-15 (ch3)
Entries 396288
1
0
24768 24768 24768 24768 24768 24768 24768 24768 24768
24768 24768 24768 24768 24768 24768 24768 24768
BHT0 BHT1 BHT2 BHT3 BHT4 Unused Unused Unused Unused UPC-topo Unused Unused Unused EHT0 EHT1 Unused
10^4 10^3 10^2 10 1


EM201 0-15 (ch3)
Entries 0
1
0
-1
BHT0 BHT1 BHT2 BHT3 BHT4 Unused Unused Unused Unused UPC-topo Unused Unused Unused EHT0 EHT1 Unused