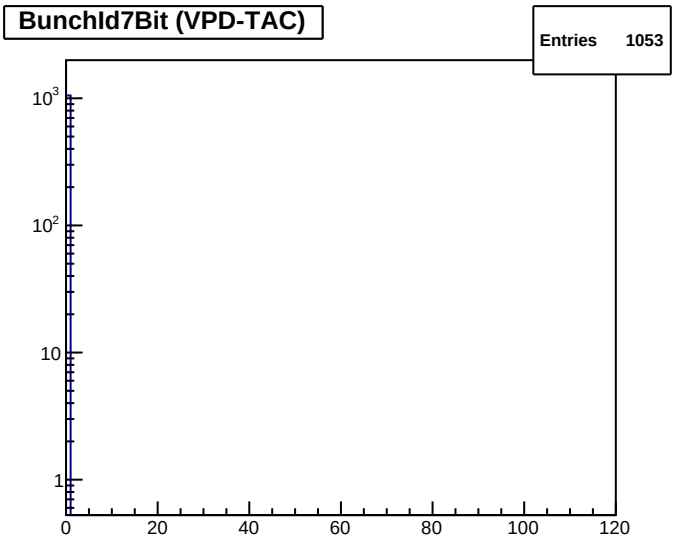
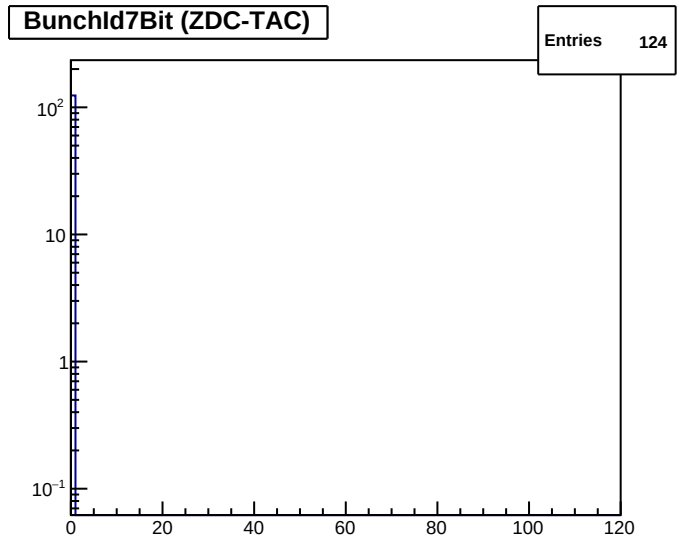
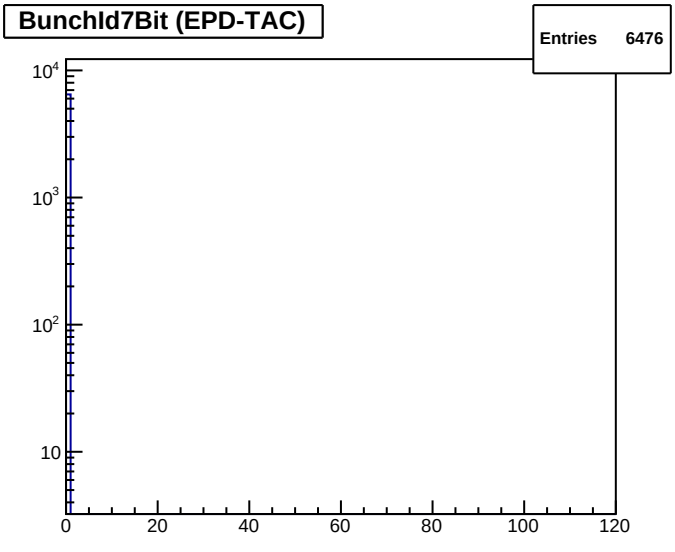
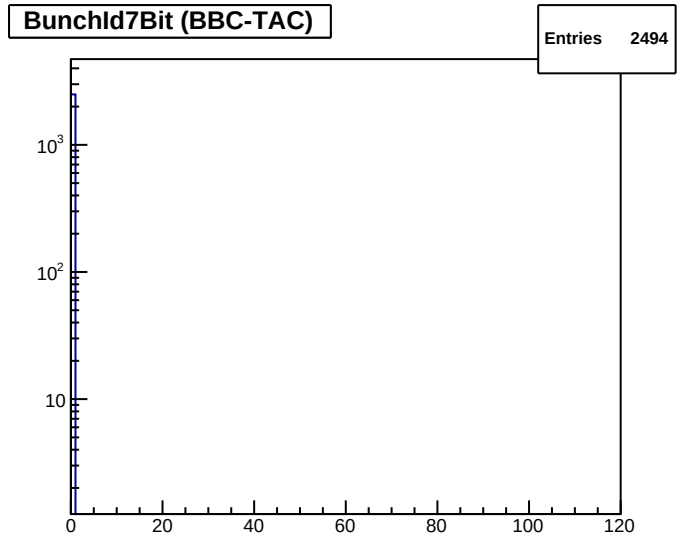
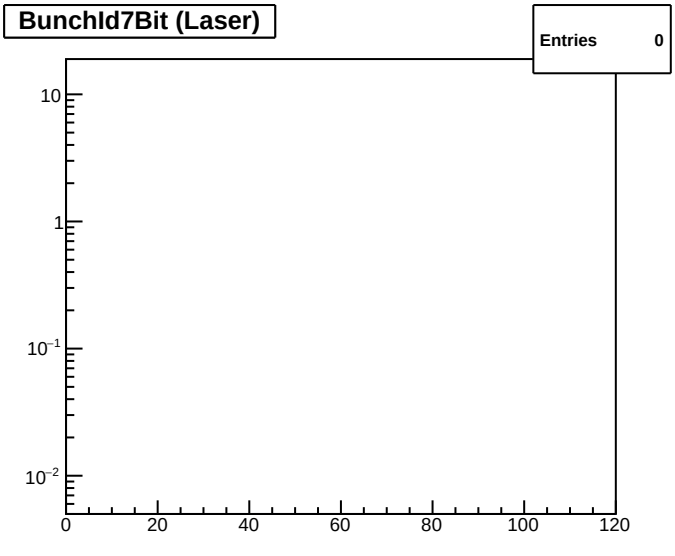
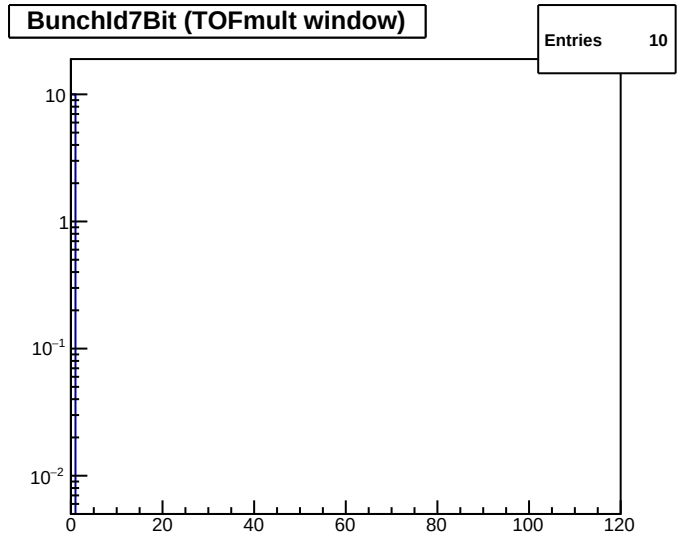

BunchId7Bit (VPD-TAC)
Entries 1053
10^3
10^2
10
1
0 20 40 60 80 100 120

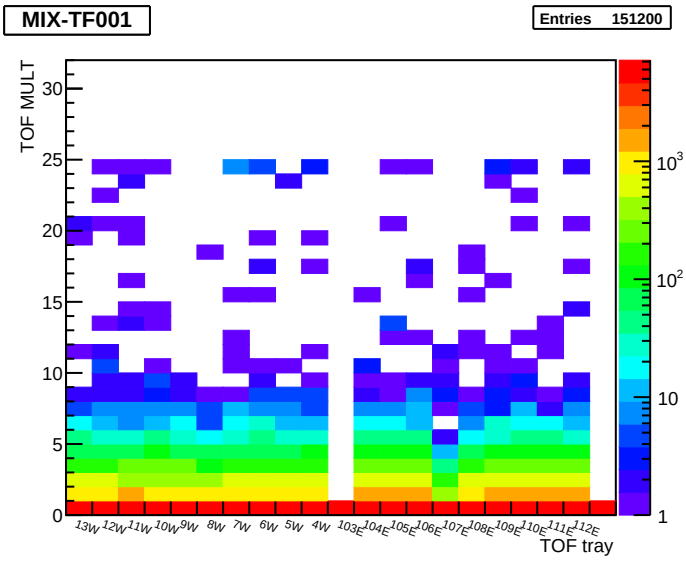

BunchId7Bit (ZDC-TAC)
Entries 124
10^2
10
1
10^{-1}
0 20 40 60 80 100 120

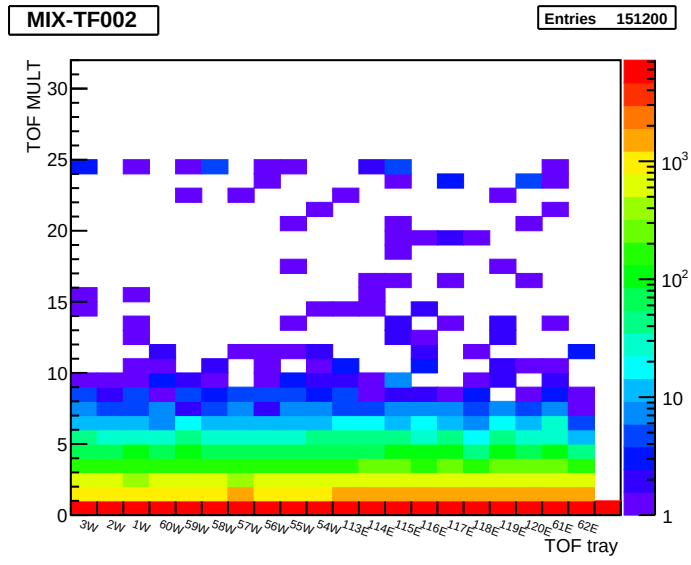

BunchId7Bit (EPD-TAC)
Entries 6476
10^4
10^3
10^2
10
0 20 40 60 80 100 120

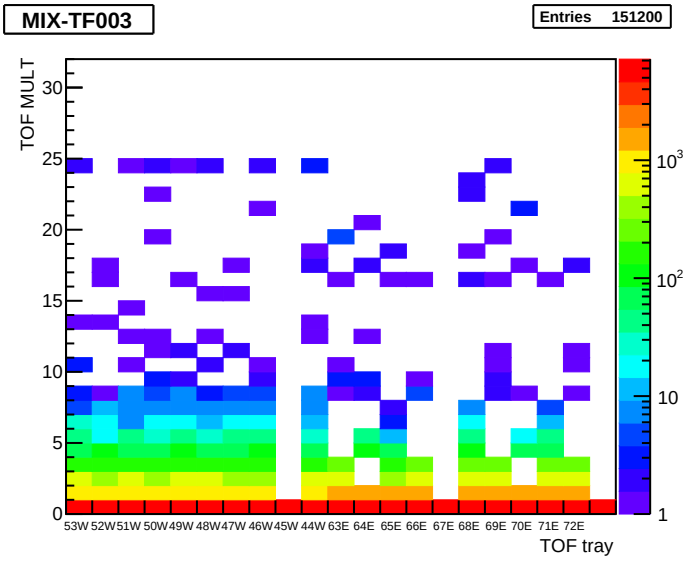

BunchId7Bit (BBC-TAC)
Entries 2494
10^3
10^2
10
0 20 40 60 80 100 120

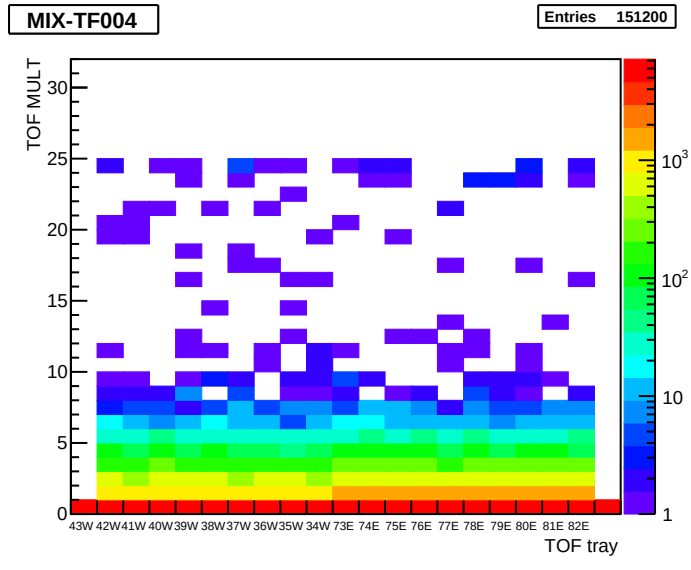

BunchId7Bit (Laser)
Entries 0
10
1
10^{-1}
10^{-2}
0 20 40 60 80 100 120

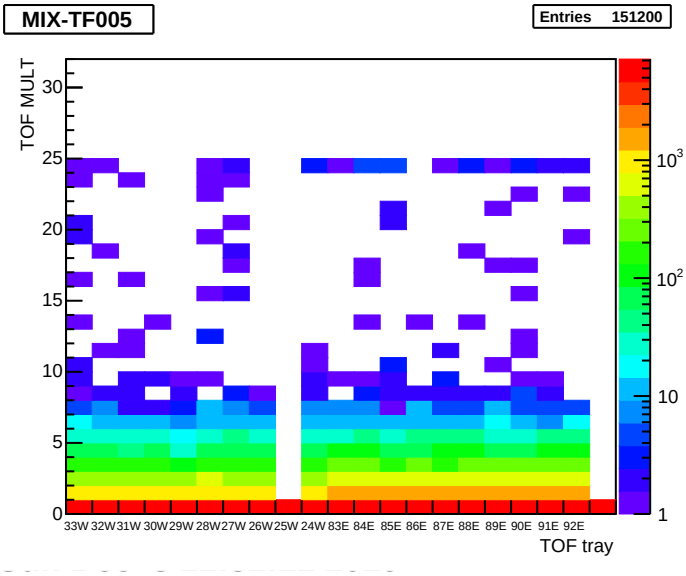

BunchId7Bit (TOFmult window)
Entries 10
10
1
10^{-1}
10^{-2}
0 20 40 60 80 100 120

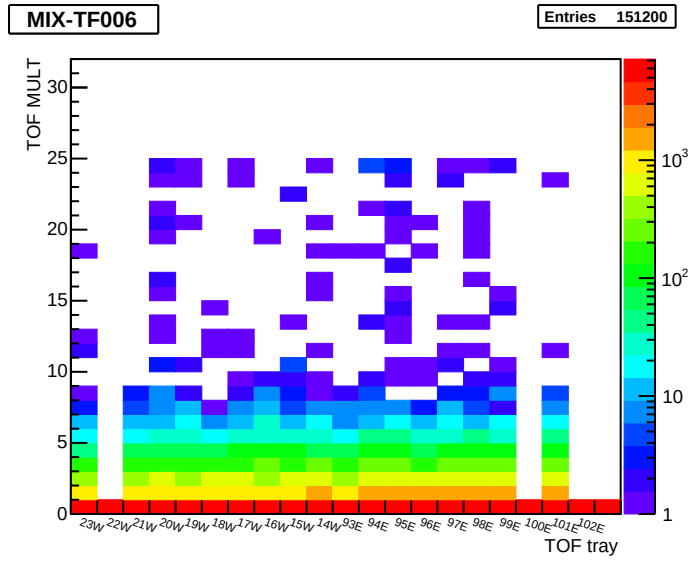

MIX-TF001
Entries 151200
TOF MULT
TOF tray

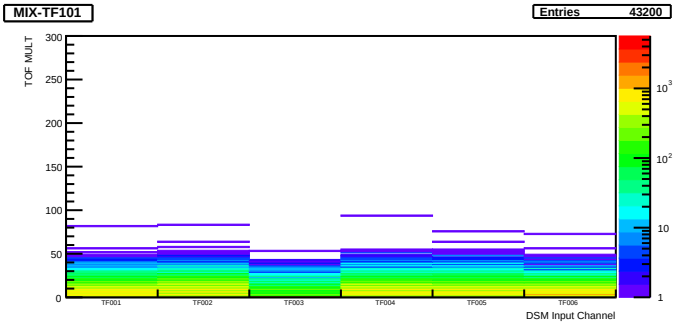

MIX-TF002
Entries 151200
TOF MULT
TOF tray

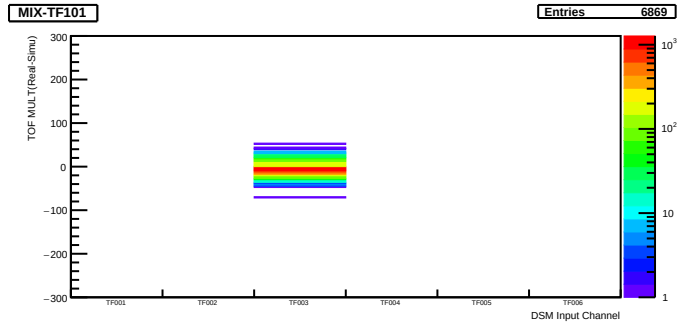

MIX-TF003
Entries 151200
TOF MULT
TOF tray

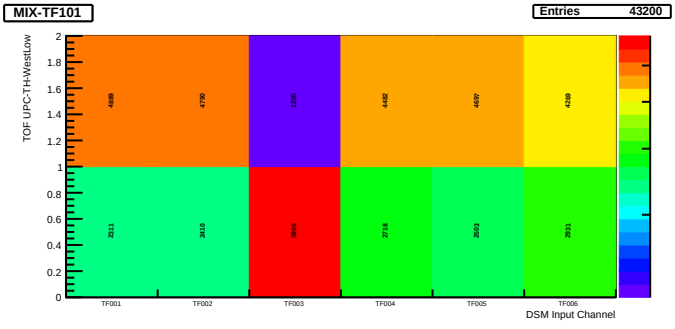

MIX-TF004
Entries 151200
TOF MULT
TOF tray

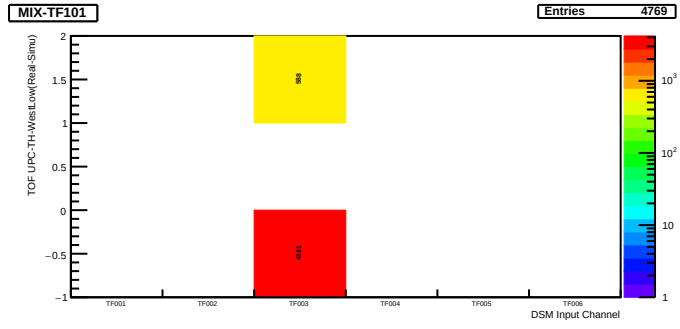

MIX-TF005
Entries 151200
TOF MULT
TOF tray

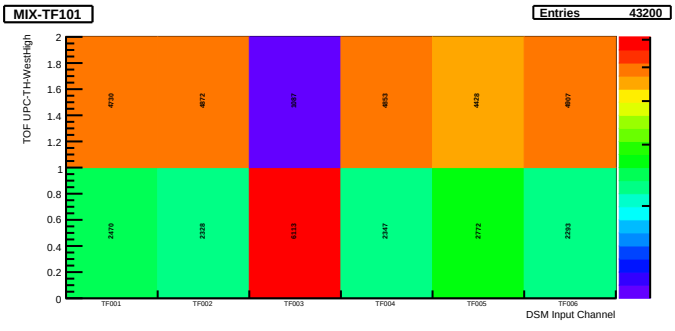

MIX-TF006
Entries 151200
TOF MULT
TOF tray

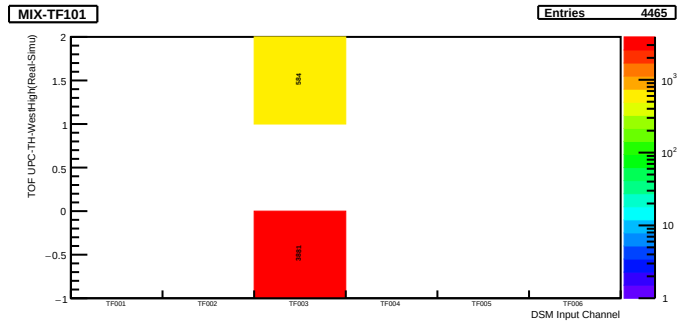

MIX-TF101
Entries 43200
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

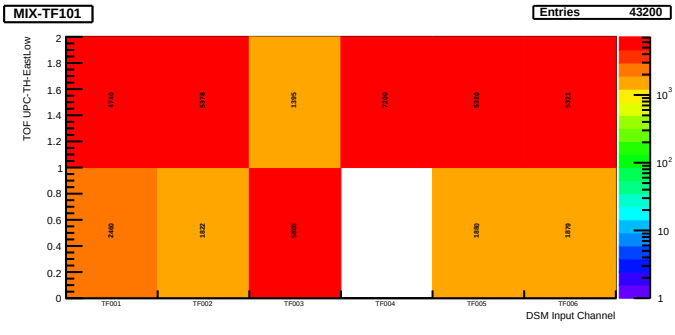

MIX-TF101
Entries 6869
TOF MULT(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

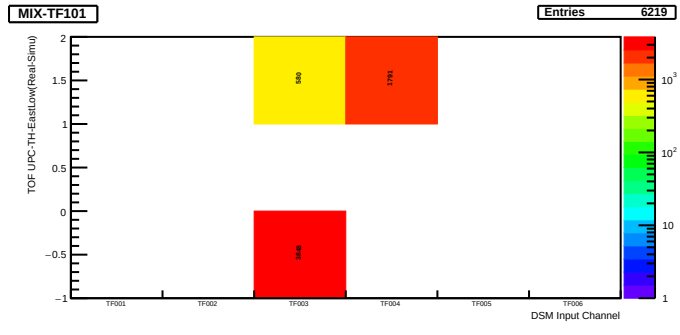

MIX-TF101
Entries 43200
TOF UPC-TH-WestLow
4889
4790
1205
4482
4697
4298
2211
2410
3995
2716
2693
2811
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

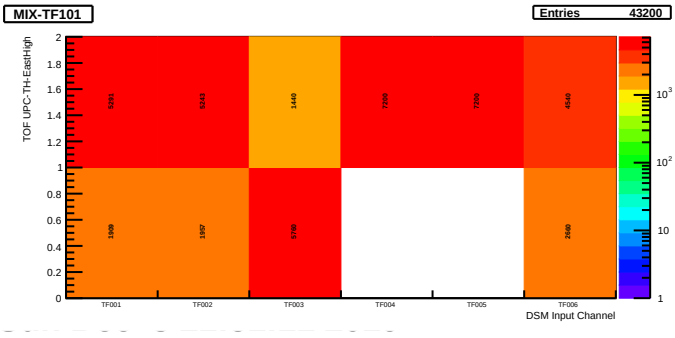

MIX-TF101
Entries 4769
TOF UPC-TH-WestLow(Real-Simu)
988
4181
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

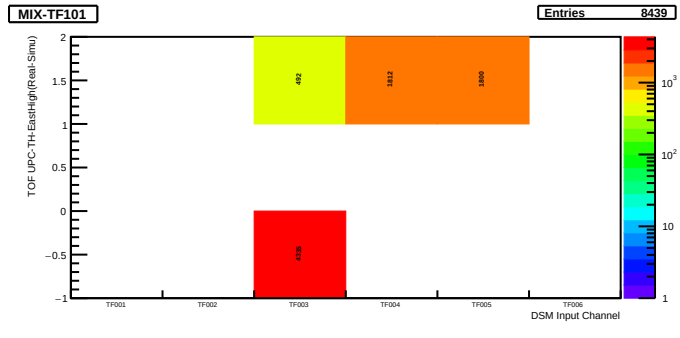

MIX-TF101
Entries 43200
TOF UPC-TH-WestHigh
4720
4872
3097
4853
4428
4907
2470
2328
6113
2347
2772
2293
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

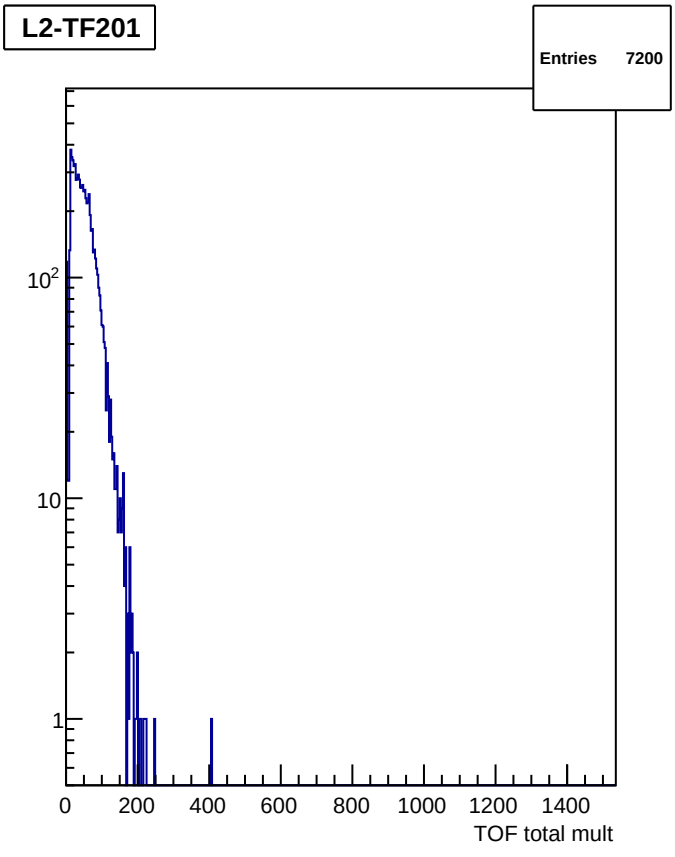

MIX-TF101
Entries 4465
TOF UPC-TH-WestHigh(Real-Simu)
584
3881
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

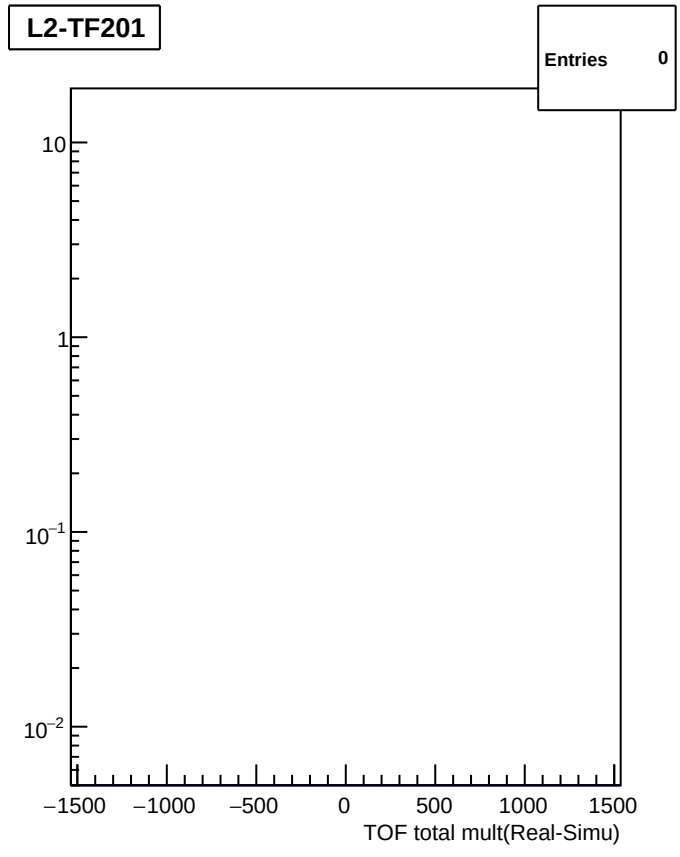

MIX-TF101
Entries 43200
TOF UPC-TH-EastLow
4740
5378
1395
7200
5320
5321
2480
1822
5805
1880
1879
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

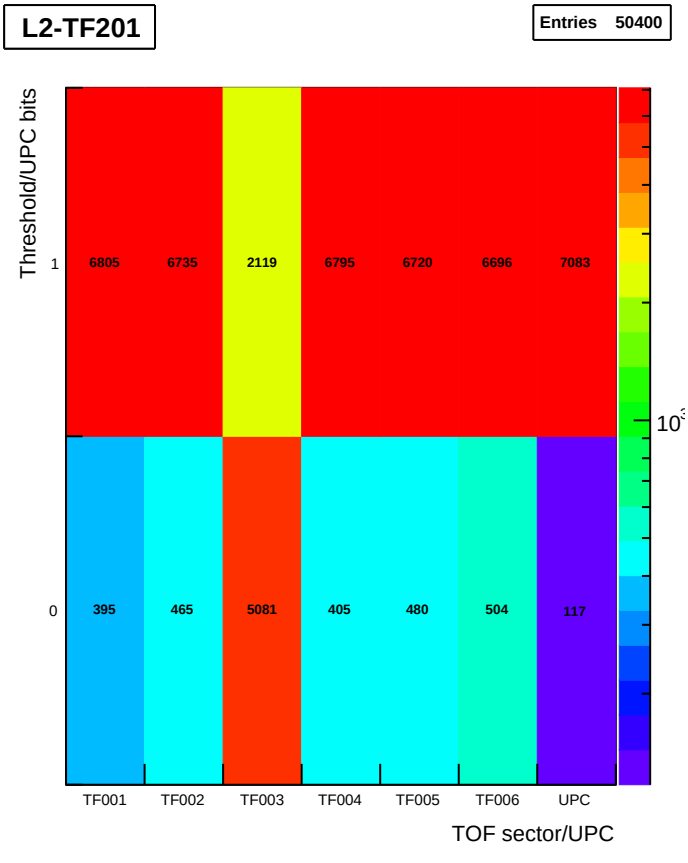

MIX-TF101
Entries 6219
TOF UPC-TH-EastLow(Real-Simu)
580
1791
3848
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

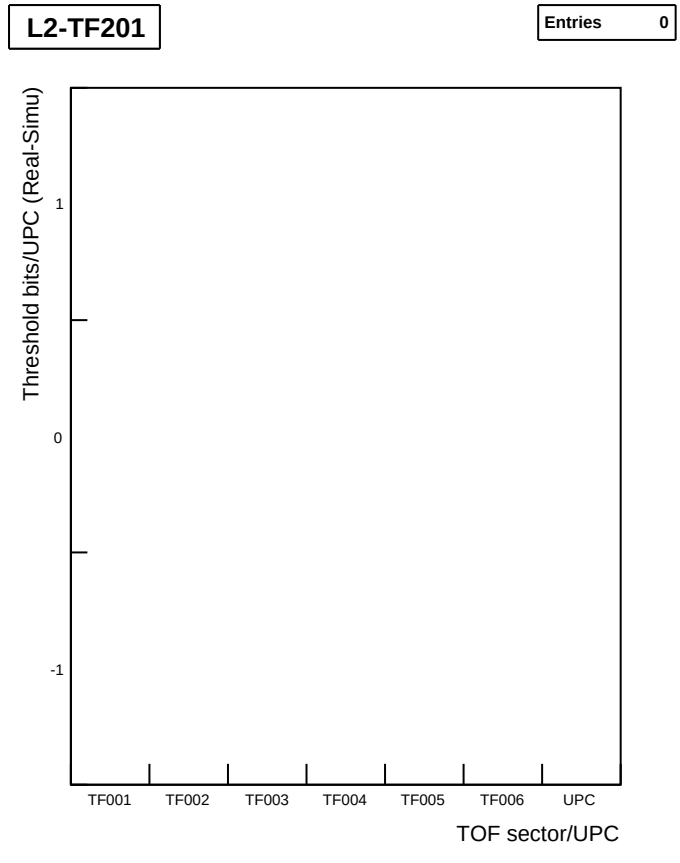

MIX-TF101
Entries 43200
TOF UPC-TH-EastHigh
5391
5243
1449
7200
7200
4540
1909
1897
5770
2640
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

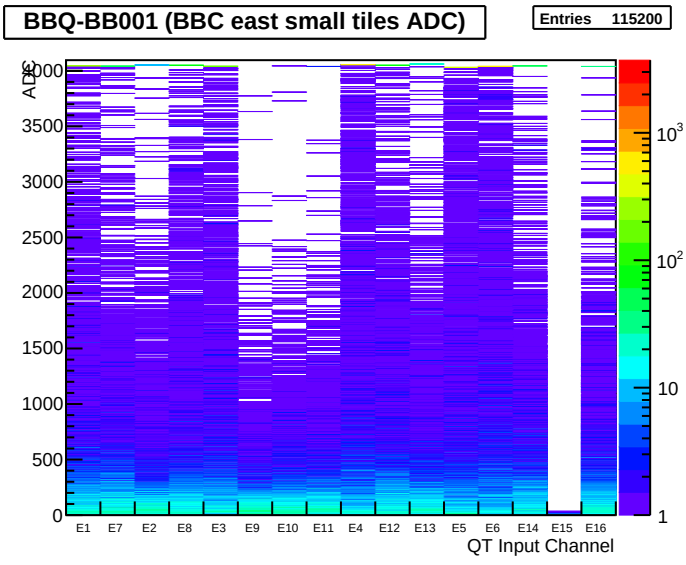

MIX-TF101
Entries 8439
TOF UPC-TH-EastHigh(Real-Simu)
492
1812
1800
4735
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

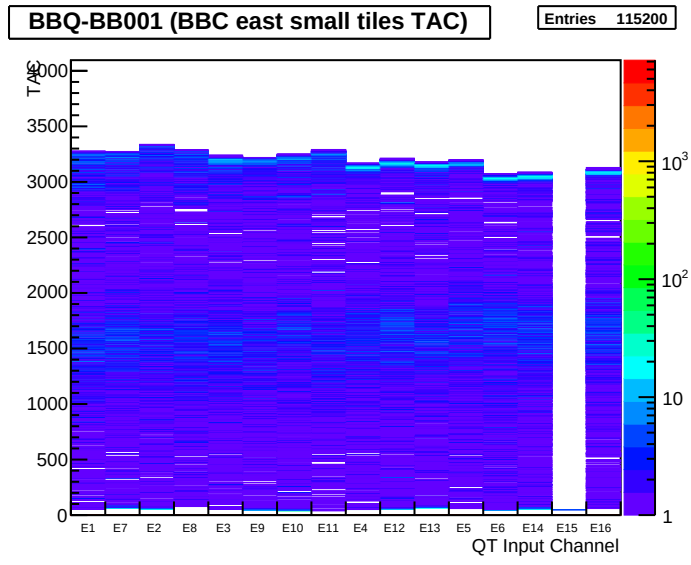

L2-TF201
Entries 7200
10^2
10
1
0
200
400
600
800
1000
1200
1400
TOF total mult

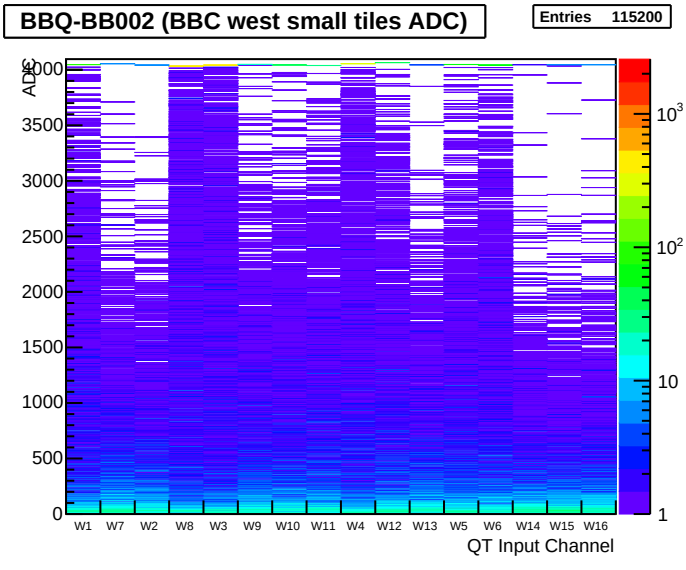

L2-TF201
Entries 0
10
1
10^{-1}
10^{-2}
−1500
−1000
−500
0
500
1000
1500
TOF total mult(Real-Simu)

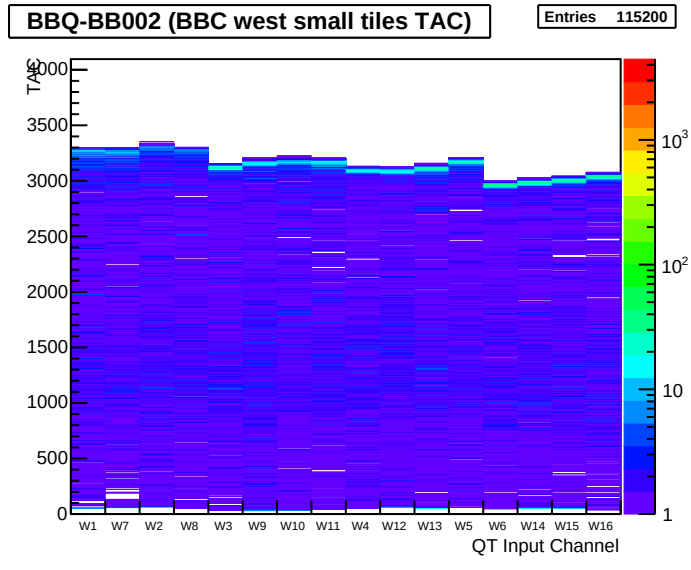

L2-TF201
Entries 50400
Threshold/UPC bits
1
0
6805
6735
2119
6795
6720
6696
7083
395
465
5081
405
480
504
117
TF001
TF002
TF003
TF004
TF005
TF006
UPC
TOF sector/UPC

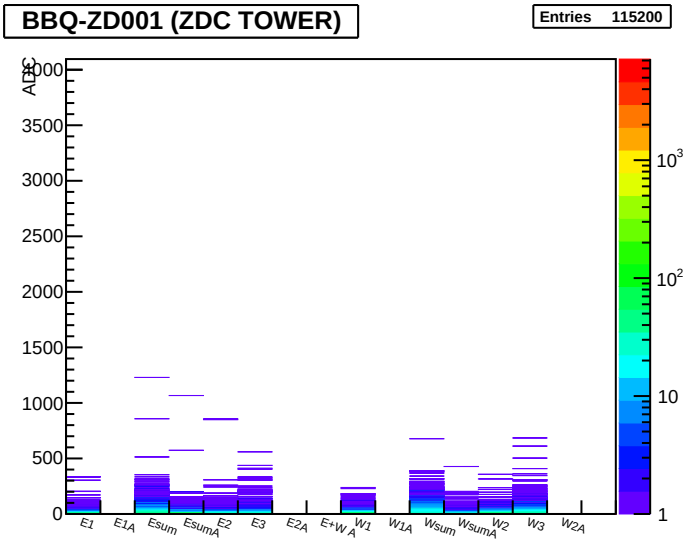

L2-TF201
Entries 0
Threshold bits/UPC (Real-Simu)
1
0
-1
TF001
TF002
TF003
TF004
TF005
TF006
UPC
TOF sector/UPC

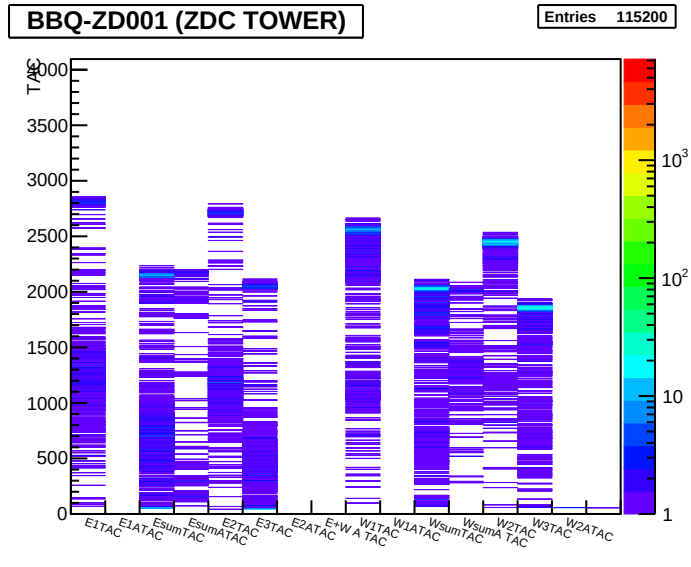

BBQ-BB001 (BBC east small tiles ADC)
Entries 115200
ADC
QT Input Channel
E1 E7 E2 E8 E3 E9 E10 E11 E4 E12 E13 E5 E6 E14 E15 E16

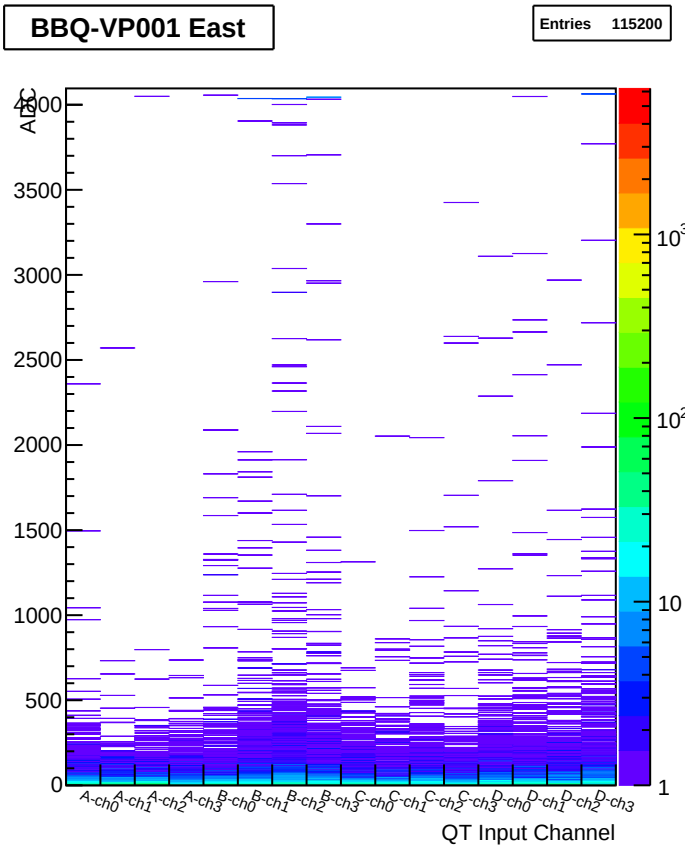

BBQ-BB001 (BBC east small tiles TAC)
Entries 115200
TAC
QT Input Channel
E1 E7 E2 E8 E3 E9 E10 E11 E4 E12 E13 E5 E6 E14 E15 E16

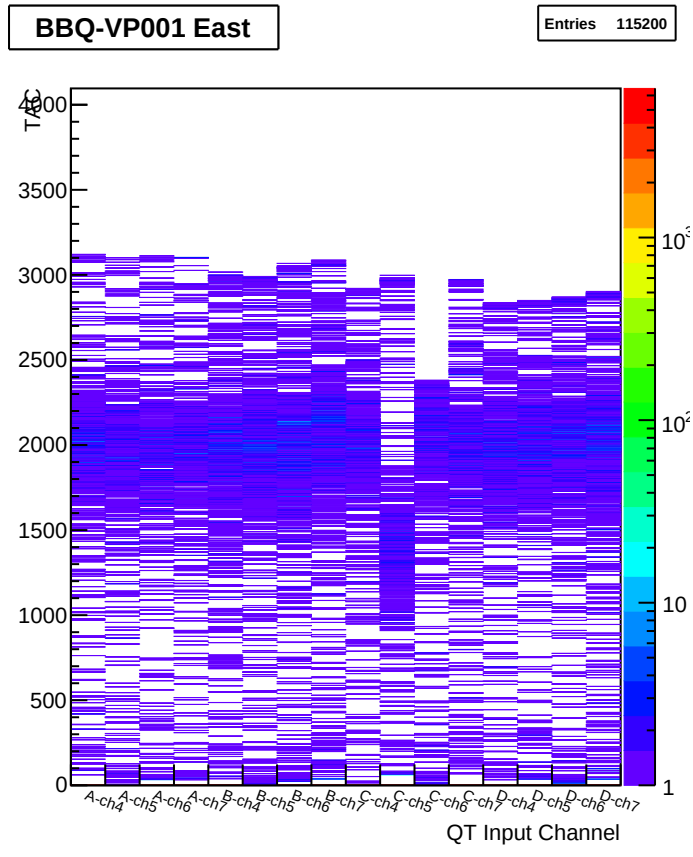

BBQ-BB002 (BBC west small tiles ADC)
Entries 115200
ADC
QT Input Channel
W1 W7 W2 W8 W3 W9 W10 W11 W4 W12 W13 W5 W6 W14 W15 W16

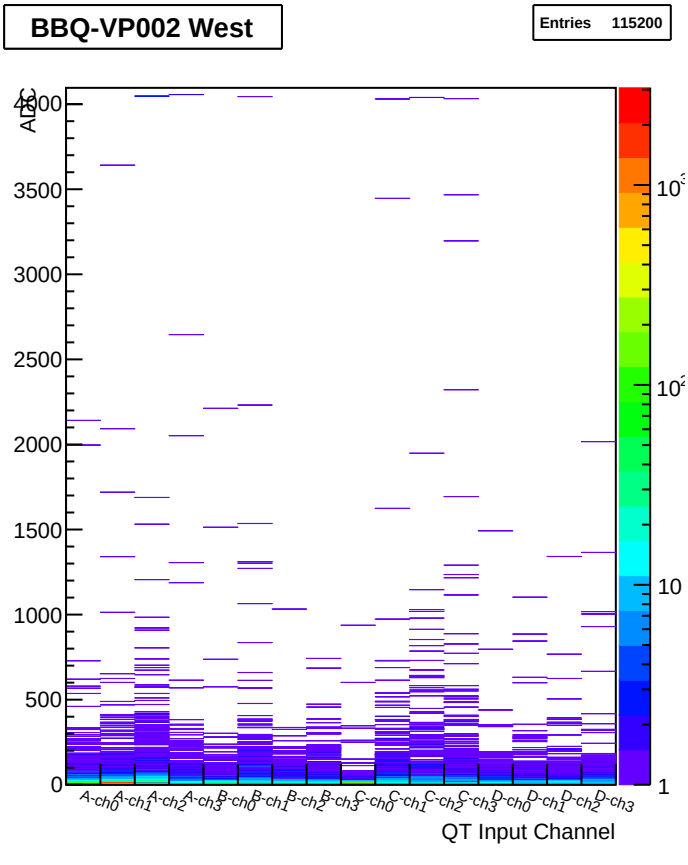

BBQ-BB002 (BBC west small tiles TAC)
Entries 115200
TAC
QT Input Channel
W1 W7 W2 W8 W3 W9 W10 W11 W4 W12 W13 W5 W6 W14 W15 W16

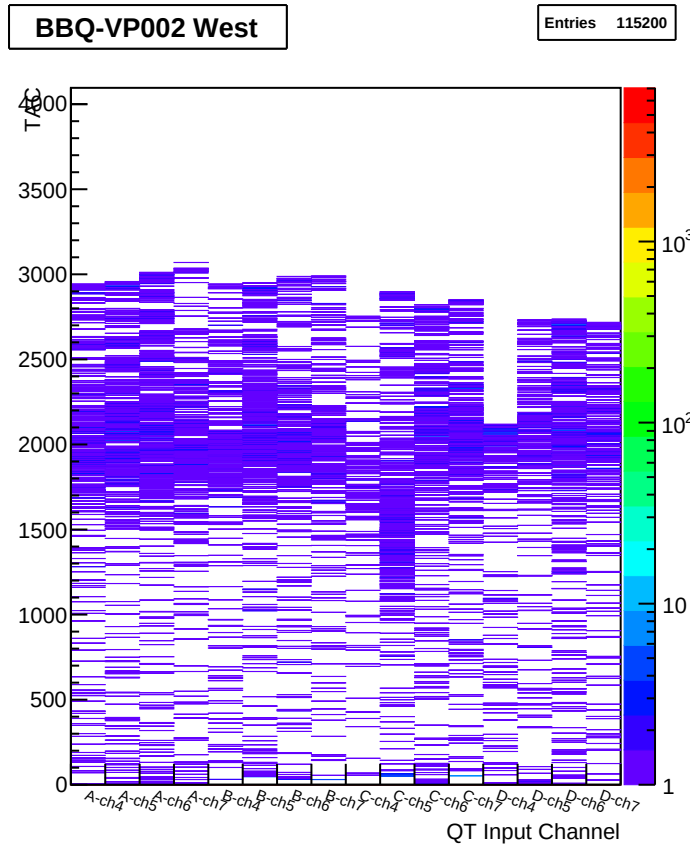

BBQ-ZD001 (ZDC TOWER)
Entries 115200
ADC
E1 E1A Esum EsumA E2 E3 E2A E+W A W1 W1A Wsum WsumA W2 W3 W2A

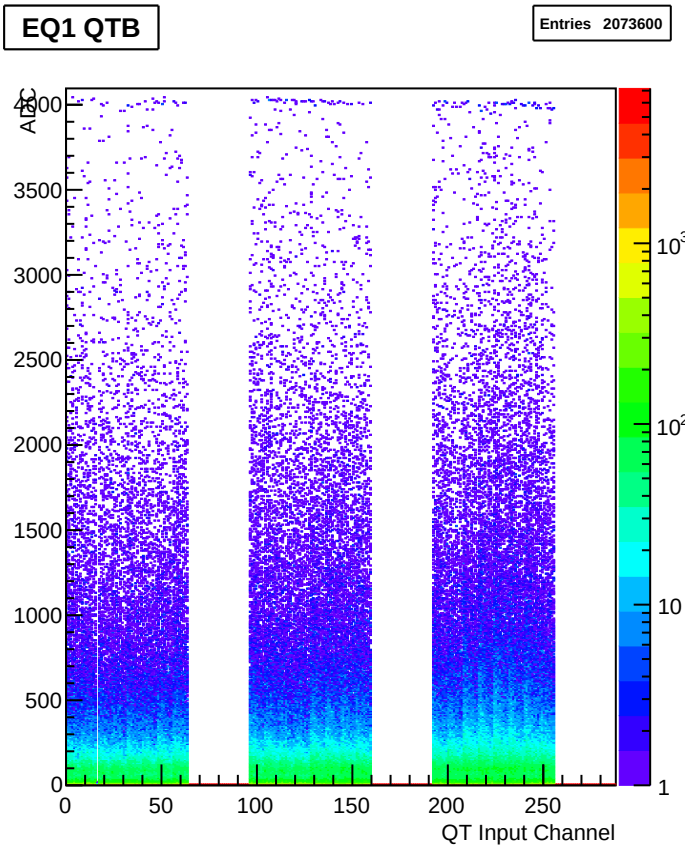

BBQ-ZD001 (ZDC TOWER)
Entries 115200
TAC
E1TAC E1ATAC EsumTAC EsumATAC E2TAC E3TAC E2ATAC E+W A TAC W1TAC W1ATAC WsumTAC WsumA TAC W2TAC W3TAC W2ATAC

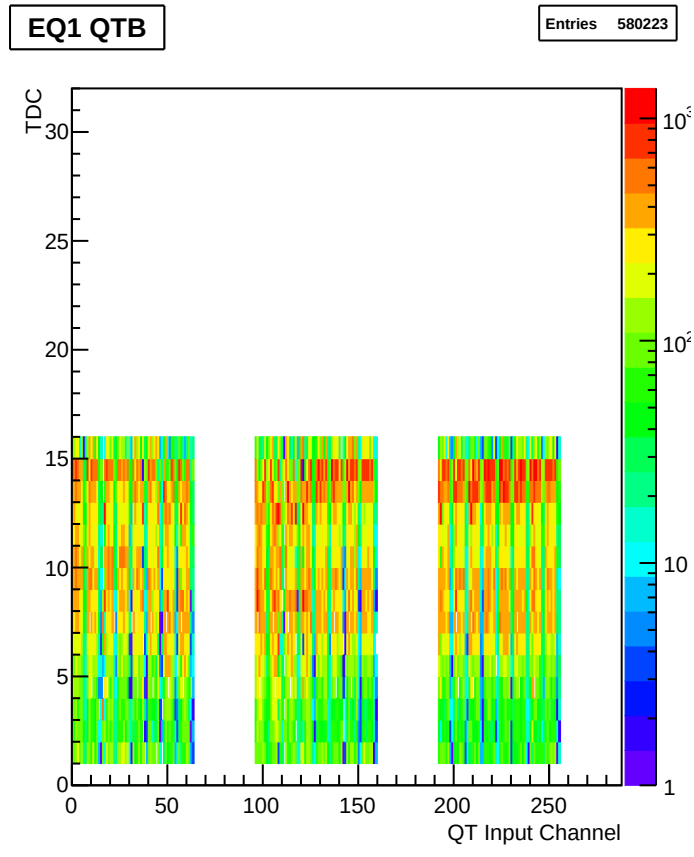

BBQ-VP001 East
Entries 115200
ADC
QT Input Channel

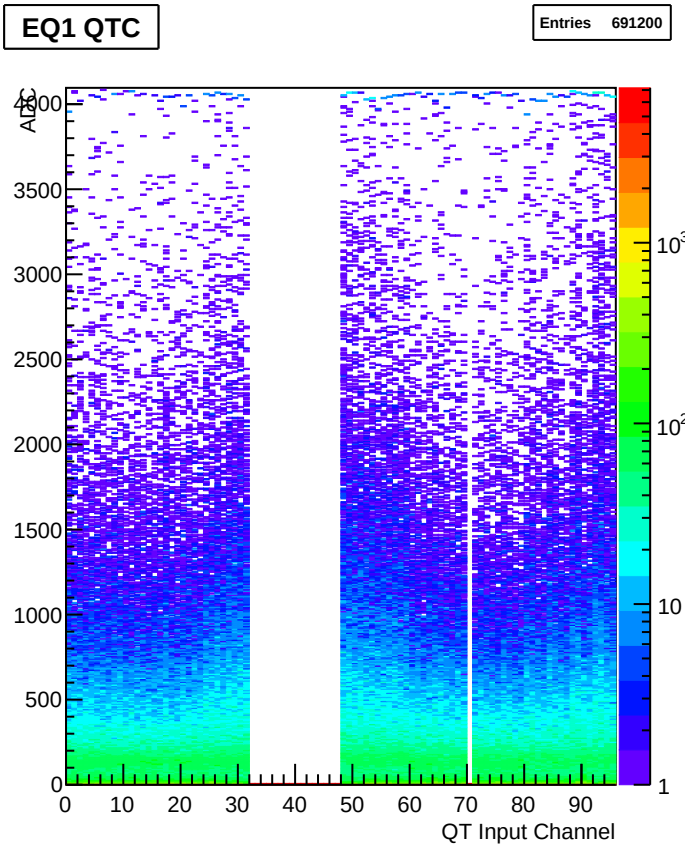

BBQ-VP001 East
Entries 115200
TADC
QT Input Channel

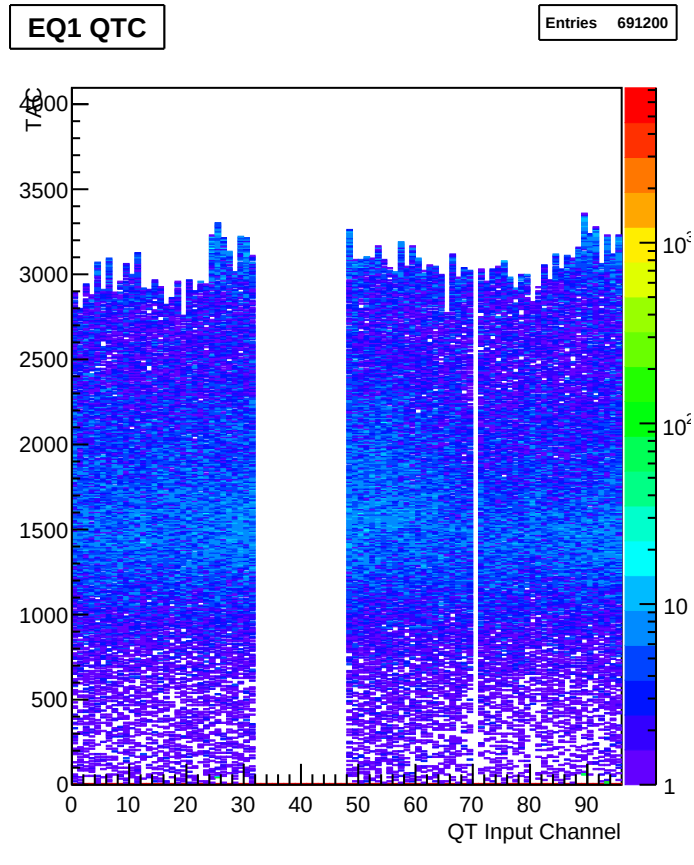

BBQ-VP002 West
Entries 115200
ADC
QT Input Channel

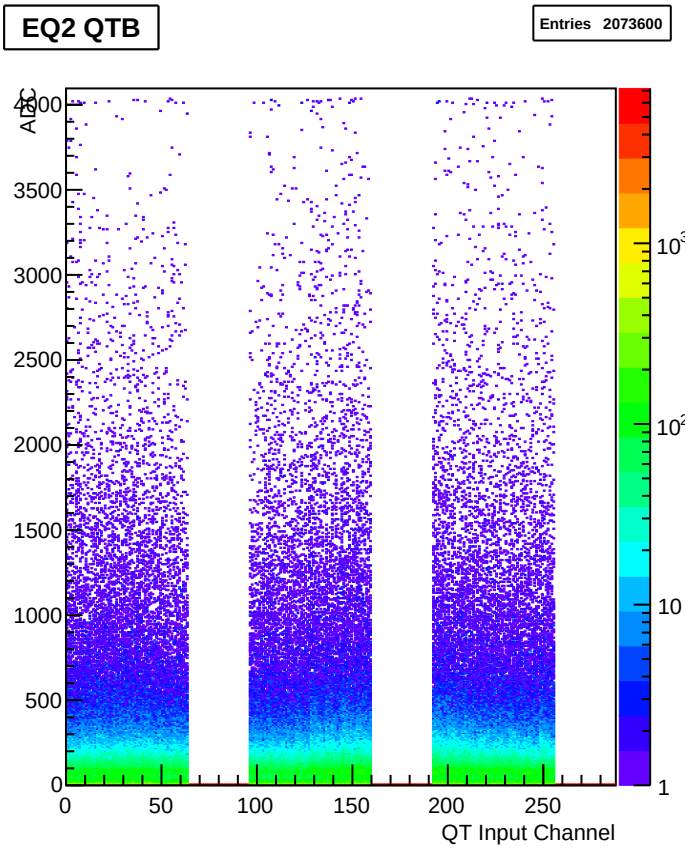

BBQ-VP002 West
Entries 115200
TADC
QT Input Channel

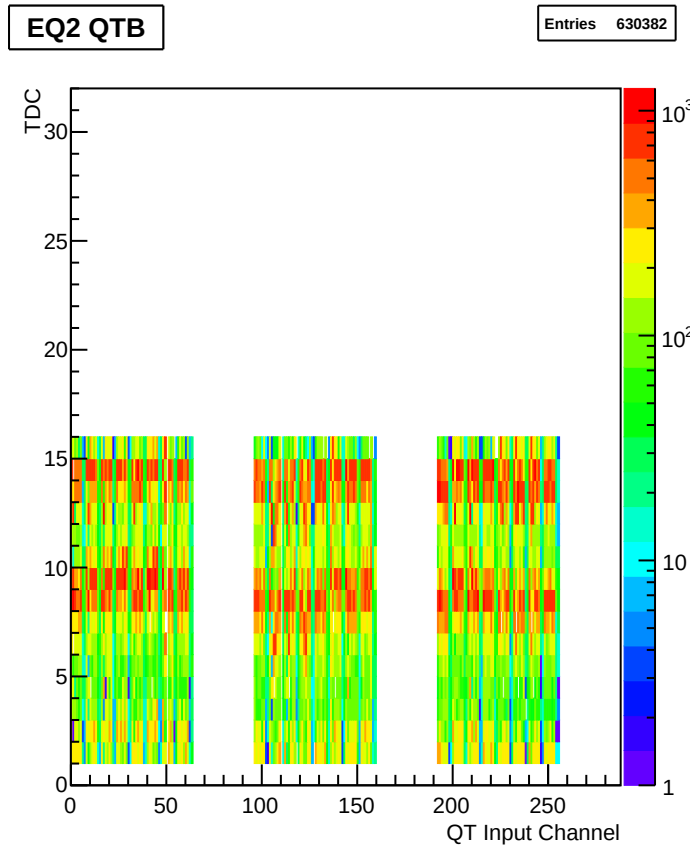

EQ1 QTB
Entries 2073600
ADC
QT Input Channel

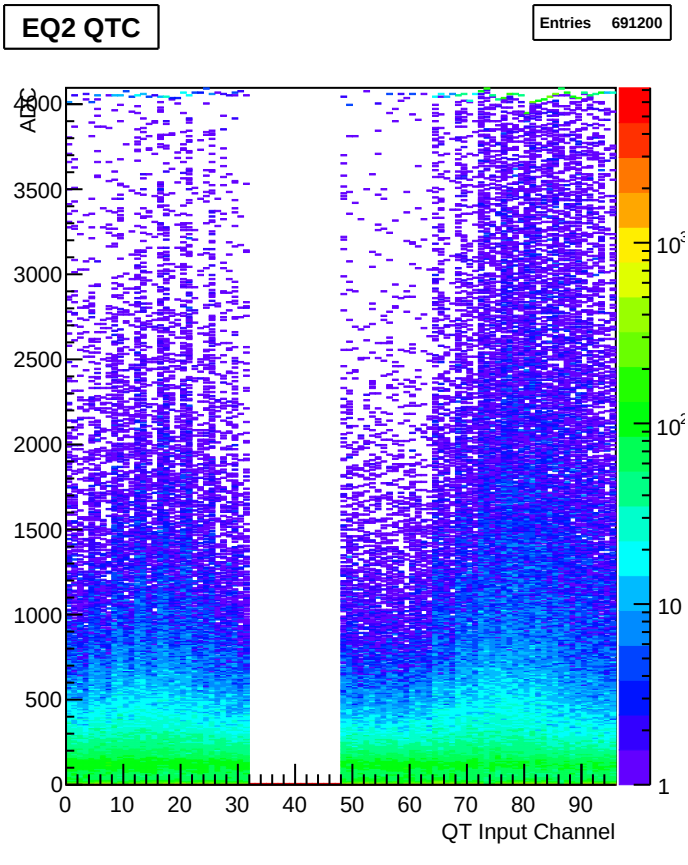

EQ1 QTB
Entries 580223
TDC
QT Input Channel

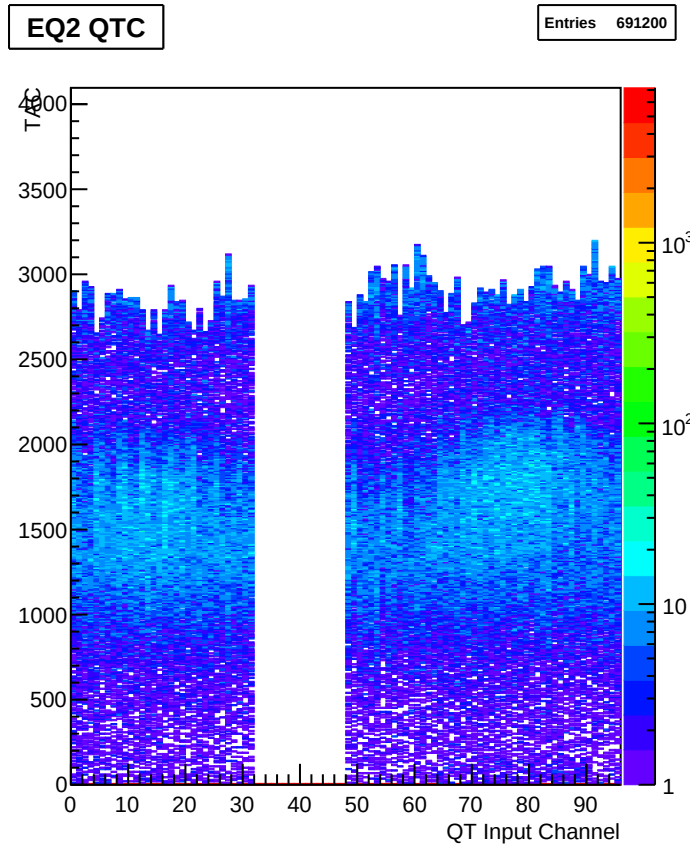

EQ1 QTC
Entries 691200
ADC
QT Input Channel

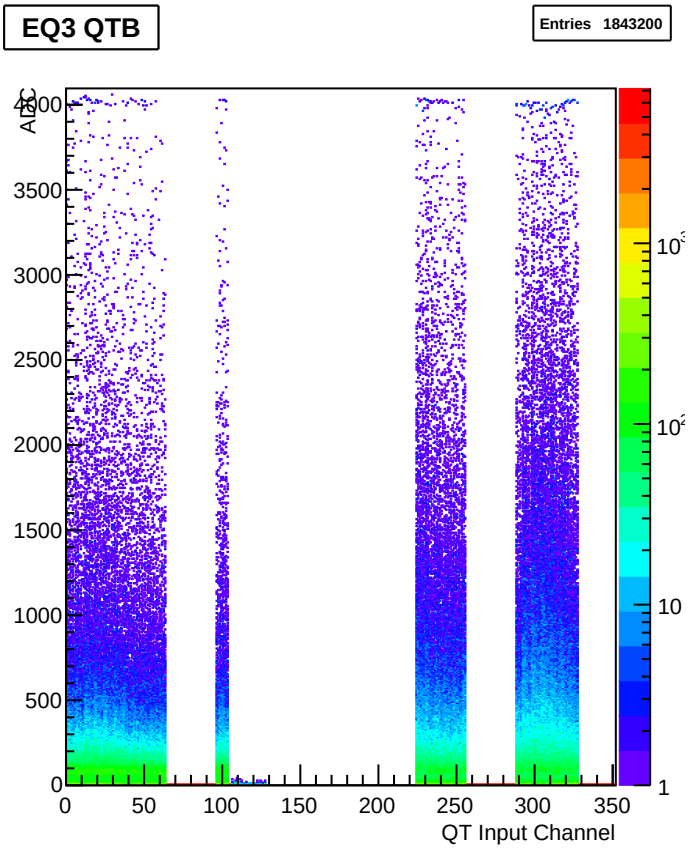

EQ1 QTC
Entries 691200
TADC
QT Input Channel

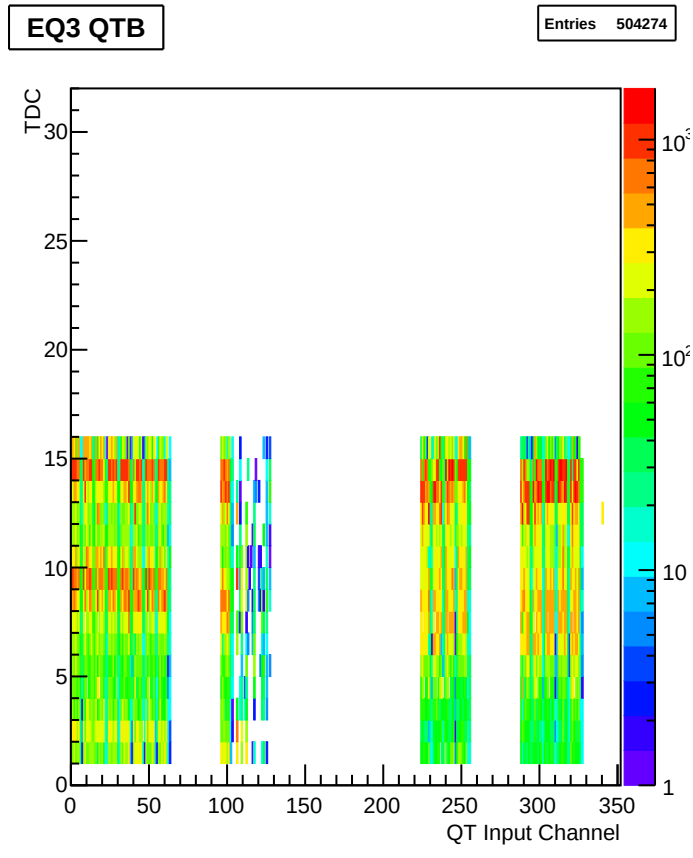

EQ2 QTB
Entries 2073600
ADC
QT Input Channel

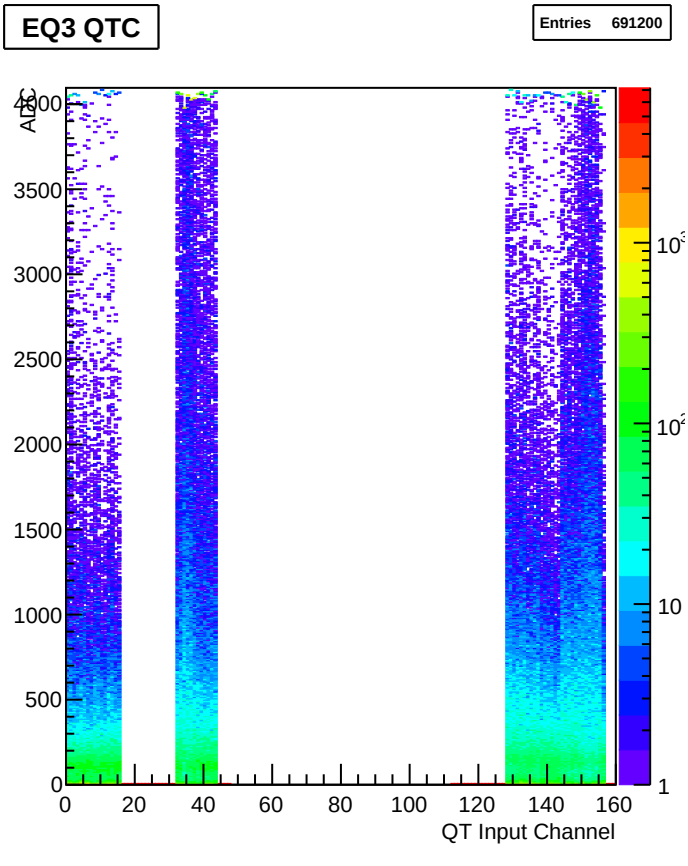

EQ2 QTB
Entries 630382
TDC
QT Input Channel

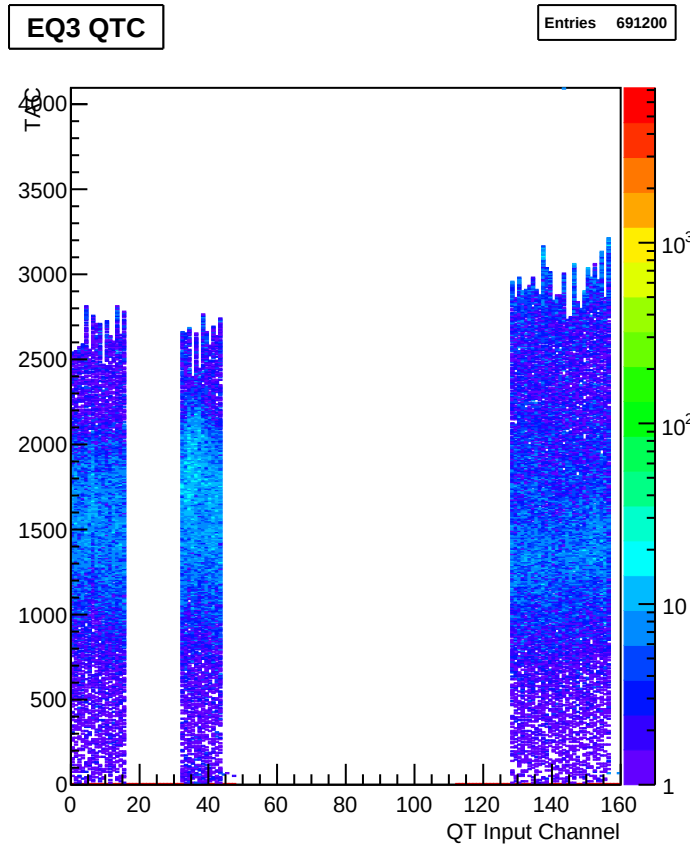

EQ2 QTC
Entries 691200
ADC
QT Input Channel

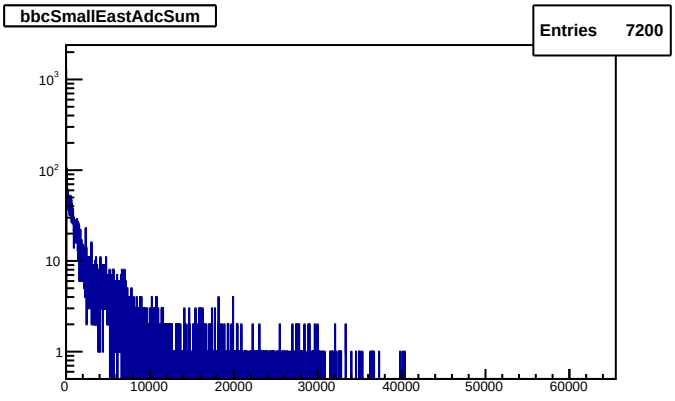

EQ2 QTC
Entries 691200
TADC
QT Input Channel

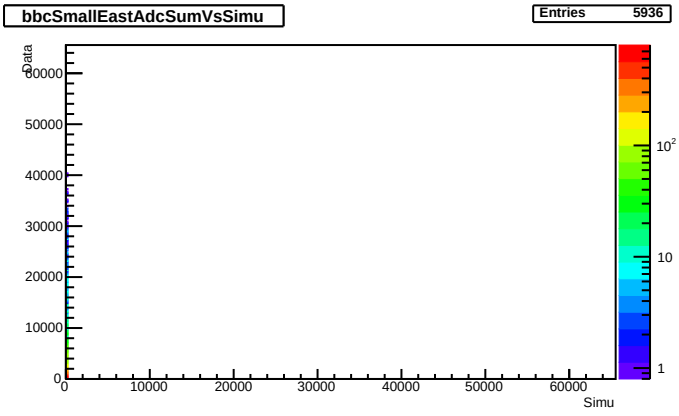

EQ3 QTB
Entries 1843200
ADC
QT Input Channel

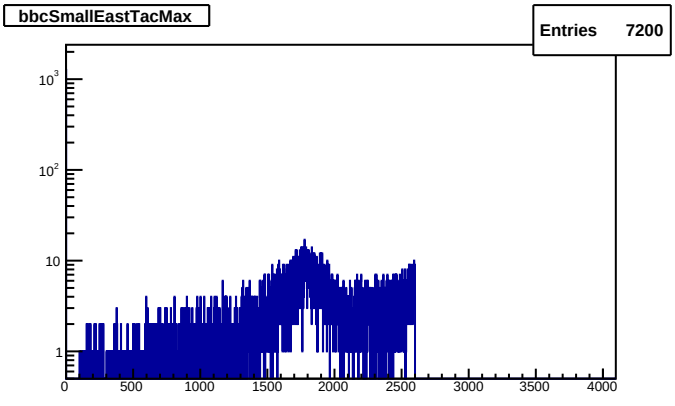

EQ3 QTB
Entries 504274
TDC
QT Input Channel

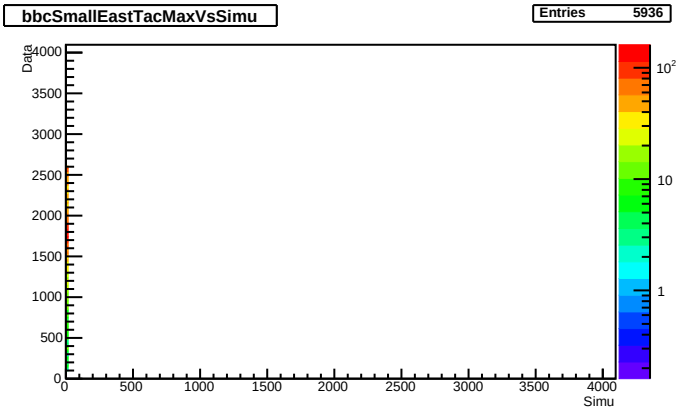

EQ3 QTC
Entries 691200
ADC
QT Input Channel

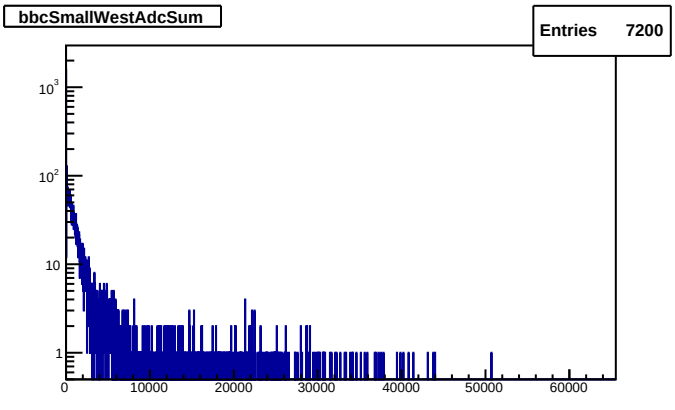

EQ3 QTC
Entries 691200
TADC
QT Input Channel

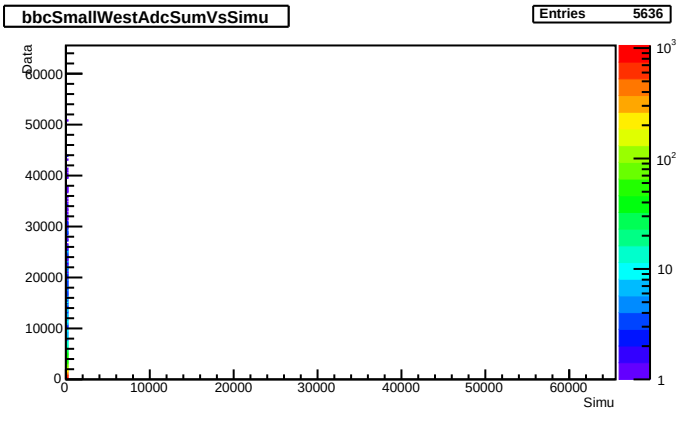

bbcSmallEastAdcSum
Entries 7200

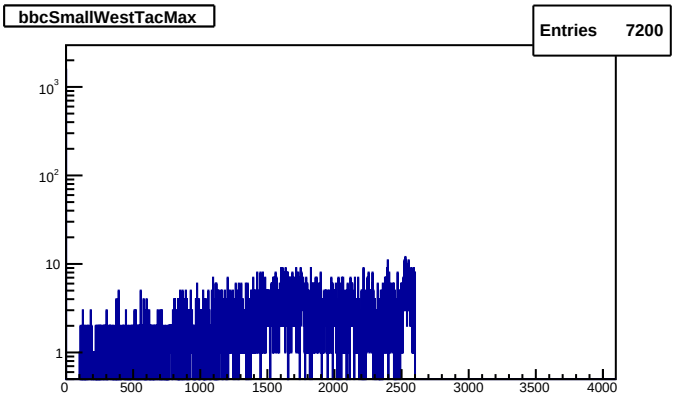

bbcSmallEastAdcSumVsSimu
Entries 5936
Data
Simu

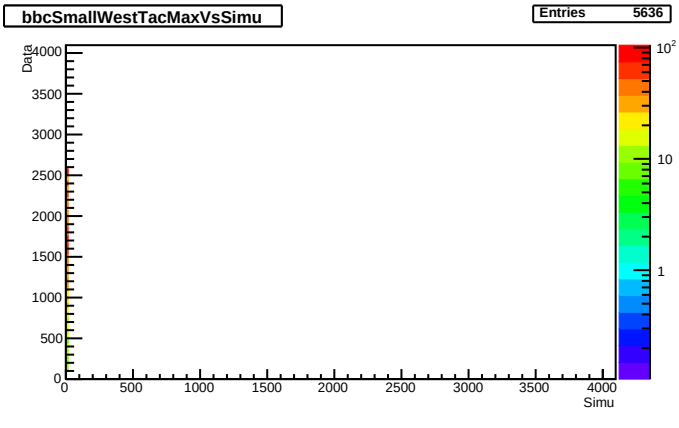

bbcSmallEastTacMax
Entries 7200

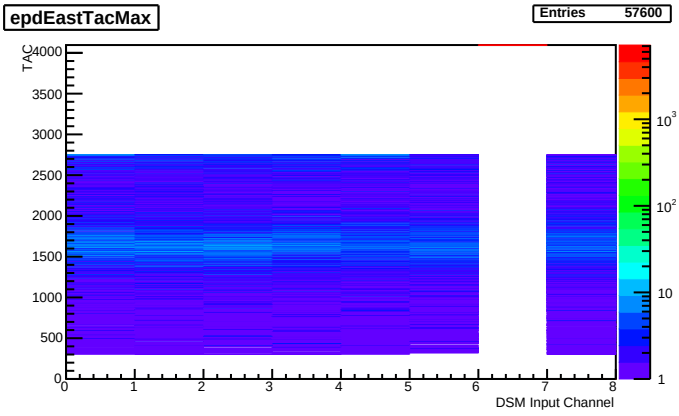

bbcSmallEastTacMaxVsSimu
Entries 5936
Data
Simu

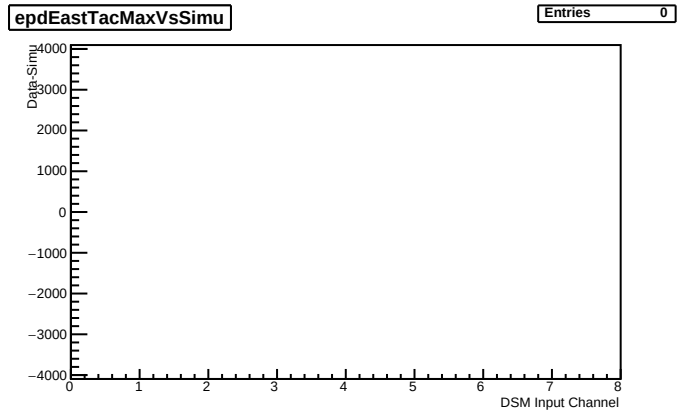

bbcSmallWestAdcSum
Entries 7200

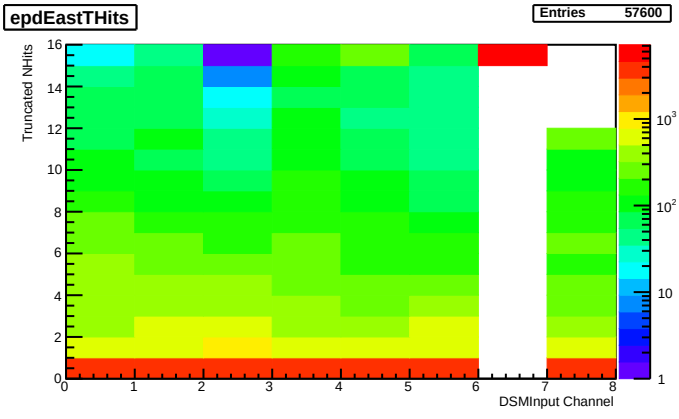

bbcSmallWestAdcSumVsSimu
Entries 5636
Data
Simu

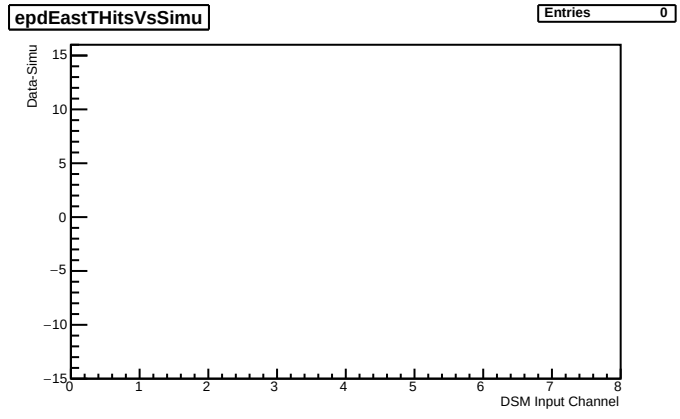

bbcSmallWestTacMax
Entries 7200

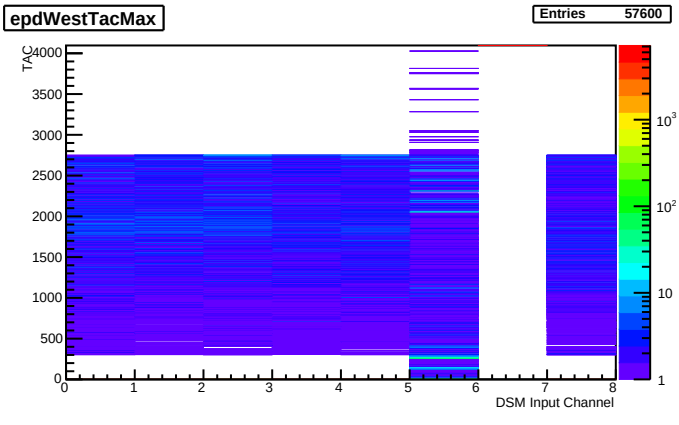

bbcSmallWestTacMaxVsSimu
Entries 5636
Data
Simu

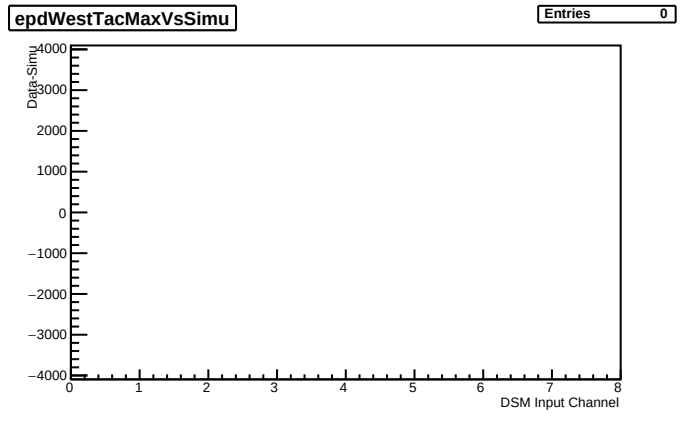

epdEastTacMax
Entries 57600
TAC
DSM Input Channel

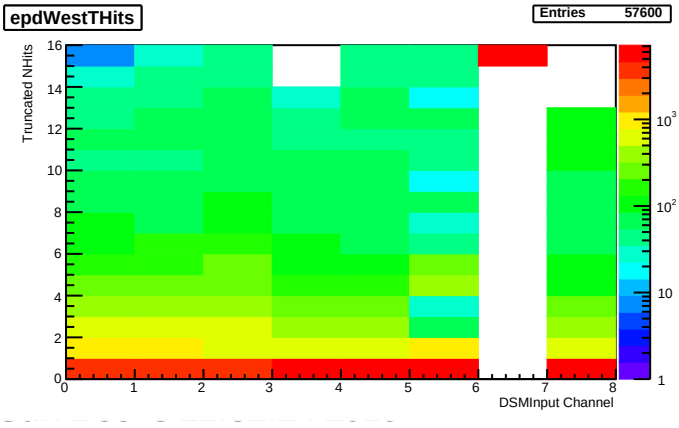

epdEastTacMaxVsSimu
Entries 0
Data-Simu
DSM Input Channel

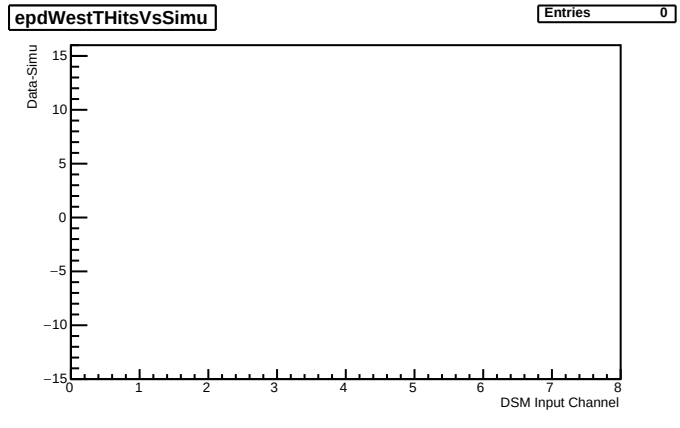

epdEastTHits
Entries 57600
Truncated NHits
DSMInput Channel

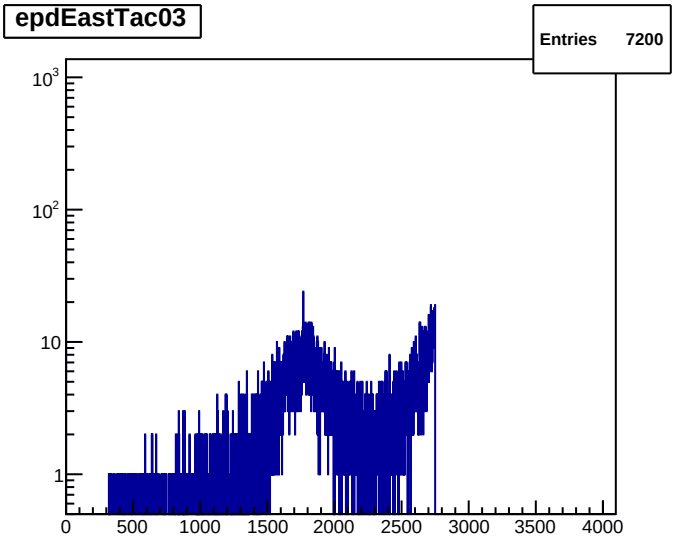

epdEastTHitsVsSimu
Entries 0
Data-Simu
DSM Input Channel

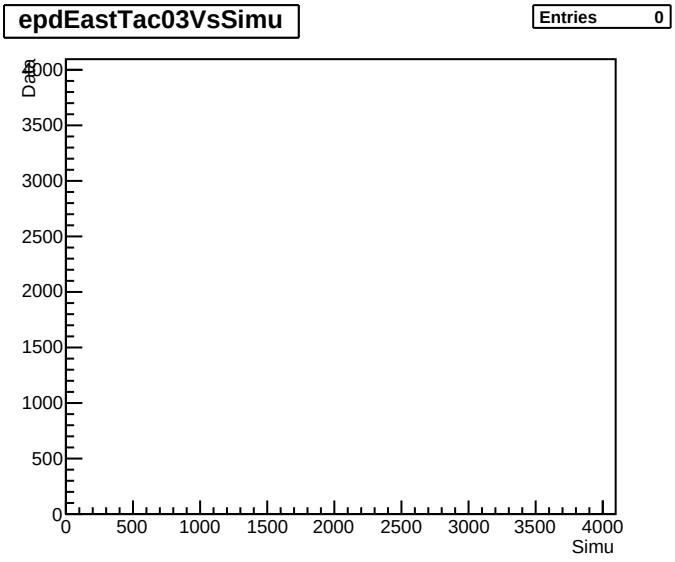

epdWestTacMax
Entries 57600
TAC
DSM Input Channel

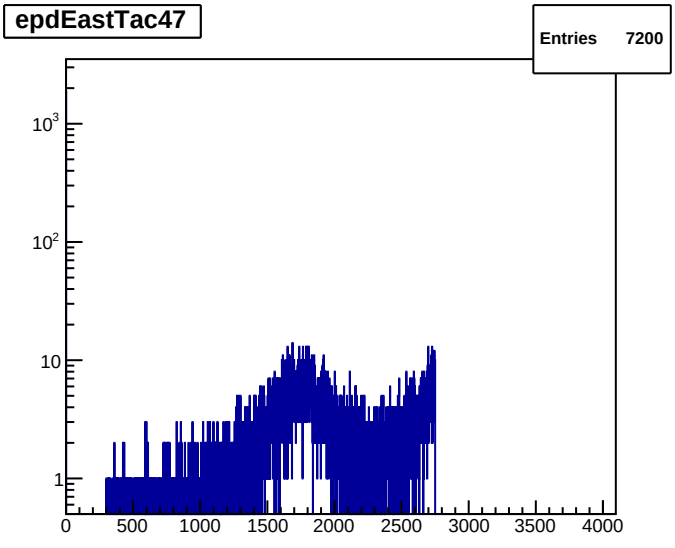

epdWestTacMaxVsSimu
Entries 0
Data-Simu
DSM Input Channel

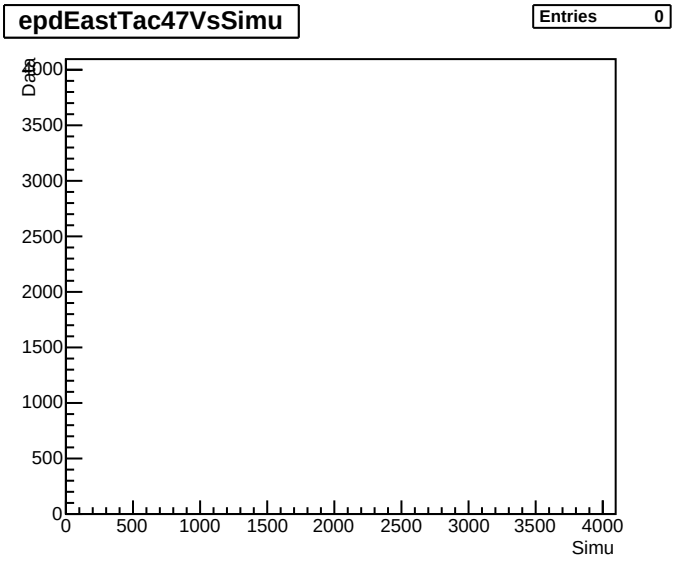

epdWestTHits
Entries 57600
Truncated NHits
DSMInput Channel

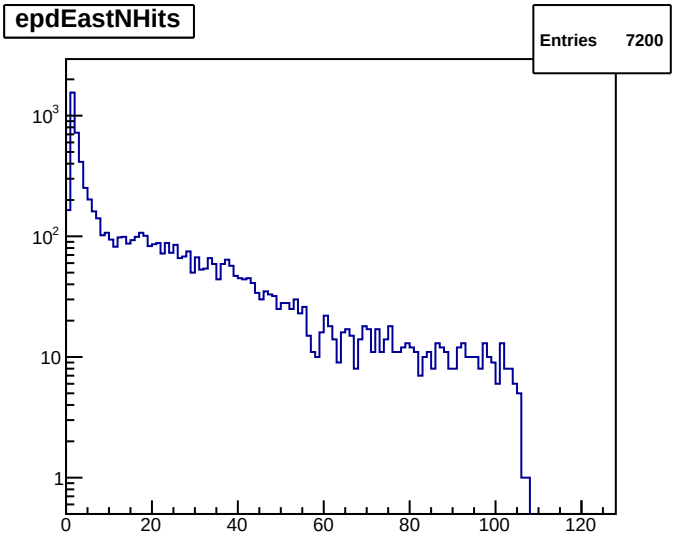

epdWestTHitsVsSimu
Entries 0
Data-Simu
DSM Input Channel

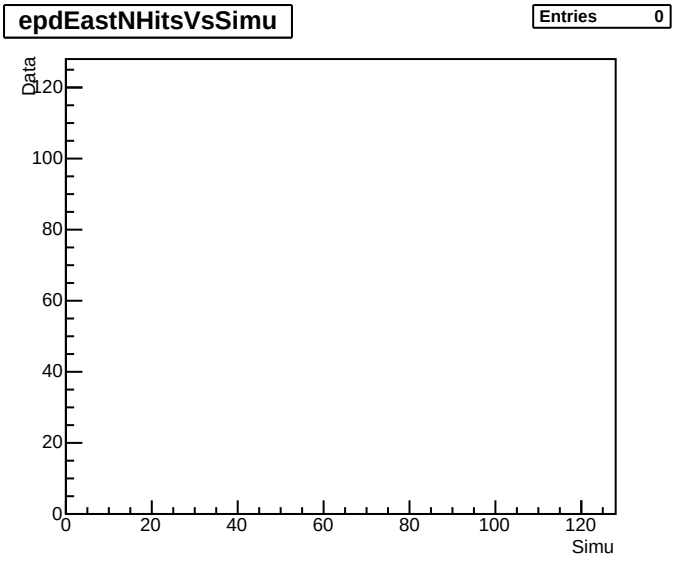

epdEastTac03
Entries 7200

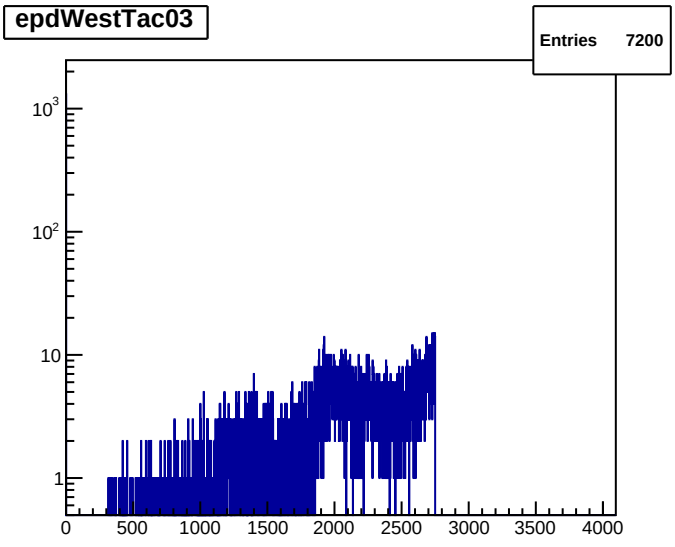

epdEastTac03VsSimu
Entries 0
Data
Simu

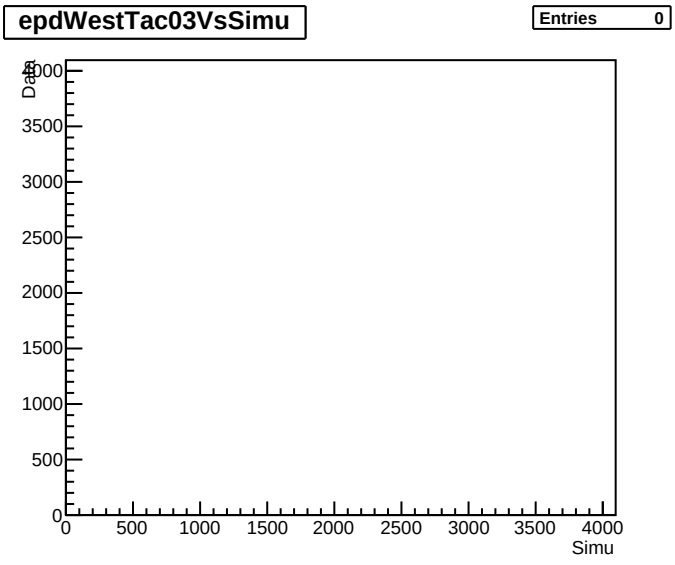

epdEastTac47
Entries 7200

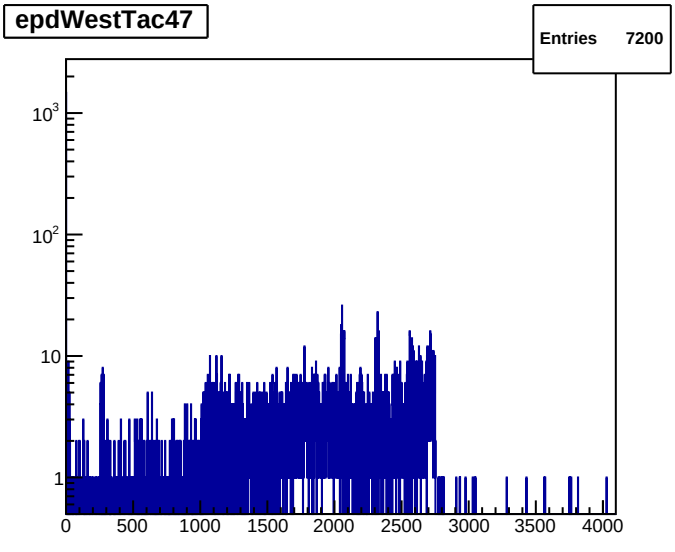

epdEastTac47VsSimu
Entries 0
Data
Simu

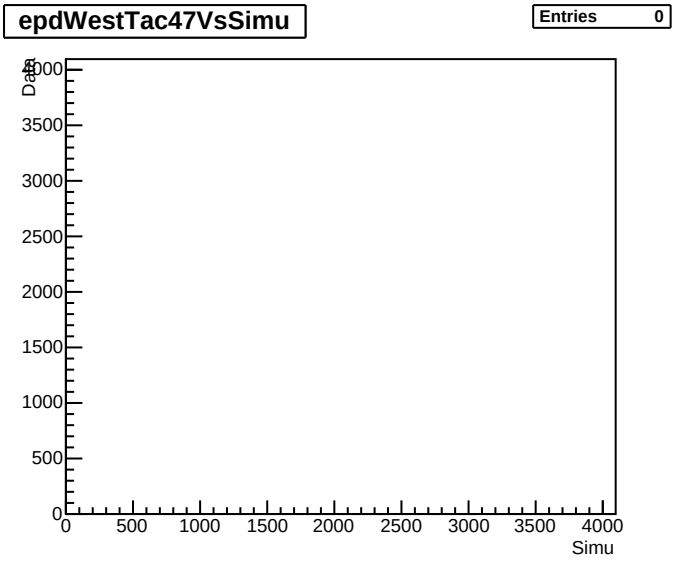

epdEastNHits
Entries 7200

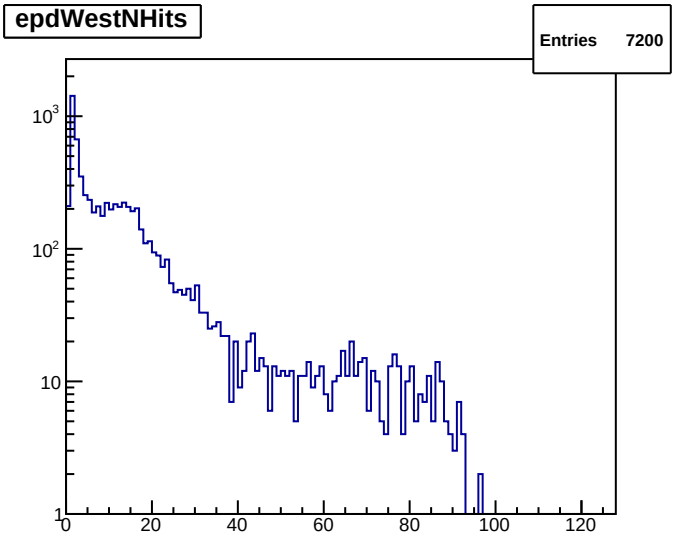

epdEastNHitsVsSimu
Entries 0
Data
Simu

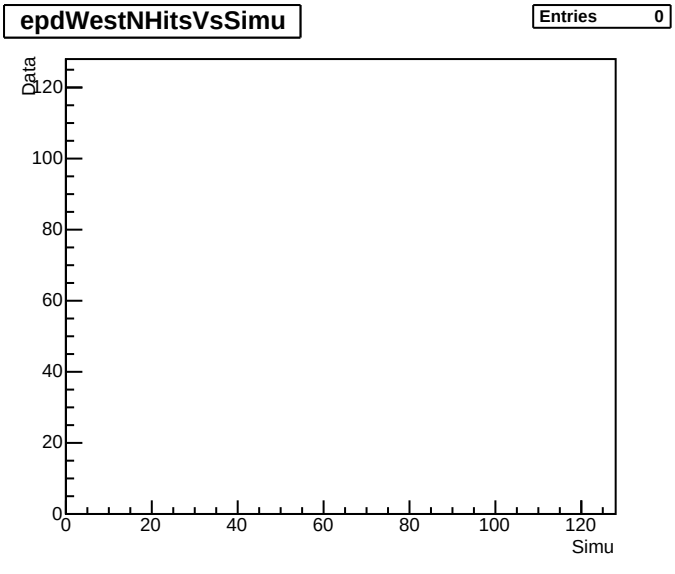

epdWestTac03
Entries 7200

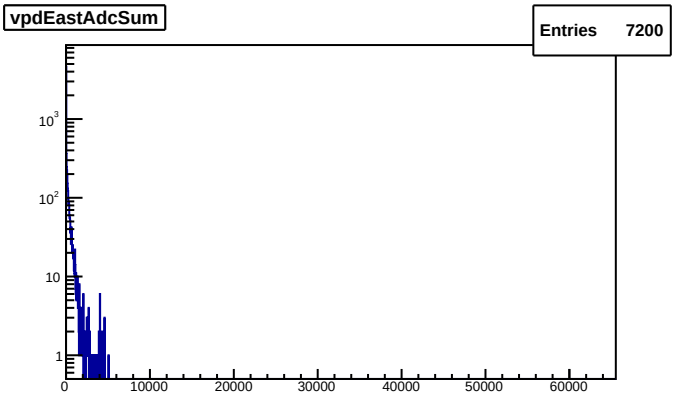

epdWestTac03VsSimu
Entries 0
Data
Simu

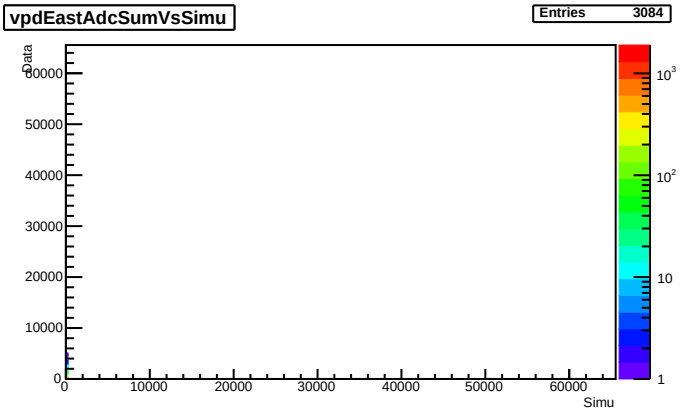

epdWestTac47
Entries 7200

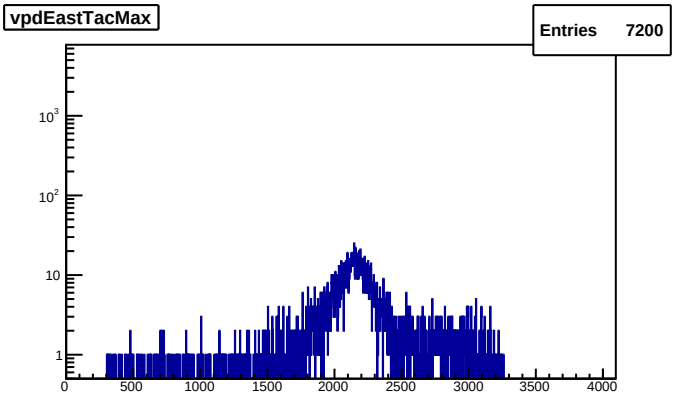

epdWestTac47VsSimu
Entries 0
Data
Simu

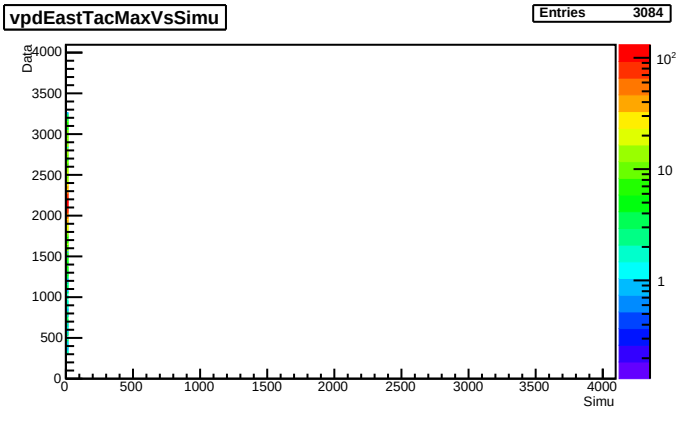

epdWestNHits
Entries 7200

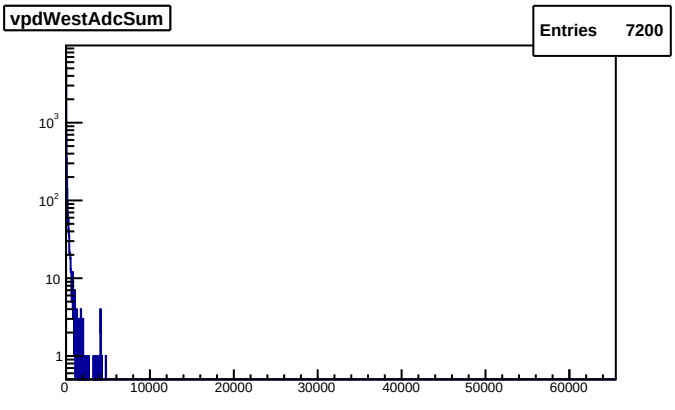

epdWestNHitsVsSimu
Entries 0
Data
Simu

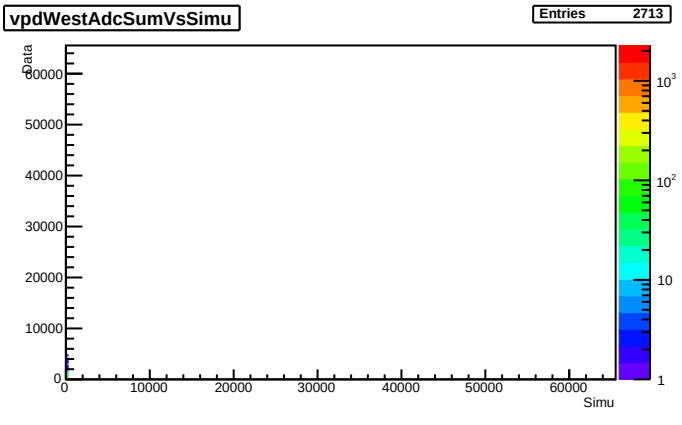

vpdEastAdcSum
Entries 7200

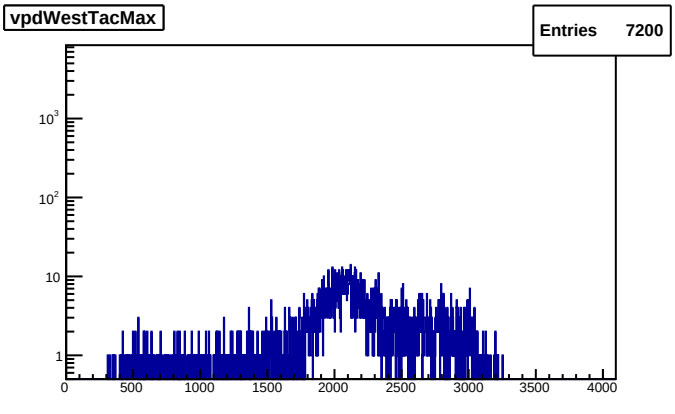

vpdEastAdcSumVsSimu
Entries 3084
Data
Simu

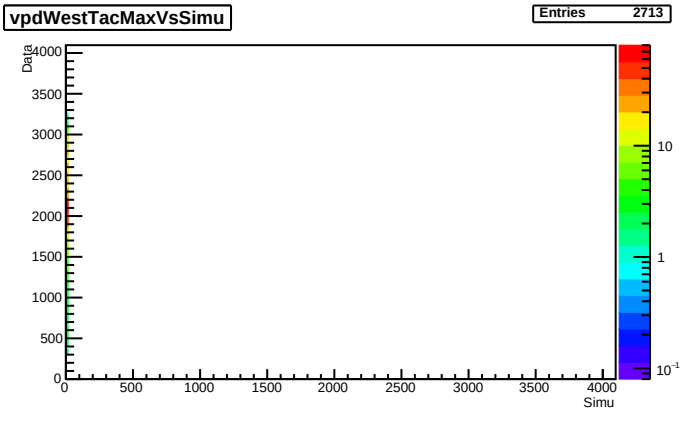

vpdEastTacMax
Entries 7200

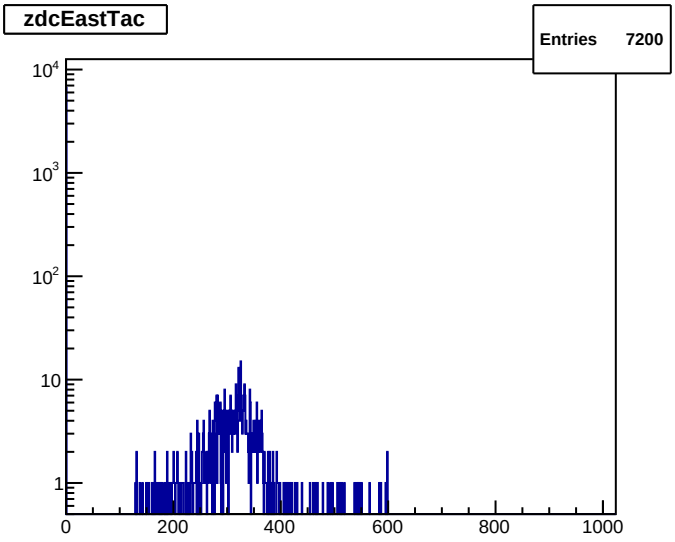

vpdEastTacMaxVsSimu
Entries 3084
Data
Simu

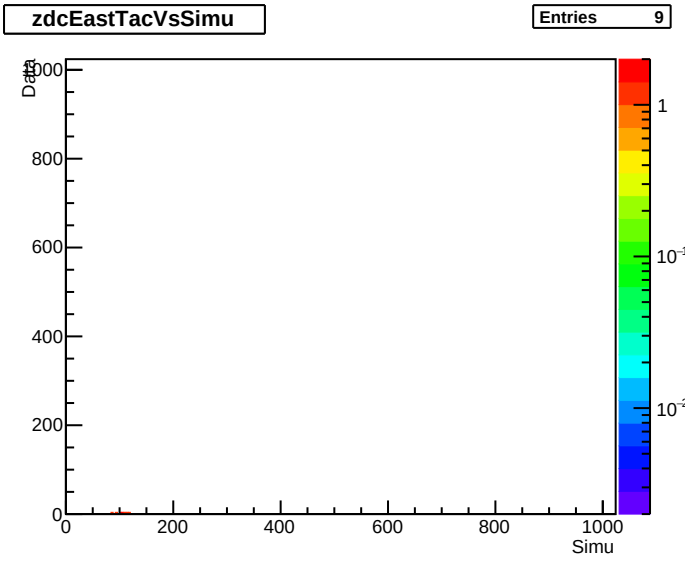

vpdWestAdcSum
Entries 7200

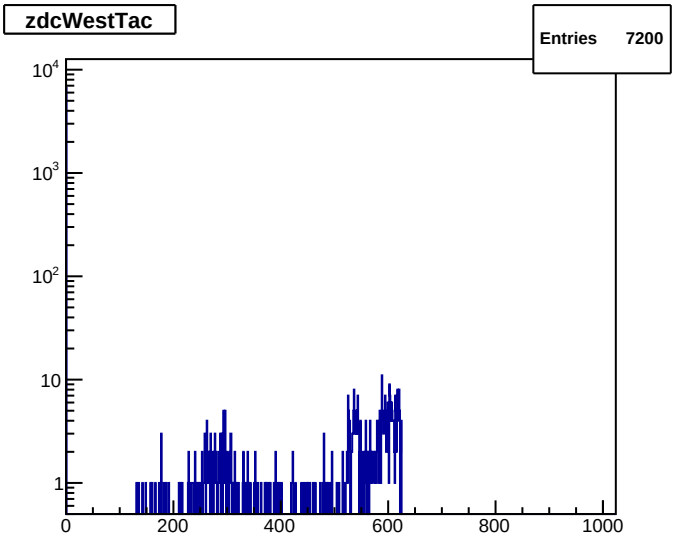

vpdWestAdcSumVsSimu
Entries 2713
Data
Simu

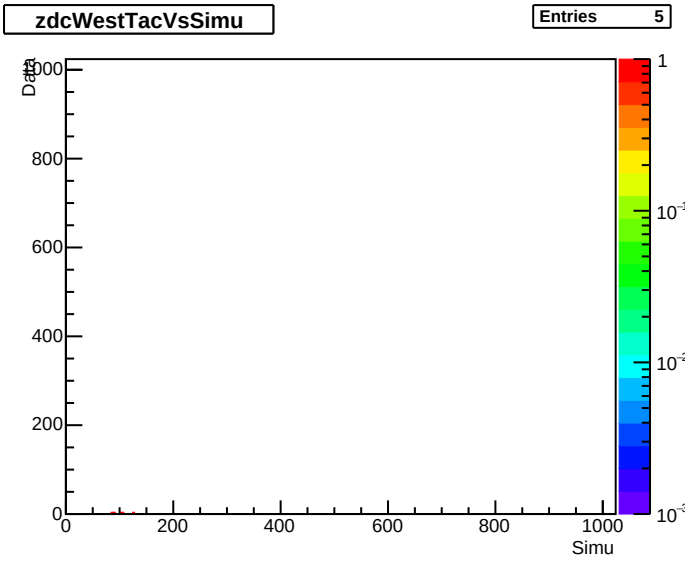

vpdWestTacMax
Entries 7200

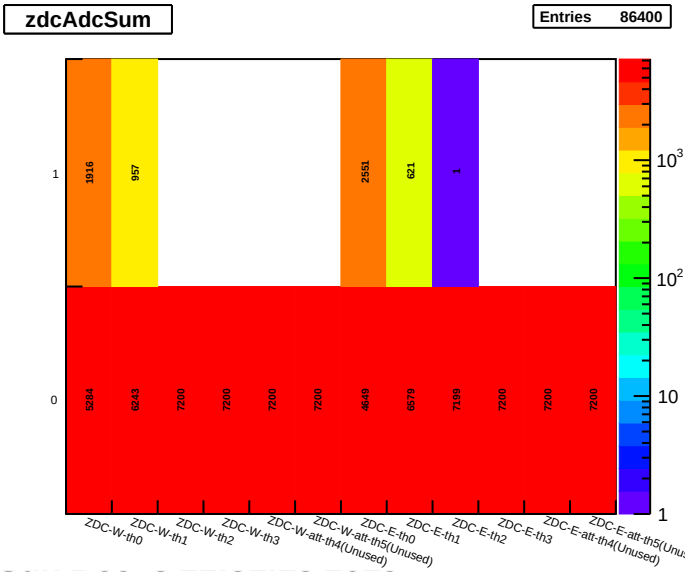

vpdWestTacMaxVsSimu
Entries 2713
Data
Simu

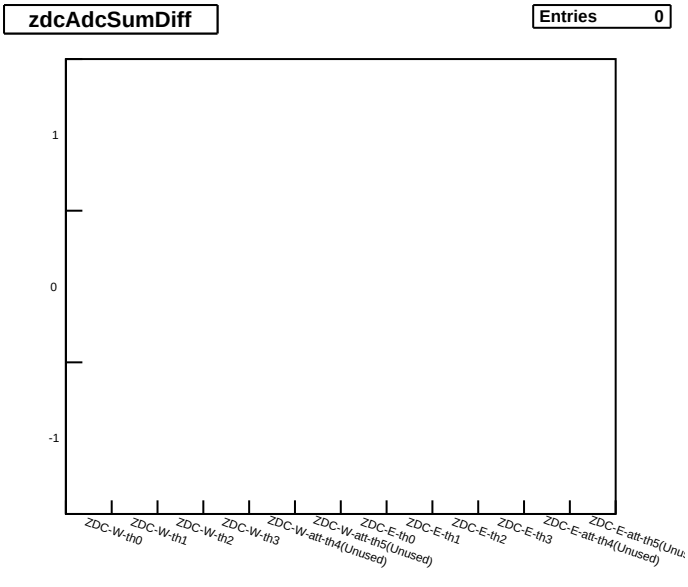

zdcEastTac
Entries 7200

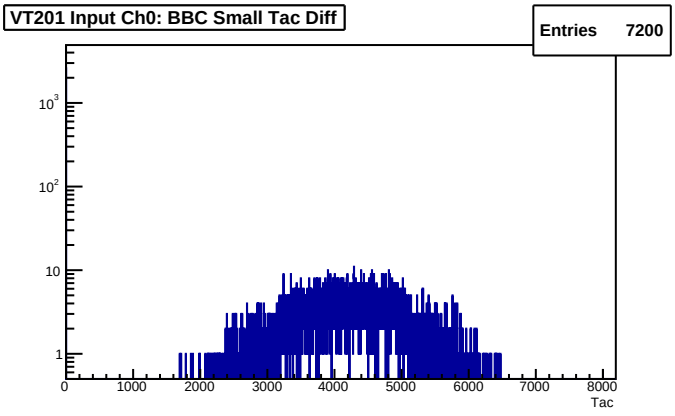

zdcEastTacVsSimu
Entries 9
Data
Simu

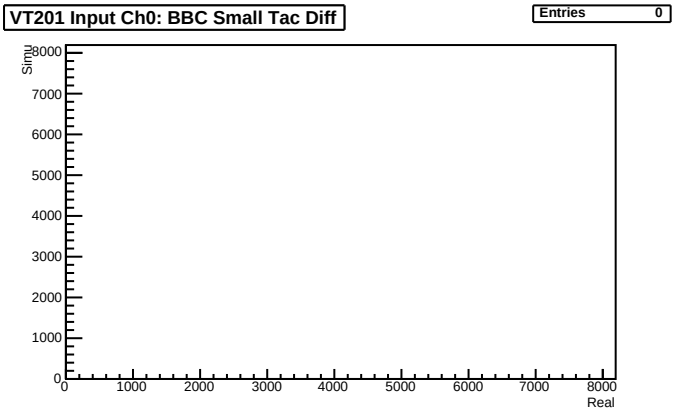

zdcWestTac
Entries 7200

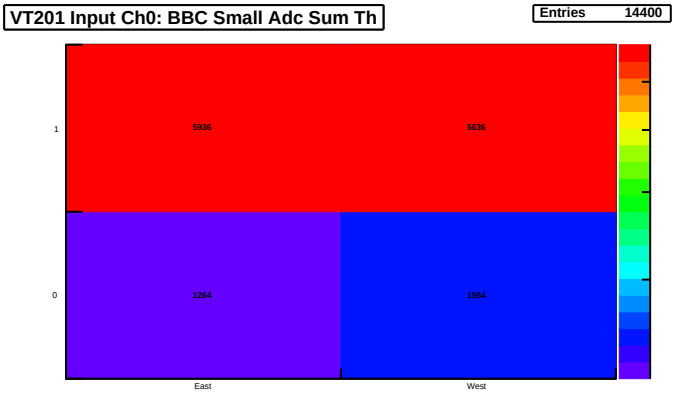

zdcWestTacVsSimu
Entries 5
Data
Simu

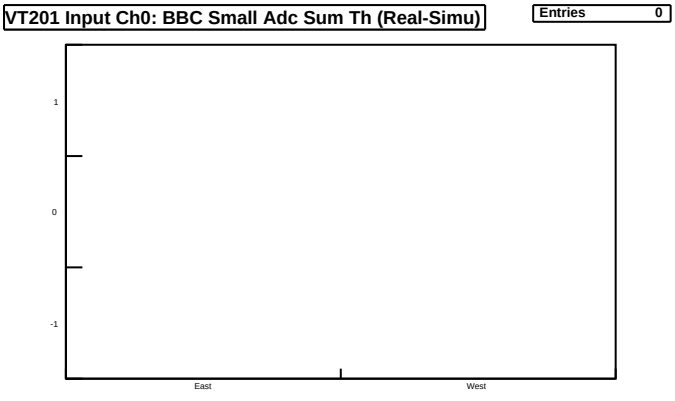

zdcAdcSum
Entries 86400
1916
957
2551
621
1
5284
6243
7200
7200
7200
7200
4649
6579
7199
7200
7200
7200
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)

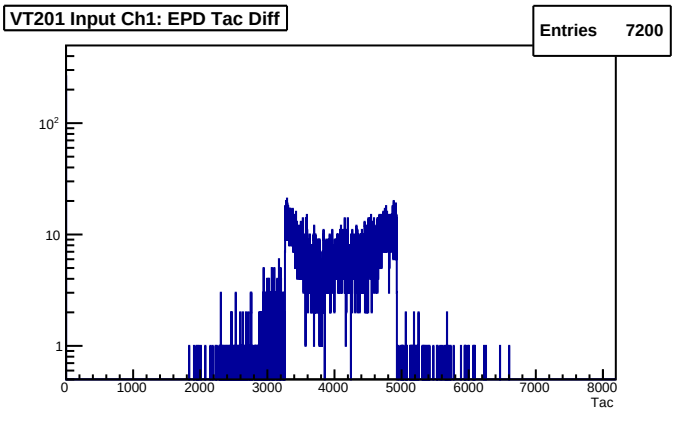

zdcAdcSumDiff
Entries 0
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)

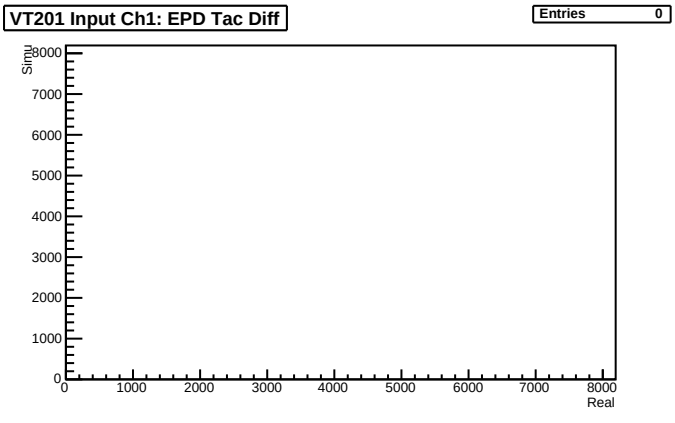

VT201 Input Ch0: BBC Small Tac Diff
Entries 7200
Tac

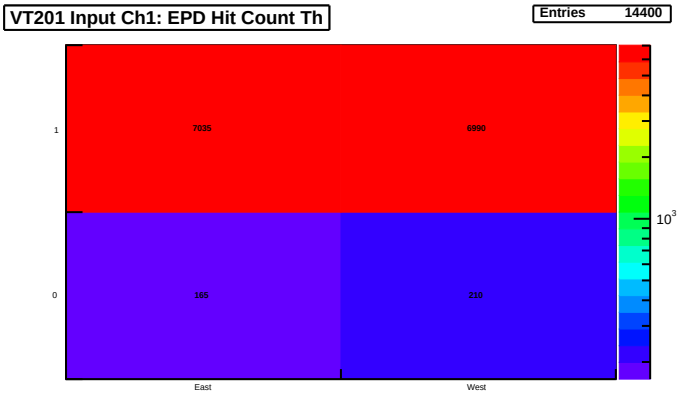

VT201 Input Ch0: BBC Small Tac Diff
Entries 0
Simu
Real

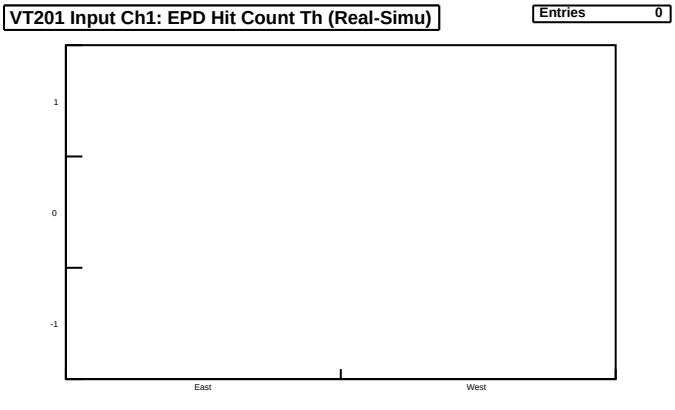

VT201 Input Ch0: BBC Small Adc Sum Th
Entries 14400
5936
5636
1264
1564
East
West

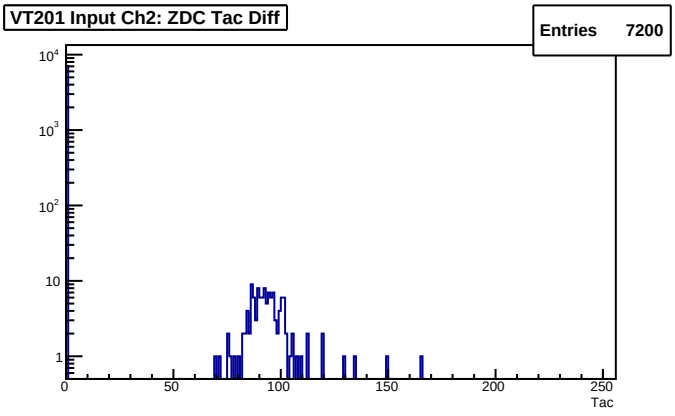

VT201 Input Ch0: BBC Small Adc Sum Th (Real-Simu)
Entries 0
East
West

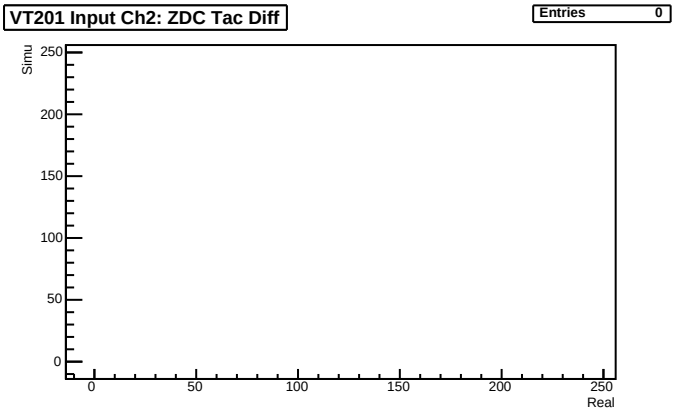

VT201 Input Ch1: EPD Tac Diff
Entries 7200
Tac

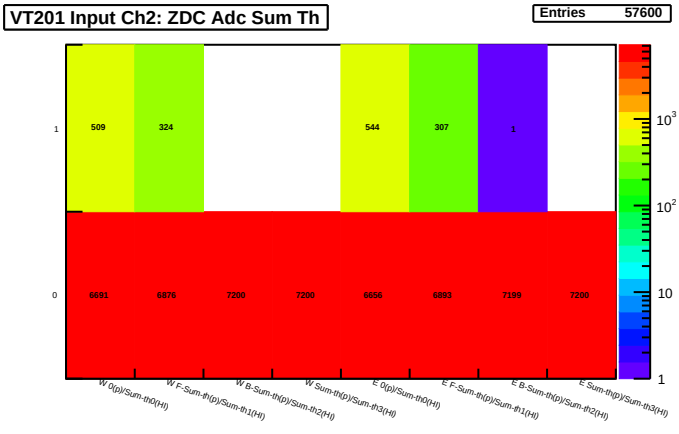

VT201 Input Ch1: EPD Tac Diff
Entries 0
Simu
Real

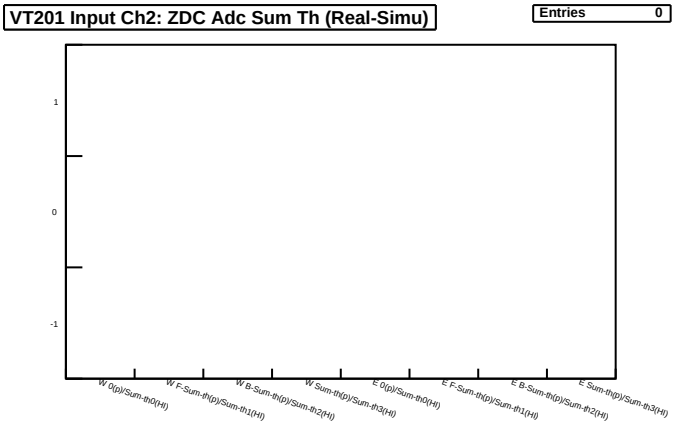

VT201 Input Ch1: EPD Hit Count Th
Entries 14400
7035
6990
165
210
East
West

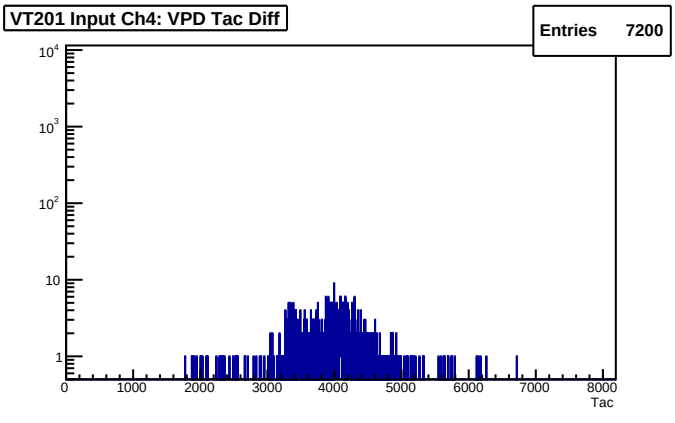

VT201 Input Ch1: EPD Hit Count Th (Real-Simu)
Entries 0
East
West

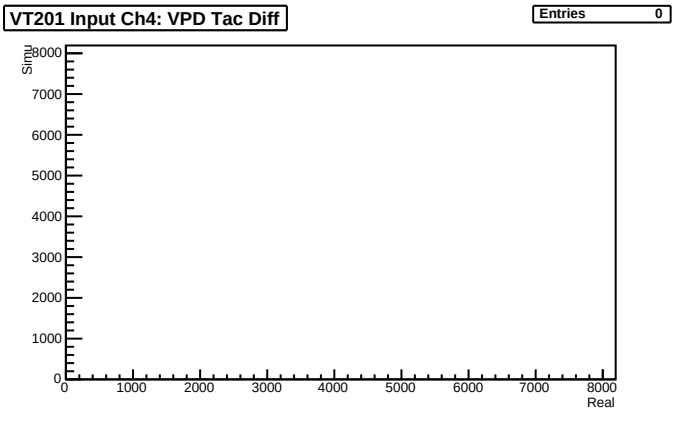

VT201 Input Ch2: ZDC Tac Diff
Entries 7200
Tac

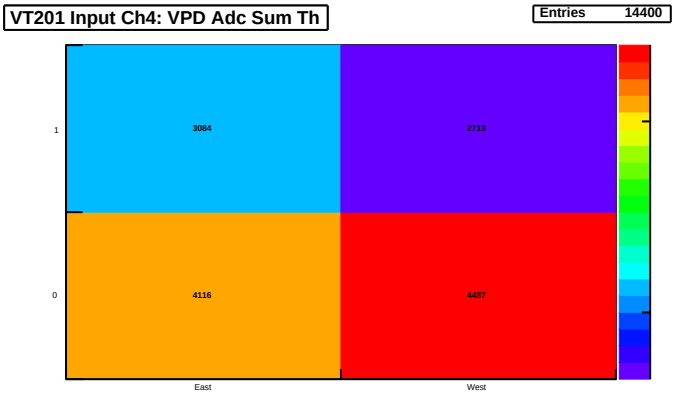

VT201 Input Ch2: ZDC Tac Diff
Entries 0
Simu
Real

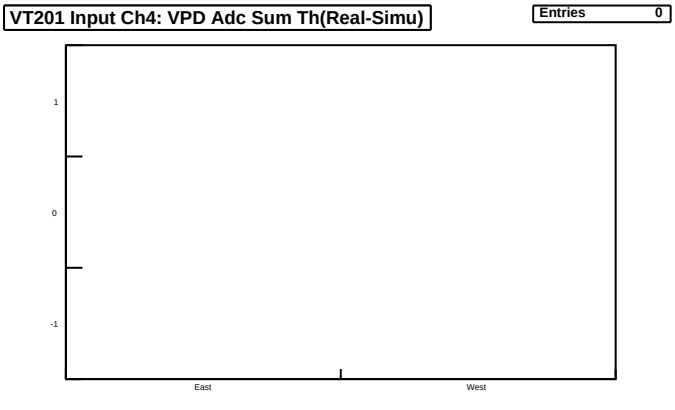

VT201 Input Ch2: ZDC Adc Sum Th
Entries 57600
509
324
544
307
1
6691
6876
7200
7200
6656
6893
7199
7200
W 0(p)/Sum-th0(H)
W F-Sum-th(p)/Sum-th1(H)
W B-Sum-th(p)/Sum-th2(H)
W Sum-th(p)/Sum-th3(H)
E 0(p)/Sum-th0(H)
E F-Sum-th(p)/Sum-th1(H)
E B-Sum-th(p)/Sum-th2(H)
E Sum-th(p)/Sum-th3(H)

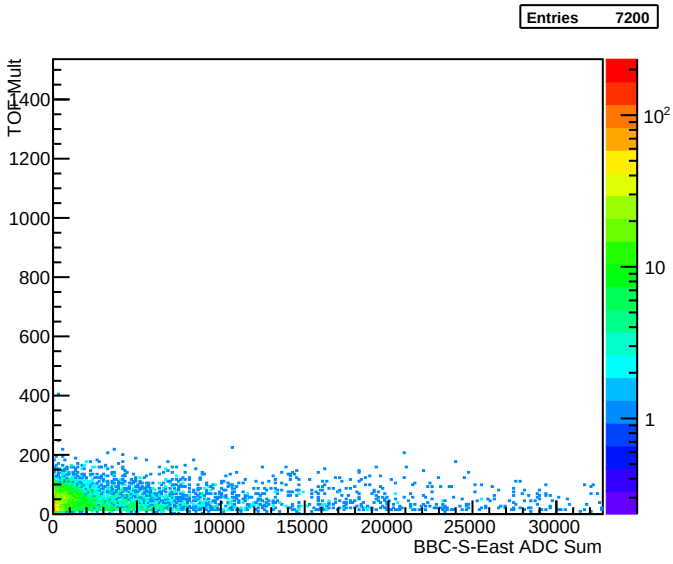

VT201 Input Ch2: ZDC Adc Sum Th (Real-Simu)
Entries 0
W 0(p)/Sum-th0(H)
W F-Sum-th(p)/Sum-th1(H)
W B-Sum-th(p)/Sum-th2(H)
W Sum-th(p)/Sum-th3(H)
E 0(p)/Sum-th0(H)
E F-Sum-th(p)/Sum-th1(H)
E B-Sum-th(p)/Sum-th2(H)
E Sum-th(p)/Sum-th3(H)

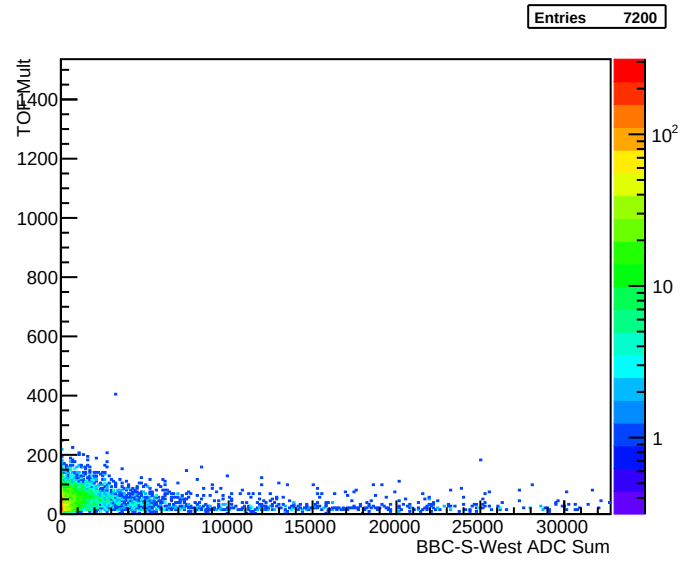

VT201 Input Ch4: VPD Tac Diff
Entries 7200
Tac

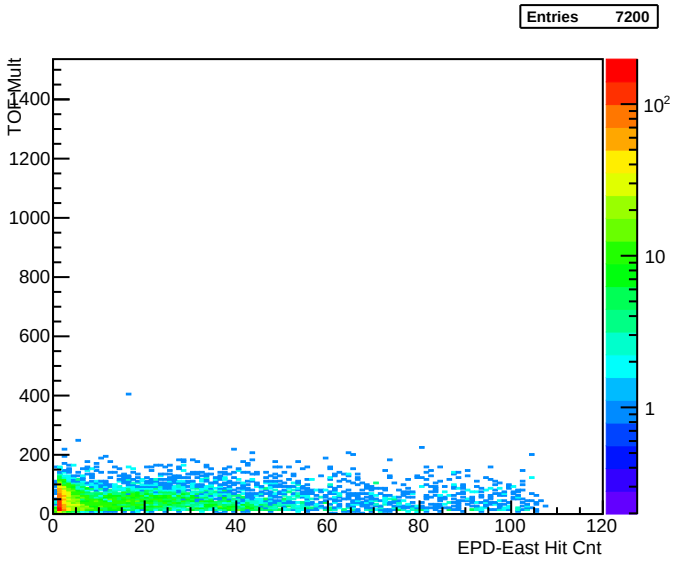

VT201 Input Ch4: VPD Tac Diff
Entries 0
Simu
Real

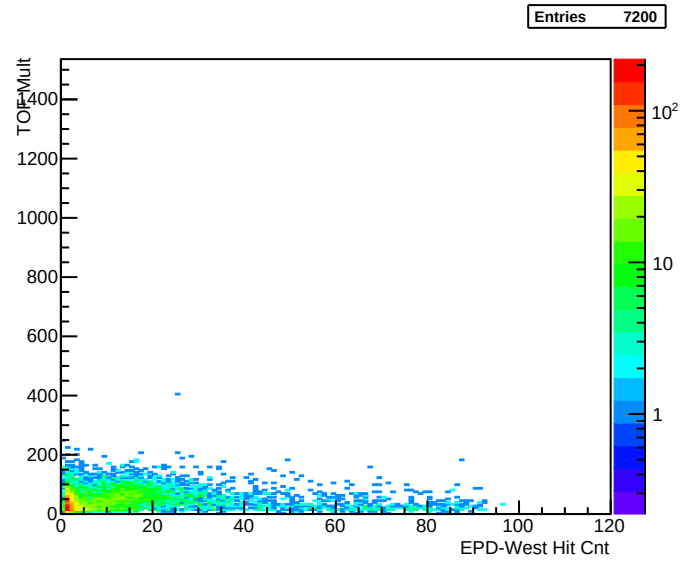

VT201 Input Ch4: VPD Adc Sum Th
Entries 14400
3084
2713
4116
4487
East
West

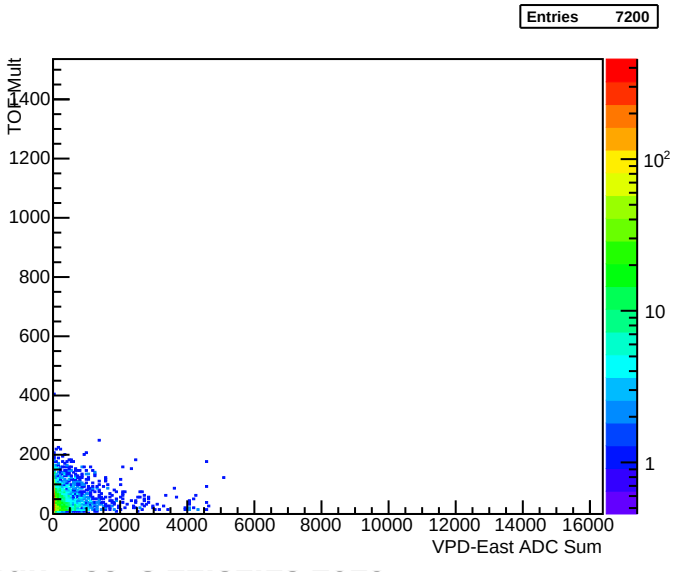

VT201 Input Ch4: VPD Adc Sum Th(Real-Simu)
Entries 0
East
West

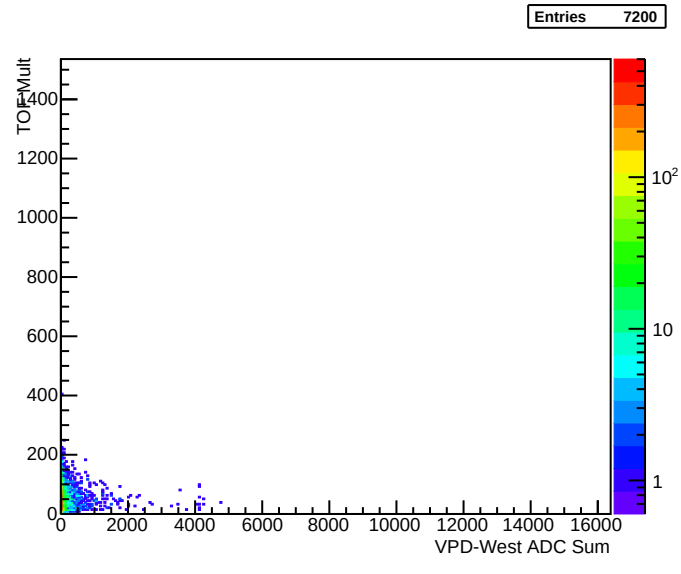

Entries 7200
TOF-Mult
BBC-S-East ADC Sum

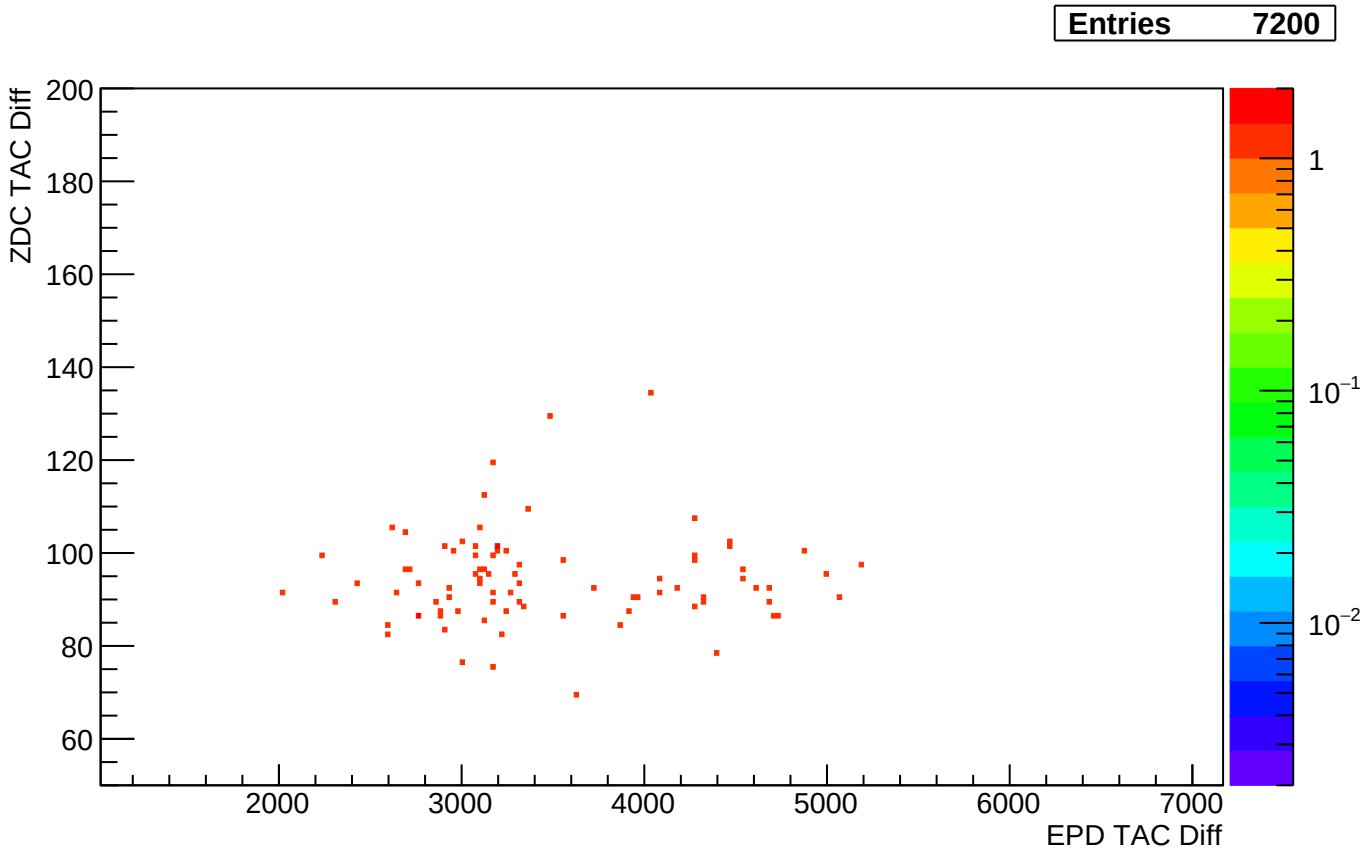

Entries 7200
TOF-Mult
BBC-S-West ADC Sum

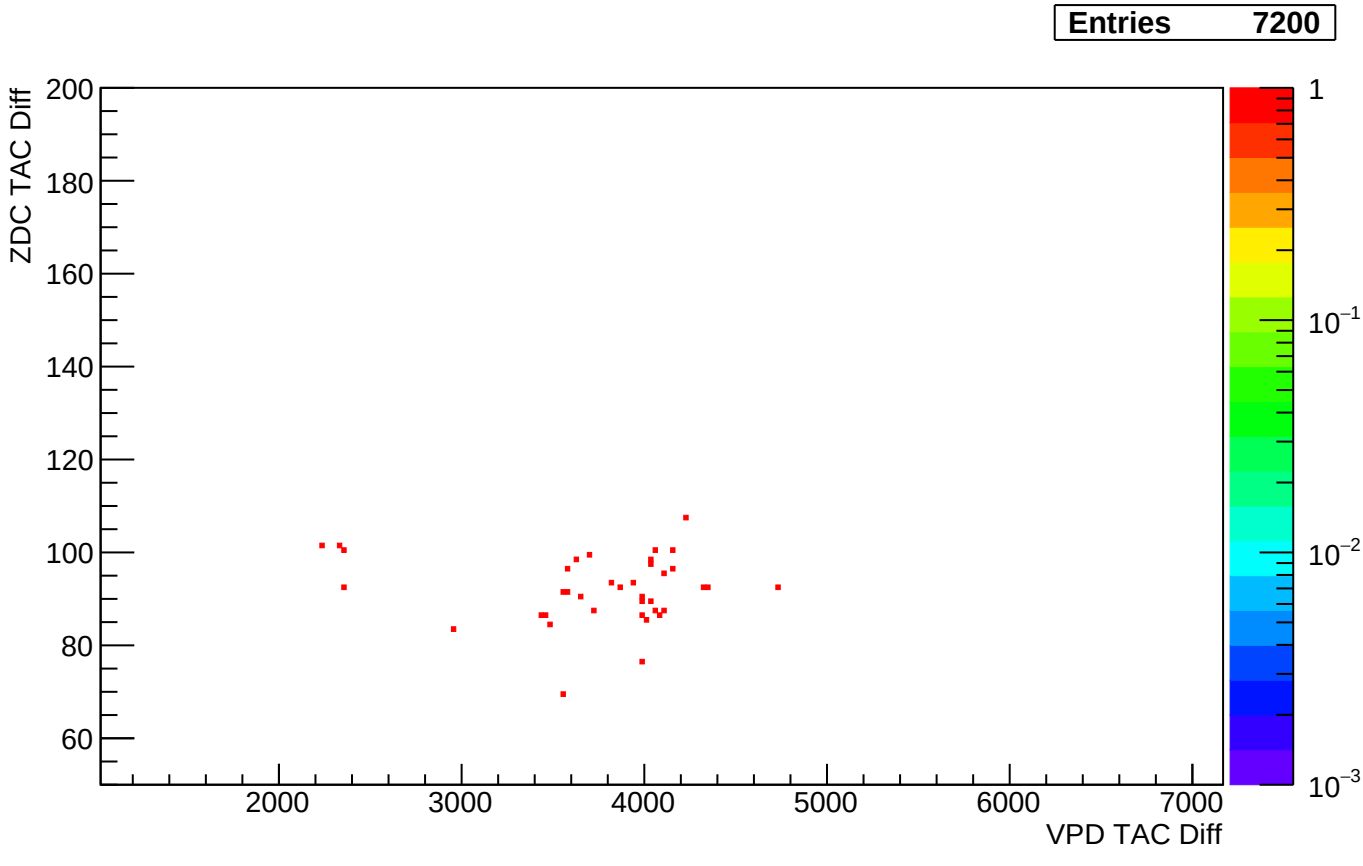

Entries 7200
TOF-Mult
EPD-East Hit Cnt

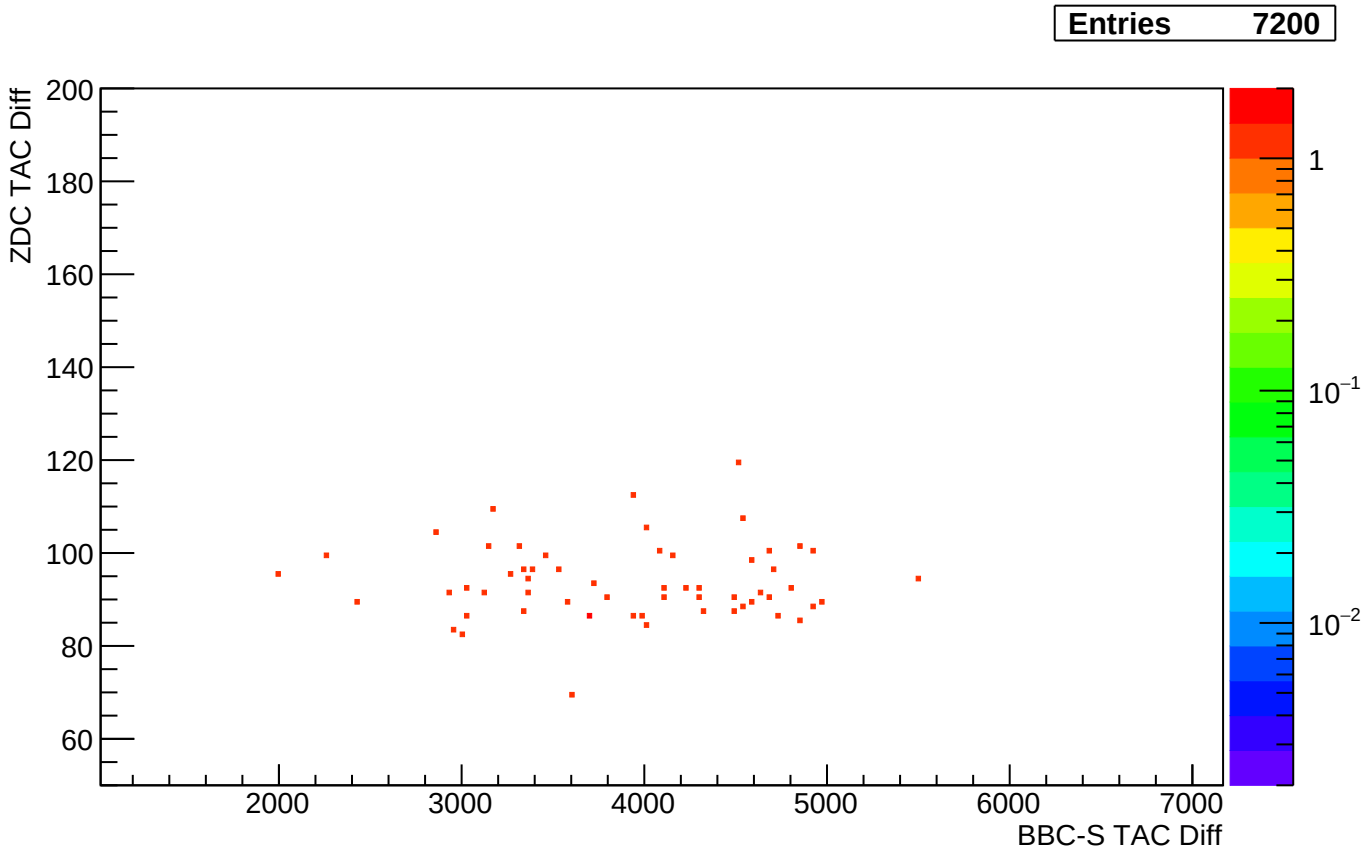

Entries 7200
TOF-Mult
EPD-West Hit Cnt

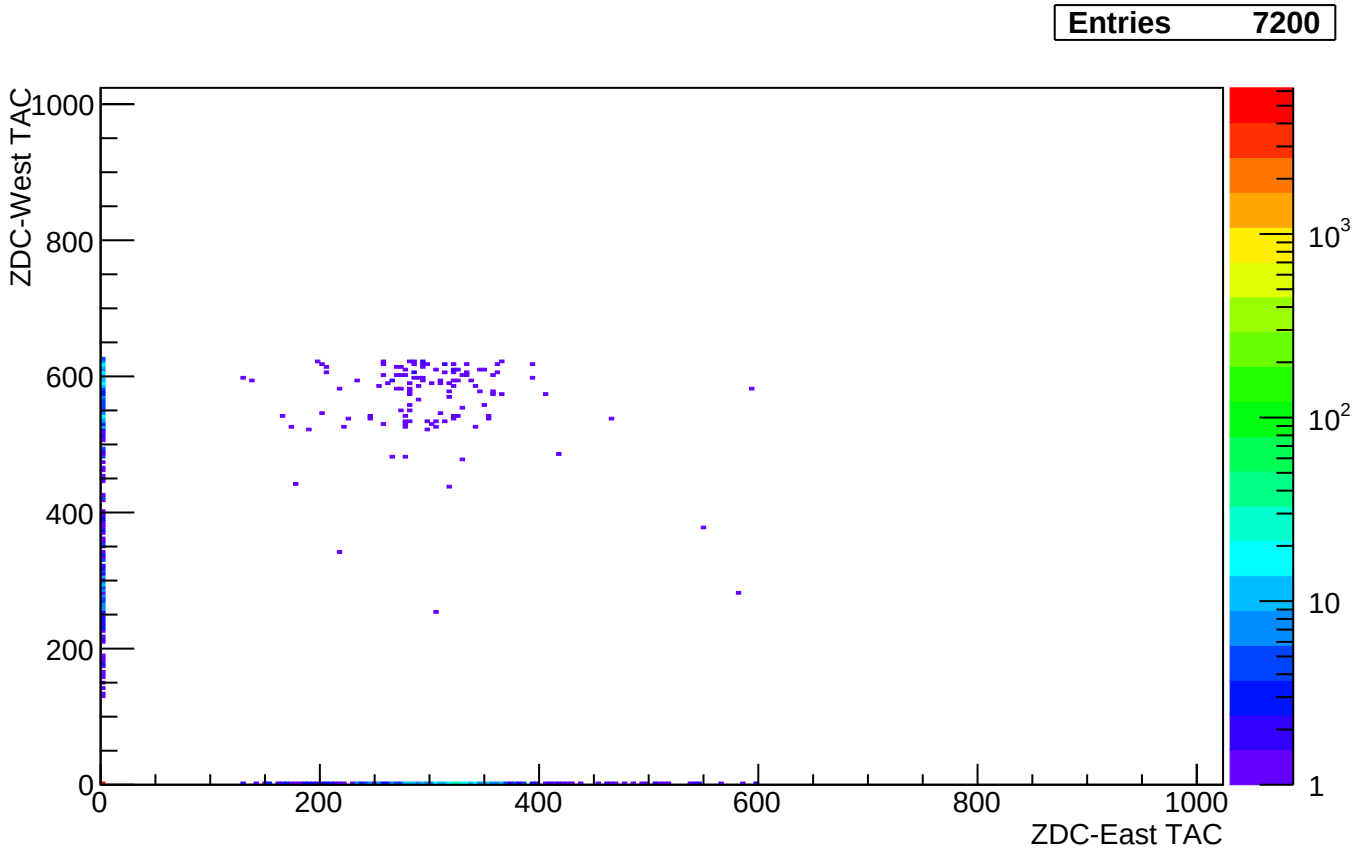

Entries 7200
TOF-Mult
VPD-East ADC Sum

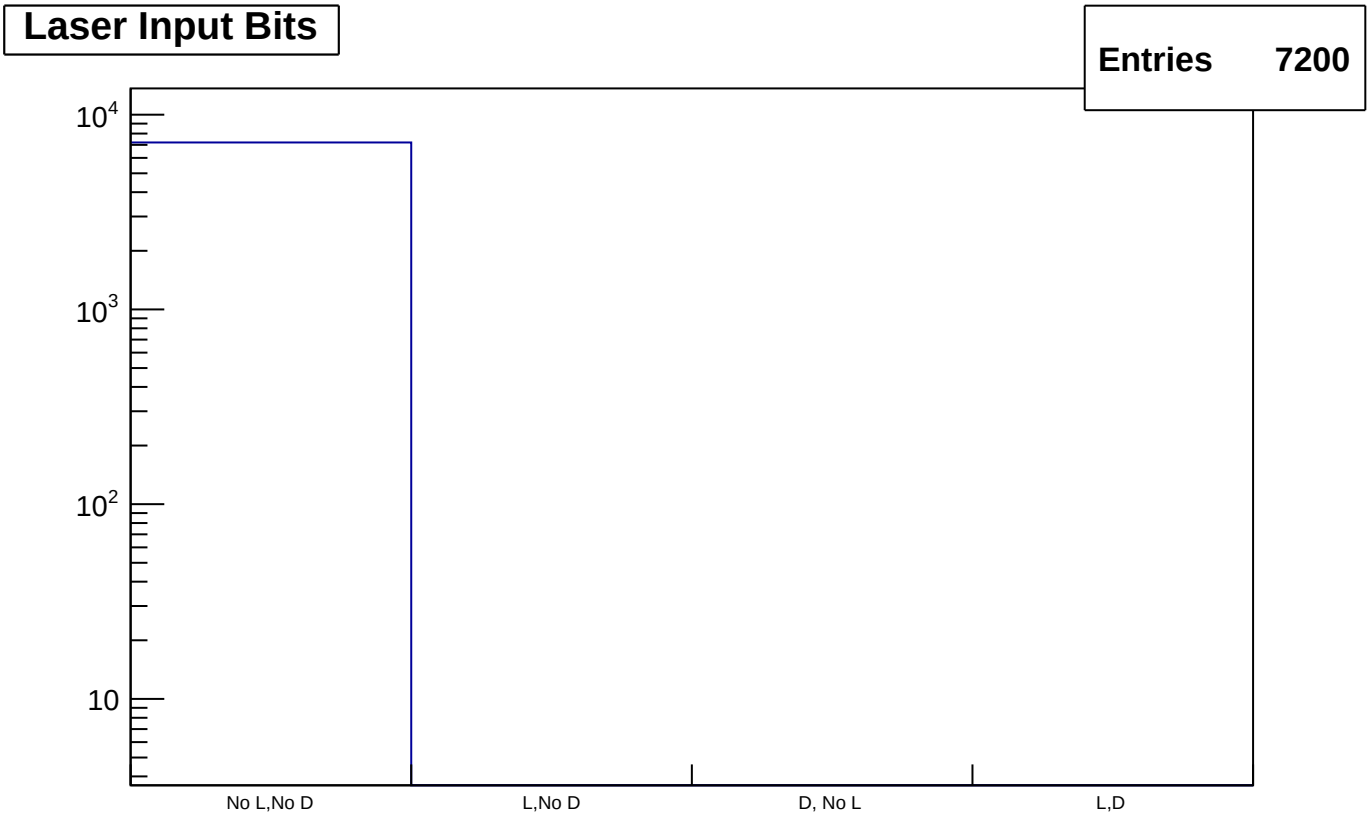

Entries 7200
TOF-Mult
VPD-West ADC Sum

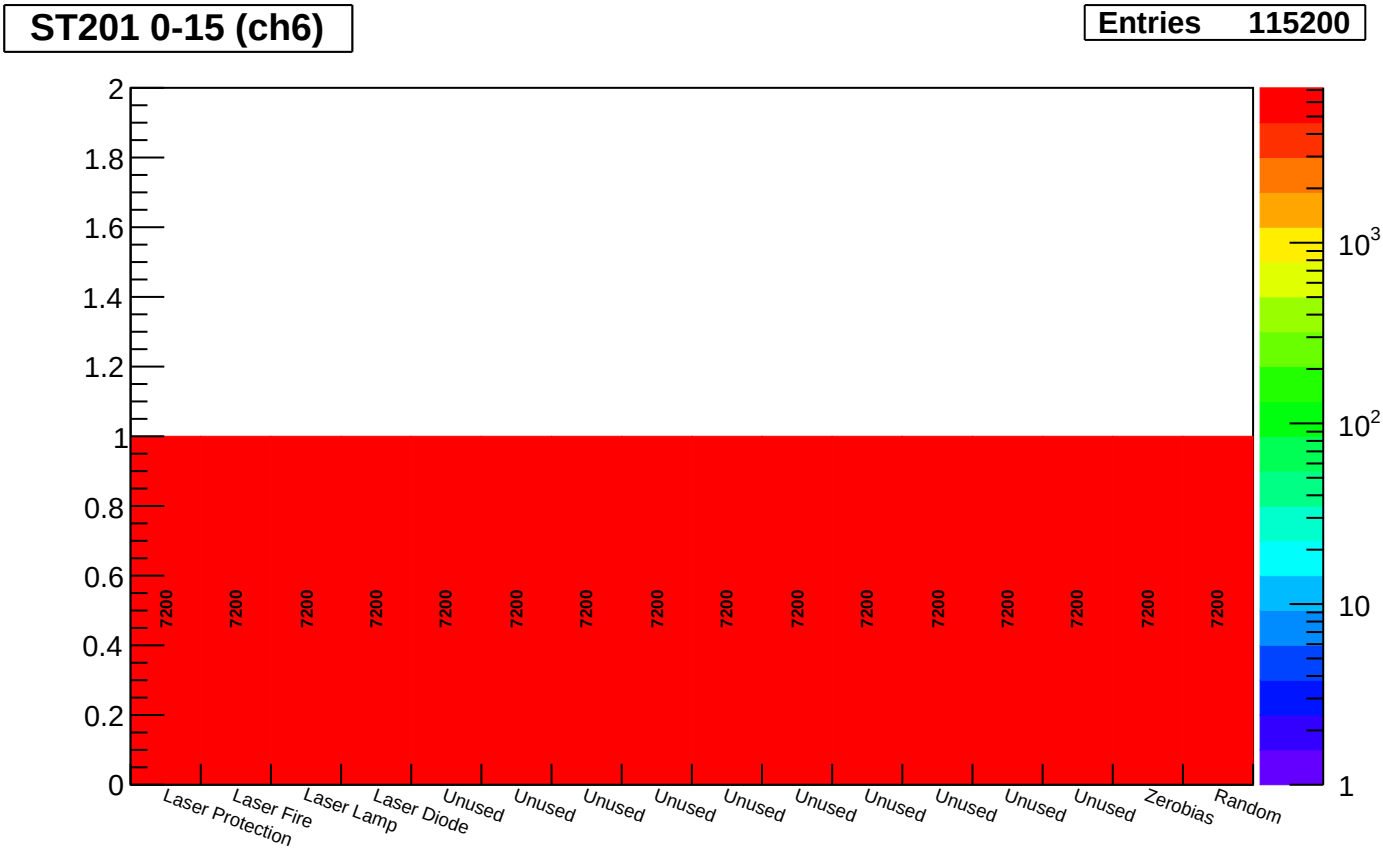

Entries 7200
ZDC TAC Diff
200
180
160
140
120
100
80
60
2000
3000
4000
5000
6000
7000
EPD TAC Diff
1
10^{-1}
10^{-2}

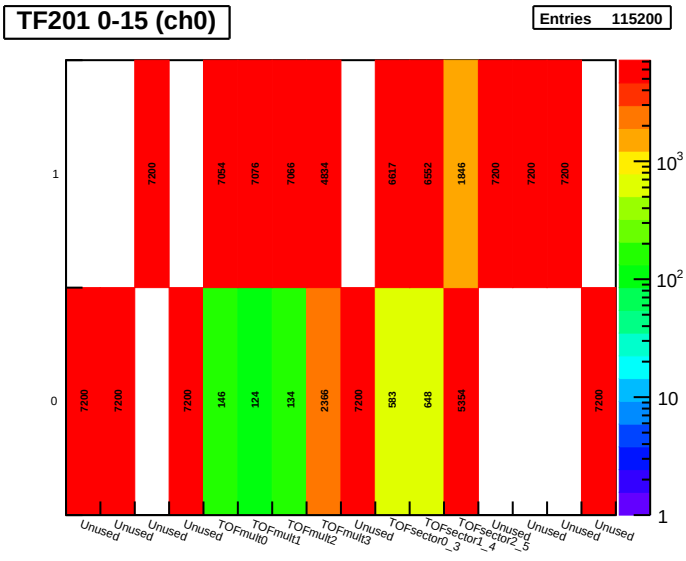

Entries 7200
ZDC TAC Diff
200
180
160
140
120
100
80
60
2000
3000
4000
5000
6000
7000
VPD TAC Diff
1
10^{-1}
10^{-2}
10^{-3}

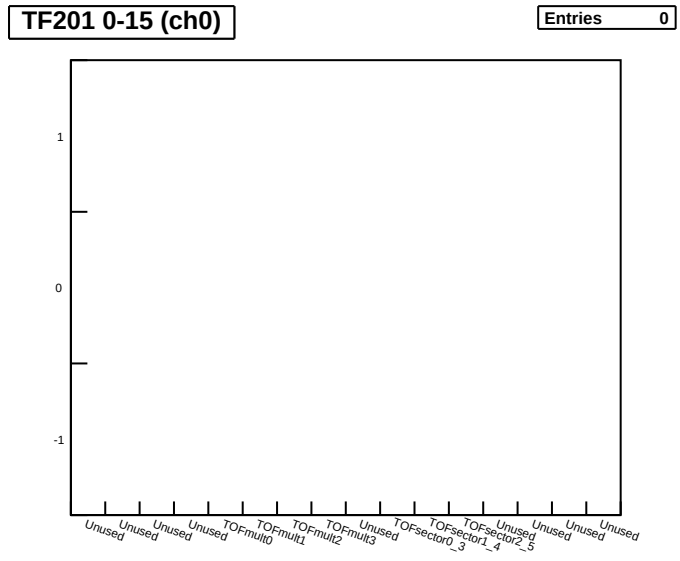

Entries 7200
ZDC TAC Diff
200
180
160
140
120
100
80
60
2000
3000
4000
5000
6000
7000
BBC-S TAC Diff
1
10^{-1}
10^{-2}

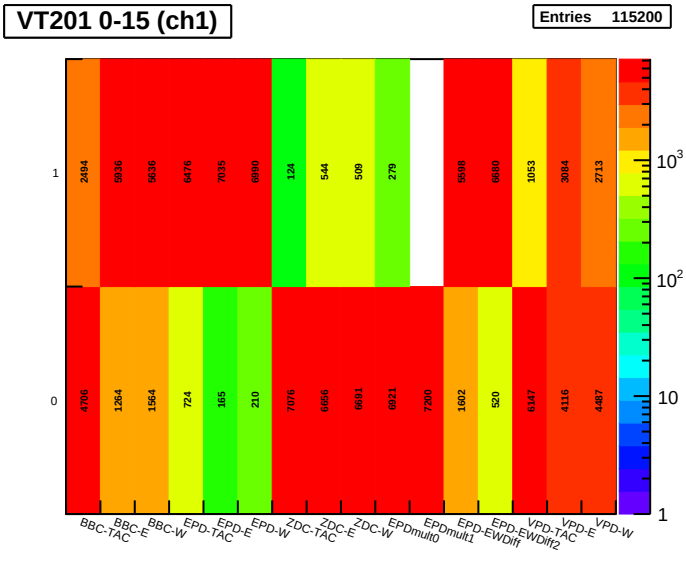

Entries 7200
ZDC-West TAC
1000
800
600
400
200
0
0
200
400
600
800
1000
ZDC-East TAC
10^3
10^2
10
1

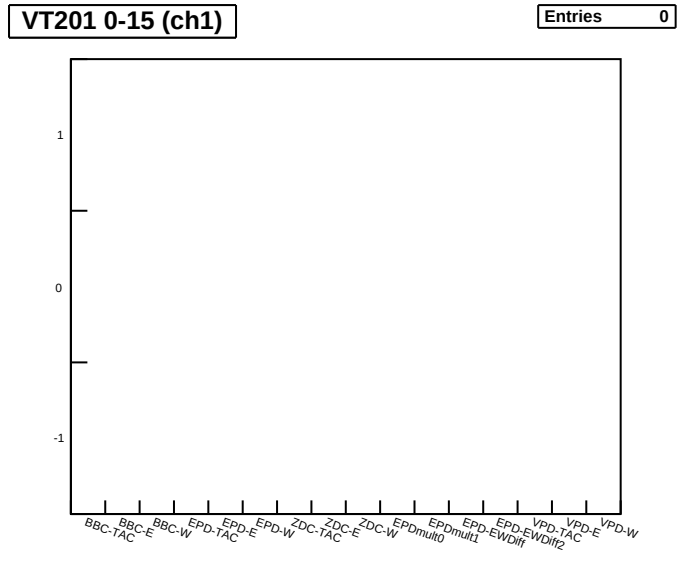

Laser Input Bits
Entries 7200
10^4
10^3
10^2
10
No L,No D
L,No D
D, No L
L,D

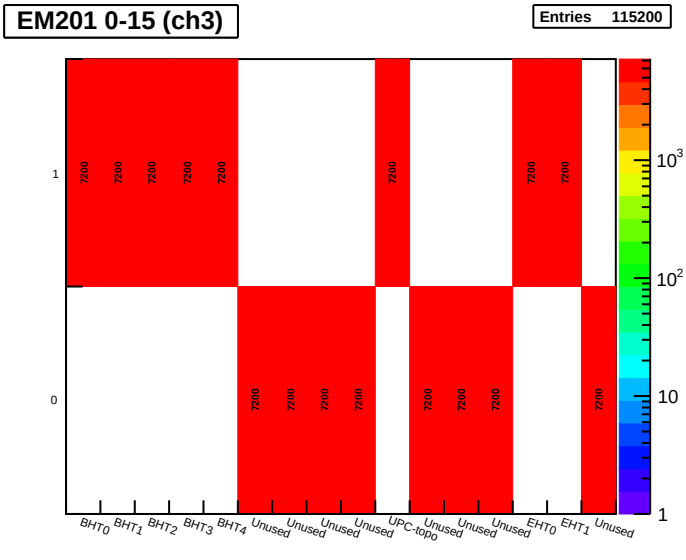

ST201 0-15 (ch6)
Entries 115200
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
7200
Laser Protection
Laser Fire
Laser Lamp
Laser Diode
Unused
Zerobias
Random
10^3
10^2
10
1

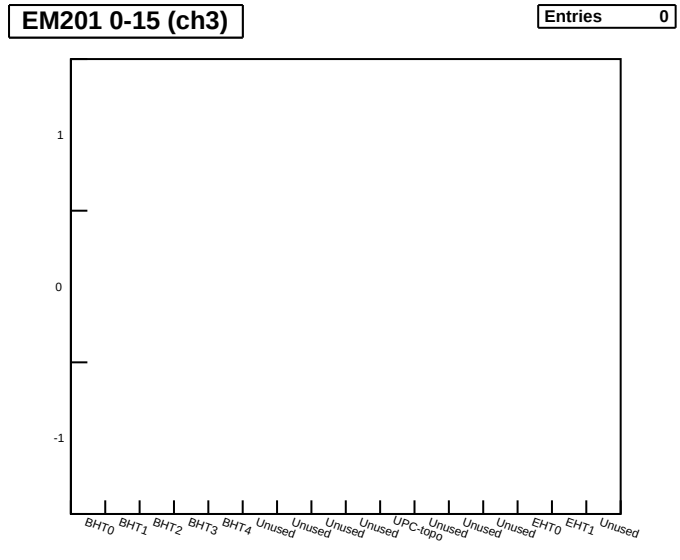

TF201 0-15 (ch0)
Entries 115200
1
0
7200 7054 7076 7066 4834 6617 6552 1846 7200 7200 7200
7200 7200 7200 146 124 134 2366 7200 583 648 5354 7200
Unused Unused Unused Unused TOFmult0 TOFmult1 TOFmult2 TOFmult3 Unused TOFsector0_3 TOFsector1_4 TOFsector2_5 Unused Unused Unused
10^3
10^2
10
1


TF201 0-15 (ch0)
Entries 0
1
0
-1
Unused Unused Unused Unused TOFmult0 TOFmult1 TOFmult2 TOFmult3 Unused TOFsector0_3 TOFsector1_4 TOFsector2_5 Unused Unused Unused


VT201 0-15 (ch1)
Entries 115200
1
0
2494 5936 5636 6476 7035 6990 124 544 509 279 5598 6680 1053 3084 2713
4706 1264 1564 724 165 210 7076 6656 6601 6921 7200 1602 520 6147 4116 4487
BBC-TAC BBC-E BBC-W EPD-TAC EPD-E EPD-W ZDC-TAC ZDC-E ZDC-W EPDmult0 EPDmult1 EPD-EWDiff EPD-EWDiff2 VPD-TAC VPD-E VPD-W
10^3
10^2
10
1


VT201 0-15 (ch1)
Entries 0
1
0
-1
BBC-TAC BBC-E BBC-W EPD-TAC EPD-E EPD-W ZDC-TAC ZDC-E ZDC-W EPDmult0 EPDmult1 EPD-EWDiff EPD-EWDiff2 VPD-TAC VPD-E VPD-W


EM201 0-15 (ch3)
Entries 115200
1
0
7200 7200 7200 7200 7200 7200 7200 7200
7200 7200 7200 7200 7200 7200 7200 7200
BHT0 BHT1 BHT2 BHT3 BHT4 Unused Unused Unused Unused UPC-topo Unused Unused Unused EHT0 EHT1 Unused
10^3
10^2
10
1


EM201 0-15 (ch3)
Entries 0
1
0
-1
BHT0 BHT1 BHT2 BHT3 BHT4 Unused Unused Unused Unused UPC-topo Unused Unused Unused EHT0 EHT1 Unused