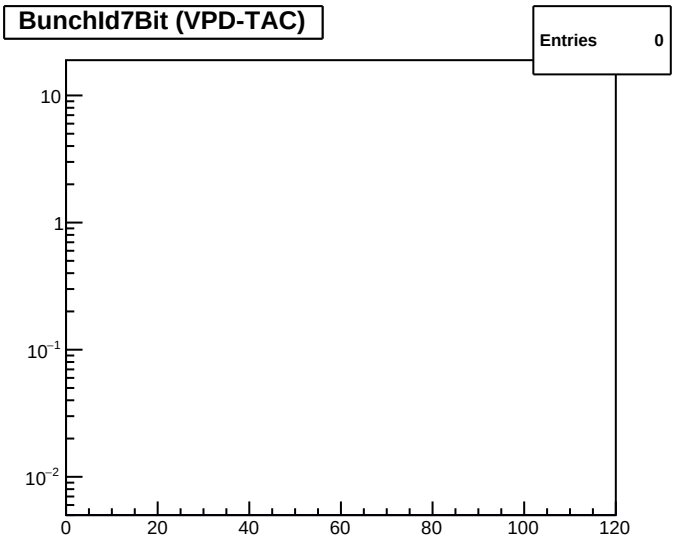
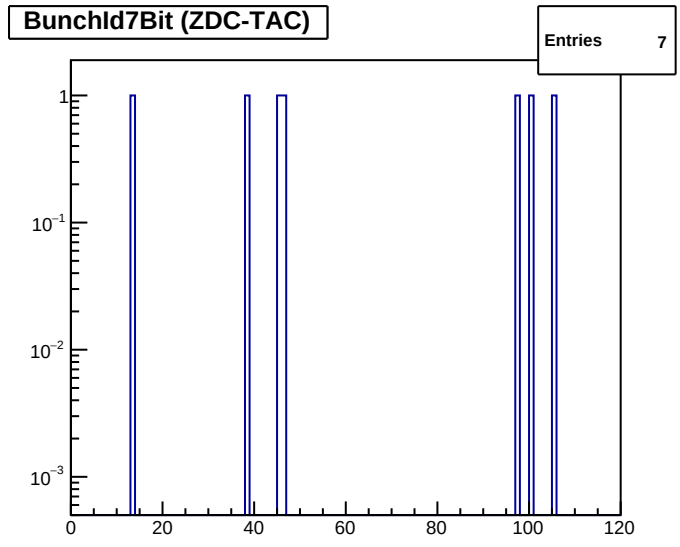
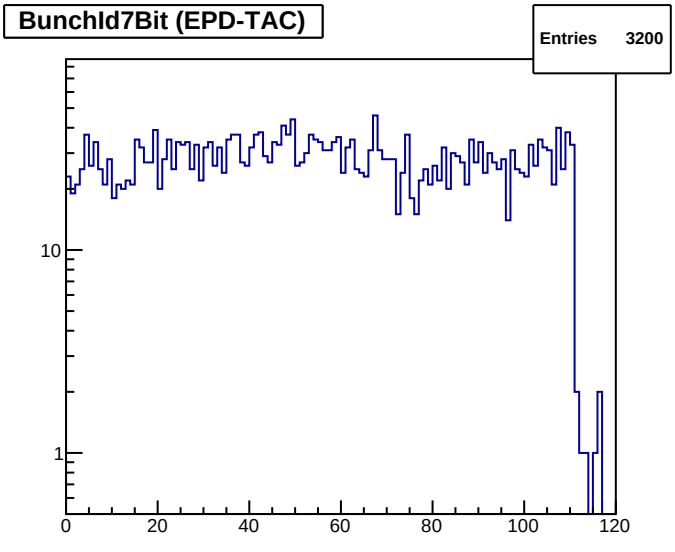
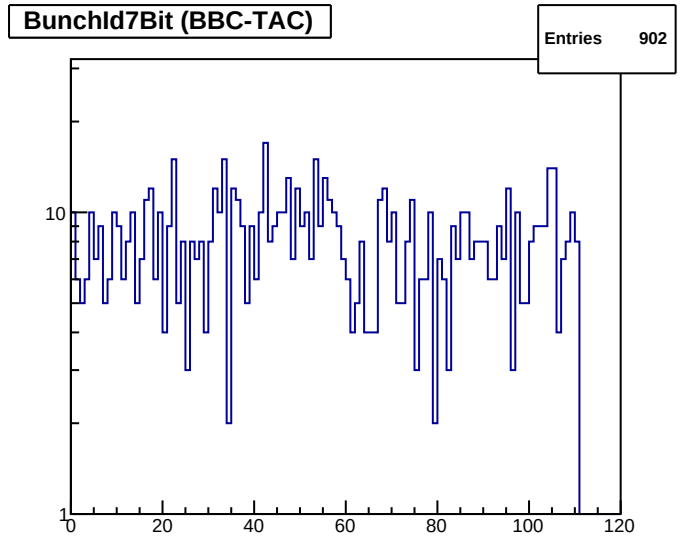
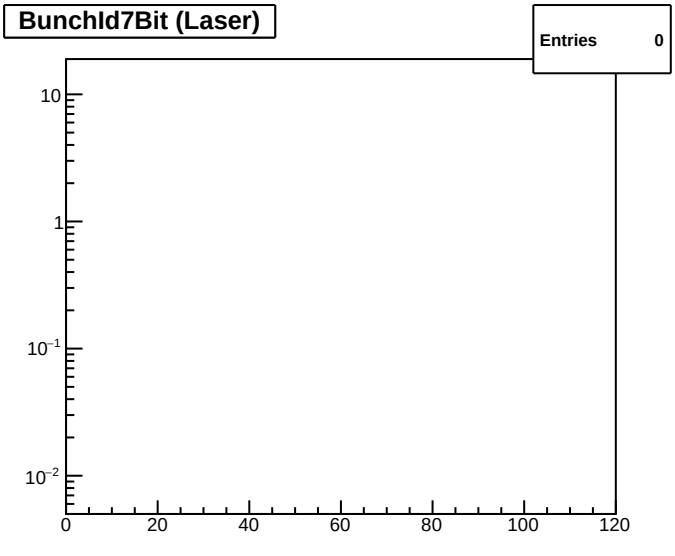
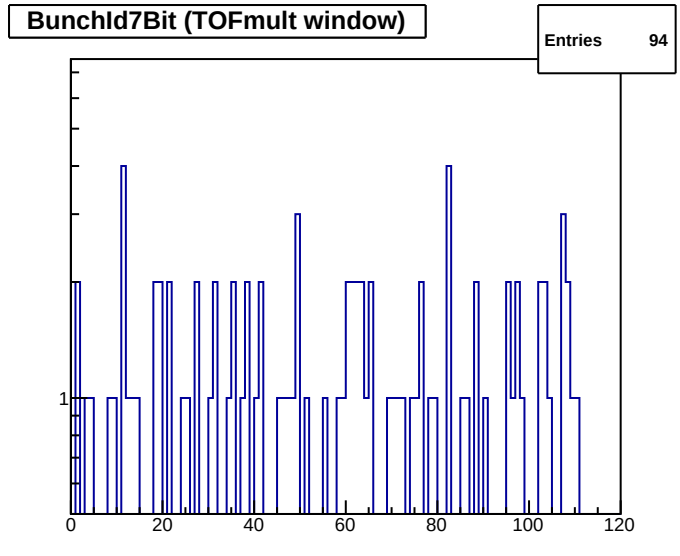

BunchId7Bit (VPD-TAC)
Entries 0
10
1
10^{-1}
10^{-2}
0 20 40 60 80 100 120

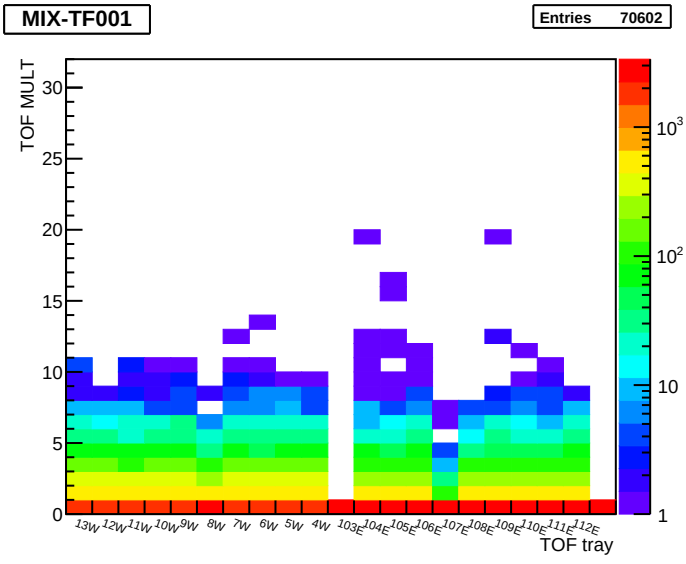

BunchId7Bit (ZDC-TAC)
Entries 7
1
10^{-1}
10^{-2}
10^{-3}
0 20 40 60 80 100 120

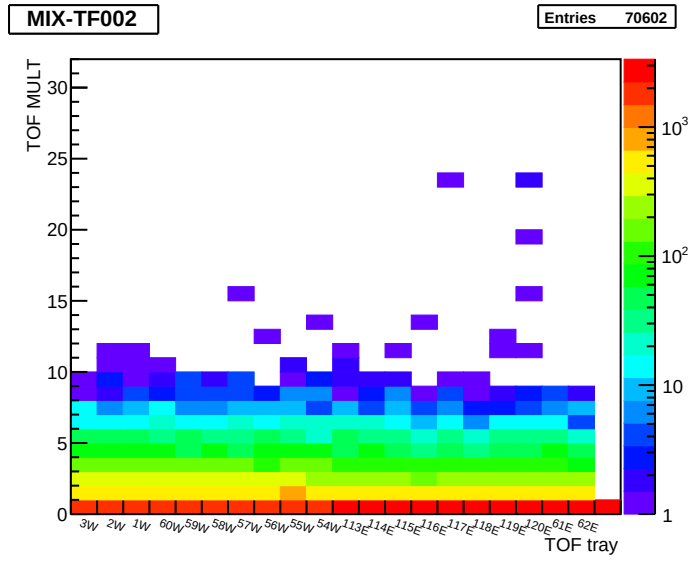

BunchId7Bit (EPD-TAC)
Entries 3200
10
1
0 20 40 60 80 100 120

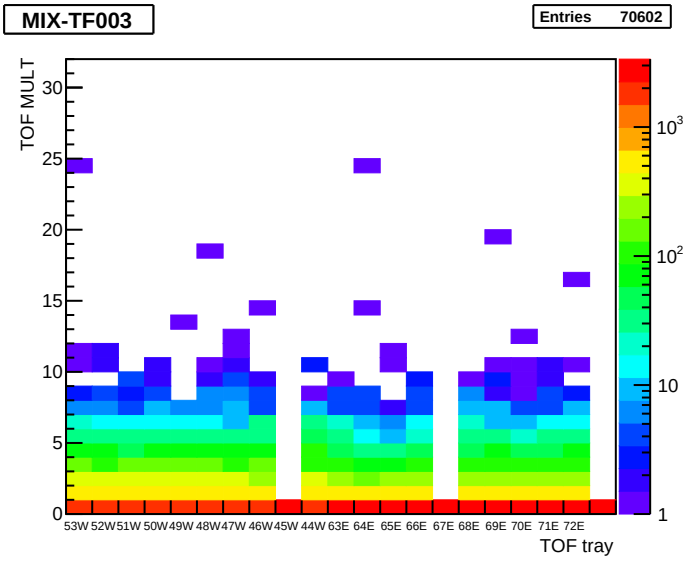

BunchId7Bit (BBC-TAC)
Entries 902
10
1
0 20 40 60 80 100 120

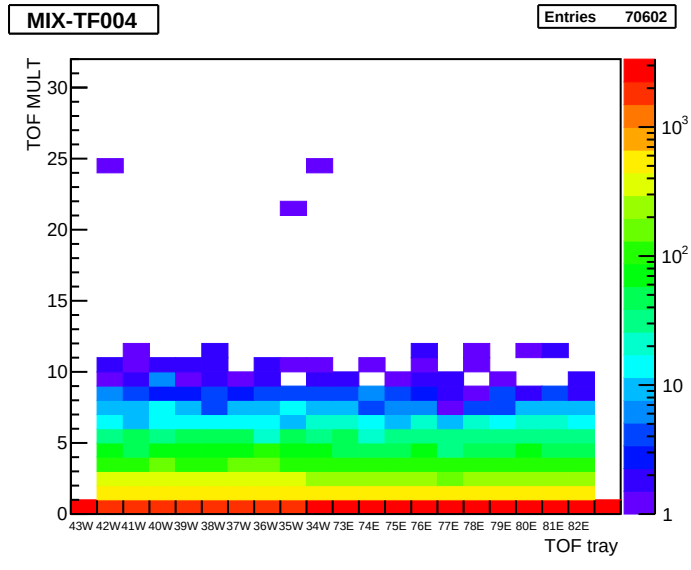

BunchId7Bit (Laser)
Entries 0
10
1
10^{-1}
10^{-2}
0 20 40 60 80 100 120

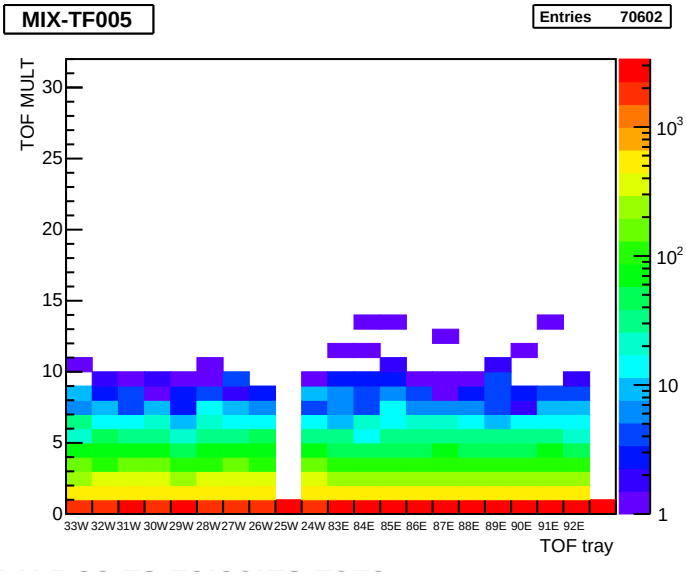

BunchId7Bit (TOFmult window)
Entries 94
1
0 20 40 60 80 100 120

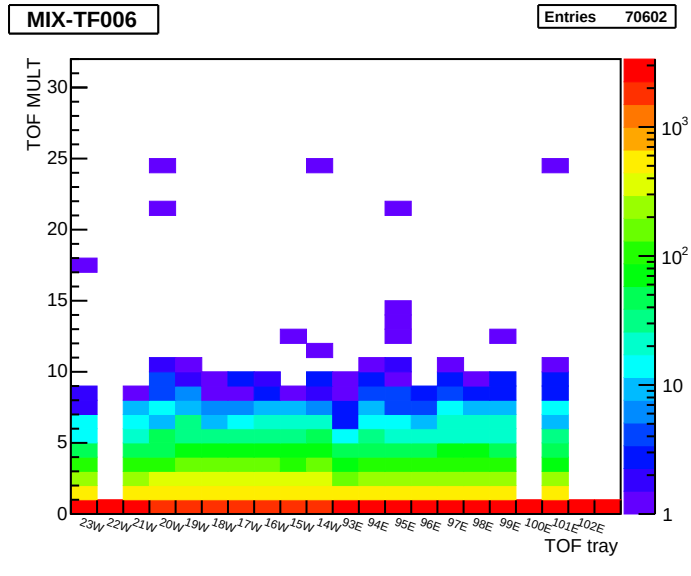

MIX-TF001
Entries 70602
TOF MULT
TOF tray

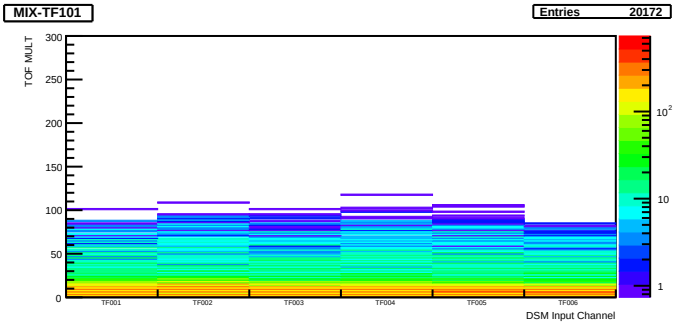

MIX-TF002
Entries 70602
TOF MULT
TOF tray

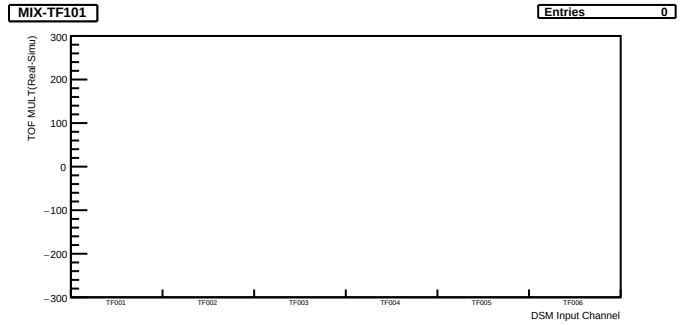

MIX-TF003
Entries 70602
TOF MULT
TOF tray

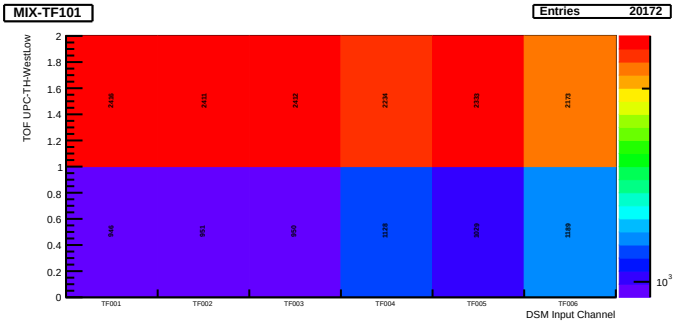

MIX-TF004
Entries 70602
TOF MULT
TOF tray

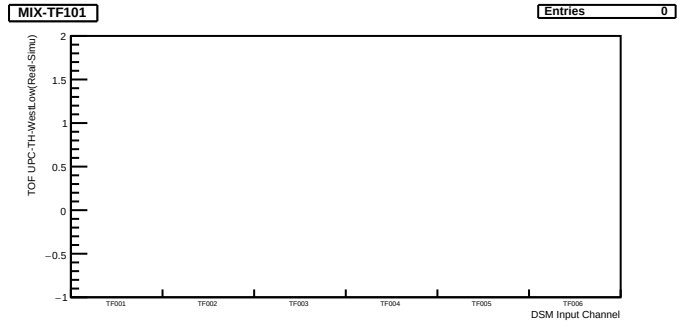

MIX-TF005
Entries 70602
TOF MULT
TOF tray

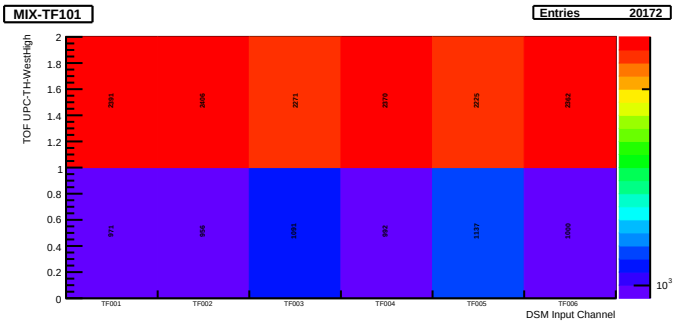

MIX-TF006
Entries 70602
TOF MULT
TOF tray

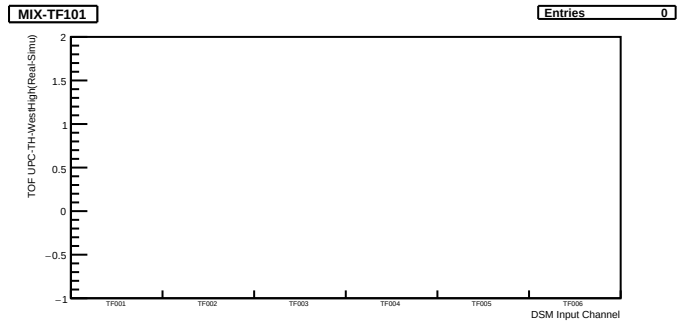

MIX-TF101
Entries 20172
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

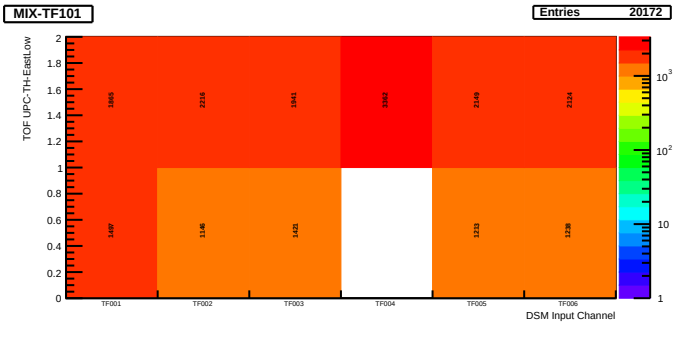

MIX-TF101
Entries 0
TOF MULT(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

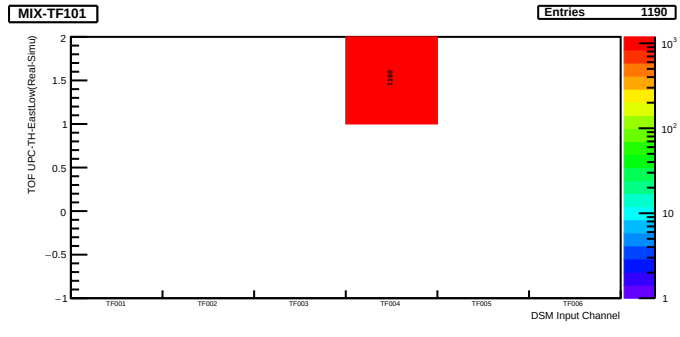

MIX-TF101
Entries 20172
TOF UPC-TH-WestLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

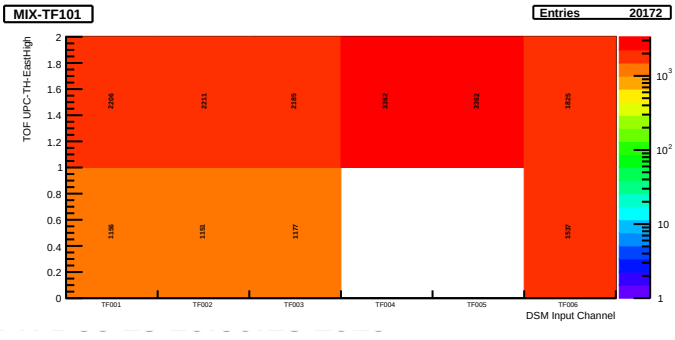

MIX-TF101
Entries 0
TOF UPC-TH-WestLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

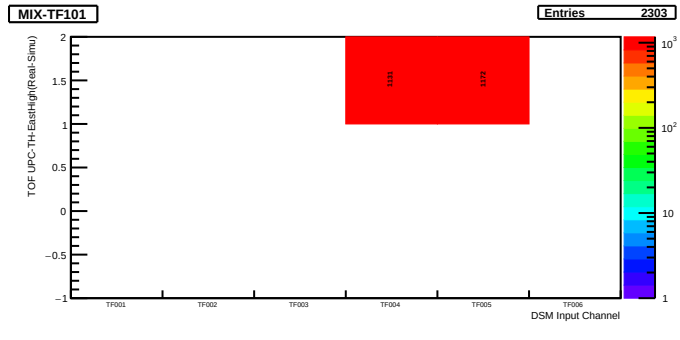

MIX-TF101
Entries 20172
TOF UPC-TH-WestHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

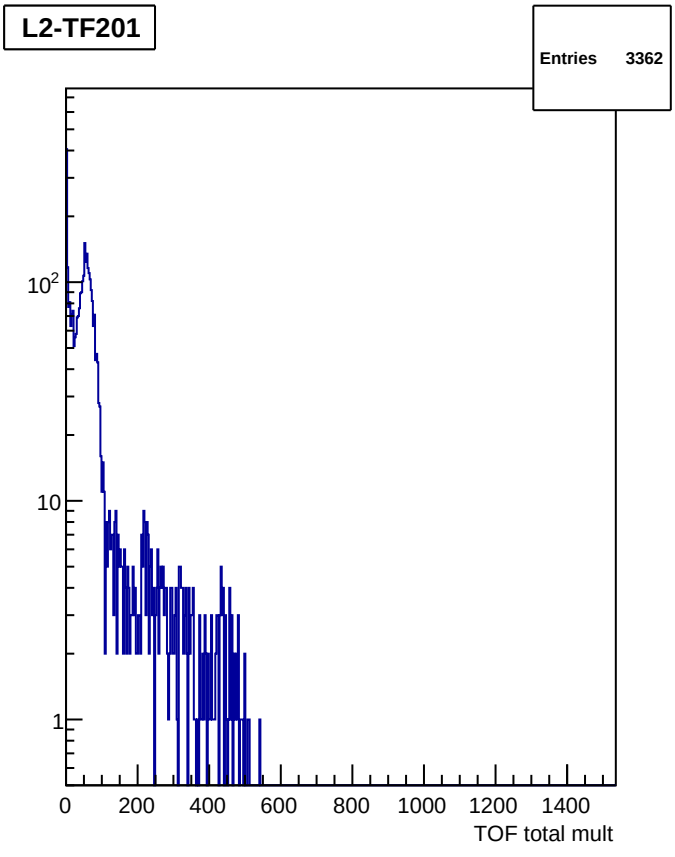

MIX-TF101
Entries 0
TOF UPC-TH-WestHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

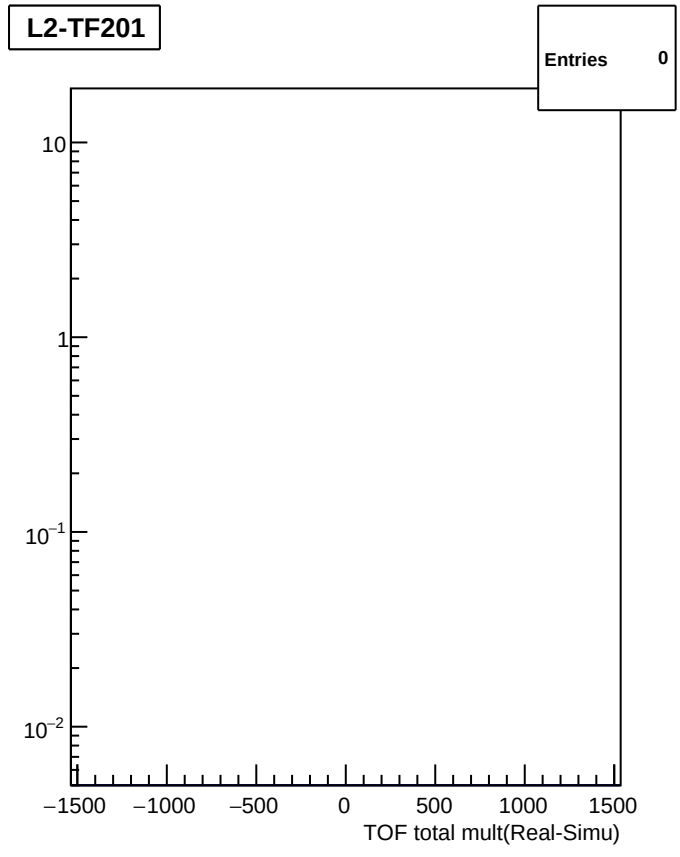

MIX-TF101
Entries 20172
TOF UPC-TH-EastLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

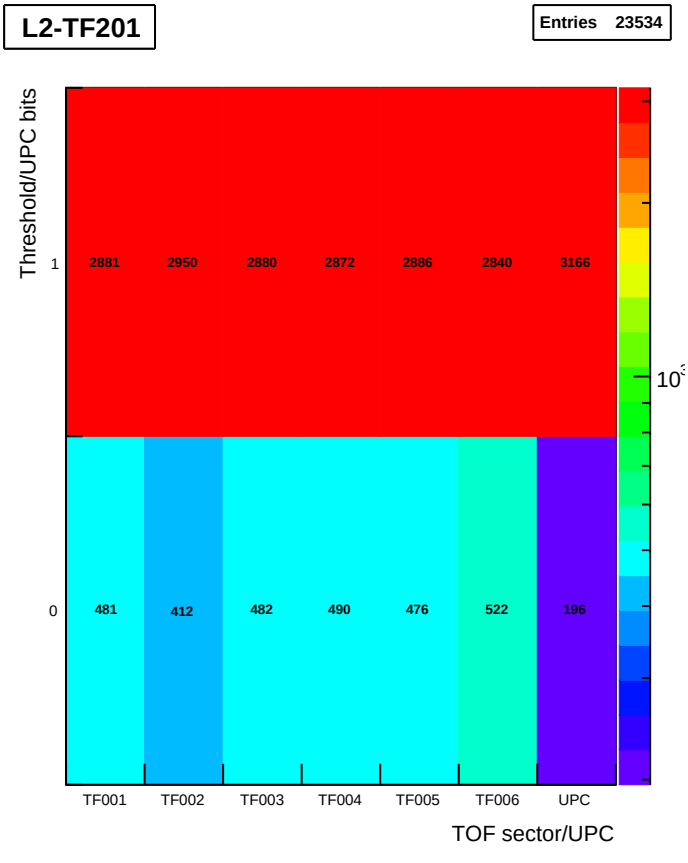

MIX-TF101
Entries 1190
TOF UPC-TH-EastLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

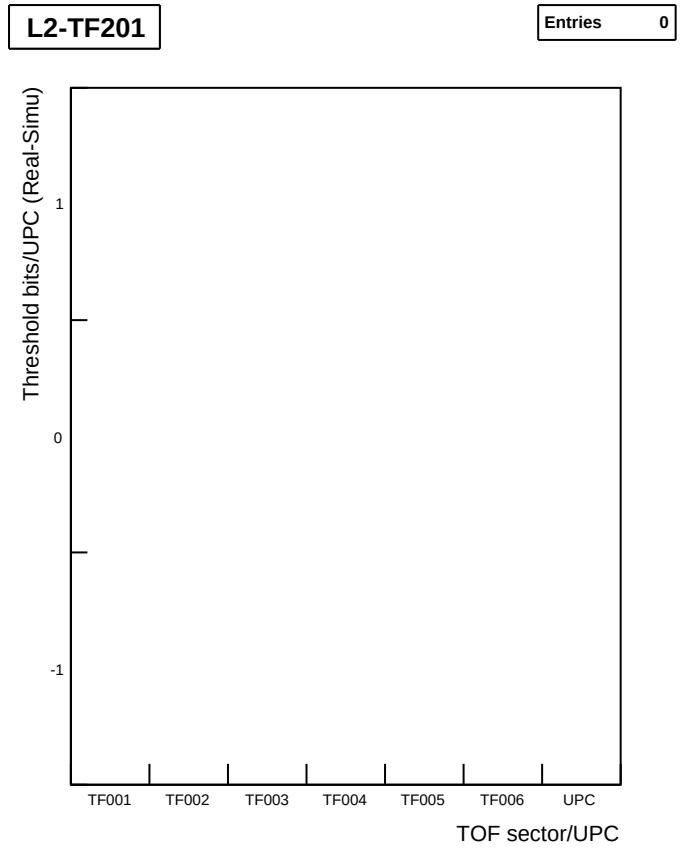

MIX-TF101
Entries 20172
TOF UPC-TH-EastHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

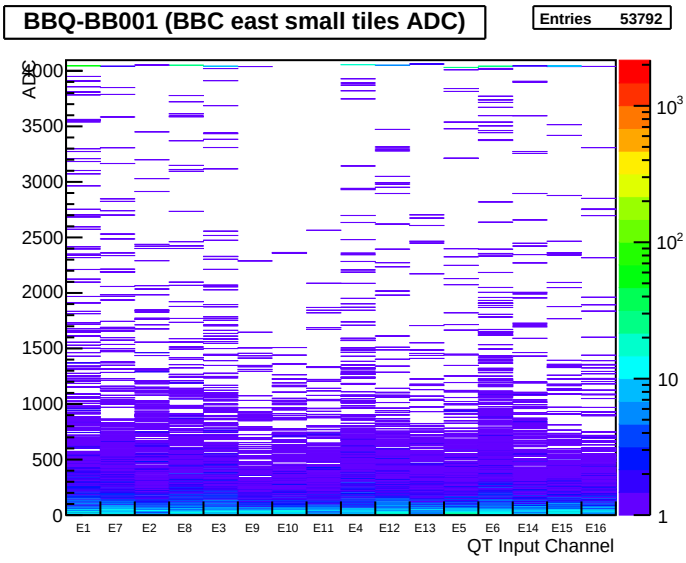

MIX-TF101
Entries 2303
TOF UPC-TH-EastHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

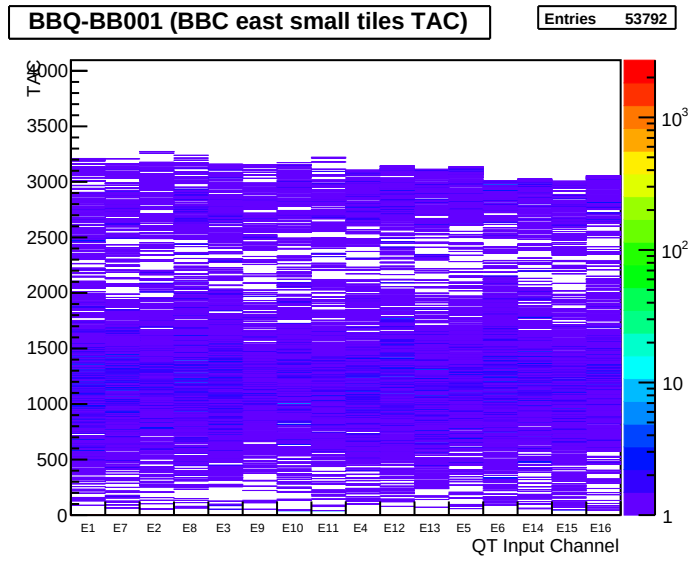

L2-TF201
Entries 3362
10^2
10
1
0
200
400
600
800
1000
1200
1400
TOF total mult

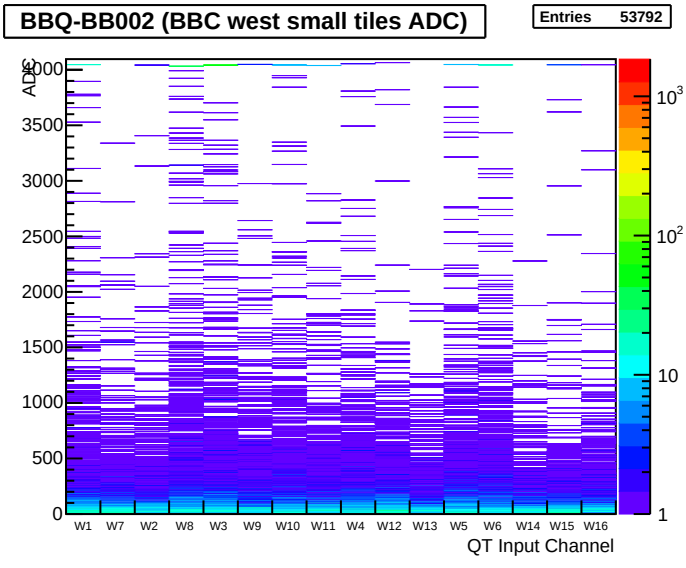

L2-TF201
Entries 0
10
1
10^{-1}
10^{-2}
−1500
−1000
−500
0
500
1000
1500
TOF total mult(Real-Simu)

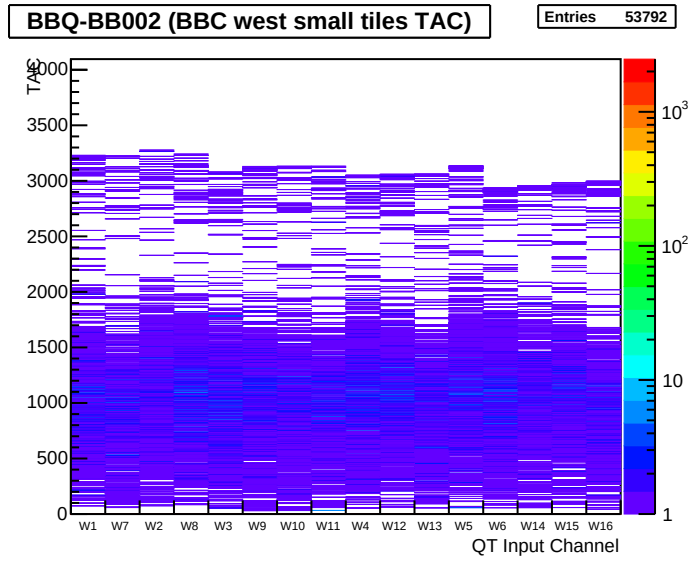

L2-TF201
Entries 23534
Threshold/UPC bits
1
0
2881
2950
2880
2872
2886
2840
3166
481
412
482
490
476
522
196
TF001
TF002
TF003
TF004
TF005
TF006
UPC
TOF sector/UPC

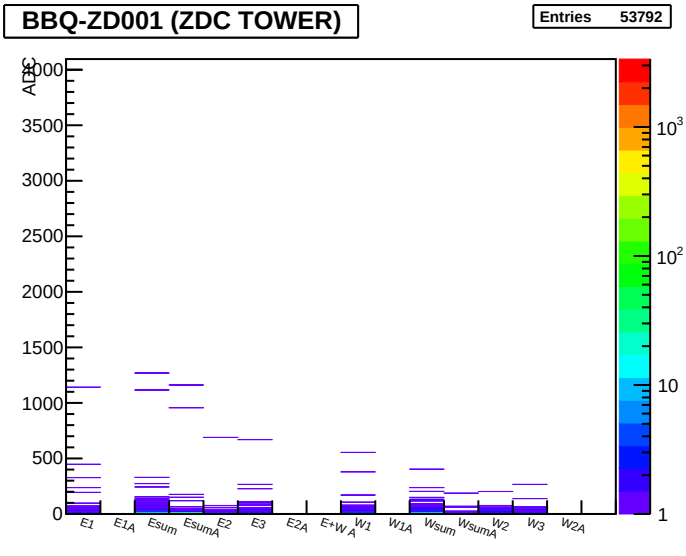

L2-TF201
Entries 0
Threshold bits/UPC (Real-Simu)
1
0
-1
TF001
TF002
TF003
TF004
TF005
TF006
UPC
TOF sector/UPC

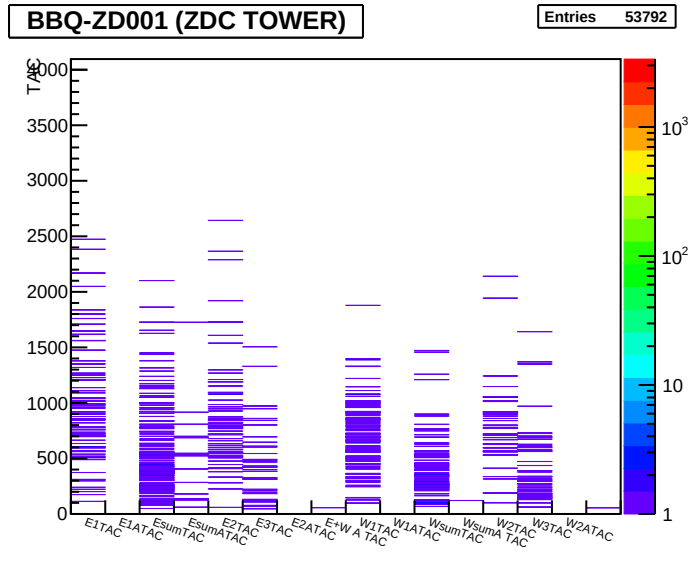

BBQ-BB001 (BBC east small tiles ADC)
Entries 53792
ADC
QT Input Channel

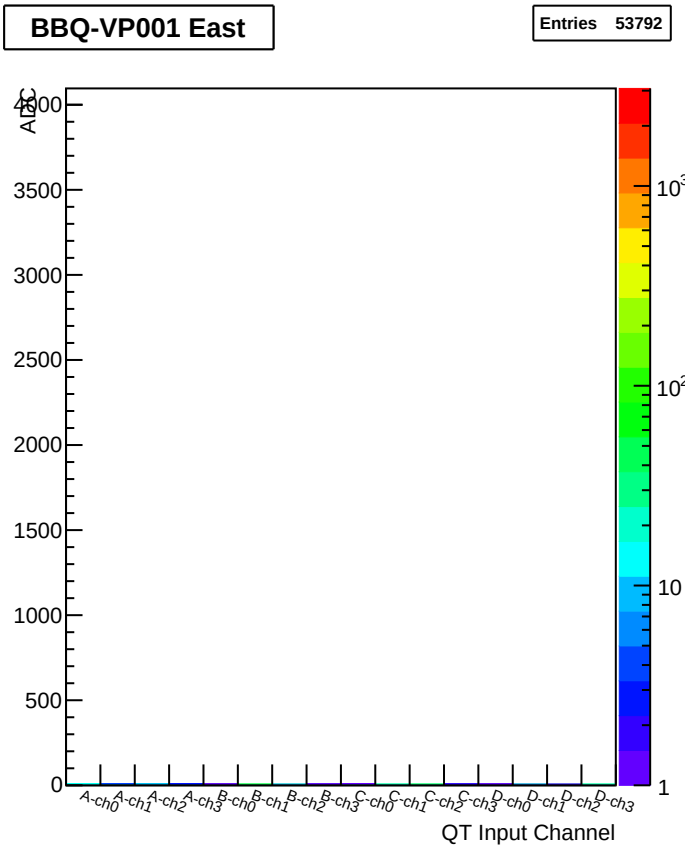

BBQ-BB001 (BBC east small tiles TAC)
Entries 53792
TAC
QT Input Channel

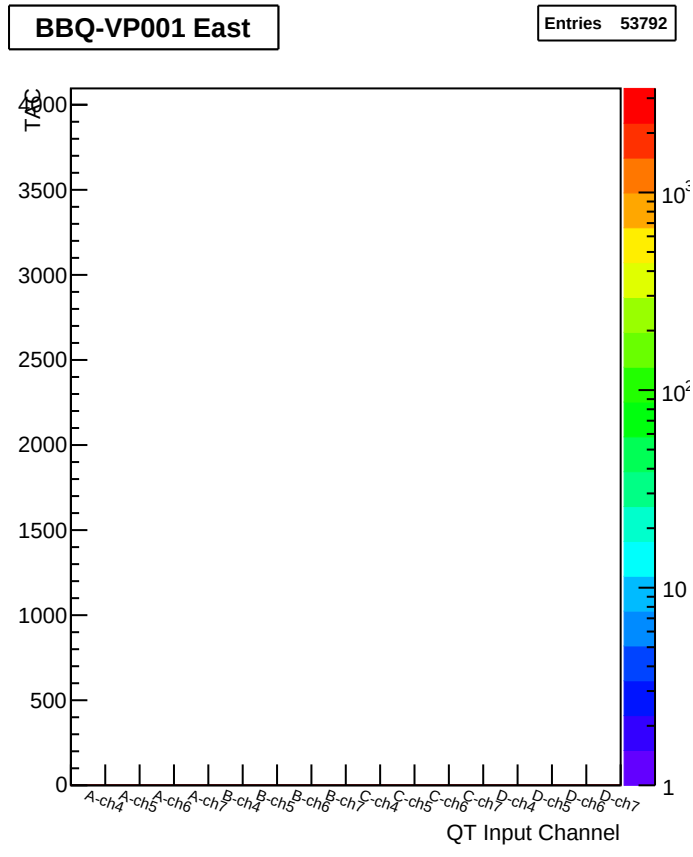

BBQ-BB002 (BBC west small tiles ADC)
Entries 53792
ADC
QT Input Channel

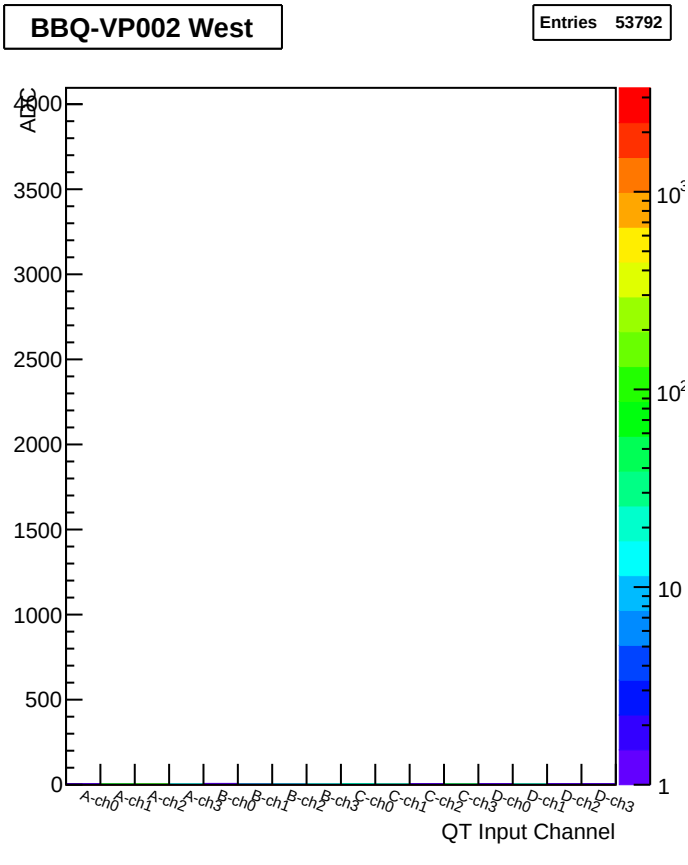

BBQ-BB002 (BBC west small tiles TAC)
Entries 53792
TAC
QT Input Channel

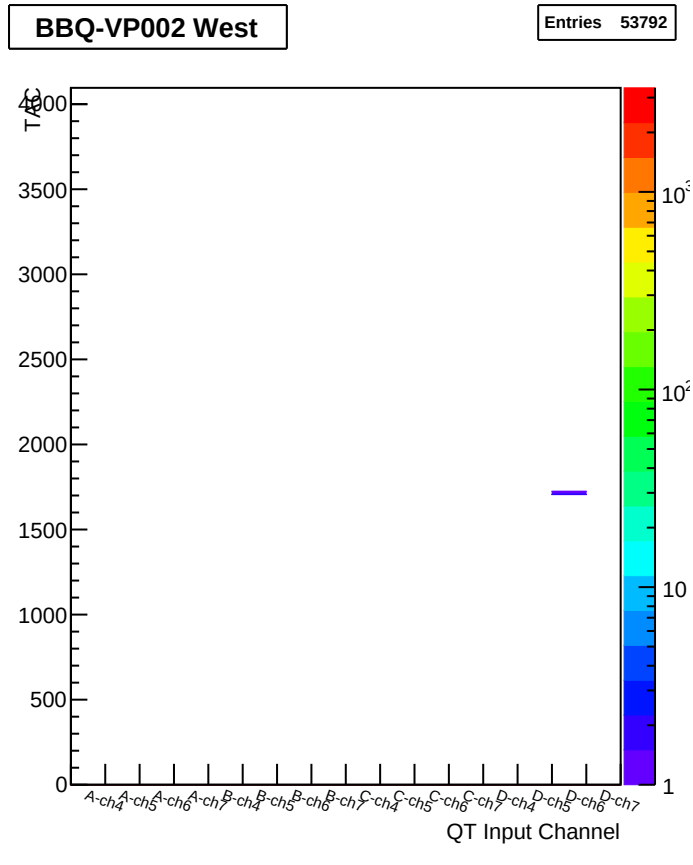

BBQ-ZD001 (ZDC TOWER)
Entries 53792
ADC

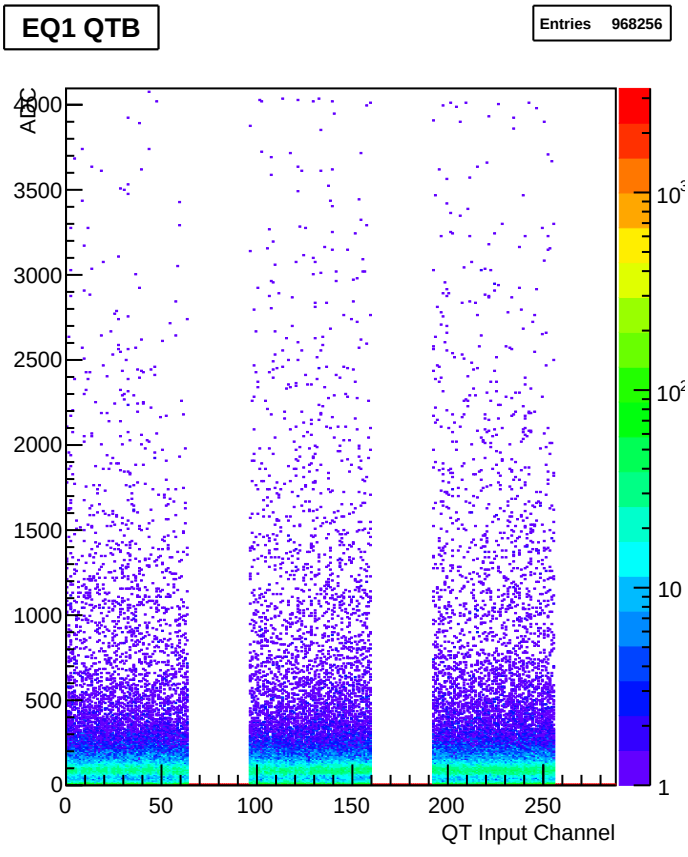

BBQ-ZD001 (ZDC TOWER)
Entries 53792
TAC

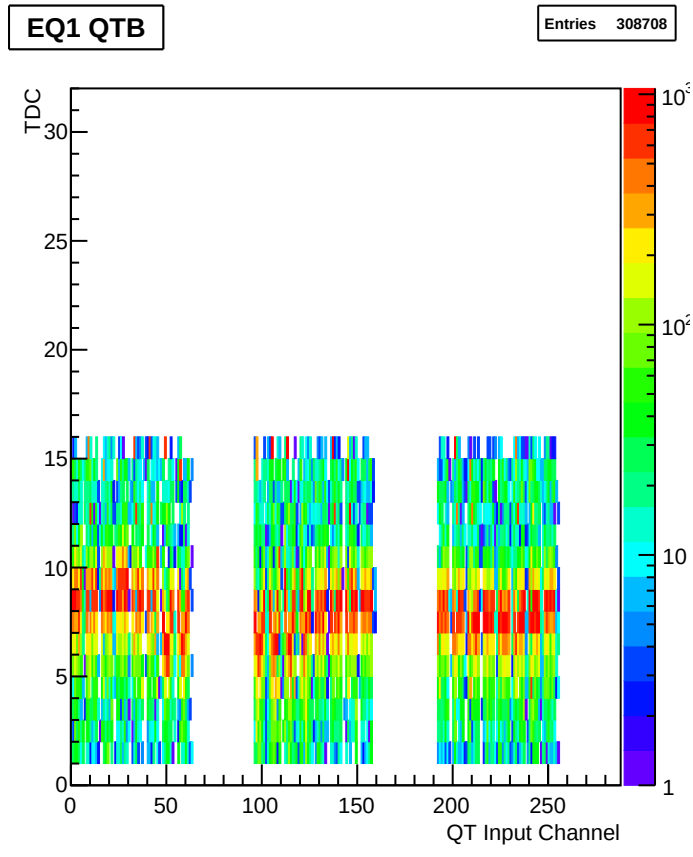

BBQ-VP001 East
Entries 53792
ADC
QT Input Channel

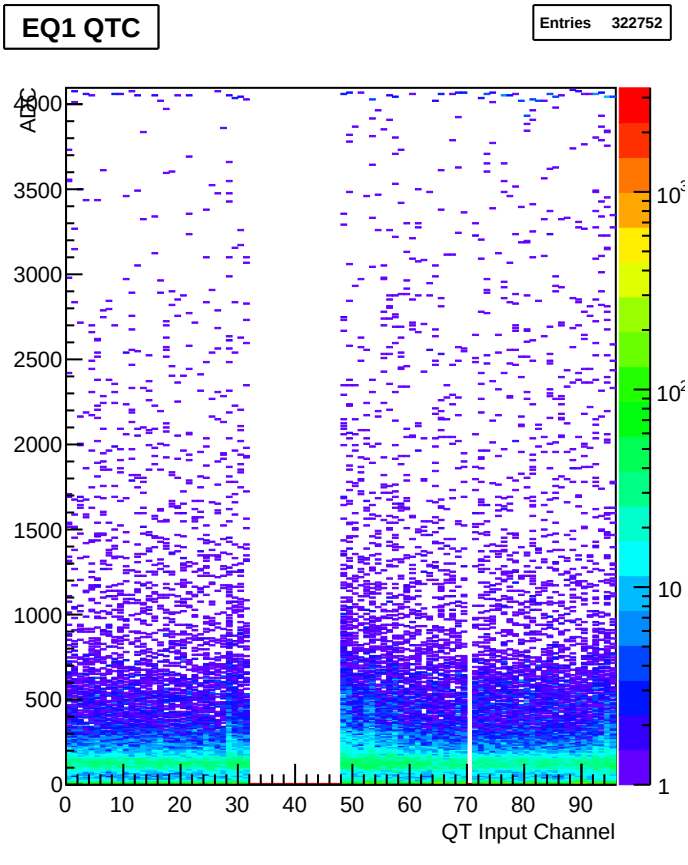

BBQ-VP001 East
Entries 53792
TADC
QT Input Channel

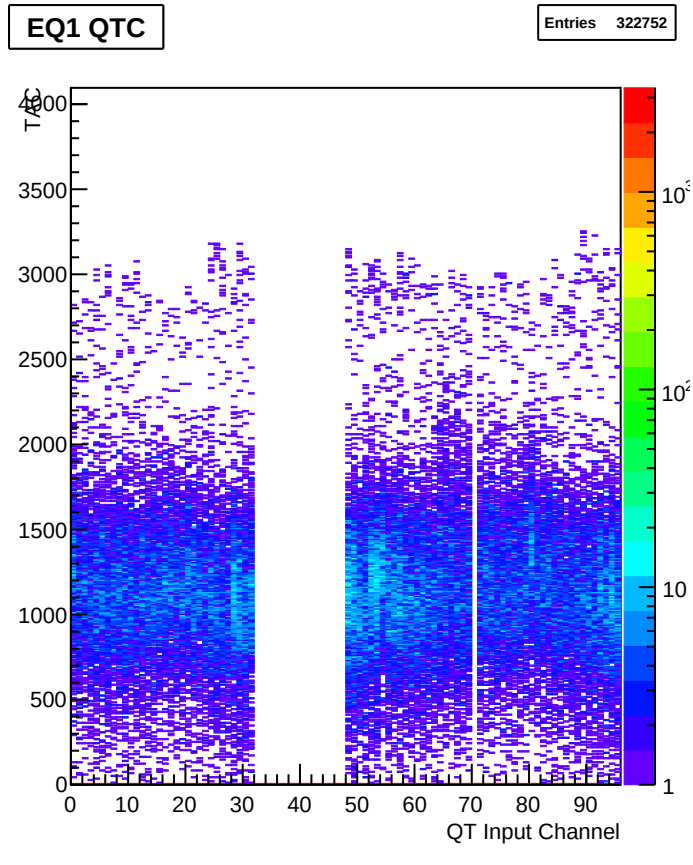

BBQ-VP002 West
Entries 53792
ADC
QT Input Channel

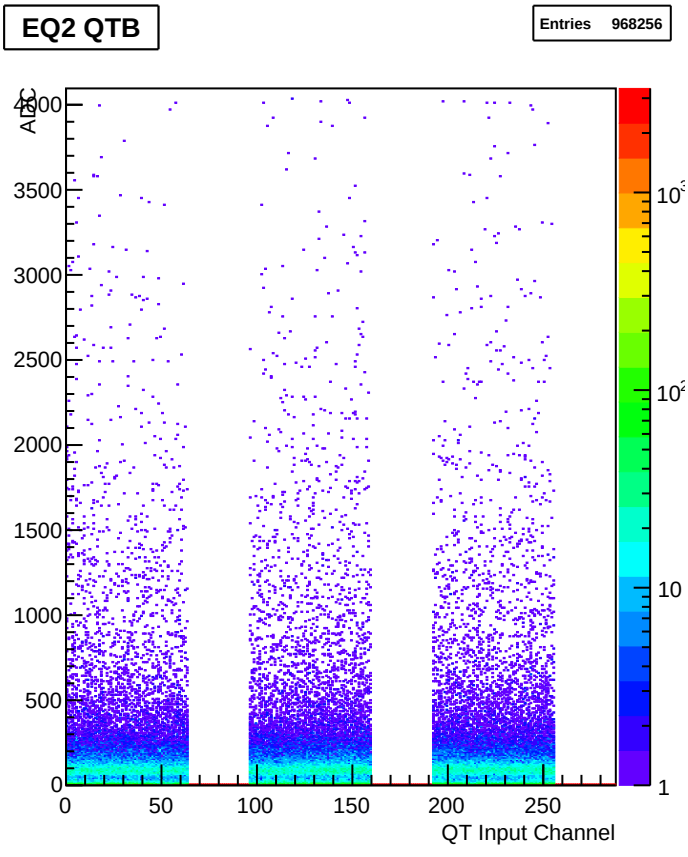

BBQ-VP002 West
Entries 53792
TADC
QT Input Channel

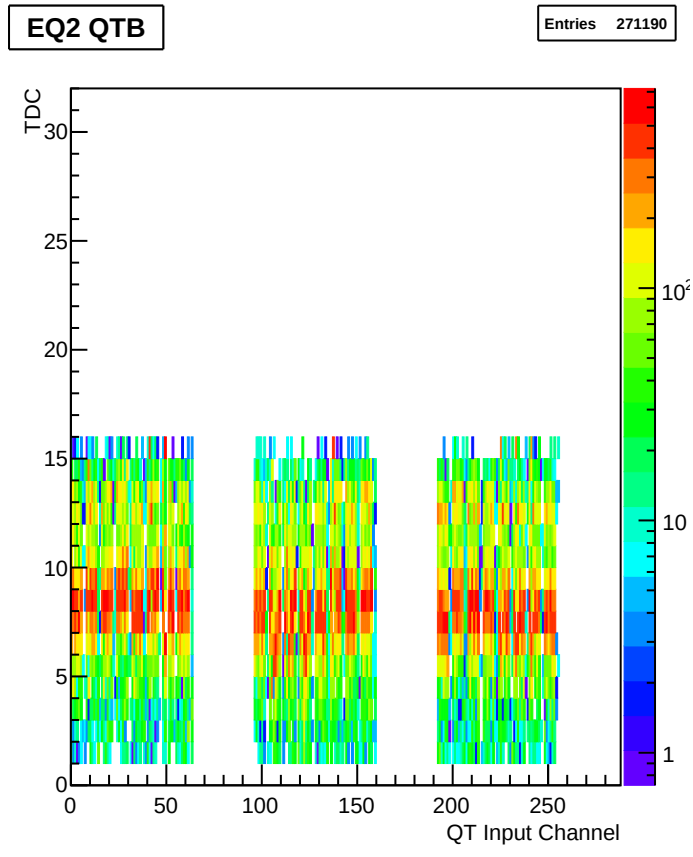

EQ1 QTB
Entries 968256
ADC
QT Input Channel

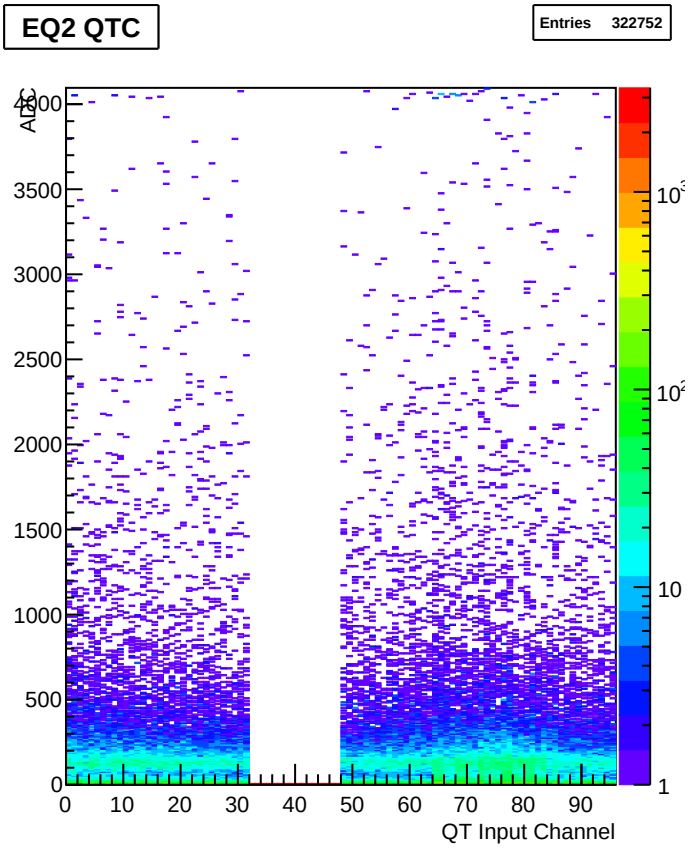

EQ1 QTB
Entries 308708
TDC
QT Input Channel

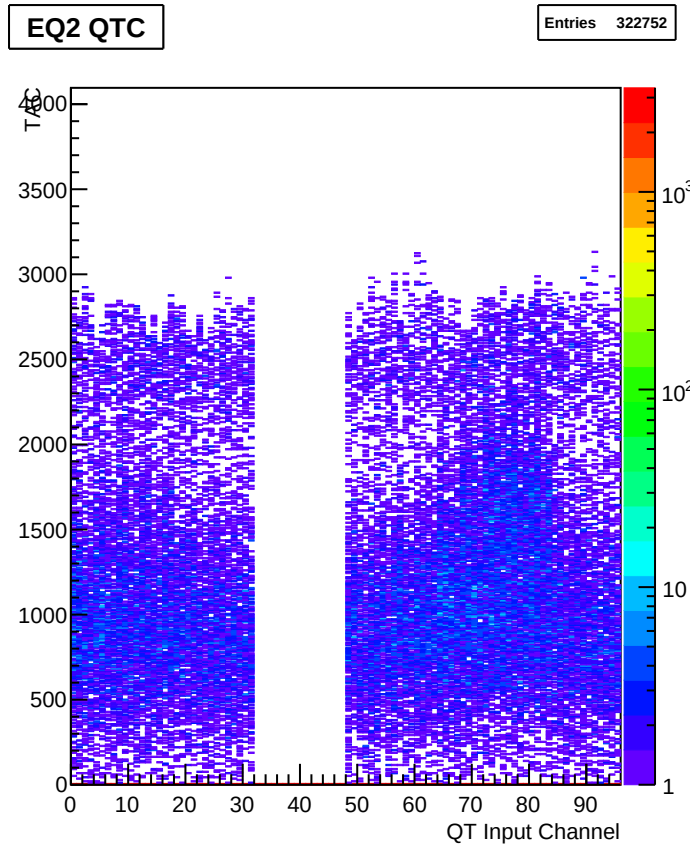

EQ1 QTC
Entries 322752
ADC
QT Input Channel

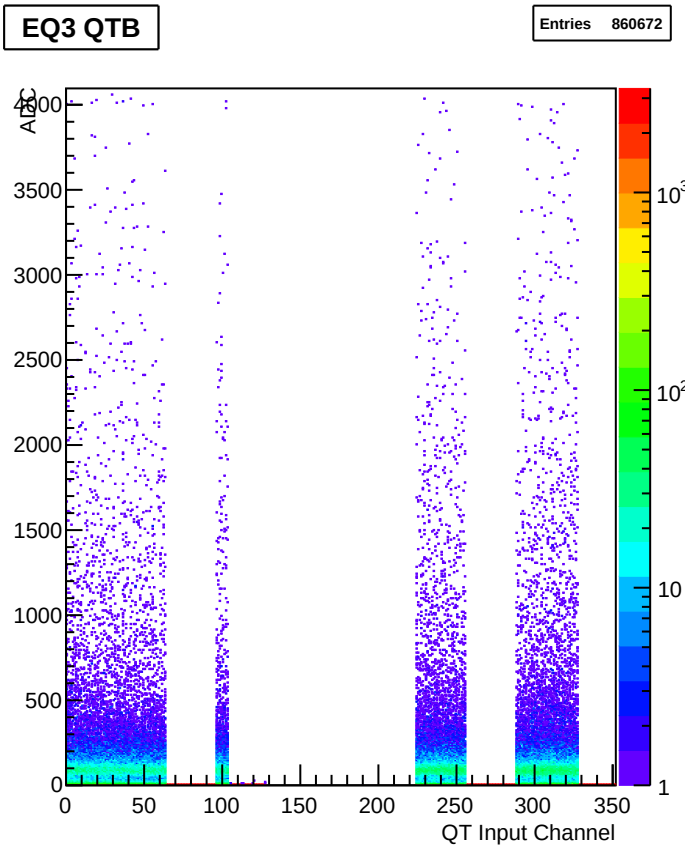

EQ1 QTC
Entries 322752
TADC
QT Input Channel

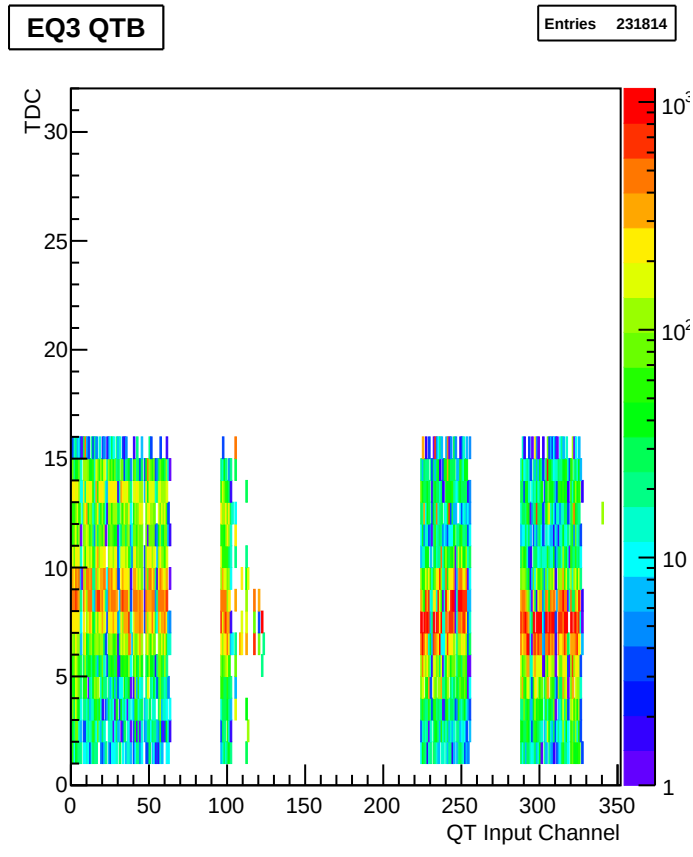

EQ2 QTB
Entries 968256
ADC
QT Input Channel

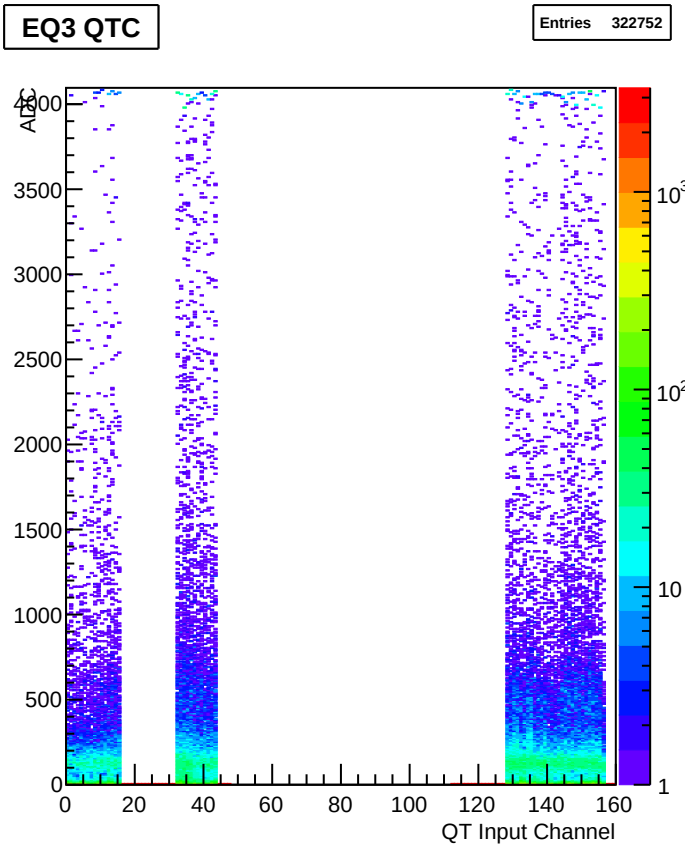

EQ2 QTB
Entries 271190
TDC
QT Input Channel

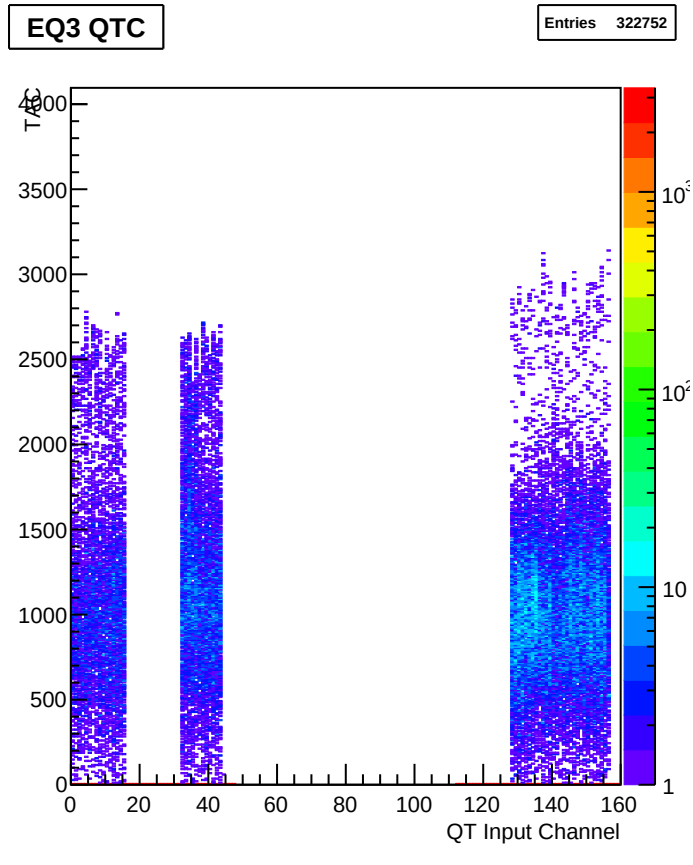

EQ2 QTC
Entries 322752
ADC
QT Input Channel

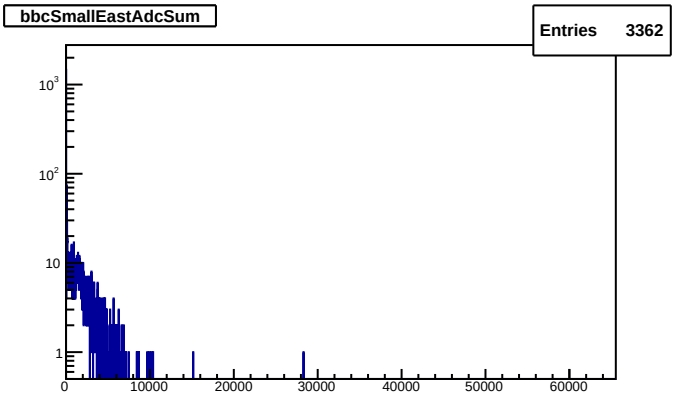

EQ2 QTC
Entries 322752
TADC
QT Input Channel

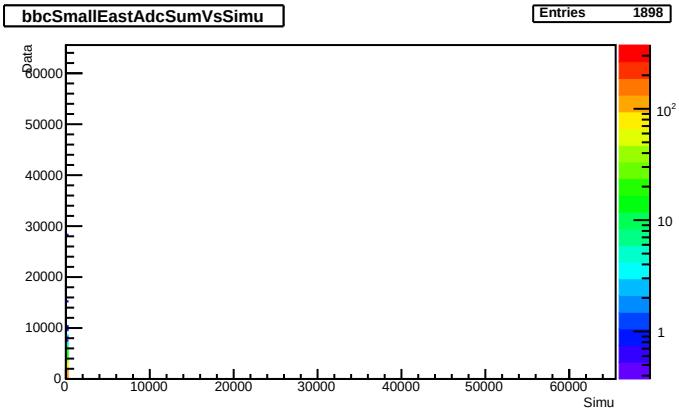

EQ3 QTB
Entries 860672
ADC
QT Input Channel

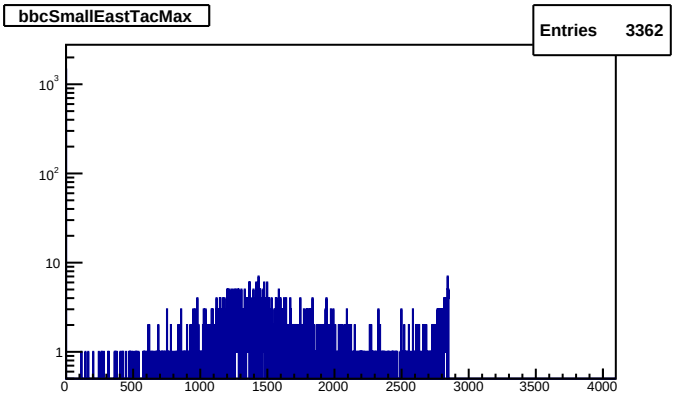

EQ3 QTB
Entries 231814
TDC
QT Input Channel

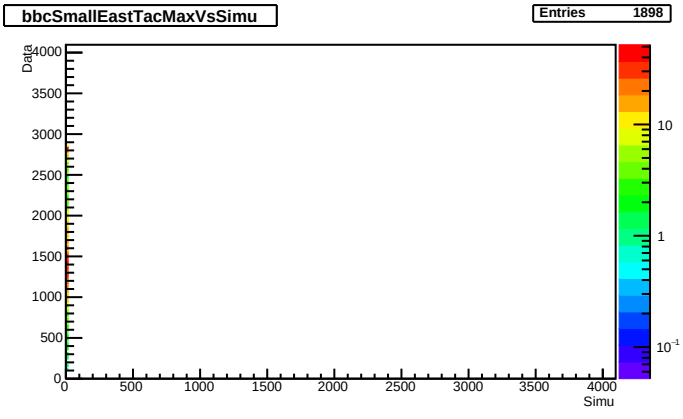

EQ3 QTC
Entries 322752
ADC
QT Input Channel

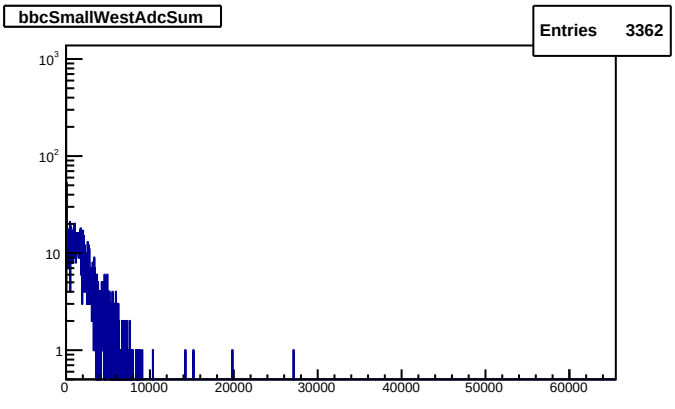

EQ3 QTC
Entries 322752
TADC
QT Input Channel

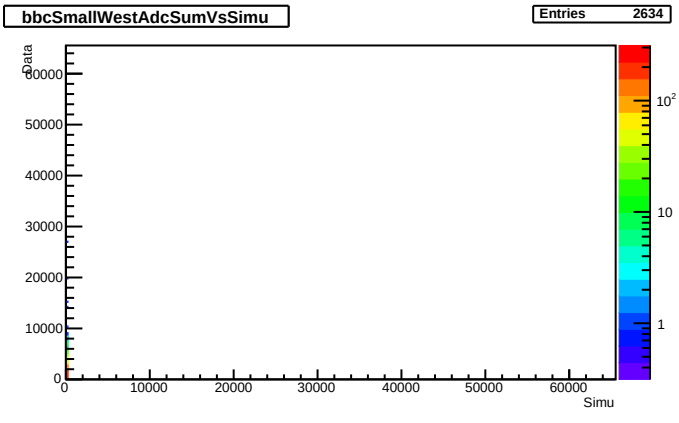

bbcSmallEastAdcSum
Entries 3362

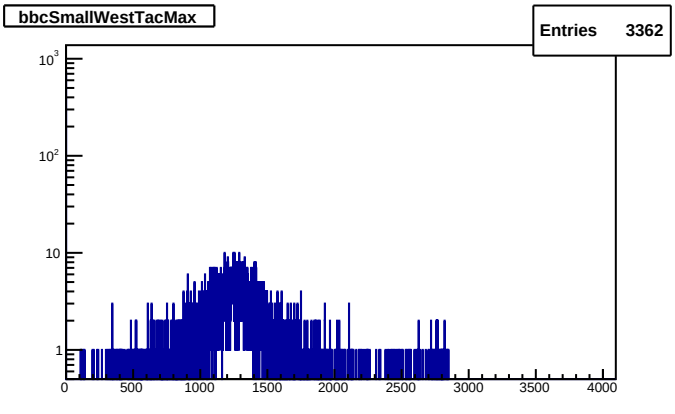

bbcSmallEastAdcSumVsSimu
Entries 1898
Data
Simu

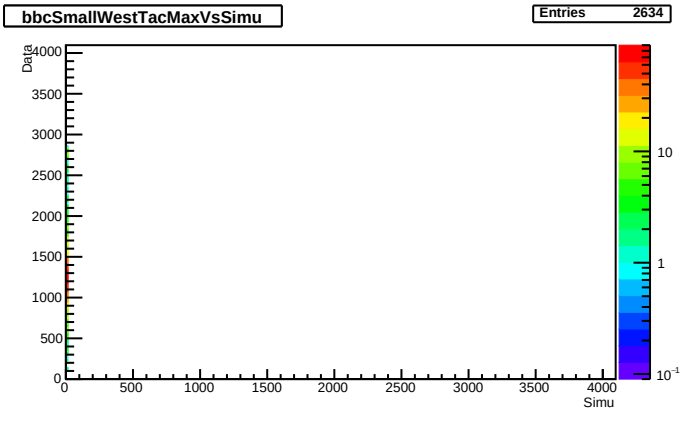

bbcSmallEastTacMax
Entries 3362

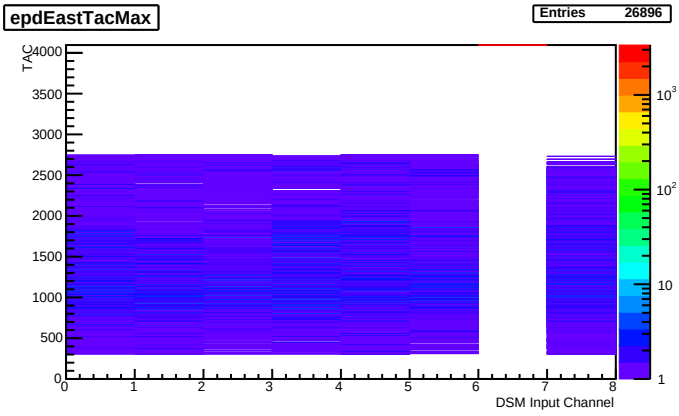

bbcSmallEastTacMaxVsSimu
Entries 1898
Data
Simu

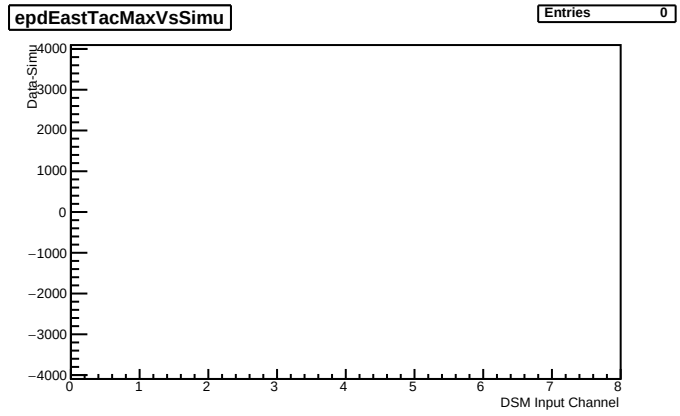

bbcSmallWestAdcSum
Entries 3362

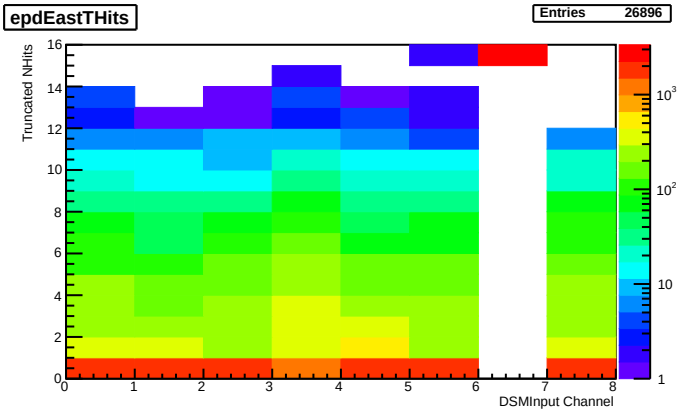

bbcSmallWestAdcSumVsSimu
Entries 2634
Data
Simu

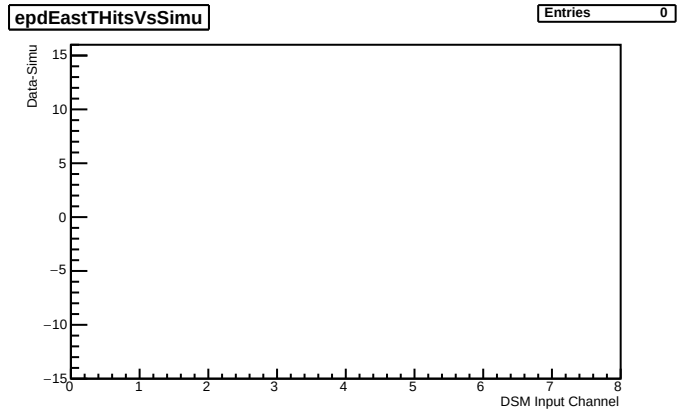

bbcSmallWestTacMax
Entries 3362

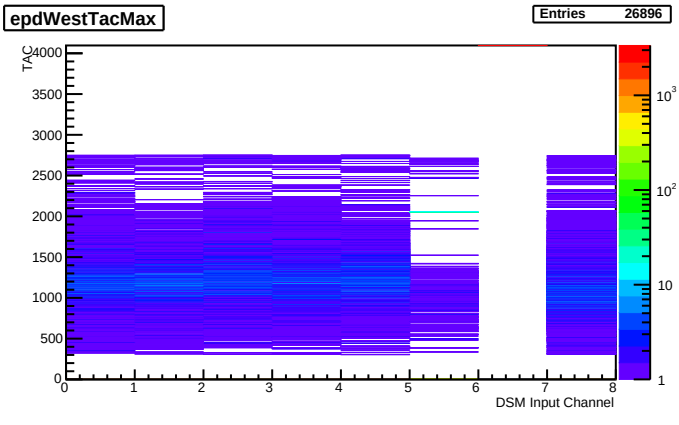

bbcSmallWestTacMaxVsSimu
Entries 2634
Data
Simu

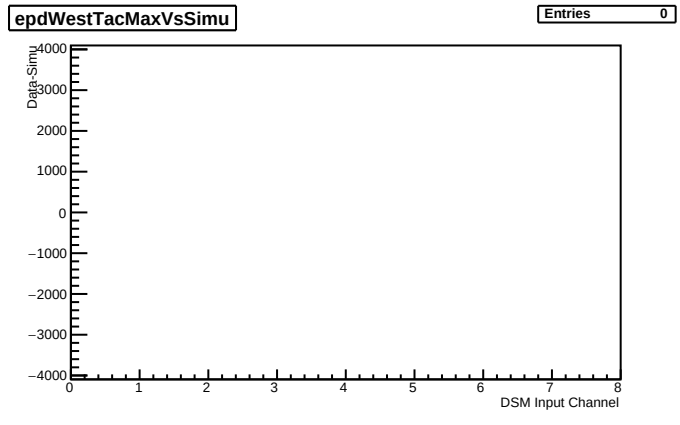

epdEastTacMax
Entries 26896
TAC
DSM Input Channel

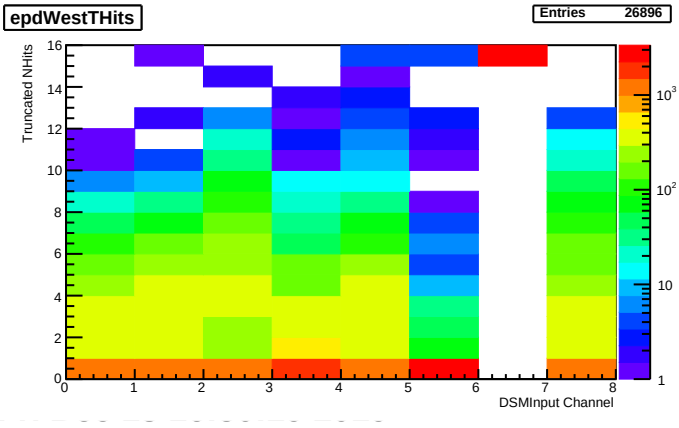

epdEastTacMaxVsSimu
Entries 0
Data-Simu
DSM Input Channel

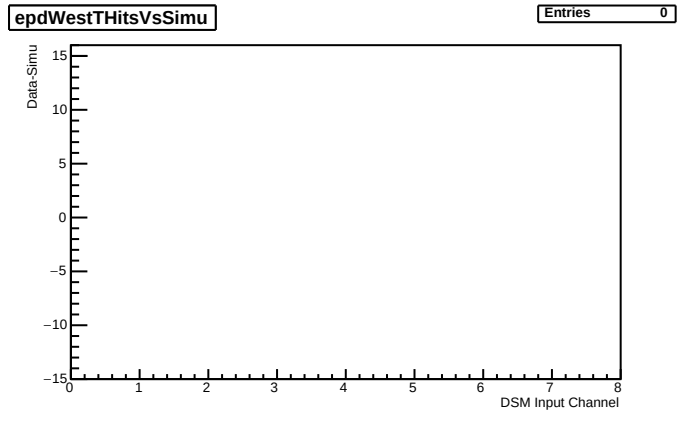

epdEastTHits
Entries 26896
Truncated NHits
DSMInput Channel

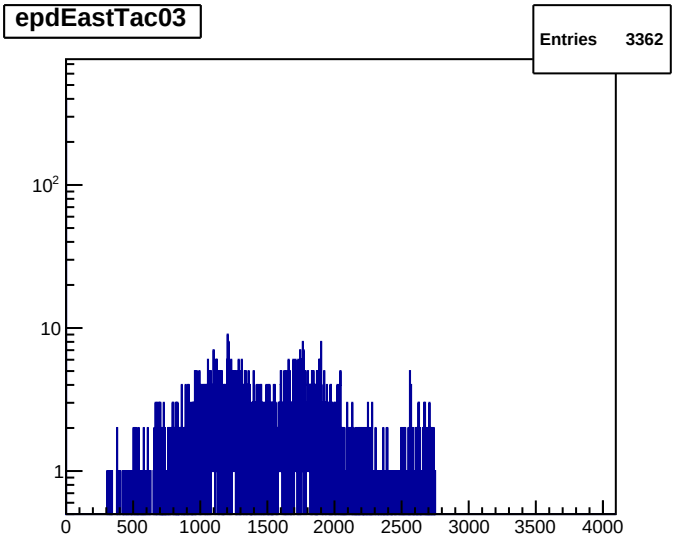

epdEastTHitsVsSimu
Entries 0
Data-Simu
DSM Input Channel

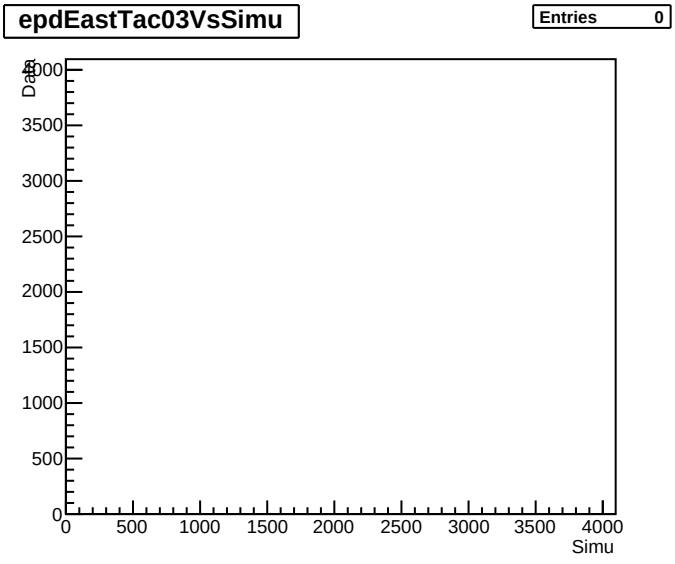

epdWestTacMax
Entries 26896
TAC
DSM Input Channel

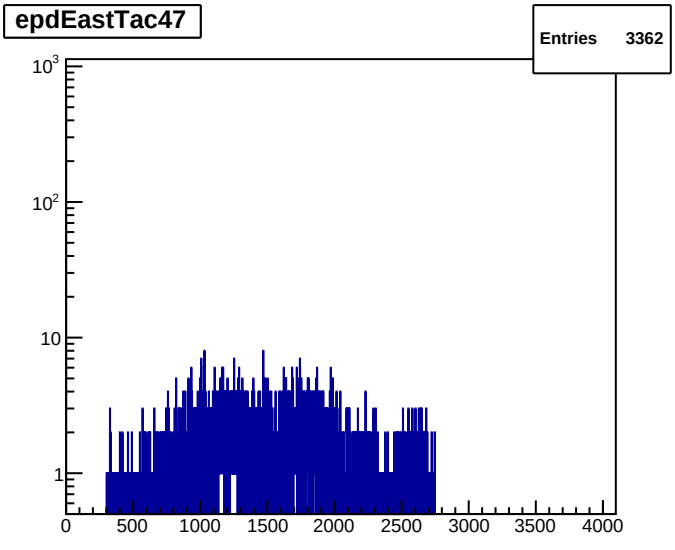

epdWestTacMaxVsSimu
Entries 0
Data-Simu
DSM Input Channel

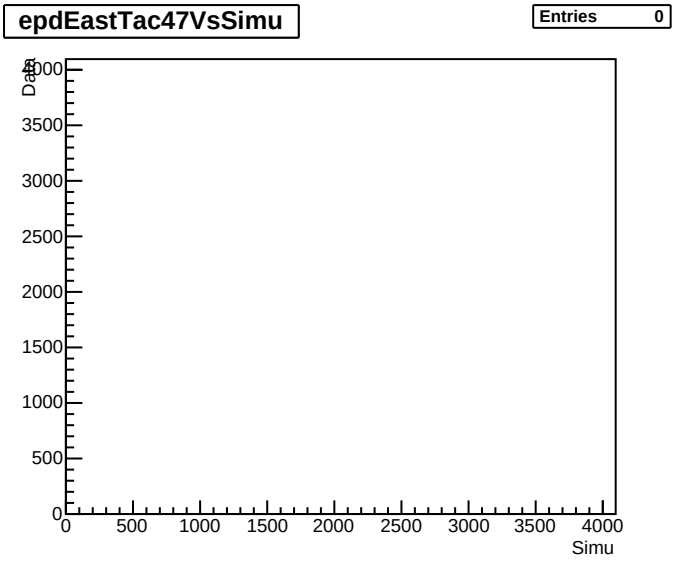

epdWestTHits
Entries 26896
Truncated NHits
DSMInput Channel


epdWestTHitsVsSimu
Entries 0
Data-Simu
DSM Input Channel


epdEastTac03
Entries 3362
10^2
10
1
0 500 1000 1500 2000 2500 3000 3500 4000


epdEastTac03VsSimu
Entries 0
Data
4000 3500 3000 2500 2000 1500 1000 500 0
0 500 1000 1500 2000 2500 3000 3500 4000
Simu


epdEastTac47
Entries 3362
10^3
10^2
10
1
0 500 1000 1500 2000 2500 3000 3500 4000


epdEastTac47VsSimu
Entries 0
Data
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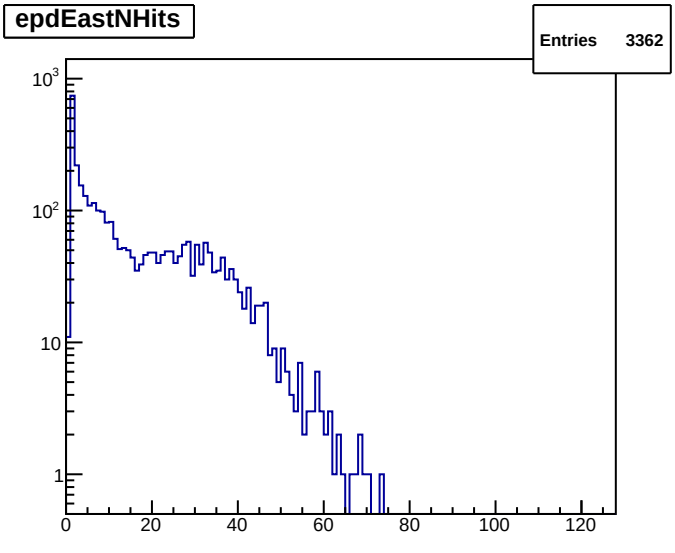
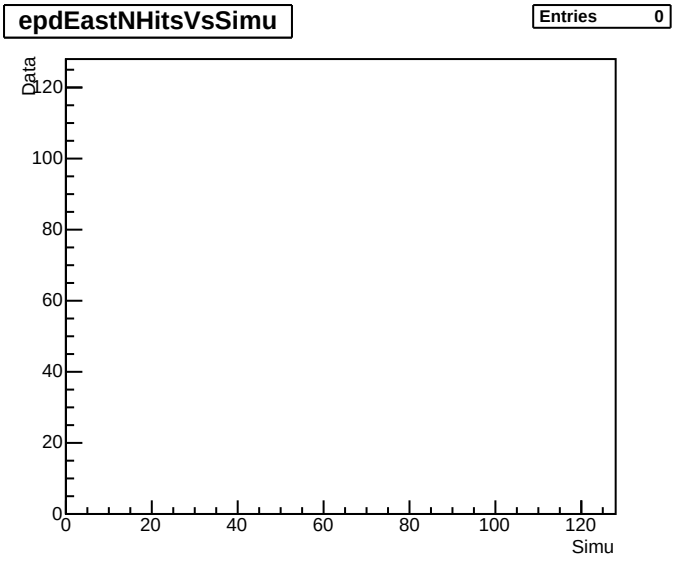
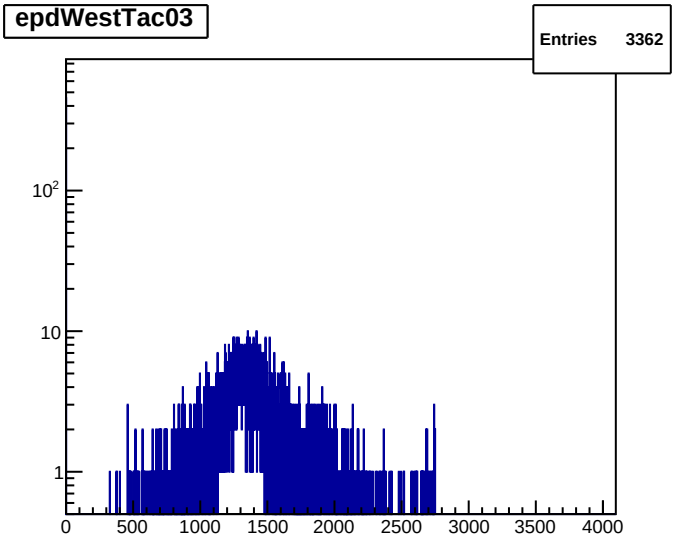
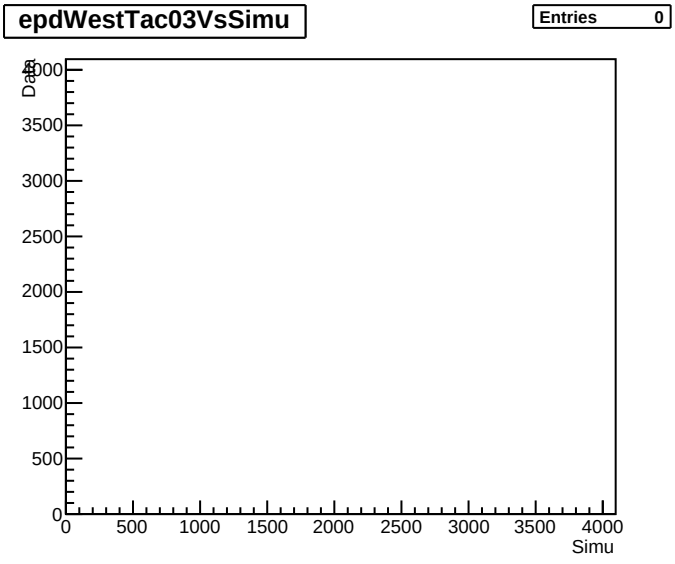
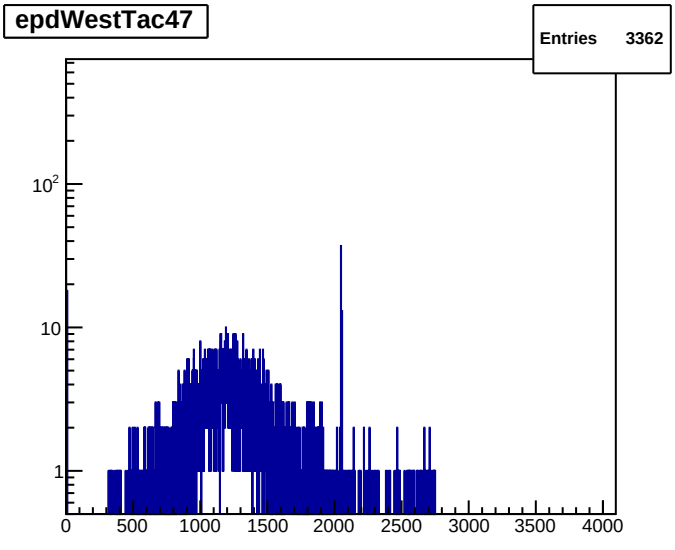
0 500 1000 1500 2000 2500 3000 3500 4000
Simu

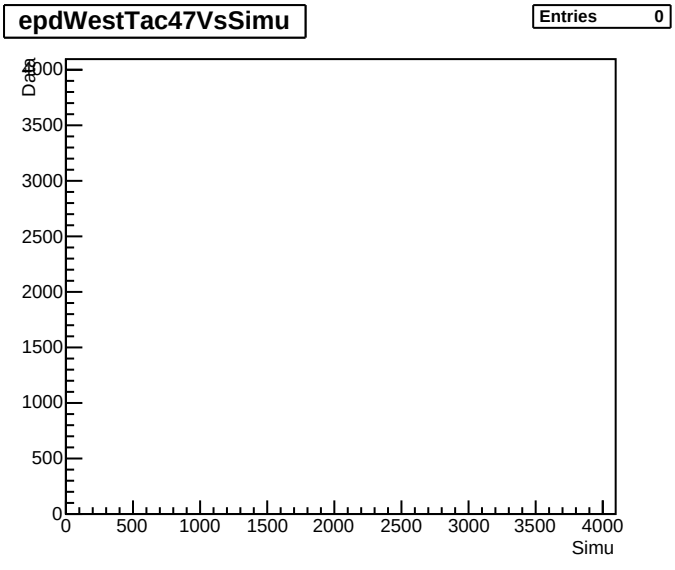

epdEastNHits
Entries 3362
10^3
10^2
10
1
0 20 40 60 80 100 120

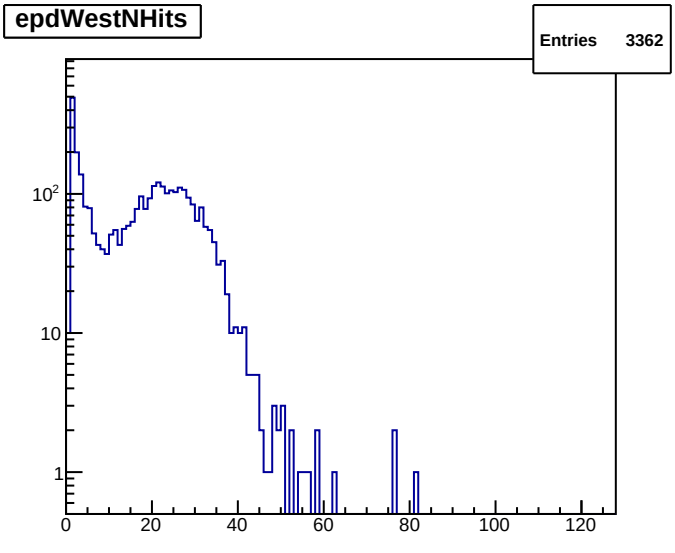

epdEastNHitsVsSimu
Entries 0
Data
120 100 80 60 40 20 0
0 20 40 60 80 100 120
Simu

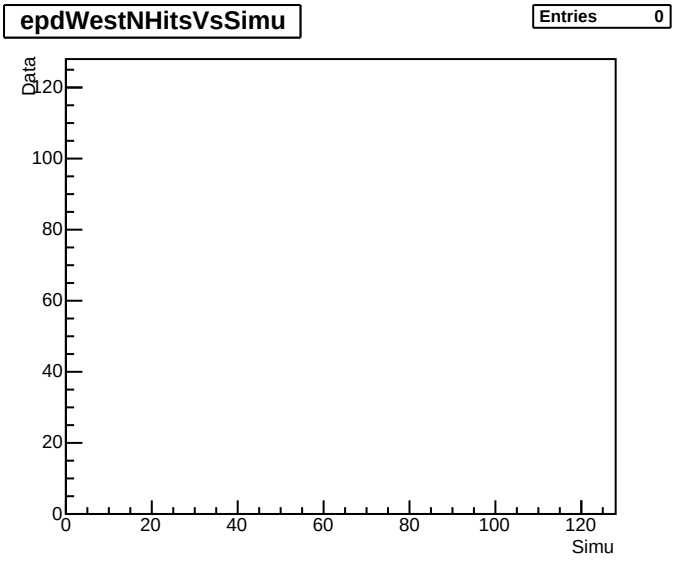

epdWestTac03
Entries 3362
10^2
10
1
0 500 1000 1500 2000 2500 3000 3500 4000

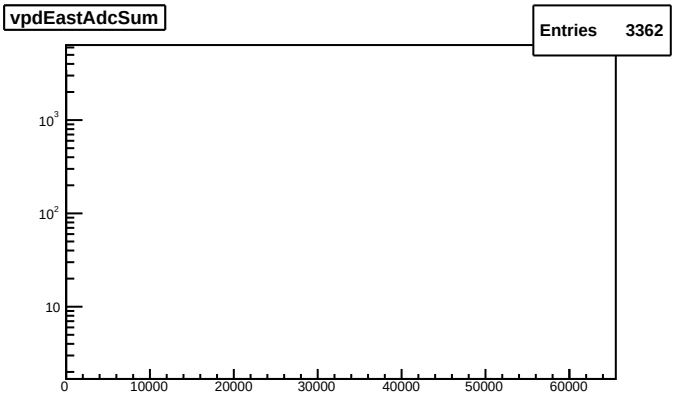

epdWestTac03VsSimu
Entries 0
Data
4000 3500 3000 2500 2000 1500 1000 500 0
0 500 1000 1500 2000 2500 3000 3500 4000
Simu

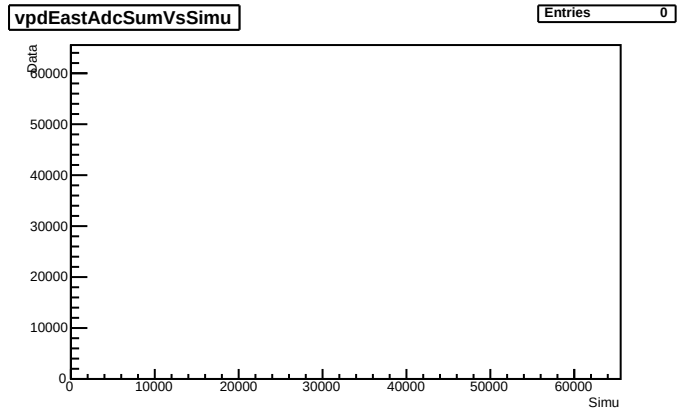

epdWestTac47
Entries 3362
10^2
10
1
0 500 1000 1500 2000 2500 3000 3500 4000

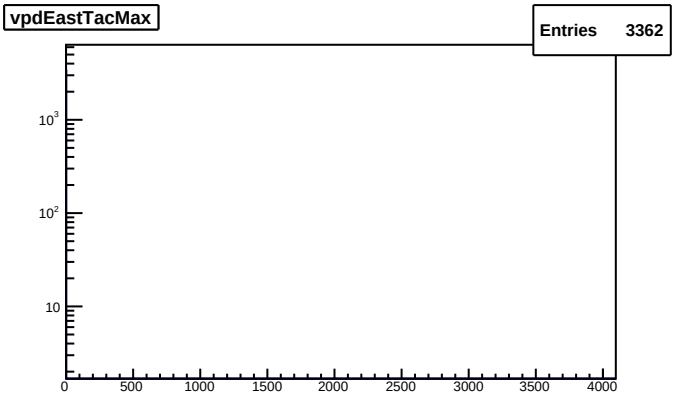

epdWestTac47VsSimu
Entries 0
Data
4000 3500 3000 2500 2000 1500 1000 500 0
0 500 1000 1500 2000 2500 3000 3500 4000
Simu

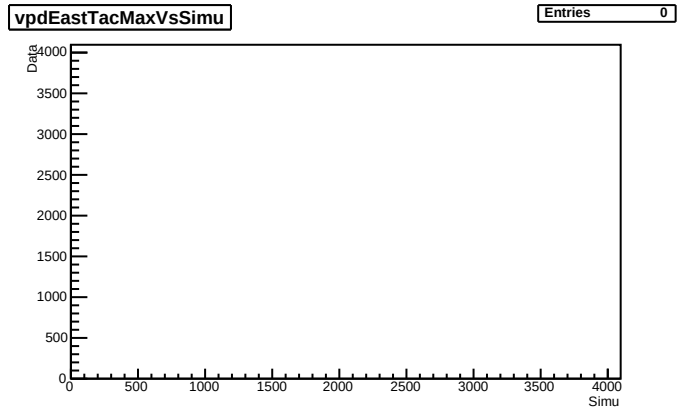

epdWestNHits
Entries 3362
10^2
10
1
0 20 40 60 80 100 120

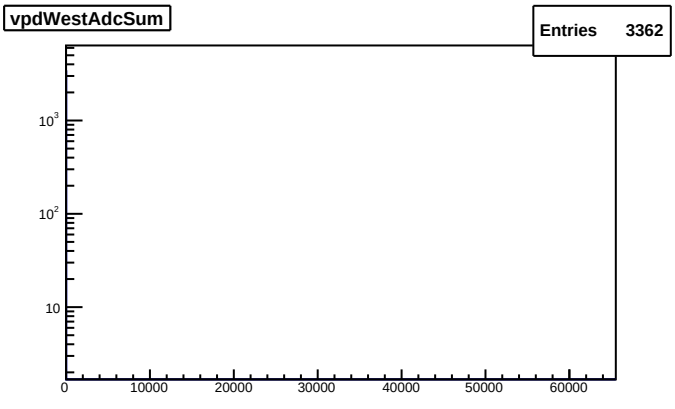

epdWestNHitsVsSimu
Entries 0
Data
120 100 80 60 40 20 0
0 20 40 60 80 100 120
Simu

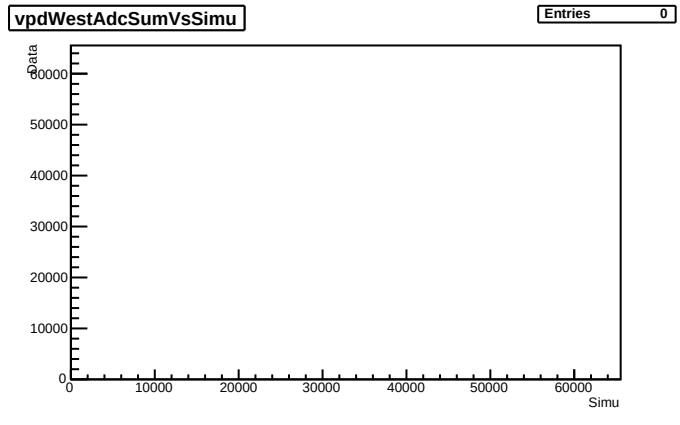

vpdEastAdcSum
Entries 3362
10^3
10^2
10
0
10000
20000
30000
40000
50000
60000

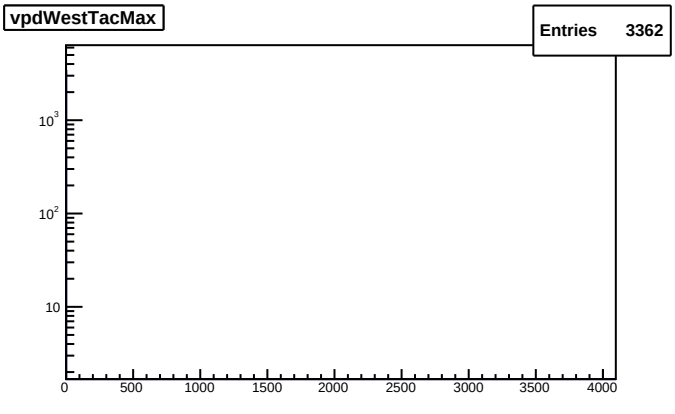

vpdEastAdcSumVsSimu
Entries 0
Data
60000
50000
40000
30000
20000
10000
0
0
10000
20000
30000
40000
50000
60000
Simu

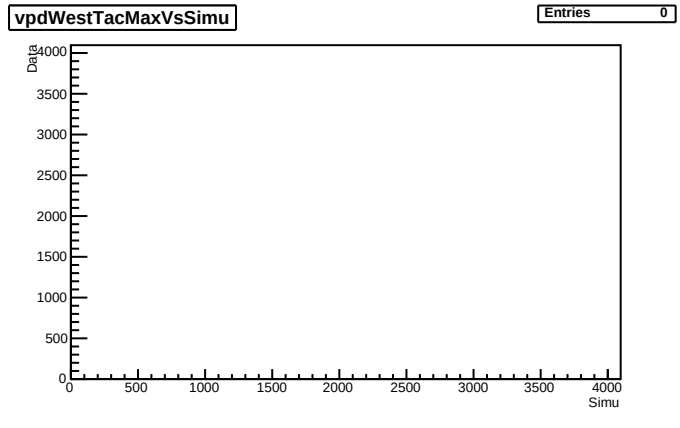

vpdEastTacMax
Entries 3362
10^3
10^2
10
0
500
1000
1500
2000
2500
3000
3500
4000

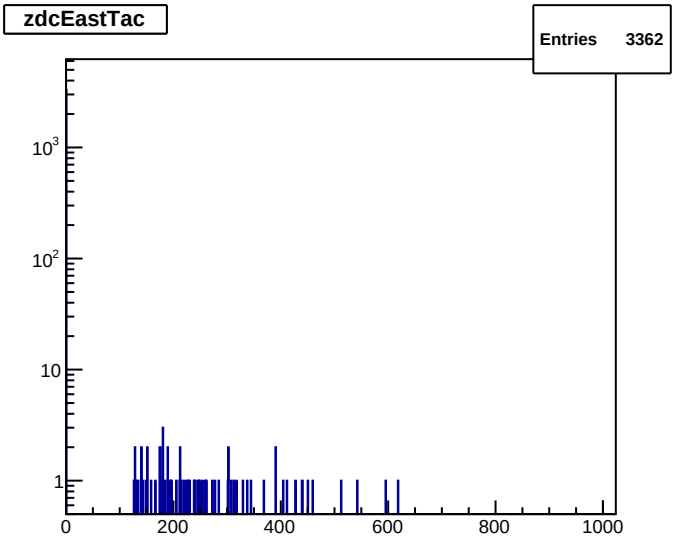

vpdEastTacMaxVsSimu
Entries 0
Data
4000
3500
3000
2500
2000
1500
1000
500
0
0
500
1000
1500
2000
2500
3000
3500
4000
Simu

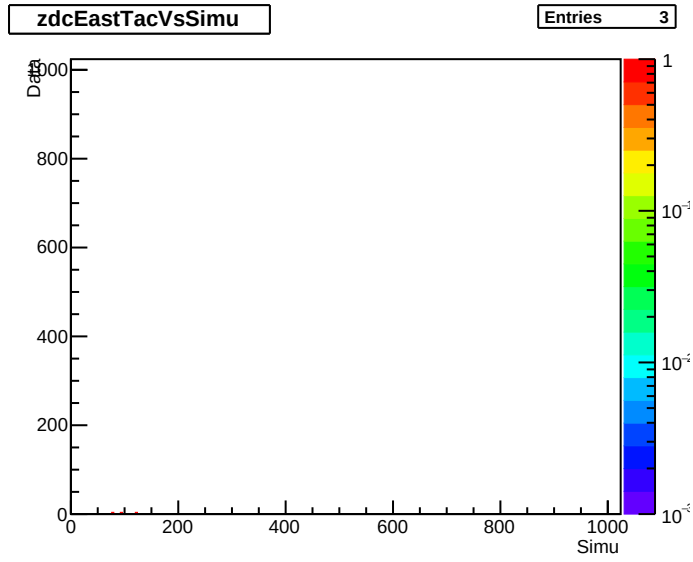

vpdWestAdcSum
Entries 3362
10^3
10^2
10
0
10000
20000
30000
40000
50000
60000

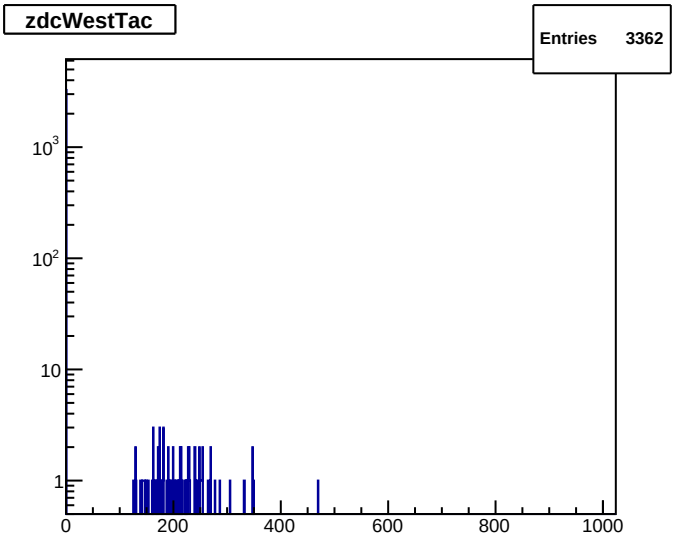

vpdWestAdcSumVsSimu
Entries 0
Data
60000
50000
40000
30000
20000
10000
0
0
10000
20000
30000
40000
50000
60000
Simu

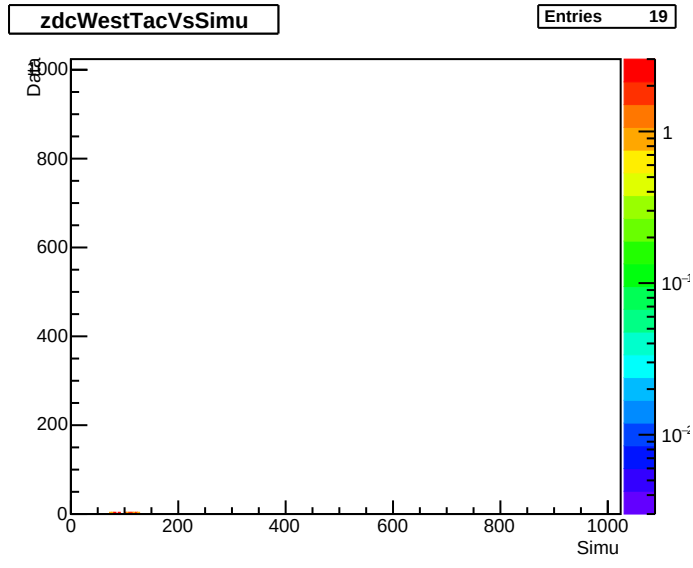

vpdWestTacMax
Entries 3362
10^3
10^2
10
0
500
1000
1500
2000
2500
3000
3500
4000

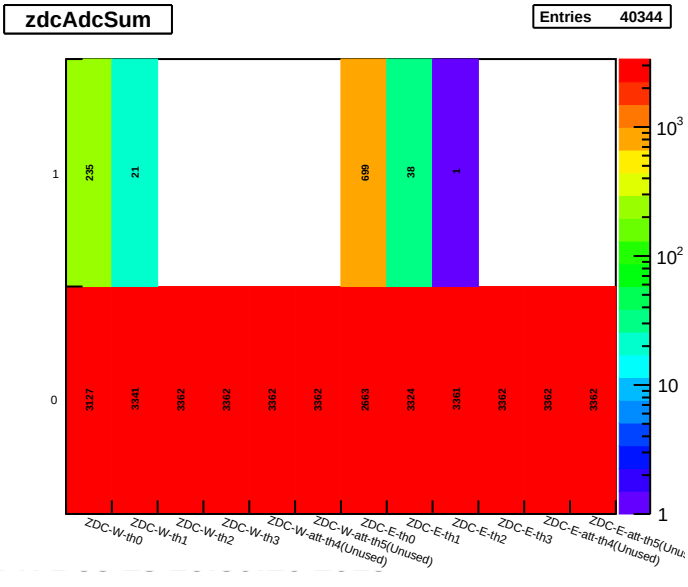

vpdWestTacMaxVsSimu
Entries 0
Data
4000
3500
3000
2500
2000
1500
1000
500
0
0
500
1000
1500
2000
2500
3000
3500
4000
Simu

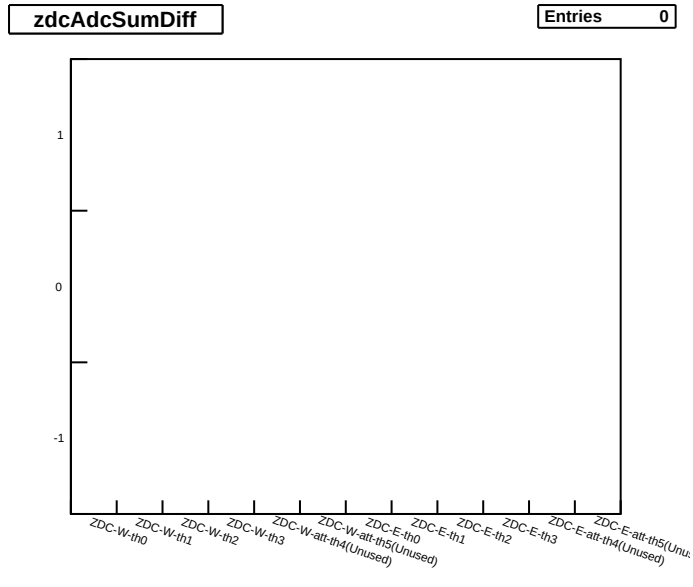

zdcEastTac
Entries 3362

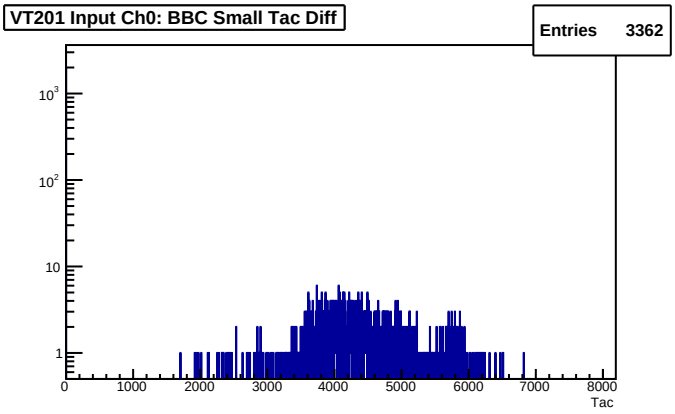

zdcEastTacVsSimu
Entries 3
Data
Simu

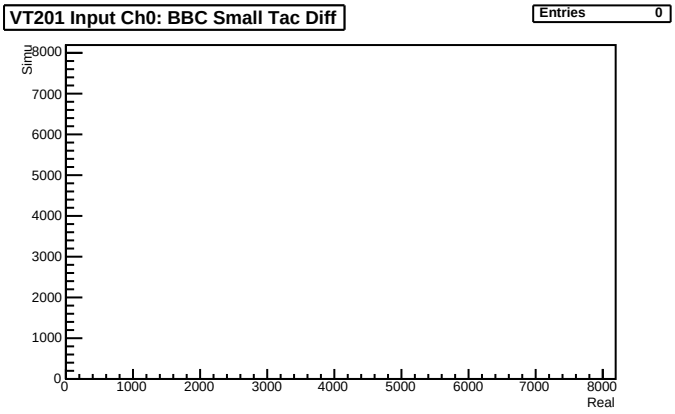

zdcWestTac
Entries 3362

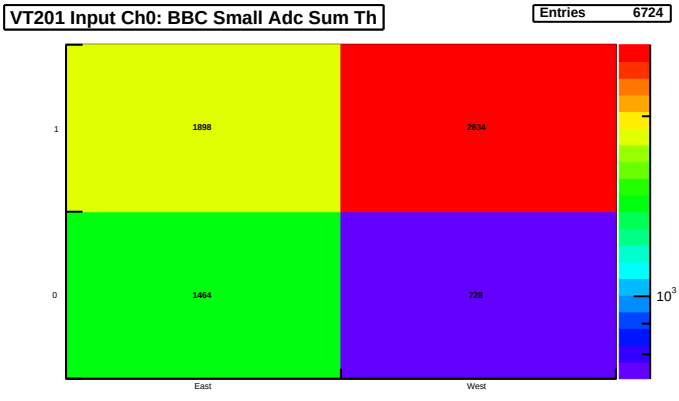

zdcWestTacVsSimu
Entries 19
Data
Simu

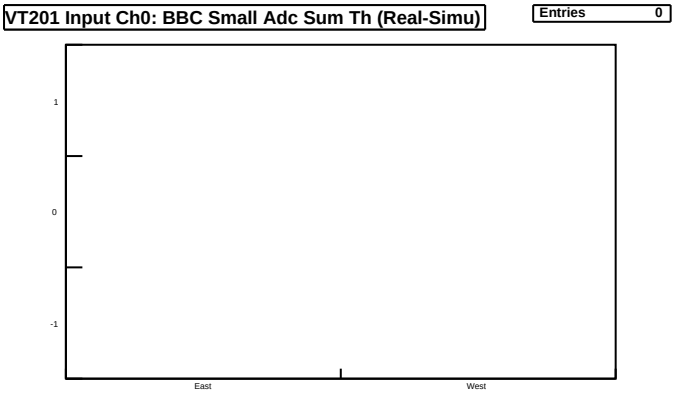

zdcAdcSum
Entries 40344
235
21
699
38
1
3127
3341
3362
3362
3362
3362
2663
3324
3361
3362
3362
3362
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)

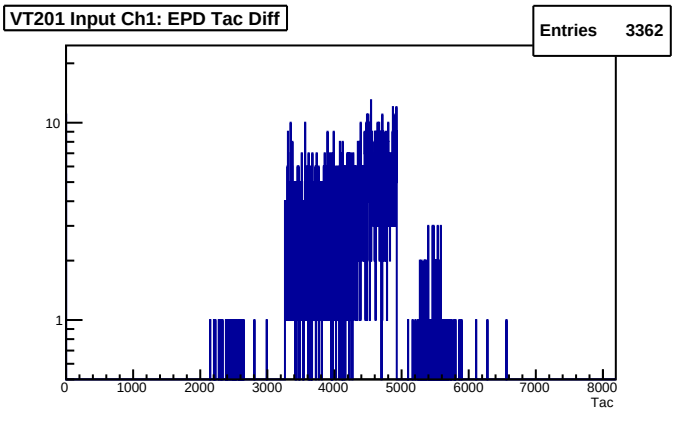

zdcAdcSumDiff
Entries 0
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)

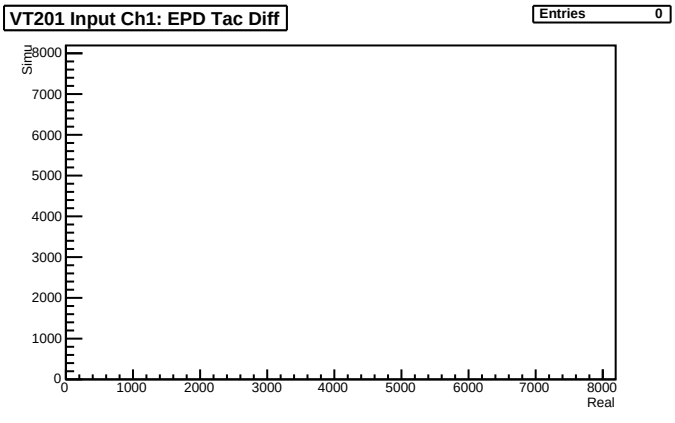

VT201 Input Ch0: BBC Small Tac Diff
Entries 3362
Tac

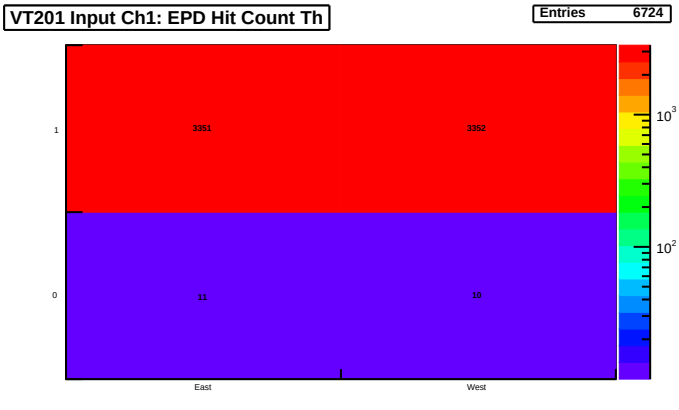

VT201 Input Ch0: BBC Small Tac Diff
Entries 0
Simu
Real

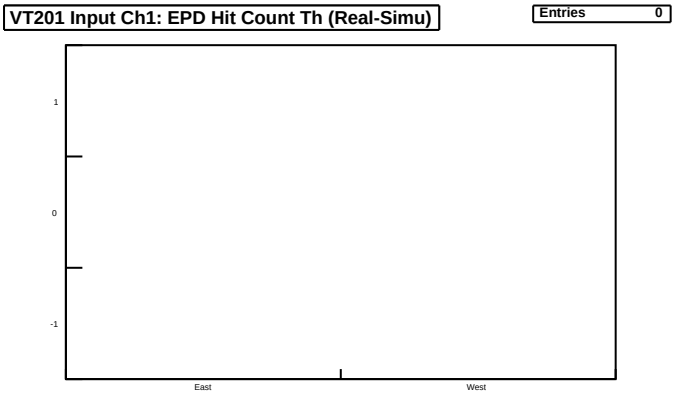

VT201 Input Ch0: BBC Small Adc Sum Th
Entries 6724
1898
2634
1464
728
East
West

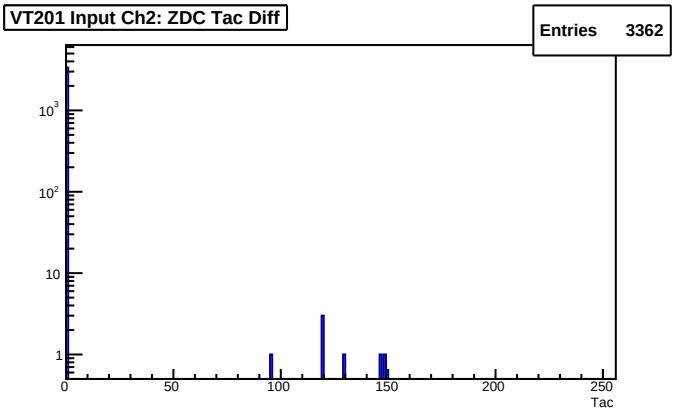

VT201 Input Ch0: BBC Small Adc Sum Th (Real-Simu)
Entries 0
East
West

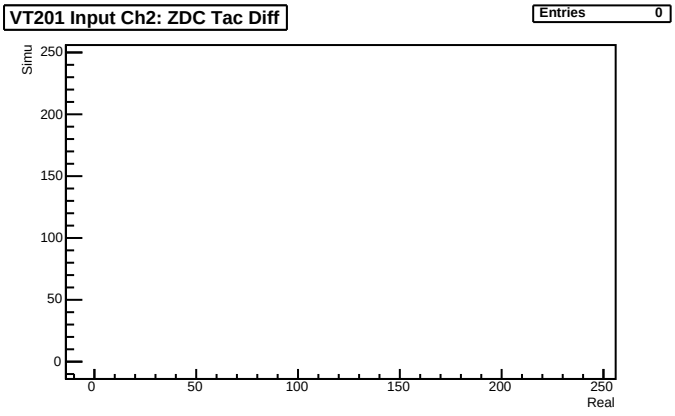

VT201 Input Ch1: EPD Tac Diff
Entries 3362
Tac

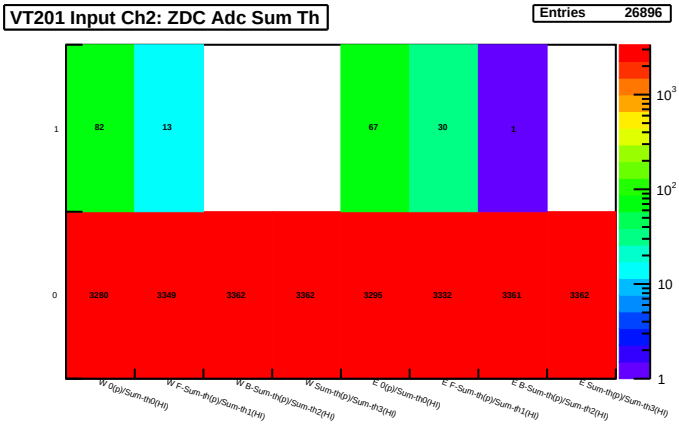

VT201 Input Ch1: EPD Tac Diff
Entries 0
Simu
Real

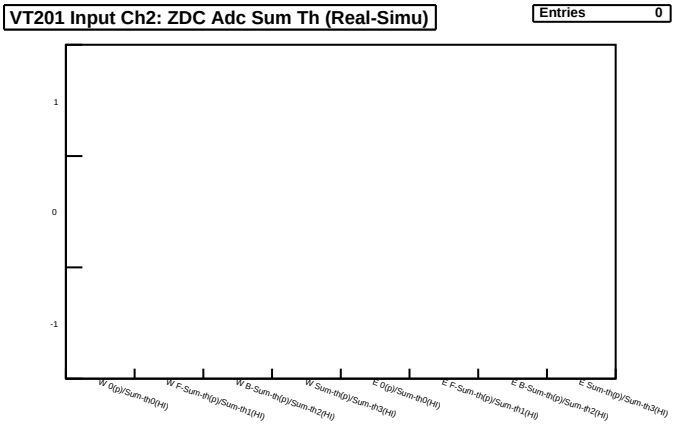

VT201 Input Ch1: EPD Hit Count Th
Entries 6724
3351
3352
11
10
East
West

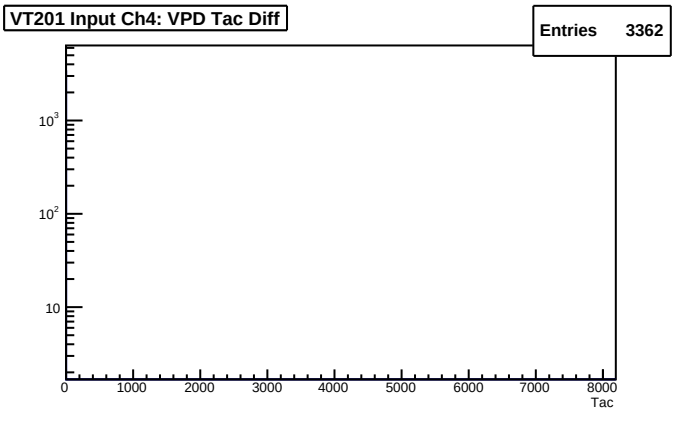

VT201 Input Ch1: EPD Hit Count Th (Real-Simu)
Entries 0
East
West

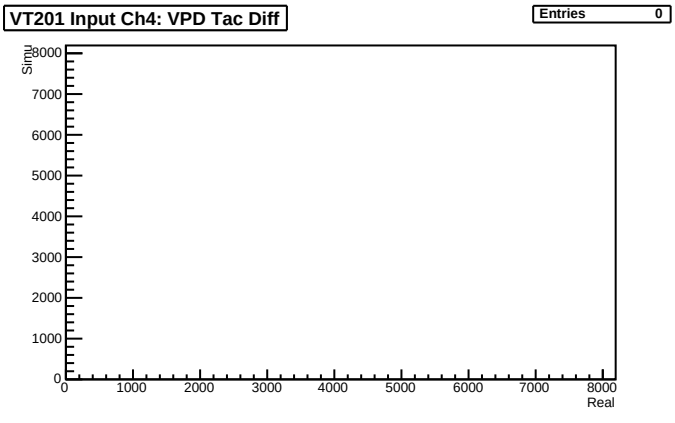

VT201 Input Ch2: ZDC Tac Diff
Entries 3362
Tac

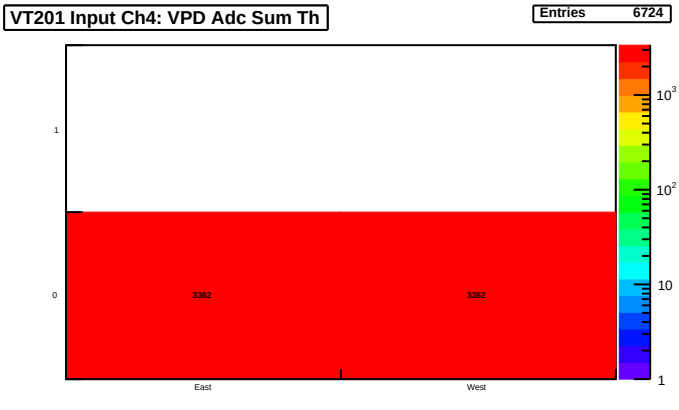

VT201 Input Ch2: ZDC Tac Diff
Entries 0
Simu
Real

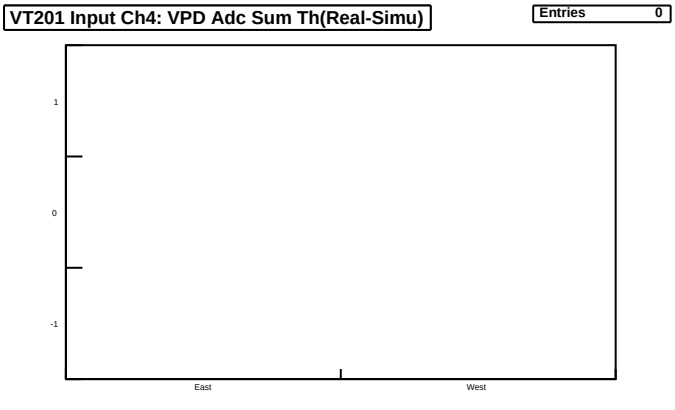

VT201 Input Ch2: ZDC Adc Sum Th
Entries 26896
82
13
67
30
1
3280
3349
3362
3362
3295
3332
3361
3362
W 0(p)/Sum-th0(Hi)
W F-Sum-th(p)/Sum-th1(Hi)
W B-Sum-th(p)/Sum-th2(Hi)
W Sum-th(p)/Sum-th3(Hi)
E 0(p)/Sum-th0(Hi)
E F-Sum-th(p)/Sum-th1(Hi)
E B-Sum-th(p)/Sum-th2(Hi)
E Sum-th(p)/Sum-th3(Hi)

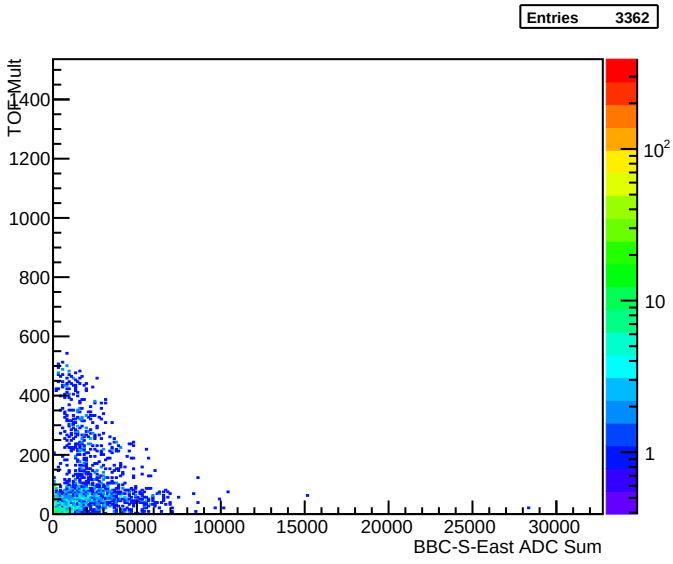

VT201 Input Ch2: ZDC Adc Sum Th (Real-Simu)
Entries 0
W 0(p)/Sum-th0(Hi)
W F-Sum-th(p)/Sum-th1(Hi)
W B-Sum-th(p)/Sum-th2(Hi)
W Sum-th(p)/Sum-th3(Hi)
E 0(p)/Sum-th0(Hi)
E F-Sum-th(p)/Sum-th1(Hi)
E B-Sum-th(p)/Sum-th2(Hi)
E Sum-th(p)/Sum-th3(Hi)

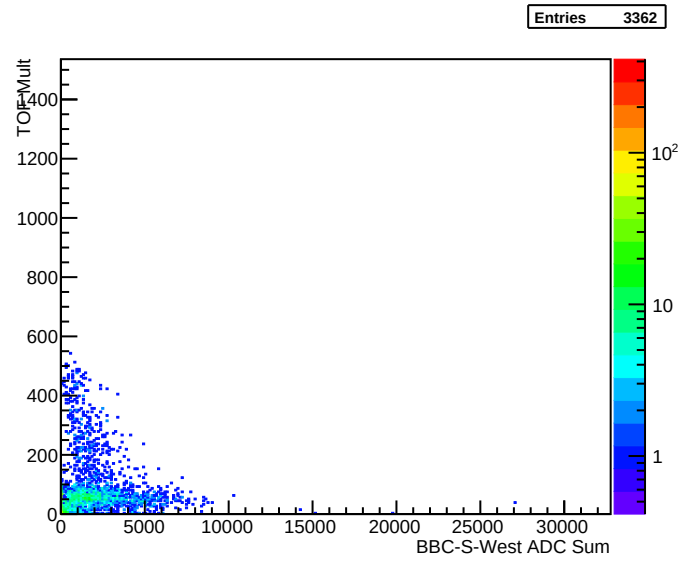

VT201 Input Ch4: VPD Tac Diff
Entries 3362
Tac

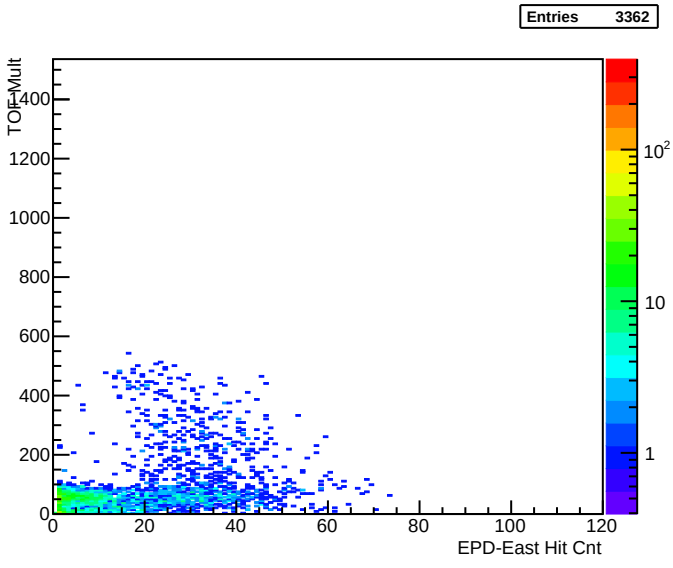

VT201 Input Ch4: VPD Tac Diff
Entries 0
Simu
Real

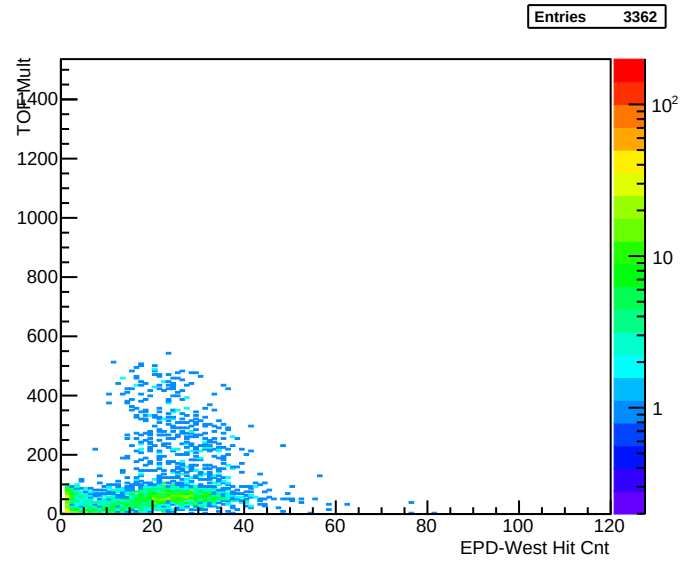

VT201 Input Ch4: VPD Adc Sum Th
Entries 6724
3362
3362
East
West

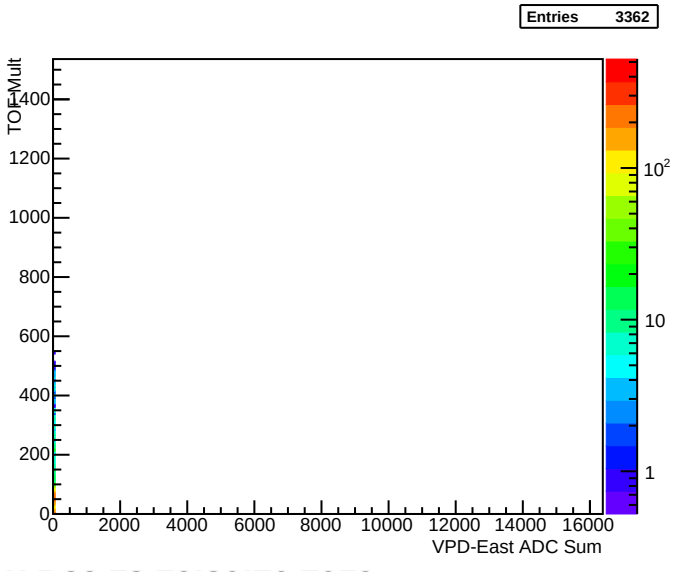

VT201 Input Ch4: VPD Adc Sum Th(Real-Simu)
Entries 0
East
West

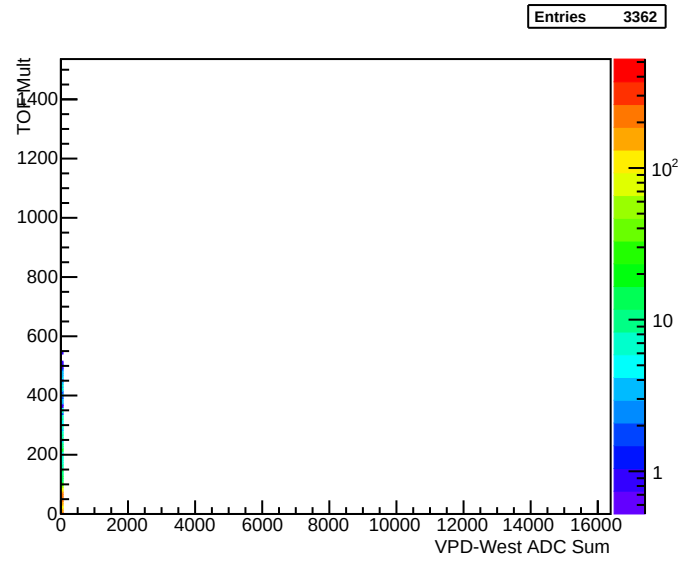

Entries 3362
TOF-Mult
BBC-S-East ADC Sum

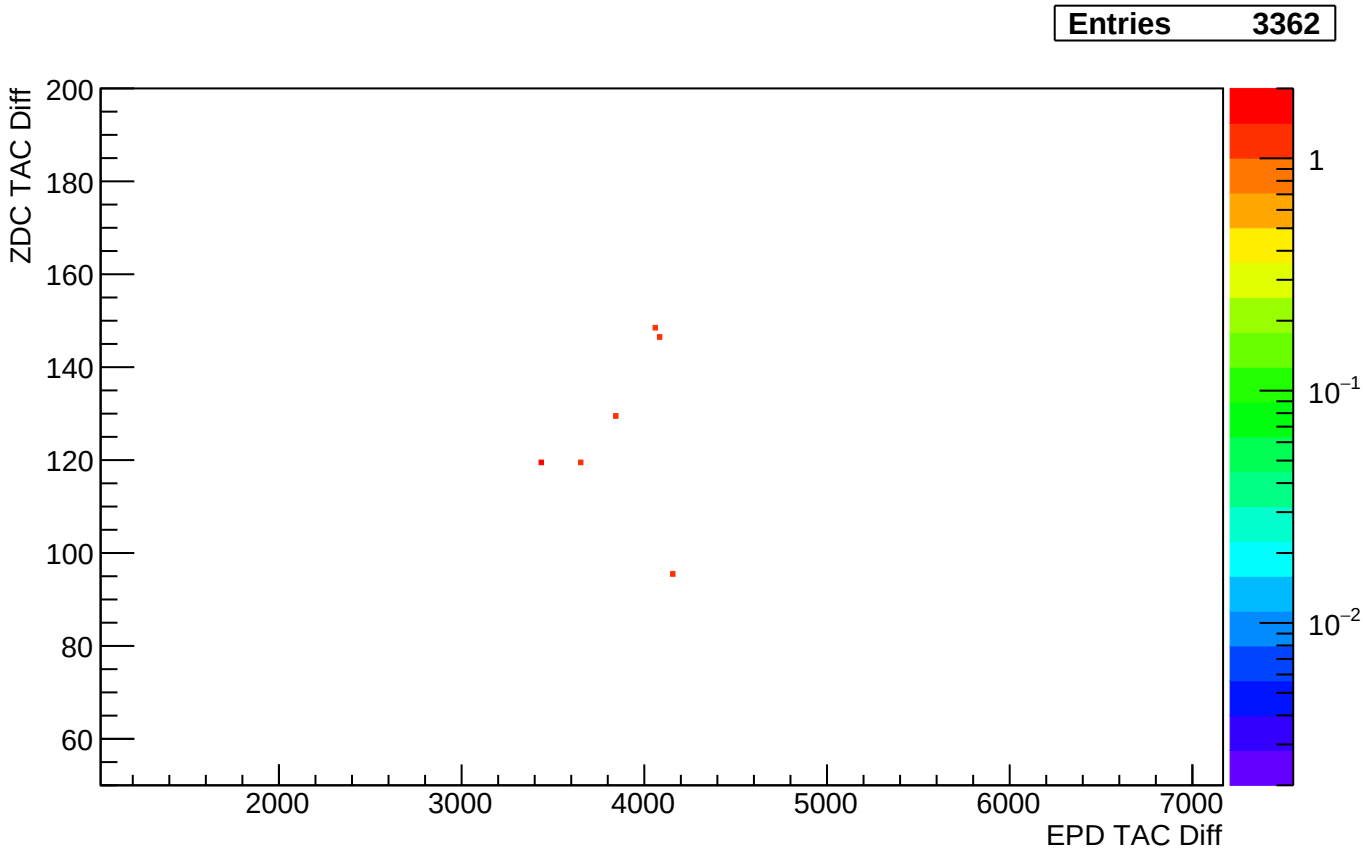

Entries 3362
TOF-Mult
BBC-S-West ADC Sum

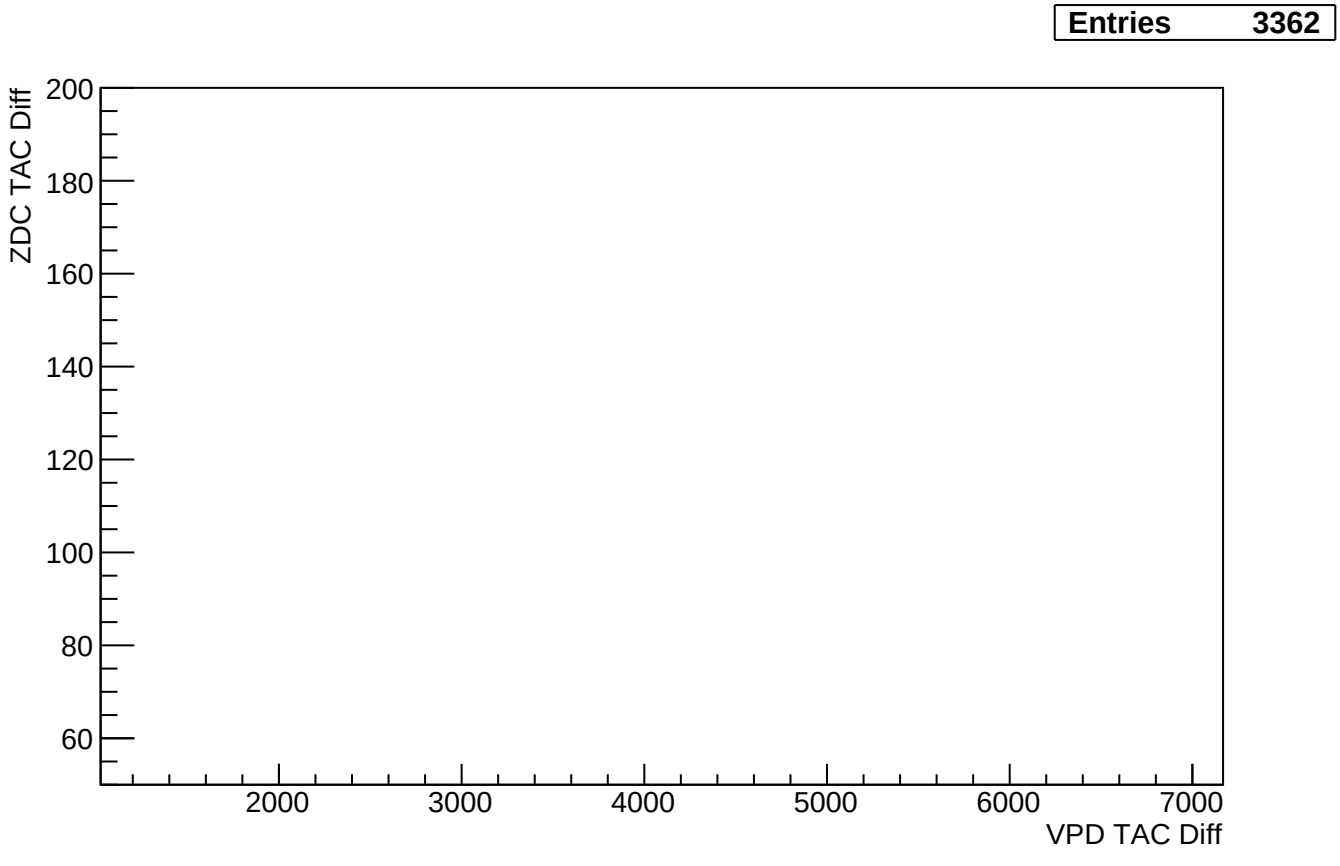

Entries 3362
TOF-Mult
EPD-East Hit Cnt

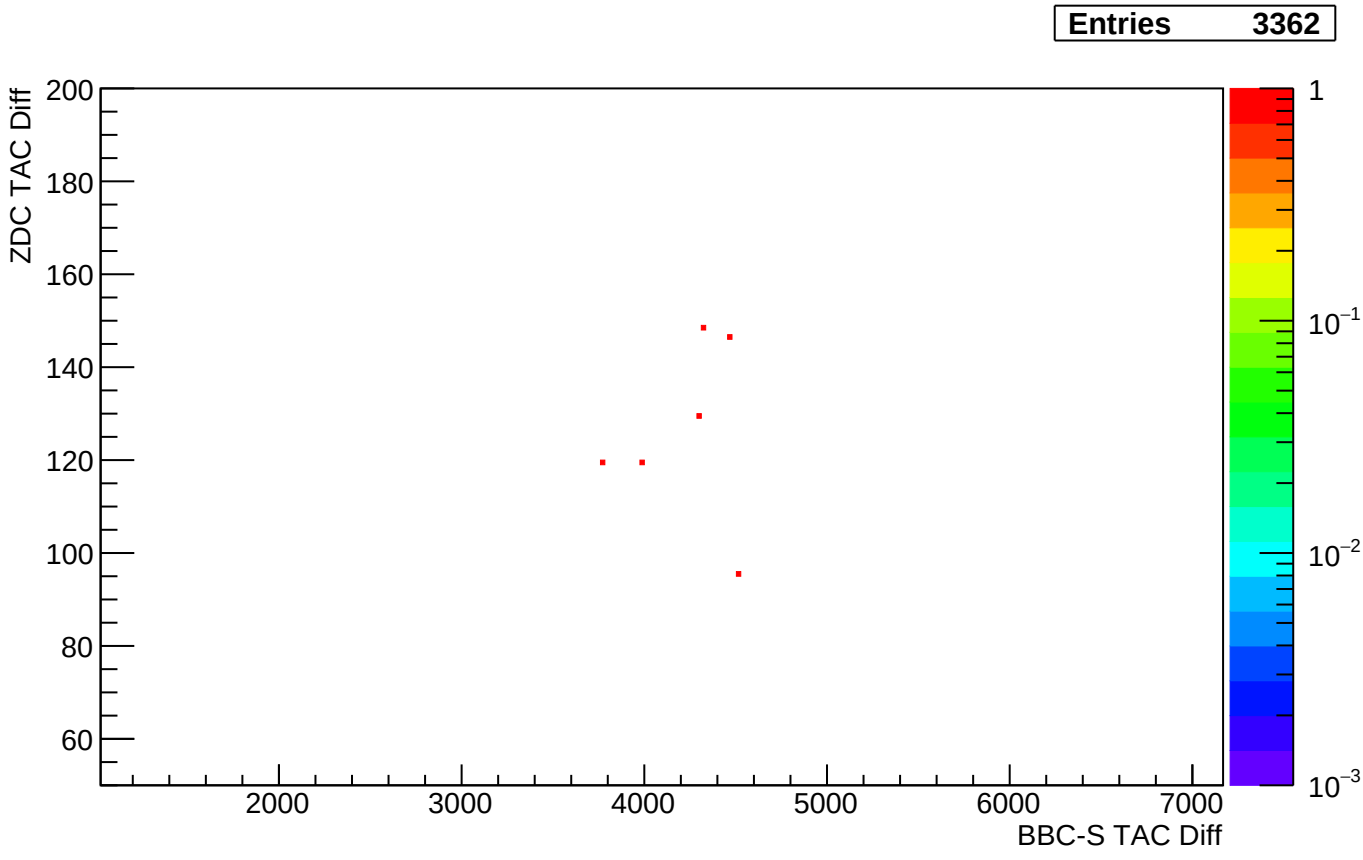

Entries 3362
TOF-Mult
EPD-West Hit Cnt

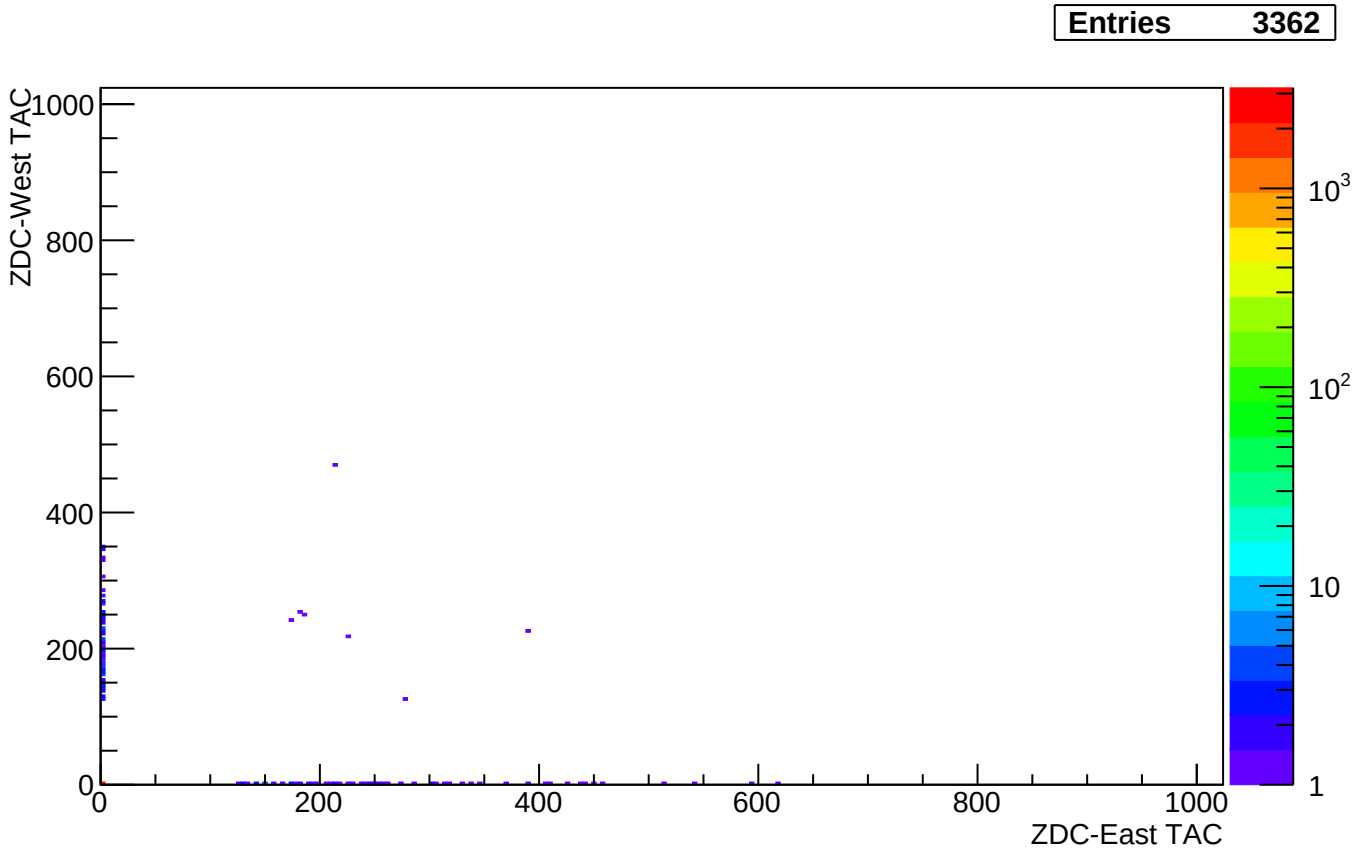

Entries 3362
TOF-Mult
VPD-East ADC Sum

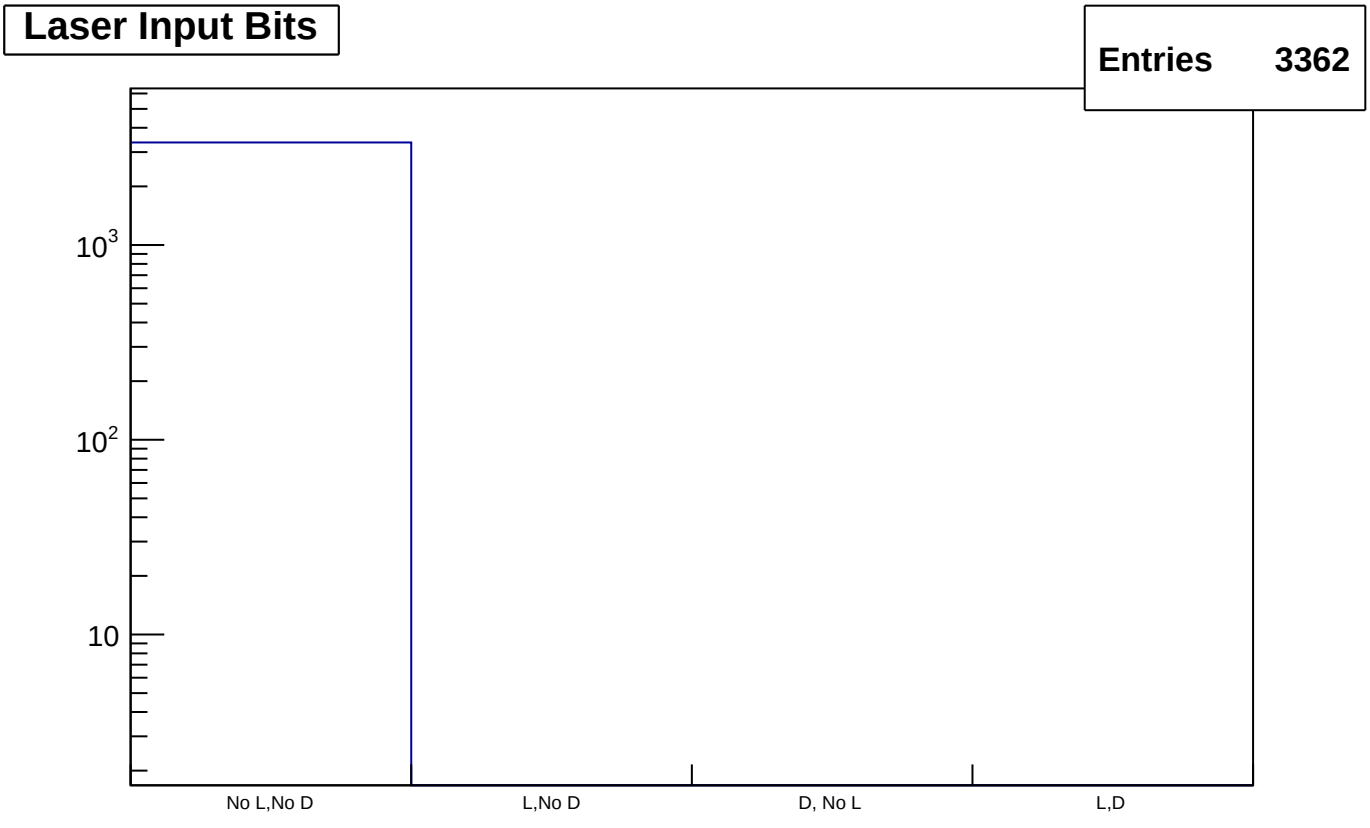

Entries 3362
TOF-Mult
VPD-West ADC Sum

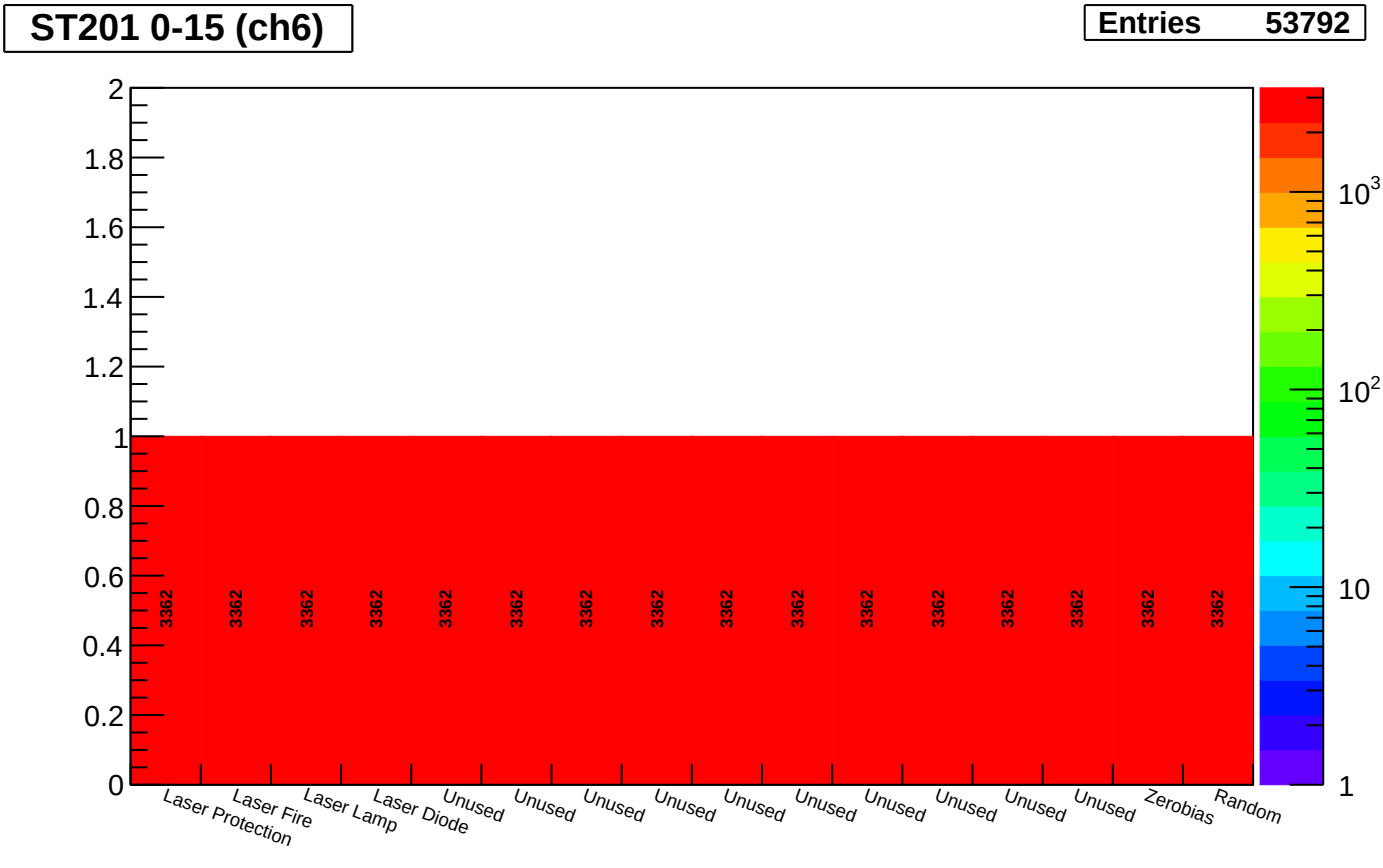

Entries 3362
ZDC TAC Diff
200
180
160
140
120
100
80
60
2000
3000
4000
5000
6000
7000
EPD TAC Diff
1
10^{-1}
10^{-2}

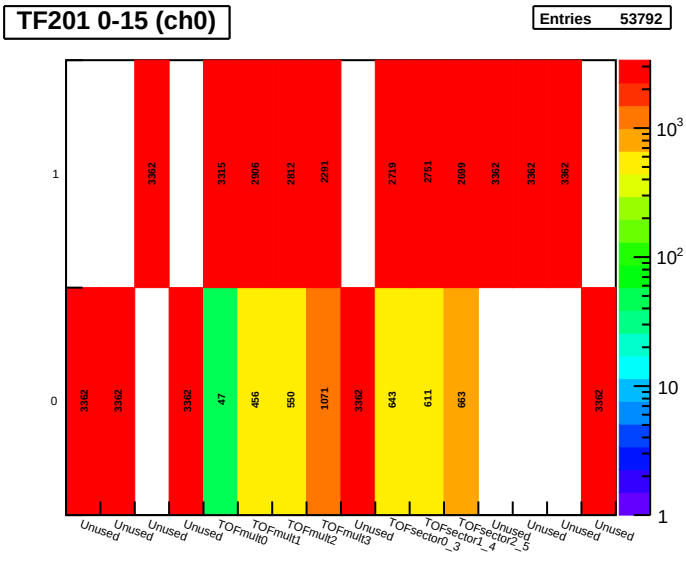

Entries 3362
ZDC TAC Diff
200
180
160
140
120
100
80
60
2000
3000
4000
5000
6000
7000
VPD TAC Diff

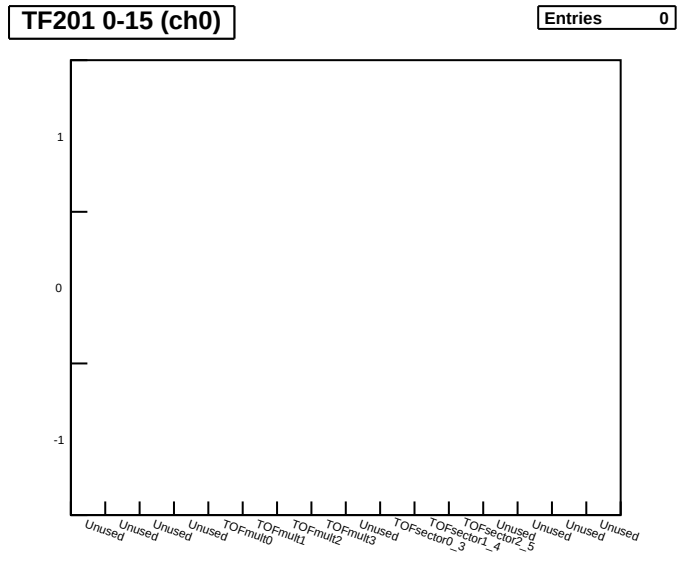

Entries 3362
ZDC TAC Diff
BBC-S TAC Diff
200
180
160
140
120
100
80
60
2000
3000
4000
5000
6000
7000
1
10^{-1}
10^{-2}
10^{-3}

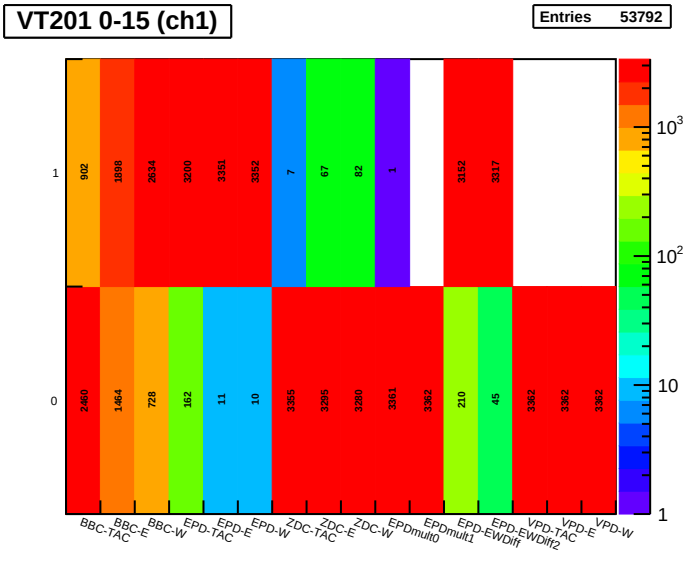

Entries 3362
ZDC-West TAC
ZDC-East TAC
1000
800
600
400
200
0
0
200
400
600
800
1000
10^3
10^2
10
1

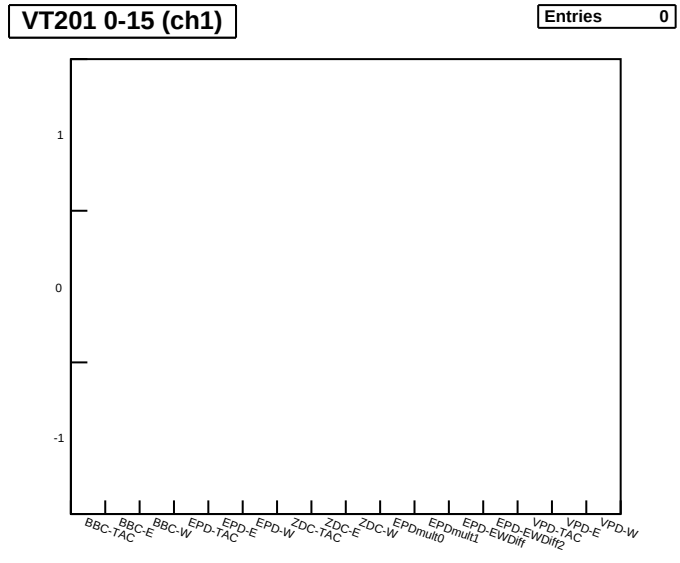

Laser Input Bits
Entries 3362
10^3
10^2
10
No L,No D
L,No D
D, No L
L,D

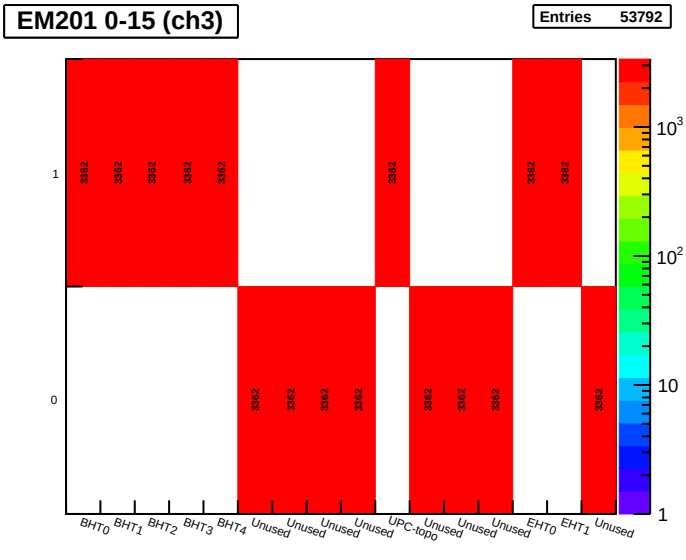

ST201 0-15 (ch6)
Entries 53792
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
3362
Laser Protection
Laser Fire
Laser Lamp
Laser Diode
Unused
Zerobias
Random
10^3
10^2
10
1

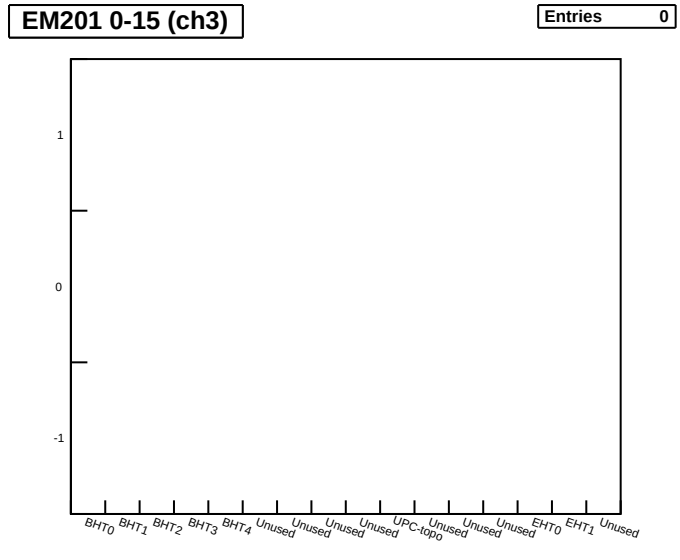

TF201 0-15 (ch0)
Entries 53792
1
0
3362 3362 3362 3362 3315 2906 2812 2291 2719 2751 2699 3362 3362 3362
3362 3362 3362 47 456 550 1071 3362 643 611 663 3362
Unused Unused Unused Unused TOFmult0 TOFmult1 TOFmult2 TOFmult3 Unused TOFsector0_3 TOFsector1_4 TOFsector2_5 Unused Unused Unused Unused
10^3
10^2
10
1


TF201 0-15 (ch0)
Entries 0
1
0
-1
Unused Unused Unused Unused TOFmult0 TOFmult1 TOFmult2 TOFmult3 Unused TOFsector0_3 TOFsector1_4 TOFsector2_5 Unused Unused Unused


VT201 0-15 (ch1)
Entries 53792
1
0
902 1898 2534 3200 3351 3352 7 67 82 1 3152 3317
2460 1464 728 162 11 10 3355 3295 3280 3361 3362 210 45 3362 3362 3362
BBC-TAC BBC-E BBC-W EPD-TAC EPD-E EPD-W ZDC-TAC ZDC-E ZDC-W EPDmult0 EPDmult1 EPD-EWDiff EPD-EWDiff2 VPD-TAC VPD-E VPD-W
10^3
10^2
10
1


VT201 0-15 (ch1)
Entries 0
1
0
-1
BBC-TAC BBC-E BBC-W EPD-TAC EPD-E EPD-W ZDC-TAC ZDC-E ZDC-W EPDmult0 EPDmult1 EPD-EWDiff EPD-EWDiff2 VPD-TAC VPD-E VPD-W


EM201 0-15 (ch3)
Entries 53792
1
0
3362 3362 3362 3362 3362 3362 3362 3362 3362
3362 3362 3362 3362 3362 3362 3362 3362
BHT0 BHT1 BHT2 BHT3 BHT4 Unused Unused Unused Unused UPC-topo Unused Unused Unused EHT0 EHT1 Unused
10^3
10^2
10
1


EM201 0-15 (ch3)
Entries 0
1
0
-1
BHT0 BHT1 BHT2 BHT3 BHT4 Unused Unused Unused Unused UPC-topo Unused Unused Unused EHT0 EHT1 Unused