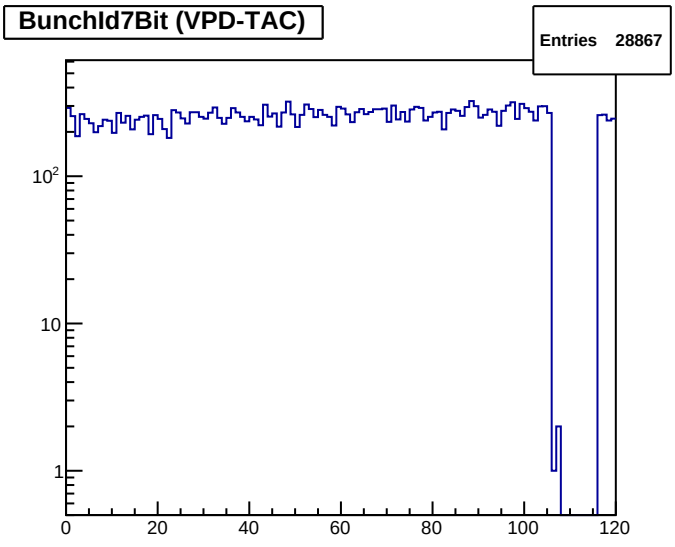
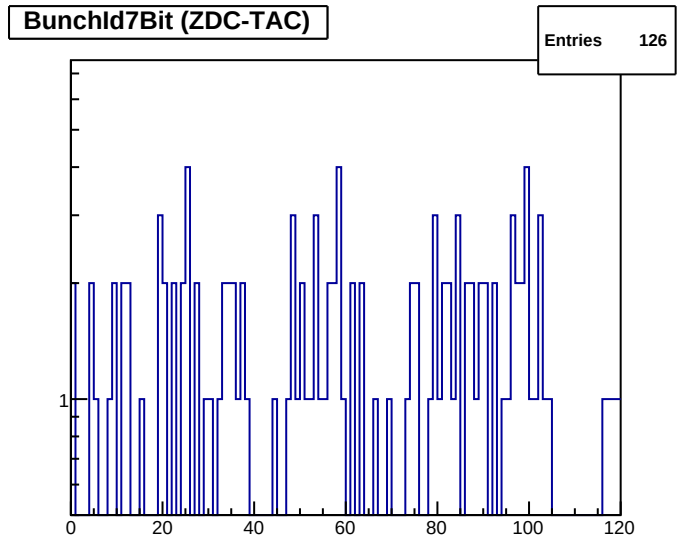
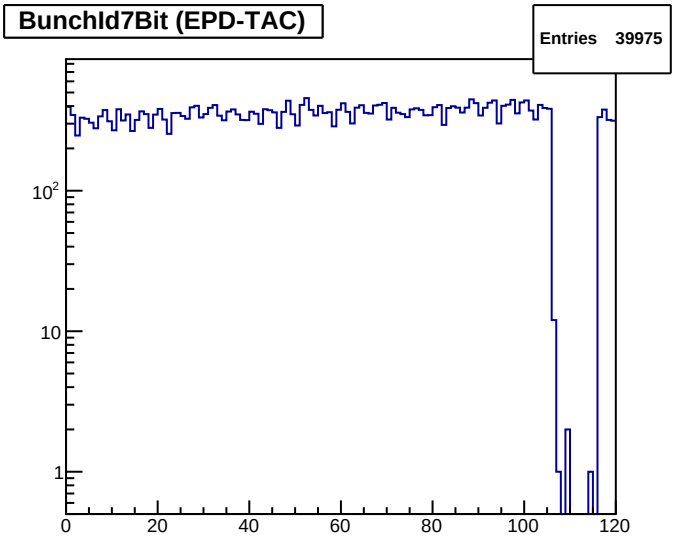
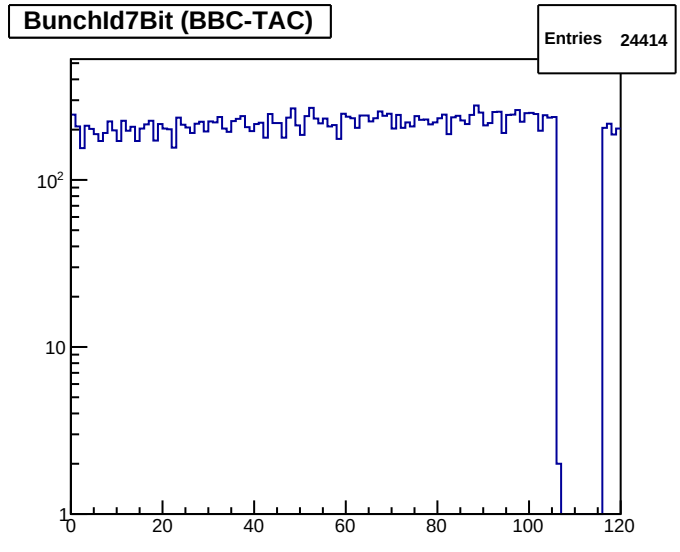
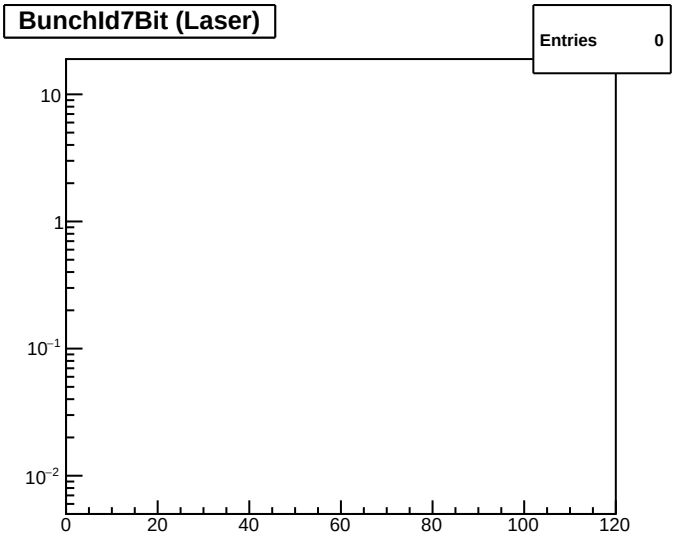
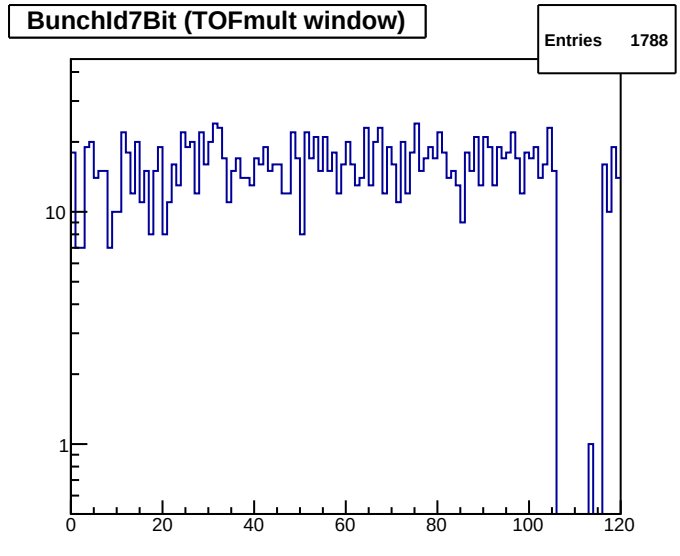

BunchId7Bit (VPD-TAC)
Entries 28867
$10^2$
10
1
0
20
40
60
80
100
120

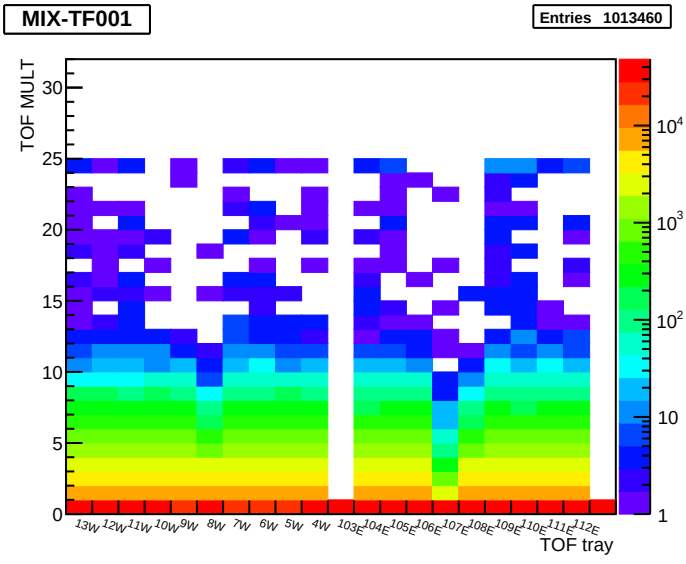

BunchId7Bit (ZDC-TAC)
Entries 126
1
0
20
40
60
80
100
120

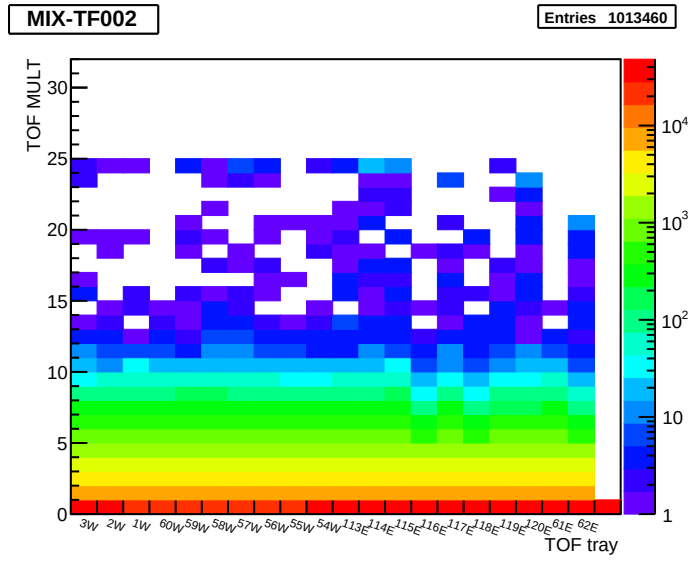

BunchId7Bit (EPD-TAC)
Entries 39975
$10^2$
10
1
0
20
40
60
80
100
120

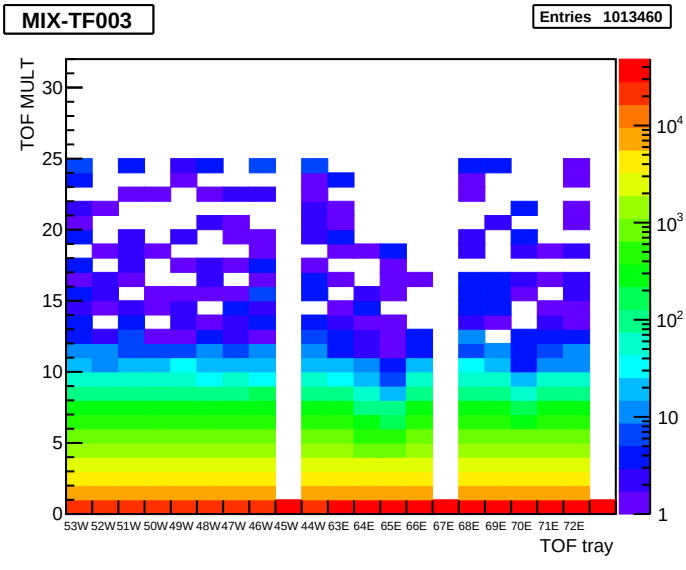

BunchId7Bit (BBC-TAC)
Entries 24414
$10^2$
10
1
0
20
40
60
80
100
120

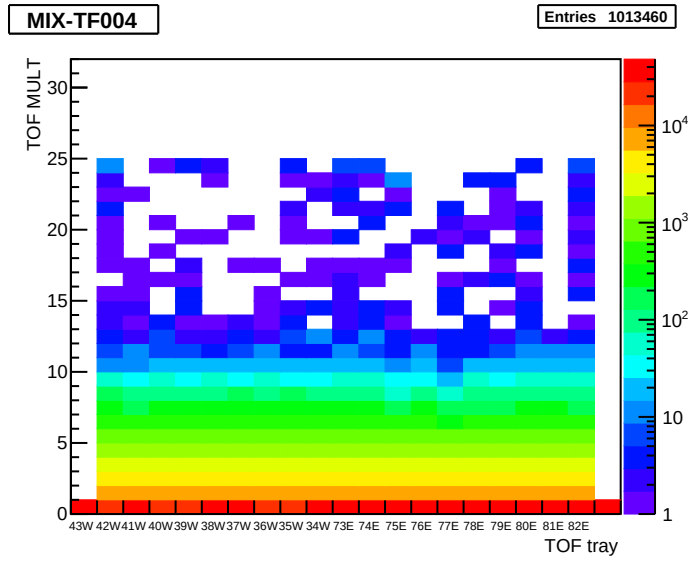

BunchId7Bit (Laser)
Entries 0
10
1
$10^{-1}$
$10^{-2}$
0
20
40
60
80
100
120

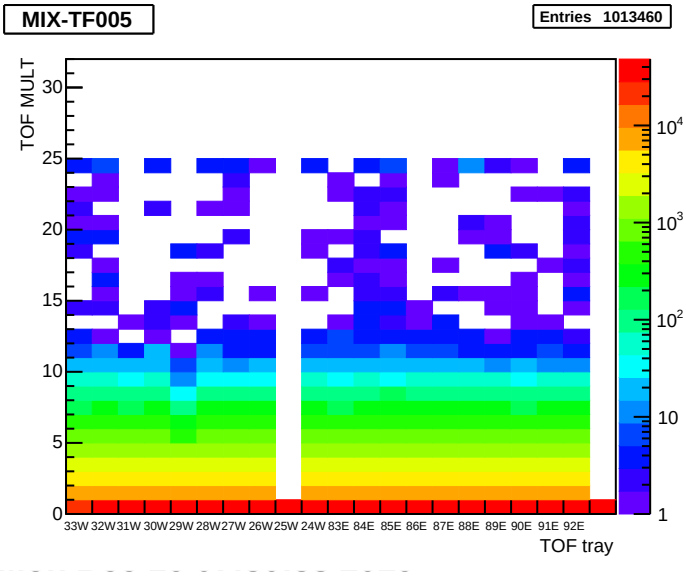

BunchId7Bit (TOFmult window)
Entries 1788
10
1
0
20
40
60
80
100
120

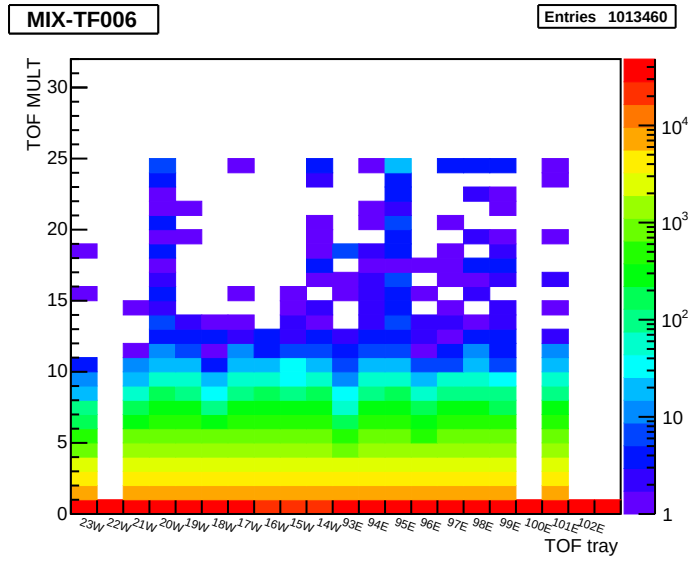

MIX-TF001
Entries 1013460
TOF MULT
TOF tray

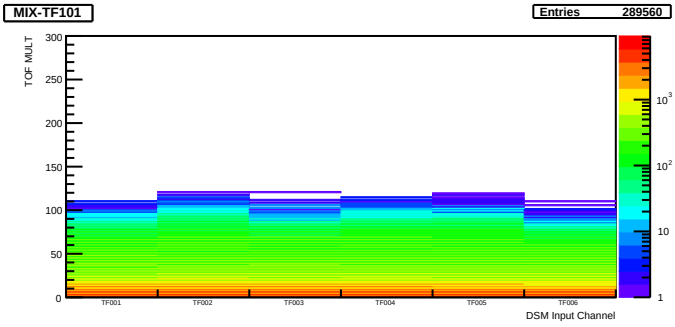

MIX-TF002
Entries 1013460
TOF MULT
TOF tray

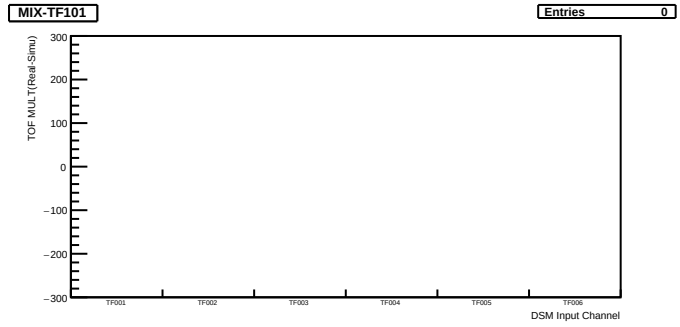

MIX-TF003
Entries 1013460
TOF MULT
TOF tray

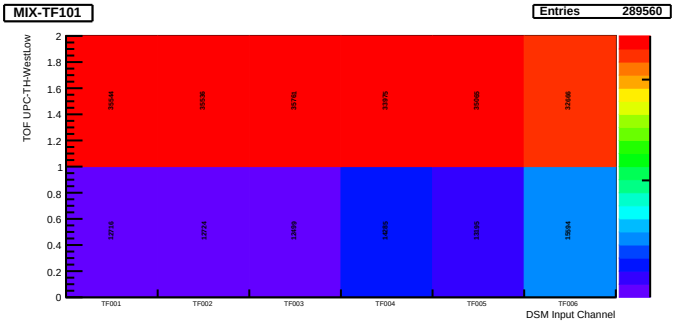

MIX-TF004
Entries 1013460
TOF MULT
TOF tray

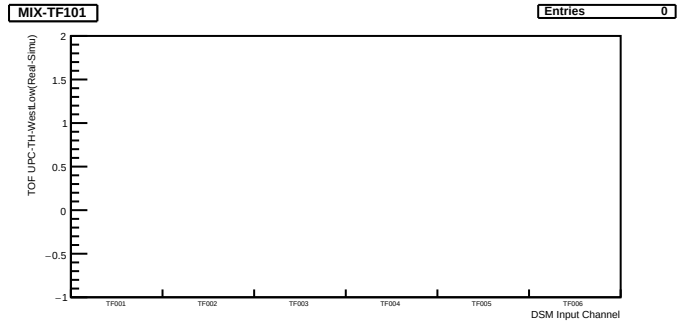

MIX-TF005
Entries 1013460
TOF MULT
TOF tray

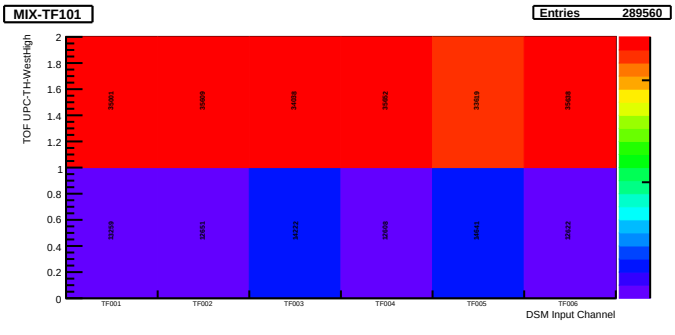

MIX-TF006
Entries 1013460
TOF MULT
TOF tray

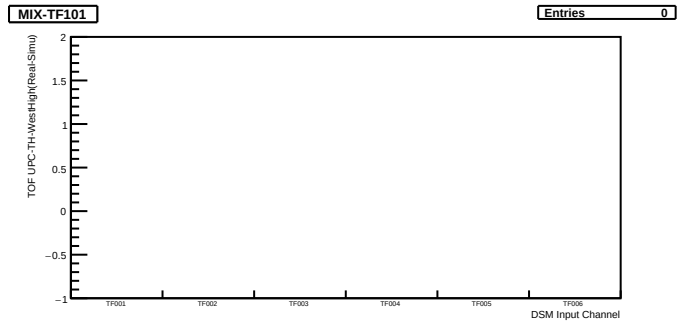

MIX-TF101
Entries 289560
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

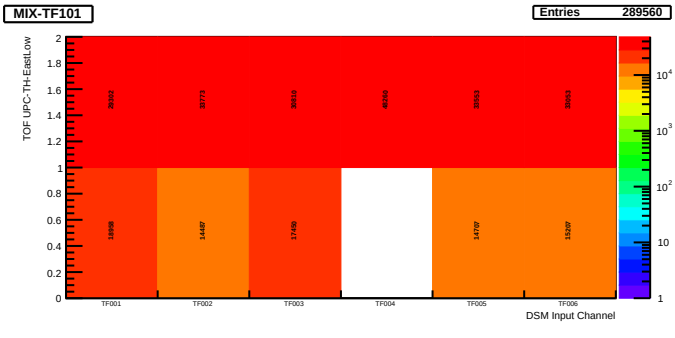

MIX-TF101
Entries 0
TOF MULT(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

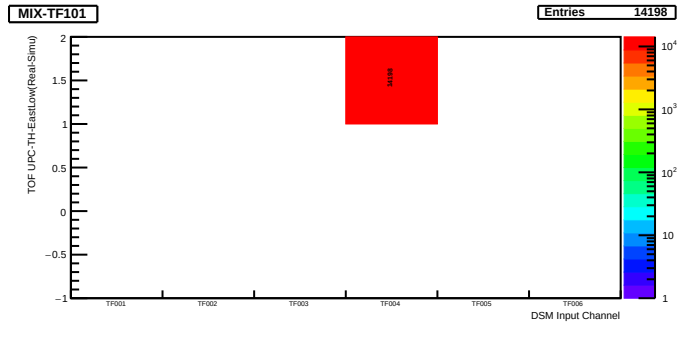

MIX-TF101
Entries 289560
TOF UPC-TH-WestLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

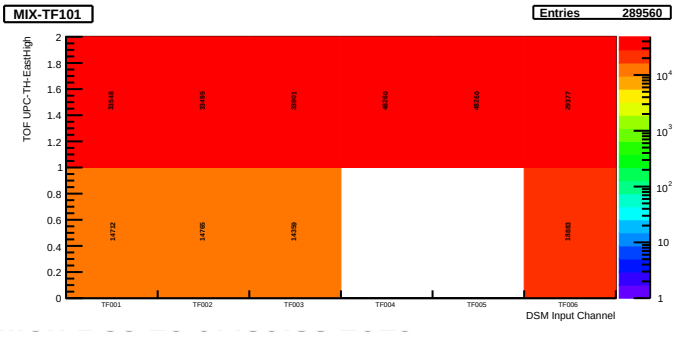

MIX-TF101
Entries 0
TOF UPC-TH-WestLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

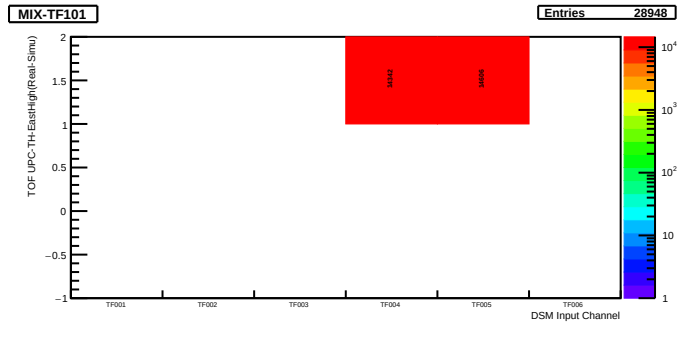

MIX-TF101
Entries 289560
TOF UPC-TH-WestHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

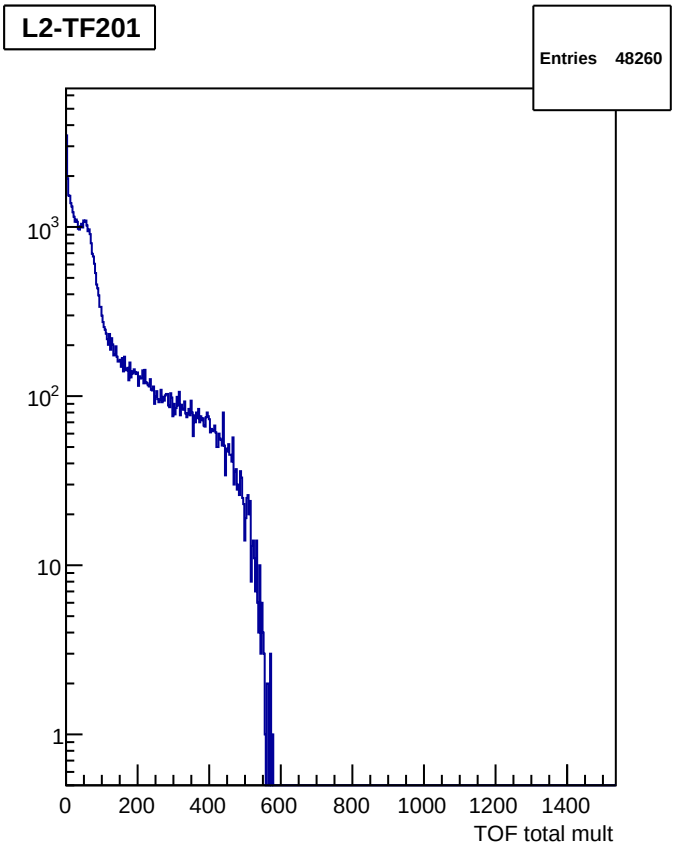

MIX-TF101
Entries 0
TOF UPC-TH-WestHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

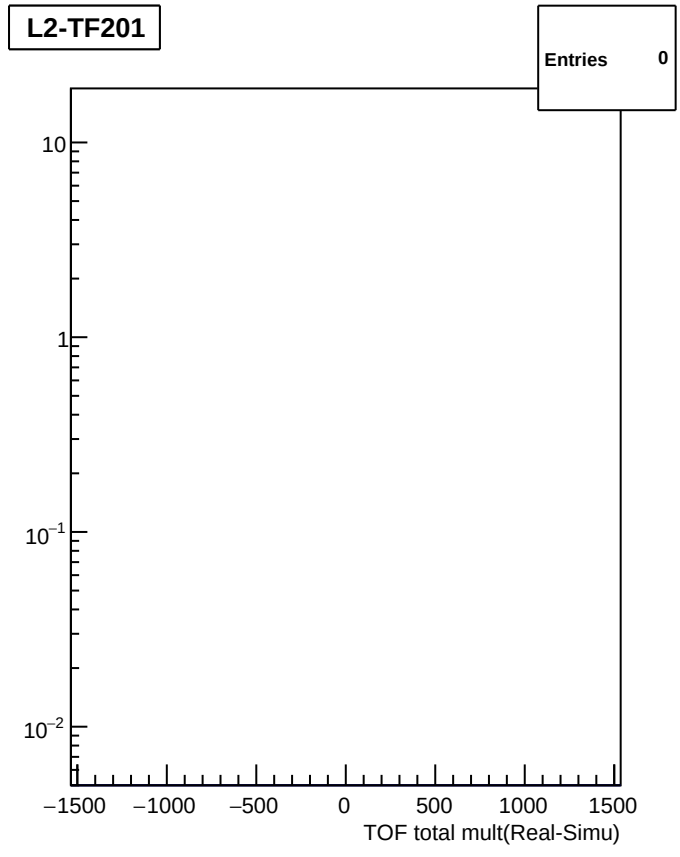

MIX-TF101
Entries 289560
TOF UPC-TH-EastLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

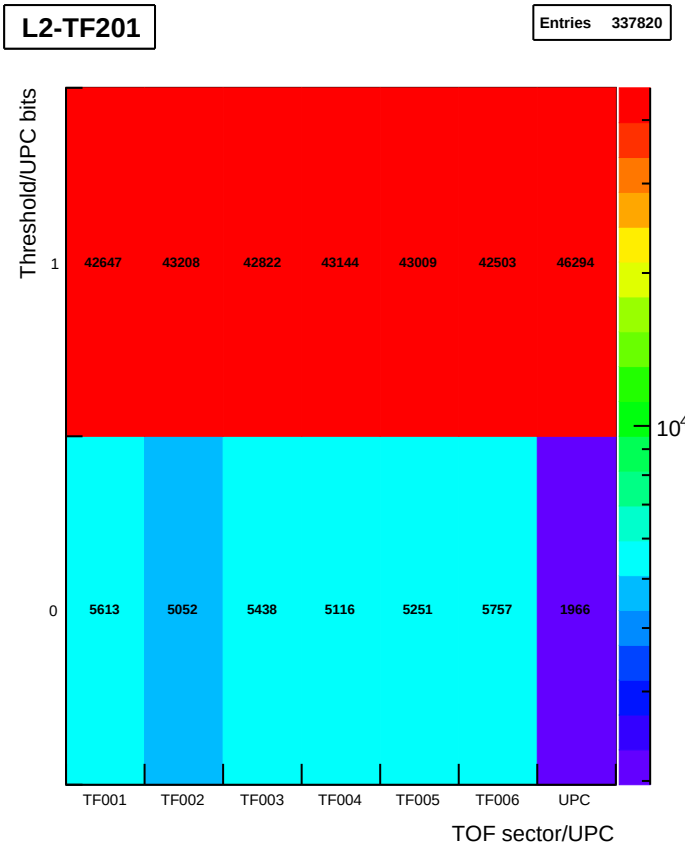

MIX-TF101
Entries 14198
TOF UPC-TH-EastLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

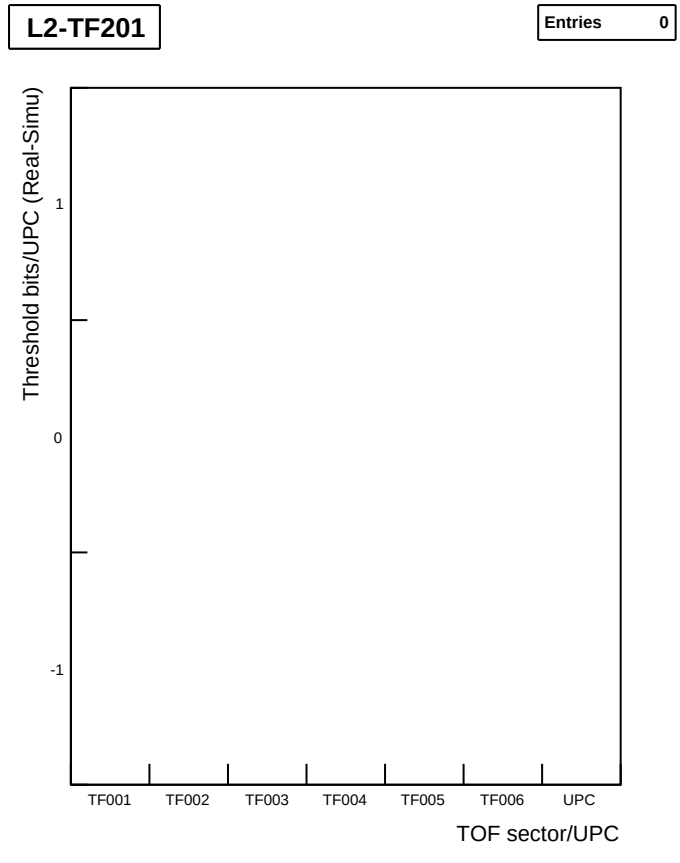

MIX-TF101
Entries 289560
TOF UPC-TH-EastHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

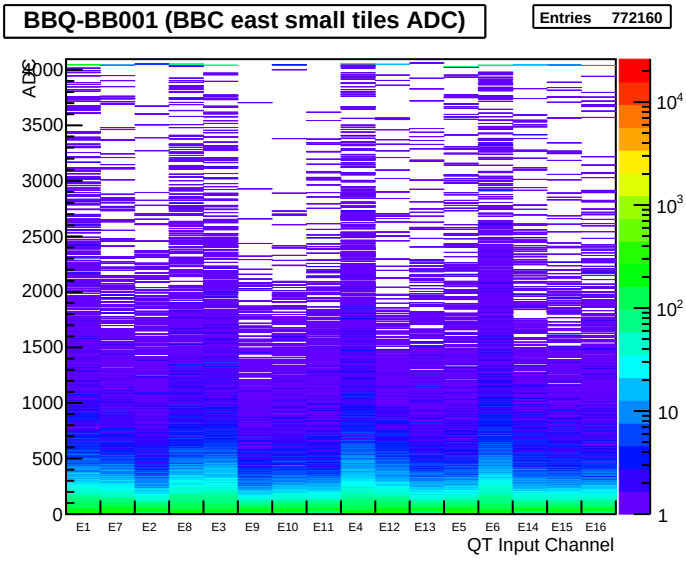

MIX-TF101
Entries 28948
TOF UPC-TH-EastHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

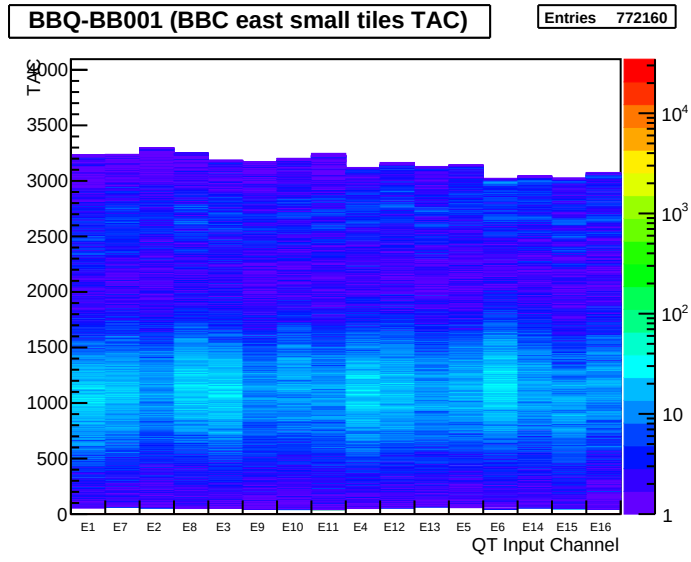

L2-TF201
Entries 48260
$10^3$
$10^2$
10
1
0
200
400
600
800
1000
1200
1400
TOF total mult

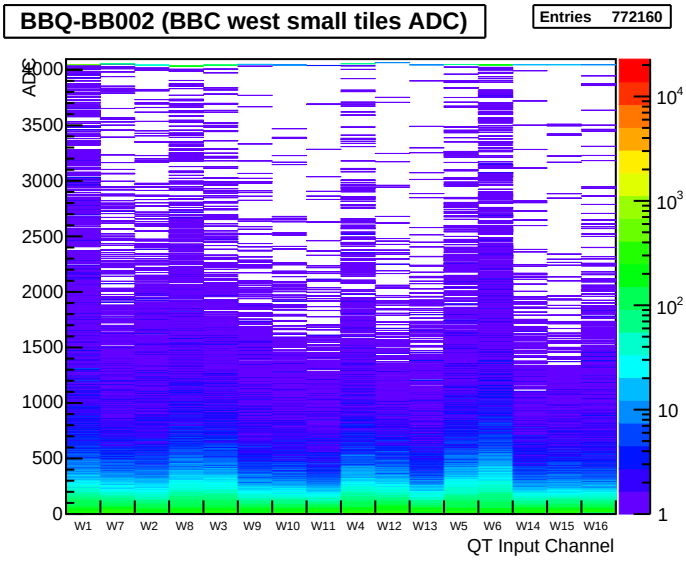

L2-TF201
Entries 0
10
1
$10^{-1}$
$10^{-2}$
−1500
−1000
−500
0
500
1000
1500
TOF total mult(Real-Simu)

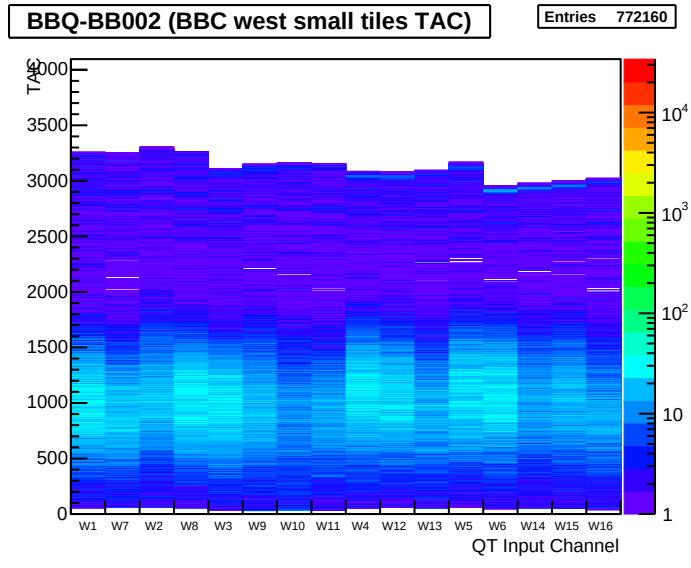

L2-TF201
Entries 337820
Threshold/UPC bits
1
0
42647
43208
42822
43144
43009
42503
46294
5613
5052
5438
5116
5251
5757
1966
TF001
TF002
TF003
TF004
TF005
TF006
UPC
TOF sector/UPC

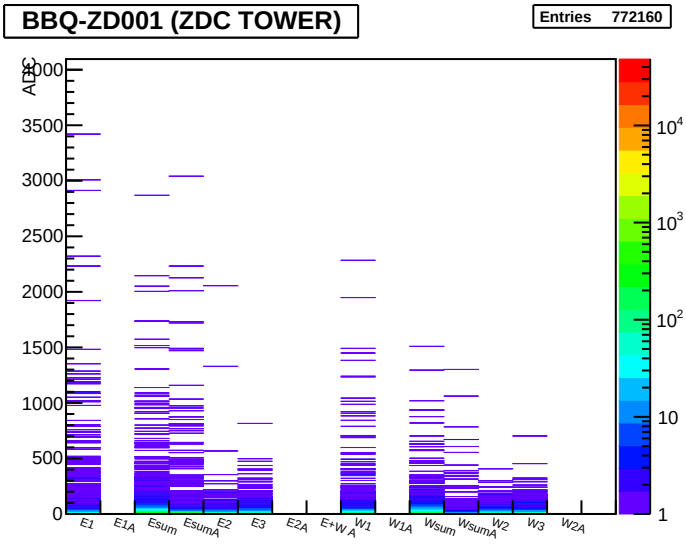

L2-TF201
Entries 0
Threshold bits/UPC (Real-Simu)
1
0
-1
TF001
TF002
TF003
TF004
TF005
TF006
UPC
TOF sector/UPC

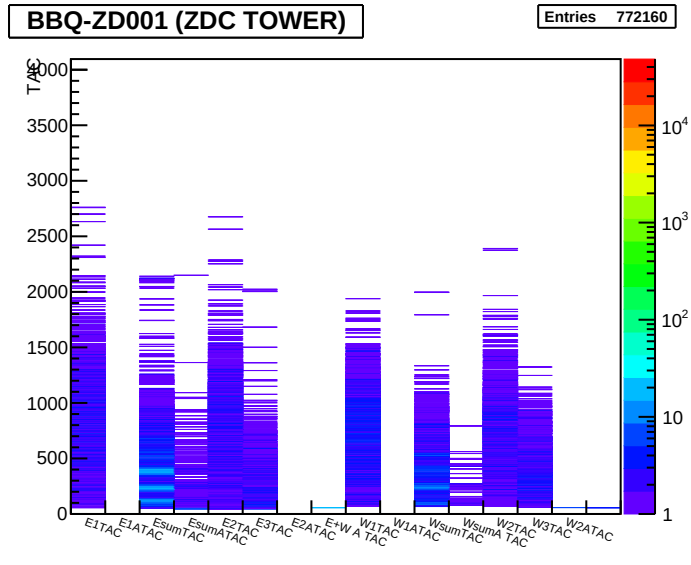

BBQ-BB001 (BBC east small tiles ADC)
Entries 772160
ADC
QT Input Channel

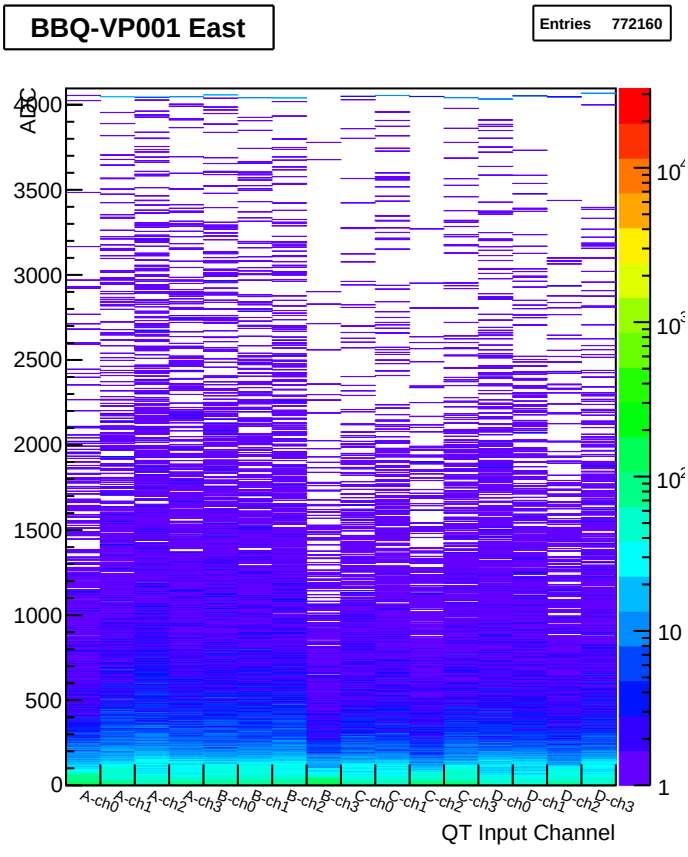

BBQ-BB001 (BBC east small tiles TAC)
Entries 772160
TAC
QT Input Channel

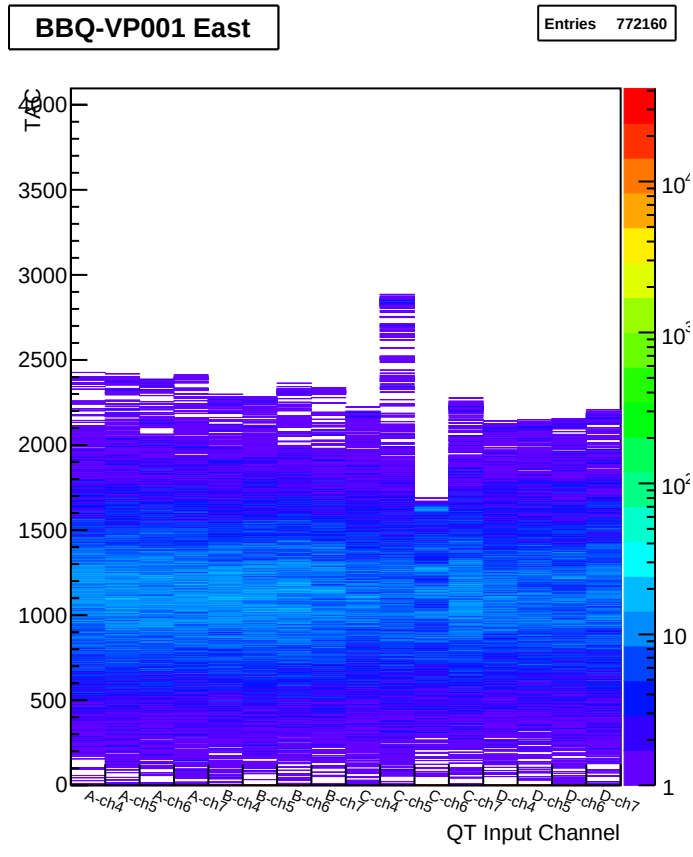

BBQ-BB002 (BBC west small tiles ADC)
Entries 772160
ADC
QT Input Channel

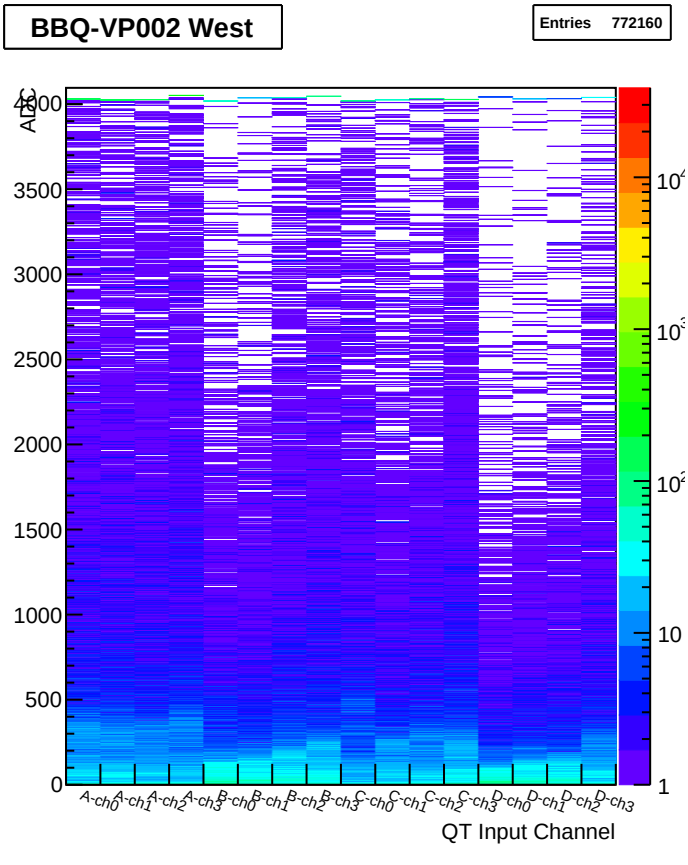

BBQ-BB002 (BBC west small tiles TAC)
Entries 772160
TAC
QT Input Channel

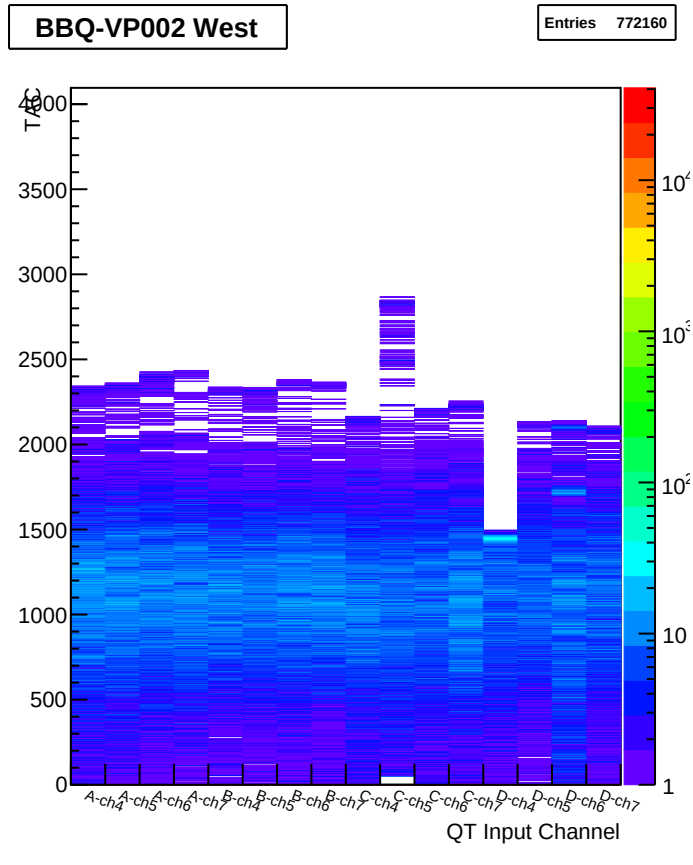

BBQ-ZD001 (ZDC TOWER)
Entries 772160
ADC

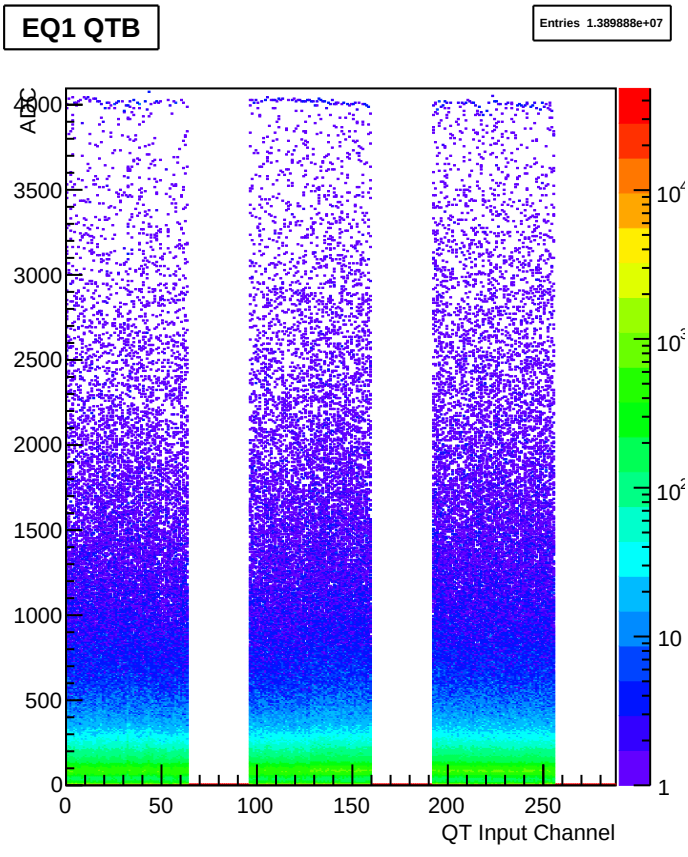

BBQ-ZD001 (ZDC TOWER)
Entries 772160
TAC

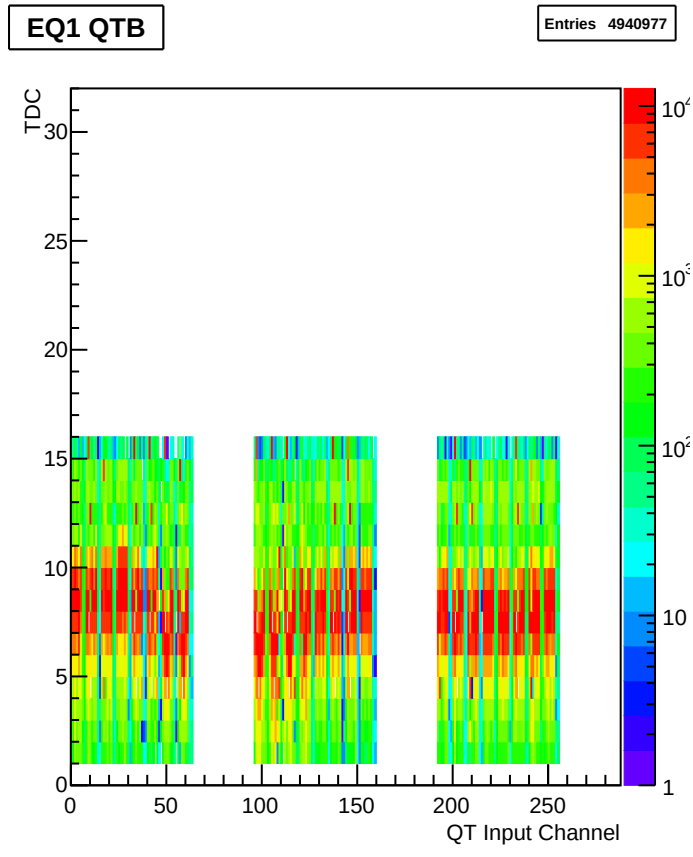

BBQ-VP001 East
Entries 772160
ADC
QT Input Channel

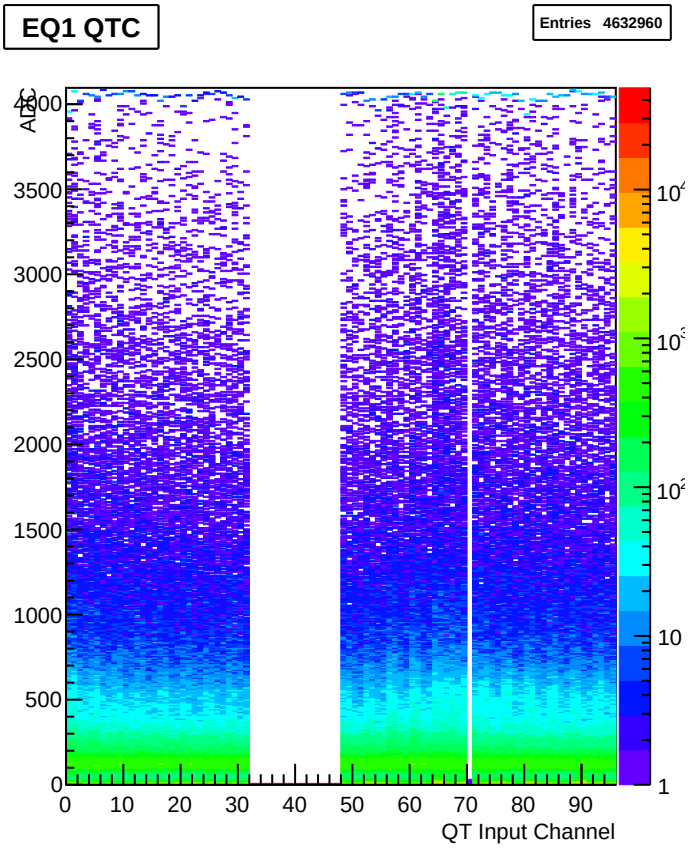

BBQ-VP001 East
Entries 772160
TADC
QT Input Channel

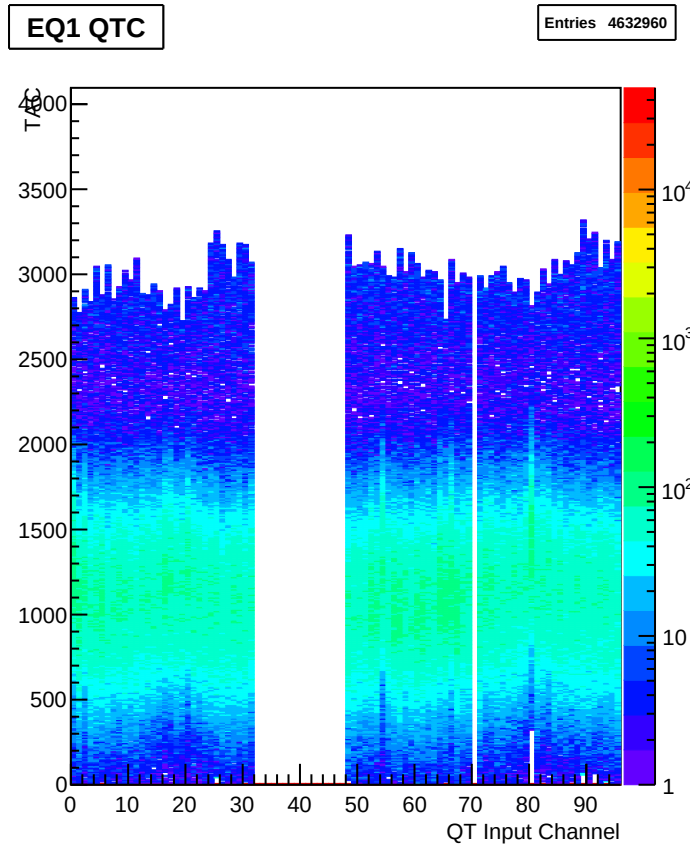

BBQ-VP002 West
Entries 772160
ADC
QT Input Channel

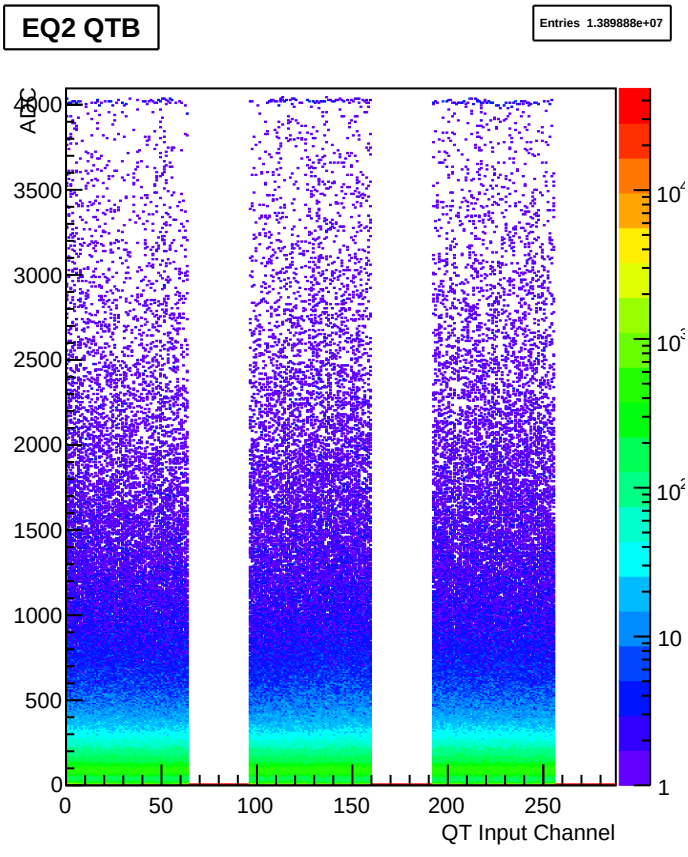

BBQ-VP002 West
Entries 772160
TADC
QT Input Channel

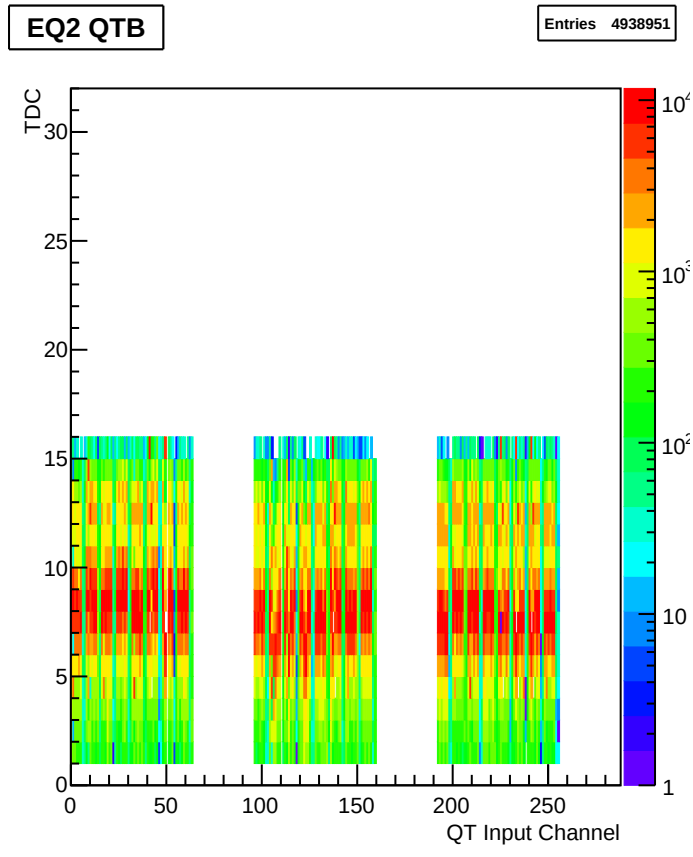

EQ1 QTB
Entries 1.389888e+07
ADC
QT Input Channel

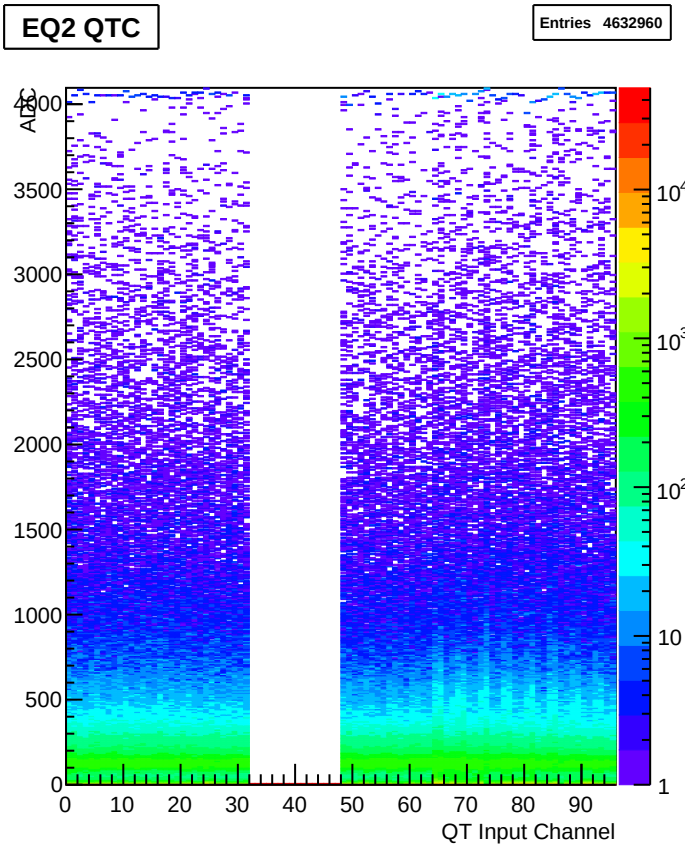

EQ1 QTB
Entries 4940977
TDC
QT Input Channel

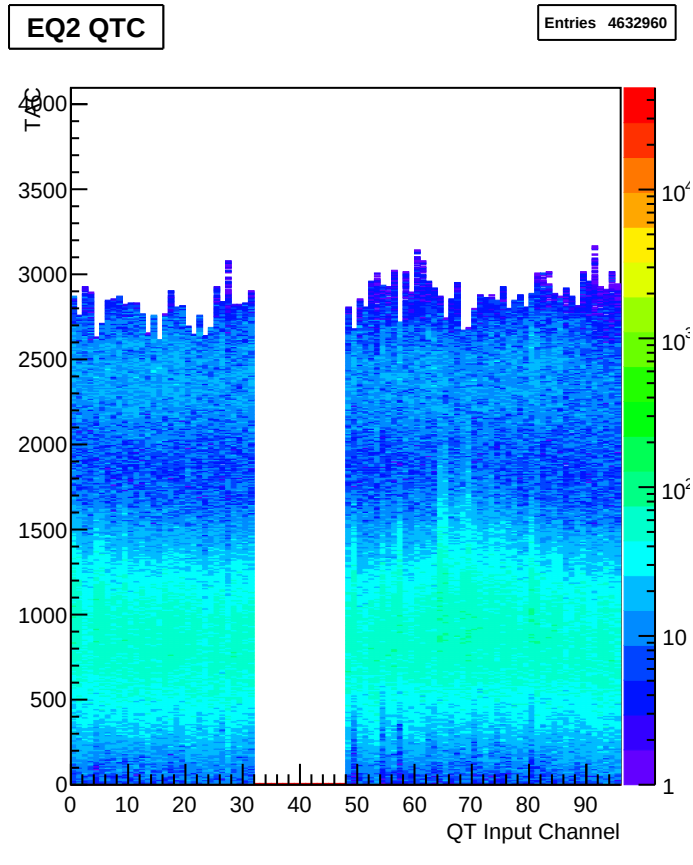

EQ1 QTC
Entries 4632960
ADC
QT Input Channel

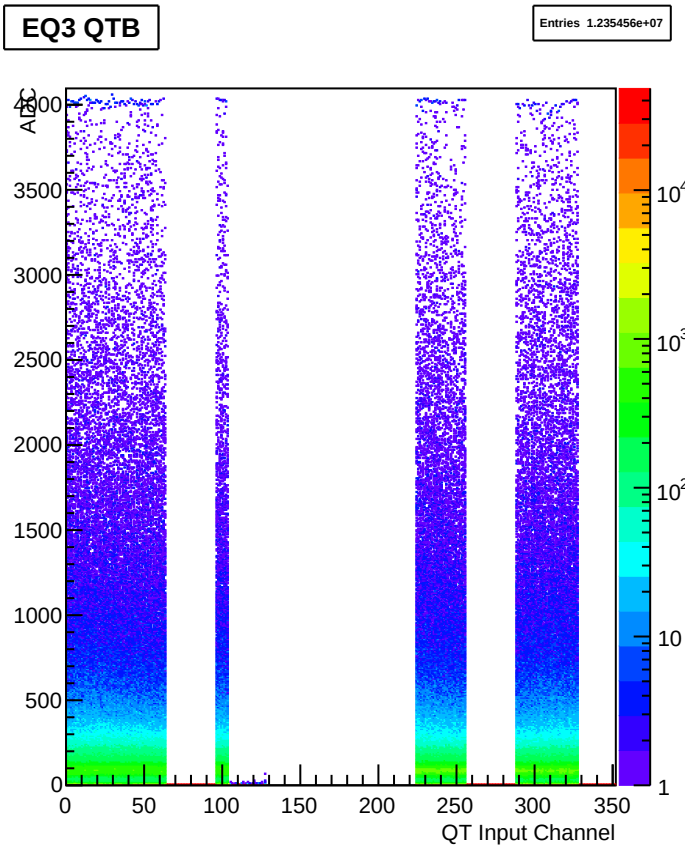

EQ1 QTC
Entries 4632960
TADC
QT Input Channel

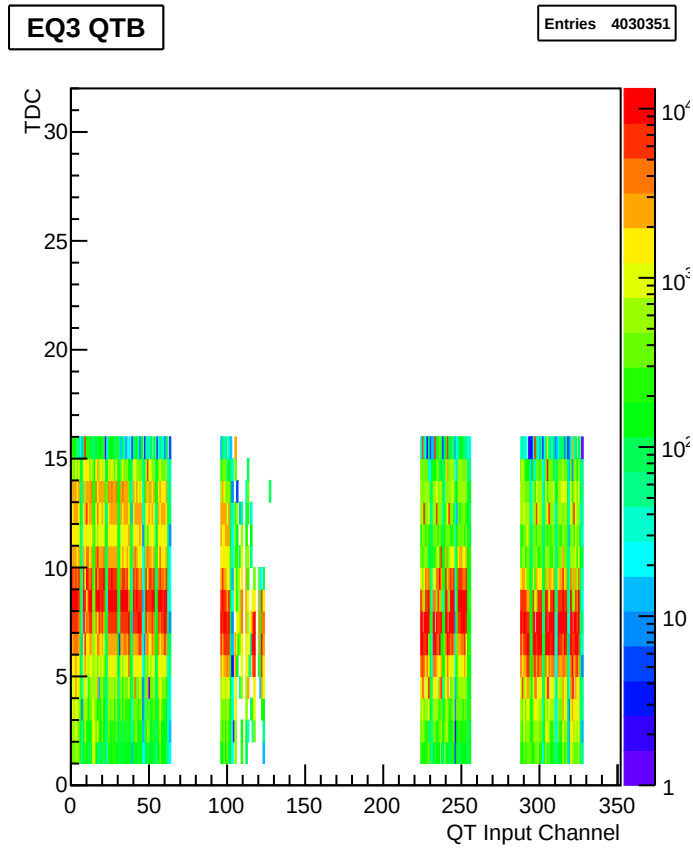

EQ2 QTB
Entries 1.389888e+07
ADC
QT Input Channel

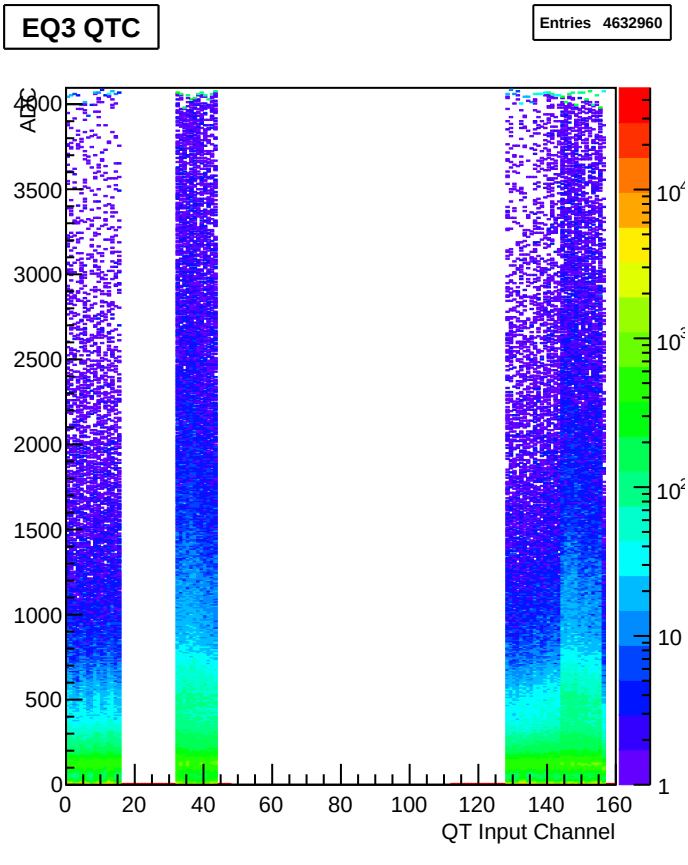

EQ2 QTB
Entries 4938951
TDC
QT Input Channel

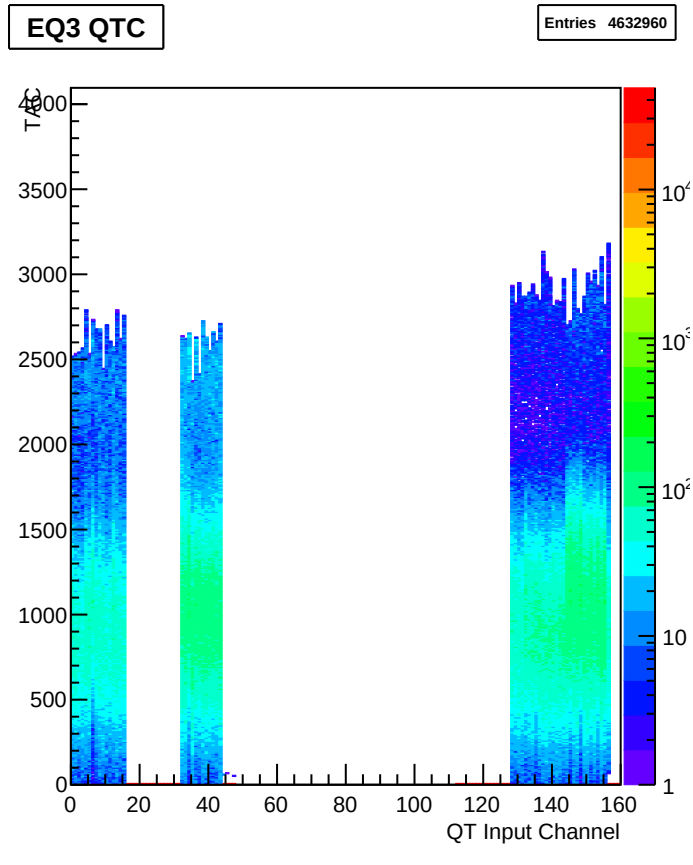

EQ2 QTC
Entries 4632960
ADC
QT Input Channel

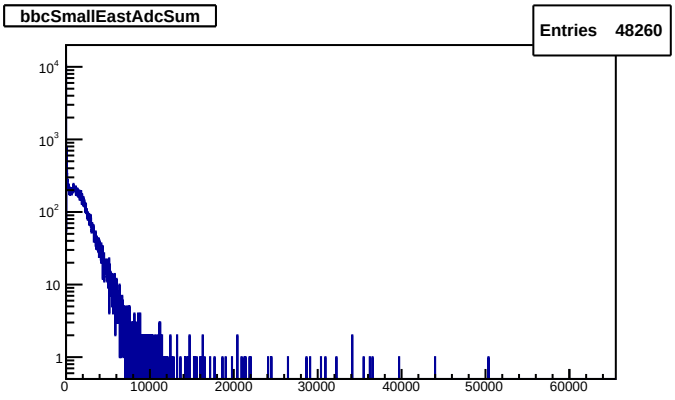

EQ2 QTC
Entries 4632960
TADC
QT Input Channel

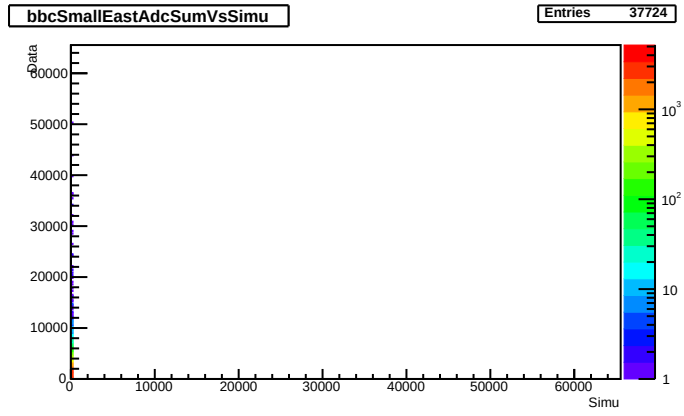

EQ3 QTB
Entries 1.235456e+07
ADC
QT Input Channel

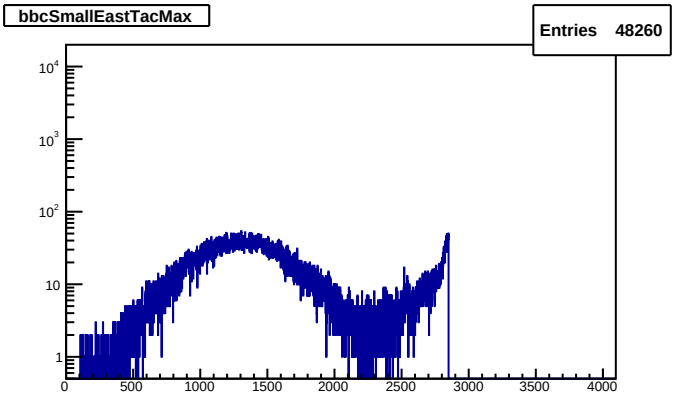

EQ3 QTB
Entries 4030351
TDC
QT Input Channel

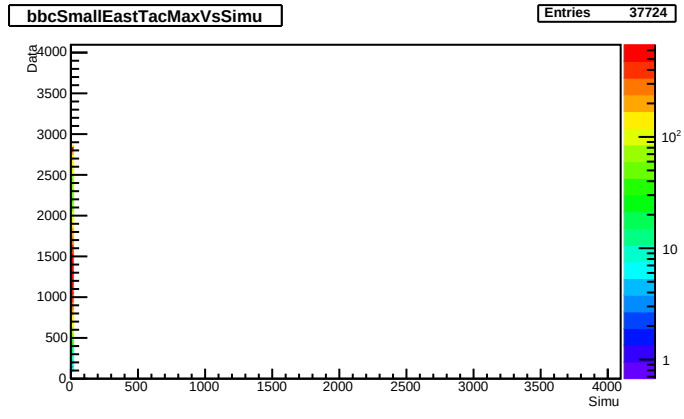

EQ3 QTC
Entries 4632960
ADC
QT Input Channel


EQ3 QTC
Entries 4632960
TADC
QT Input Channel


bbcSmallEastAdcSum
Entries 48260


bbcSmallEastAdcSumVsSimu
Entries 37724
Data
Simu


bbcSmallEastTacMax
Entries 48260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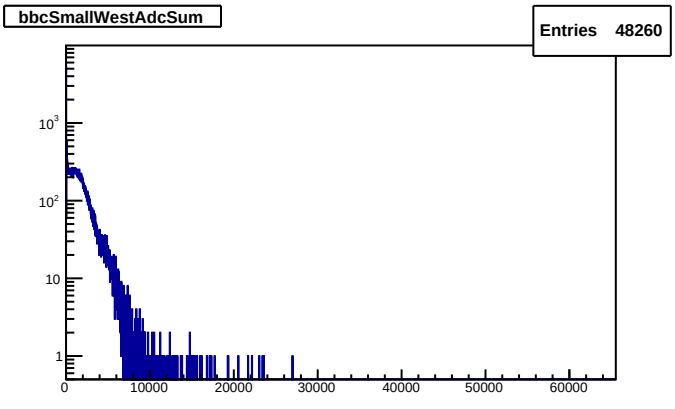
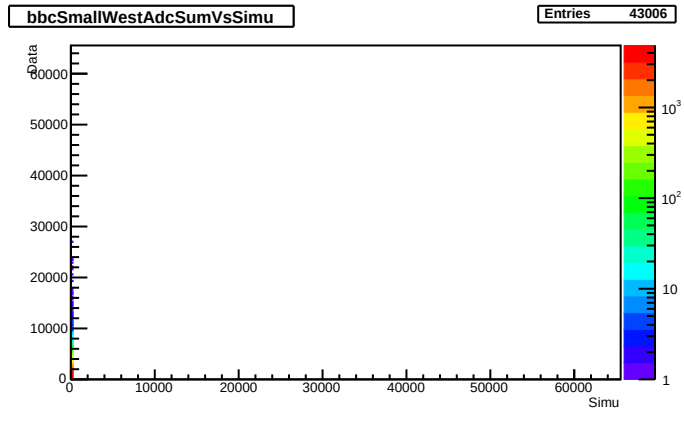
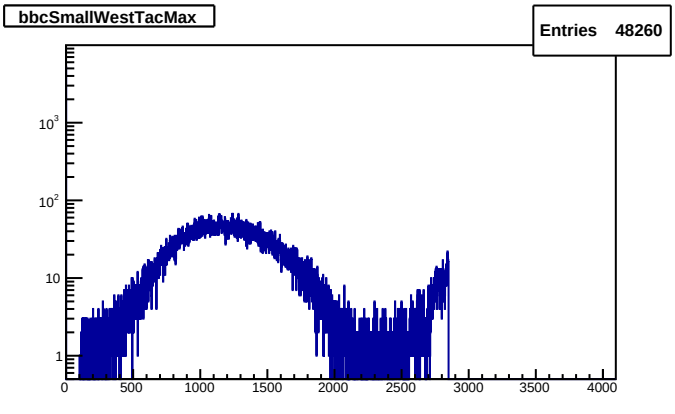
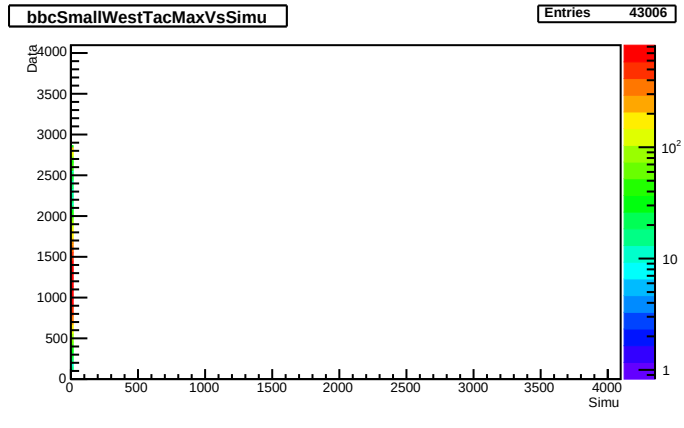
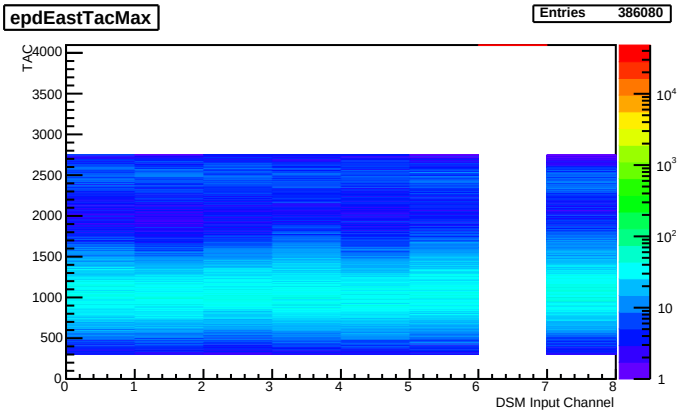
Entries 37724
Data
Simu

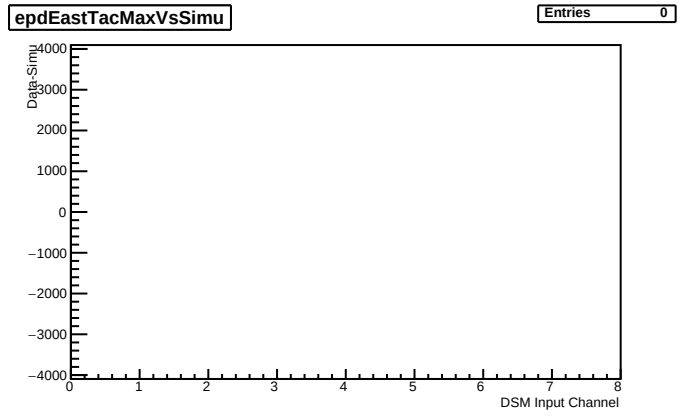

bbcSmallWestAdcSum
Entries 48260

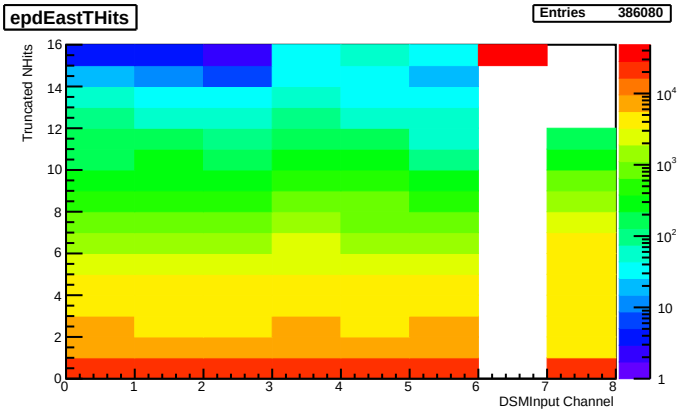

bbcSmallWestAdcSumVsSimu
Entries 43006
Data
Simu

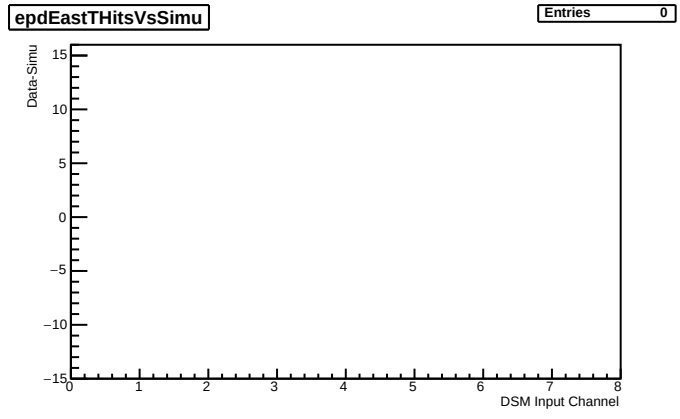

bbcSmallWestTacMax
Entries 48260

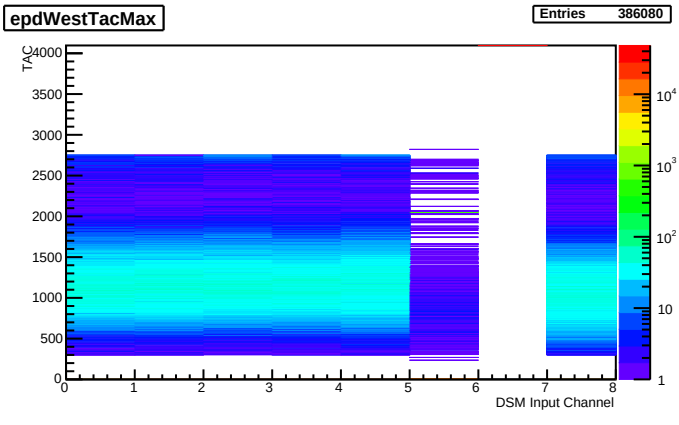

bbcSmallWestTacMaxVsSimu
Entries 43006
Data
Simu

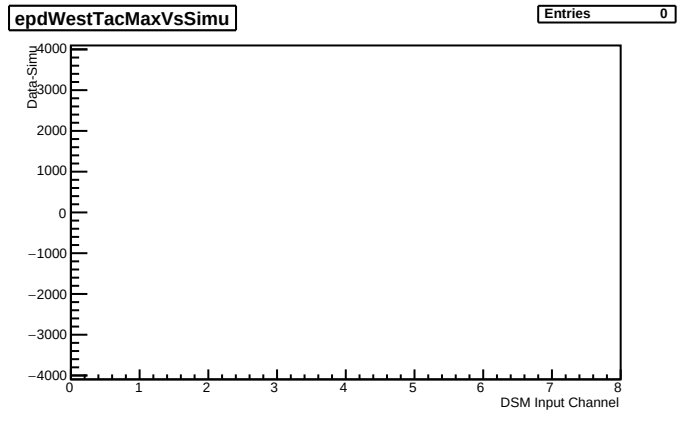

epdEastTacMax
Entries 386080
TAC
DSM Input Channel

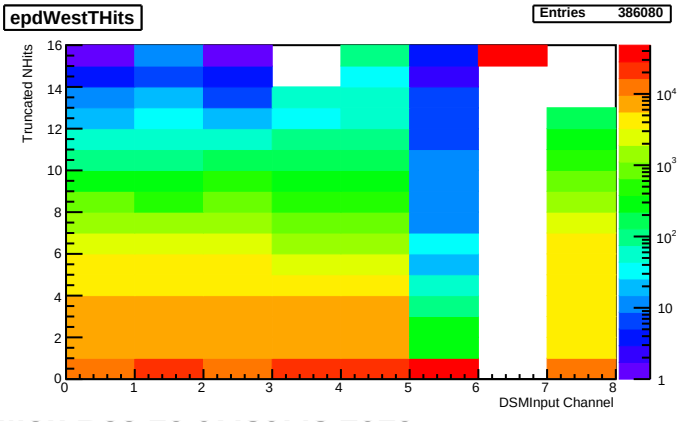

epdEastTacMaxVsSimu
Entries 0
Data-Simu
DSM Input Channel

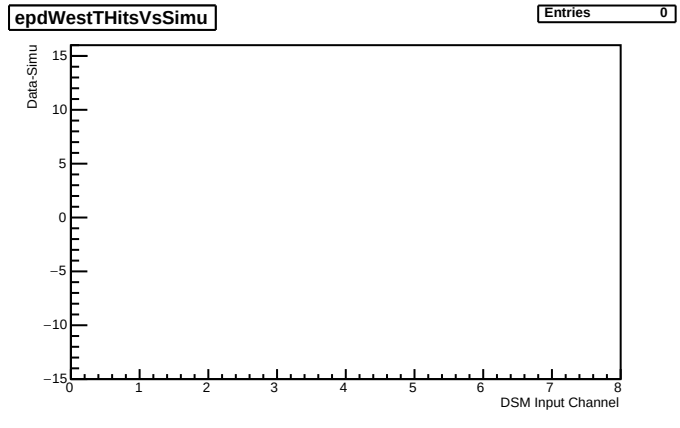

epdEastTHits
Entries 386080
Truncated NHits
DSMInput Channel

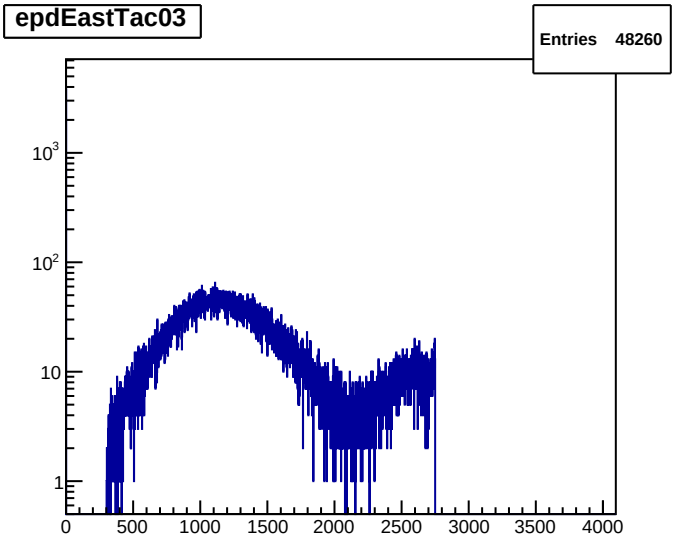

epdEastTHitsVsSimu
Entries 0
Data-Simu
DSM Input Channel

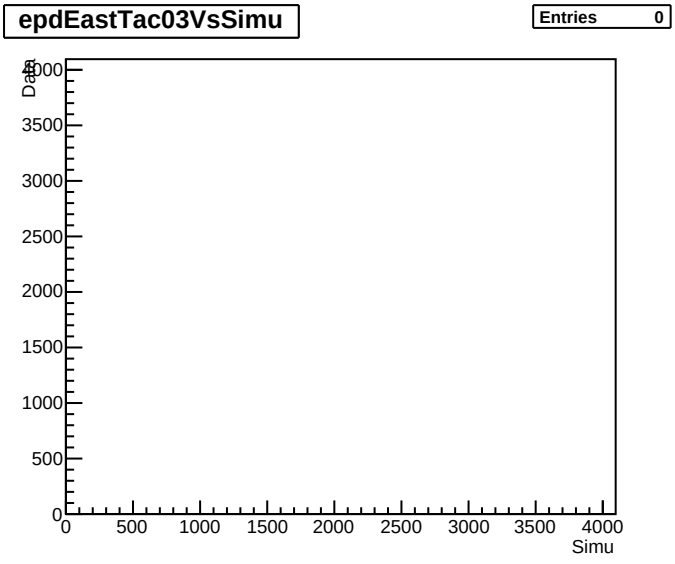

epdWestTacMax
Entries 386080
TAC
DSM Input Channel

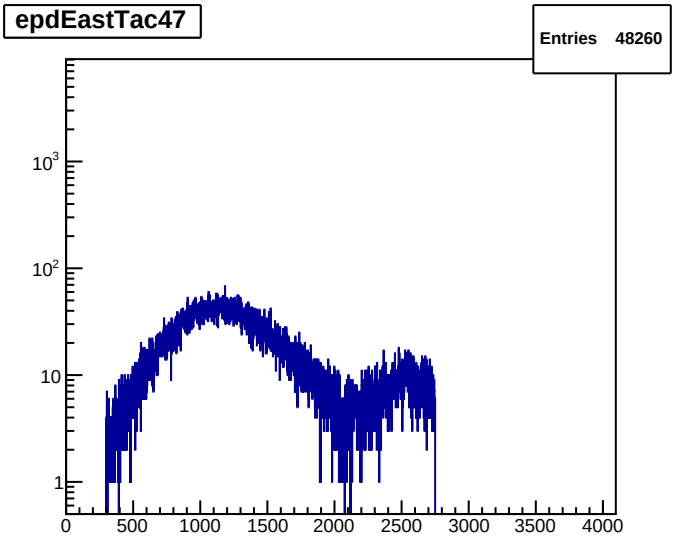

epdWestTacMaxVsSimu
Entries 0
Data-Simu
DSM Input Channel

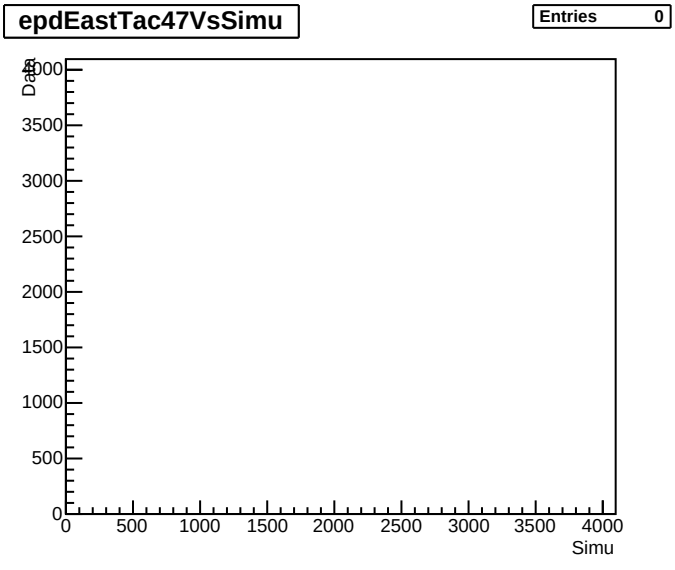

epdWestTHits
Entries 386080
Truncated NHits
DSMInput Channel

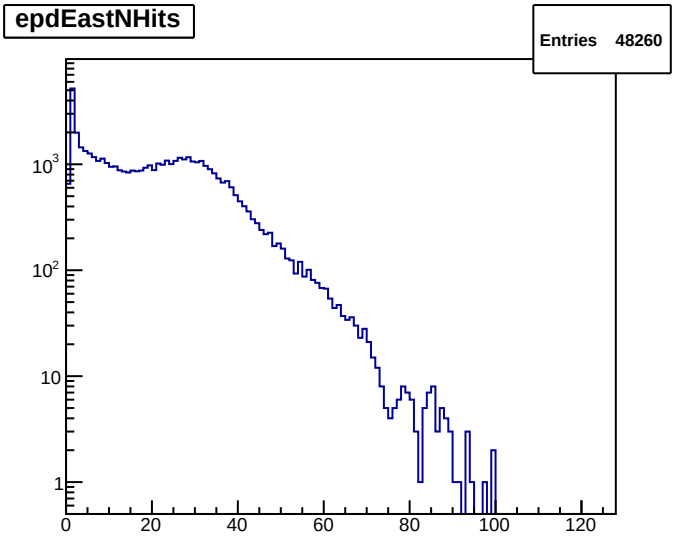

epdWestTHitsVsSimu
Entries 0
Data-Simu
DSM Input Channel

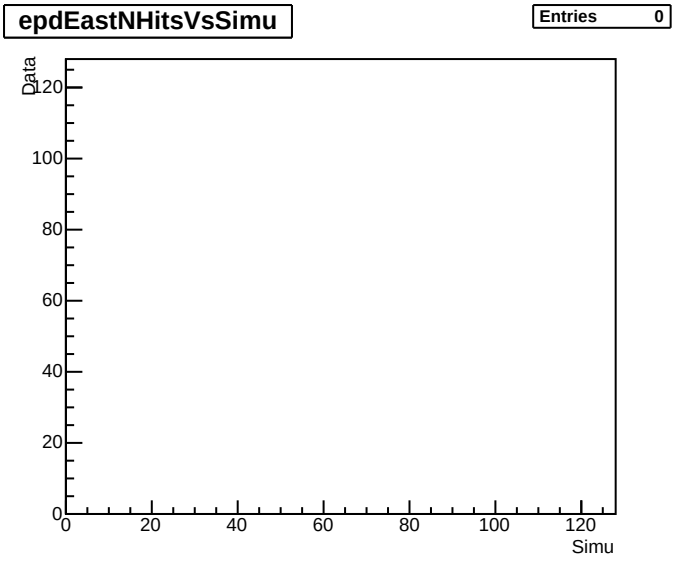

epdEastTac03
Entries 48260

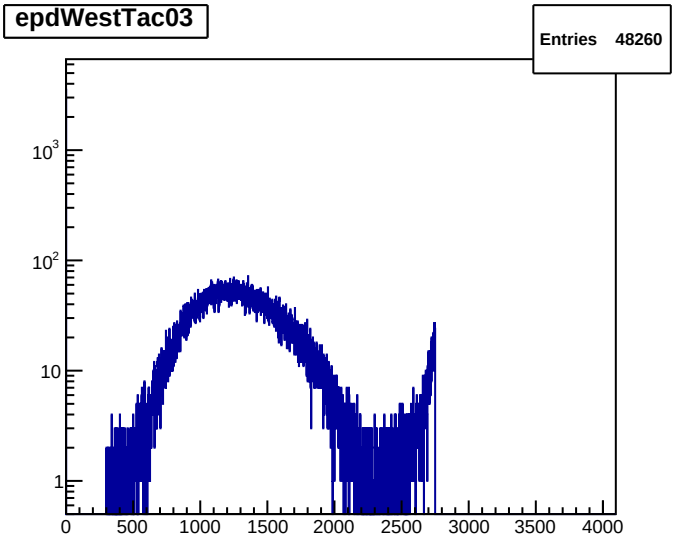

epdEastTac03VsSimu
Entries 0
Data
Simu

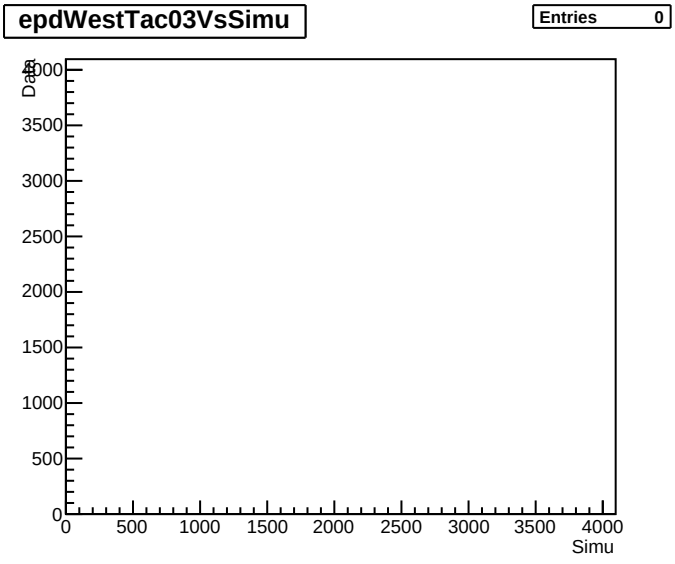

epdEastTac47
Entries 48260

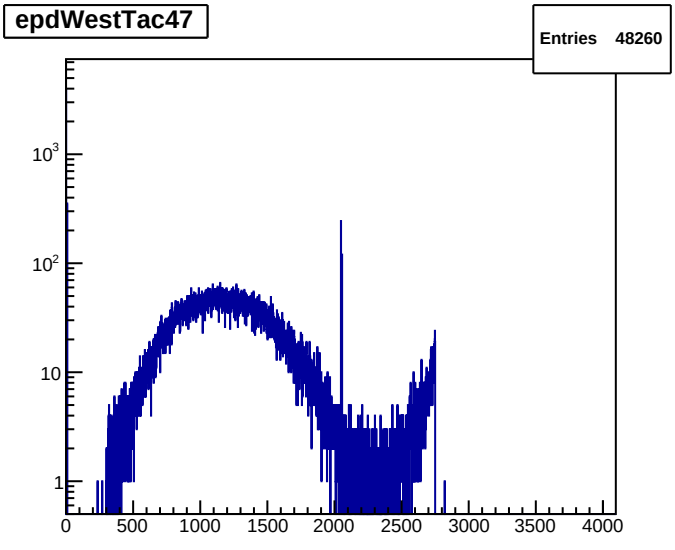

epdEastTac47VsSimu
Entries 0
Data
Simu

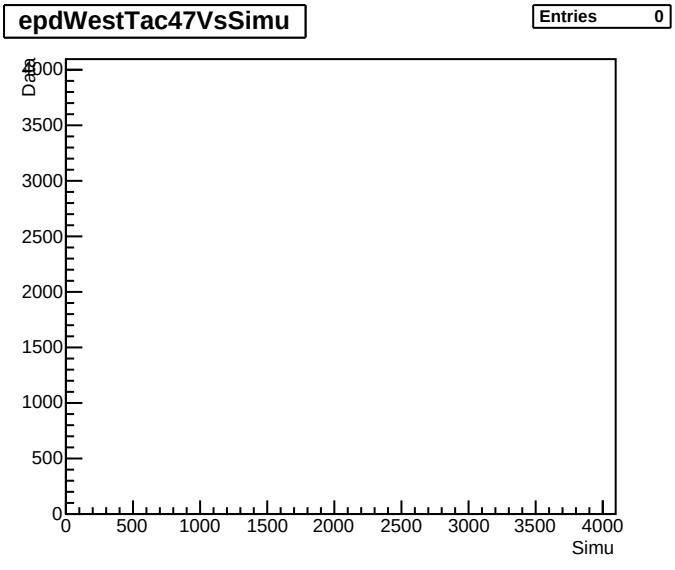

epdEastNHits
Entries 48260


epdEastNHitsVsSimu
Entries 0
Data
Simu


epdWestTac03
Entries 48260


epdWestTac03VsSimu
Entries 0
Data
Simu


epdWestTac47
Entries 48260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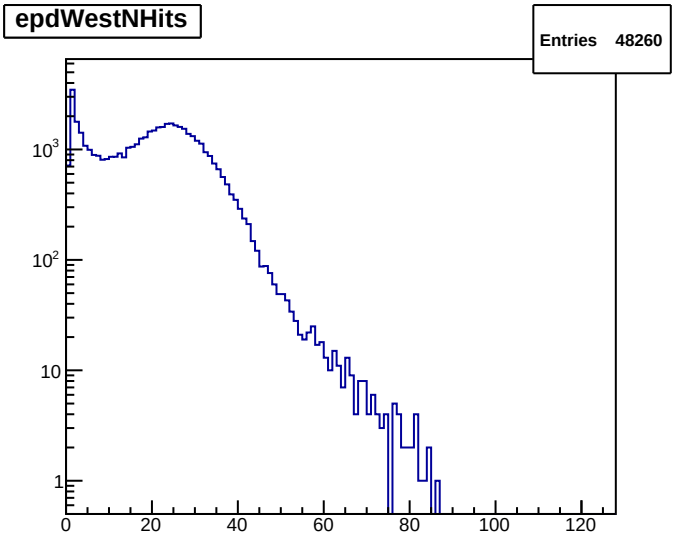
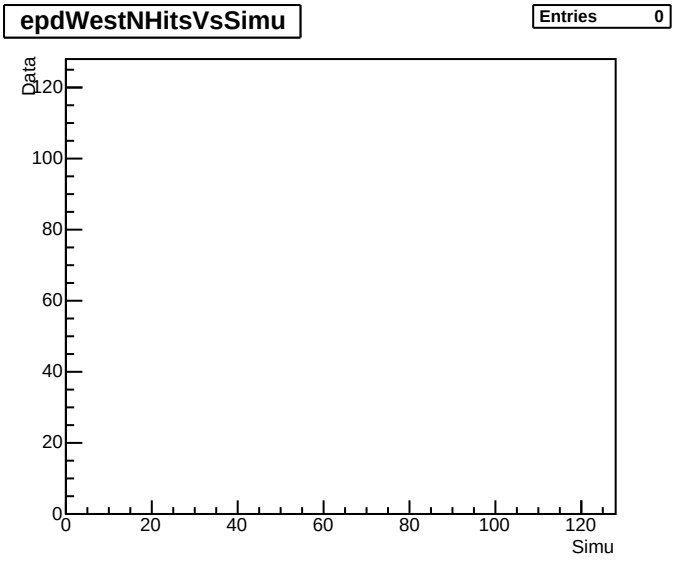
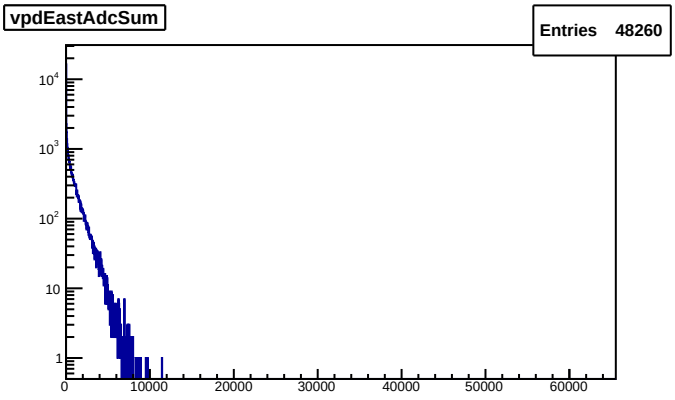
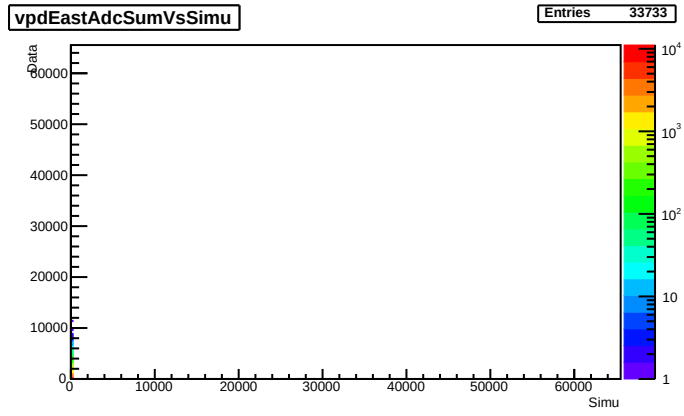
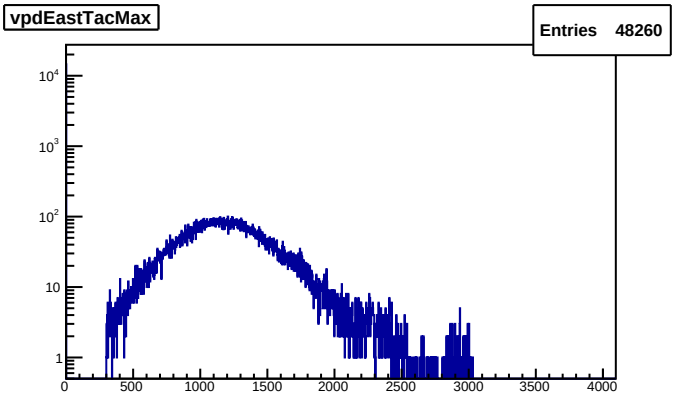
Entries 0
Data
Simu

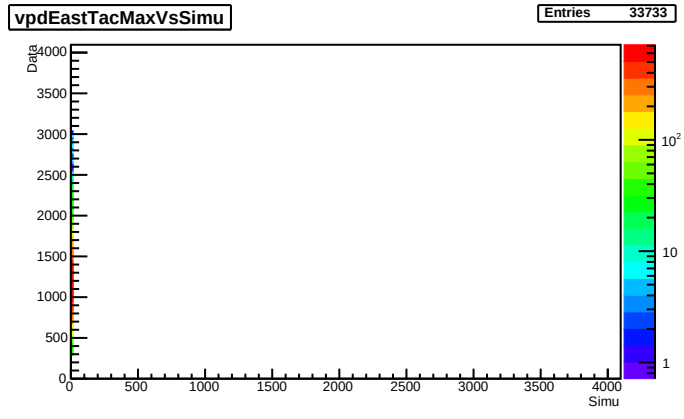

epdWestNHits
Entries 48260

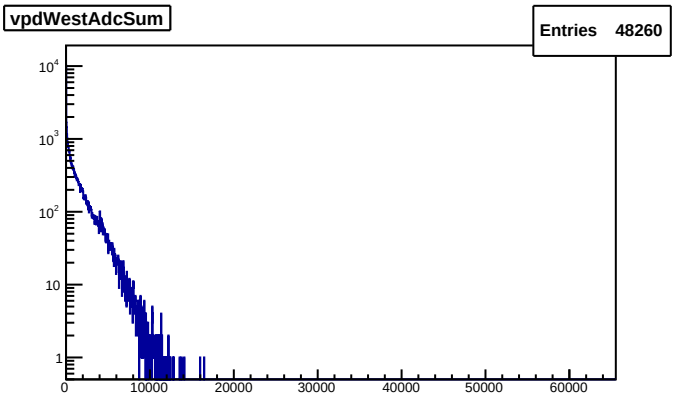

epdWestNHitsVsSimu
Entries 0
Data
Simu

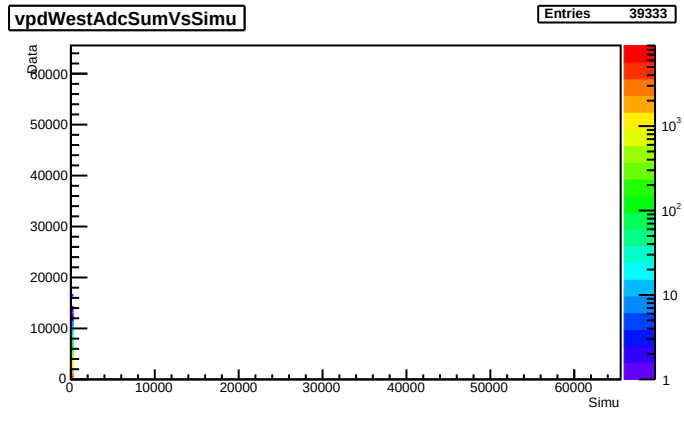

vpdEastAdcSum
Entries 48260

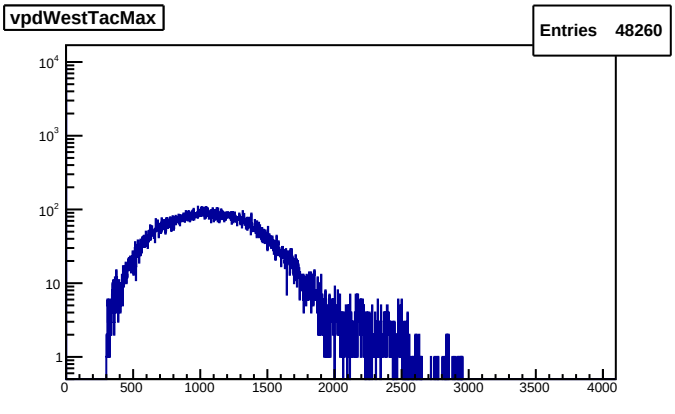

vpdEastAdcSumVsSimu
Entries 33733
Data
Simu

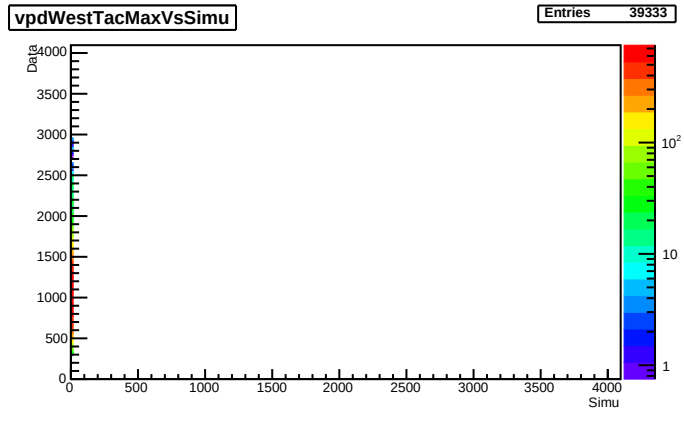

vpdEastTacMax
Entries 48260

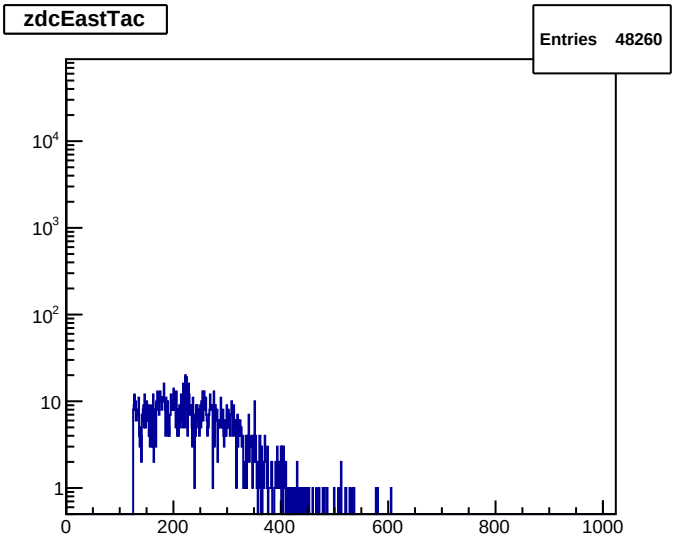

vpdEastTacMaxVsSimu
Entries 33733
Data
Simu

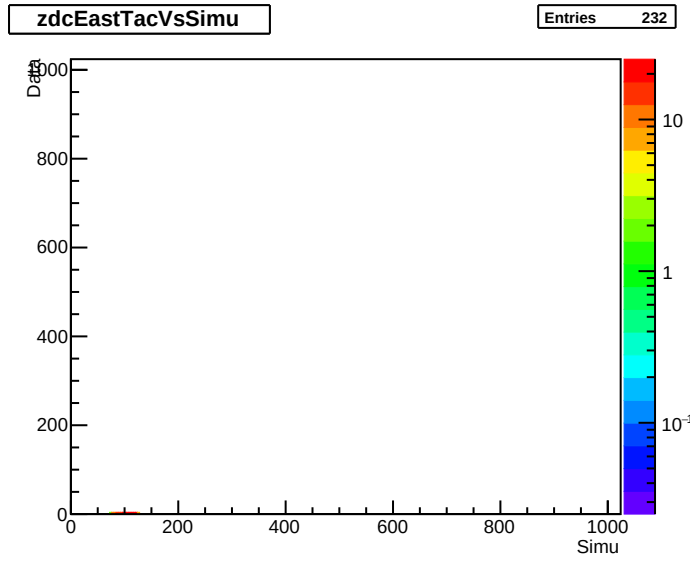

vpdWestAdcSum
Entries 48260

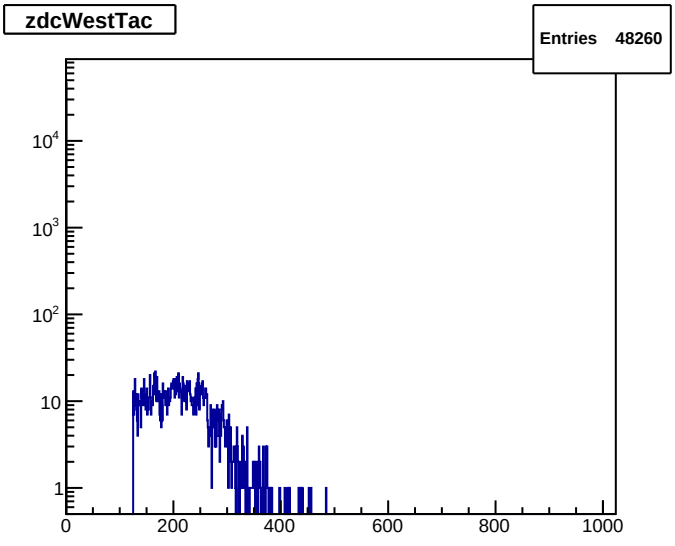

vpdWestAdcSumVsSimu
Entries 39333
Data
Simu

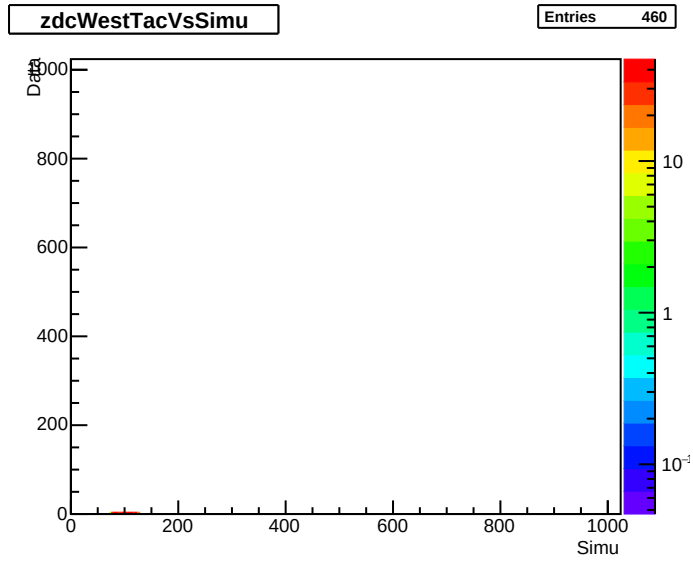

vpdWestTacMax
Entries 48260

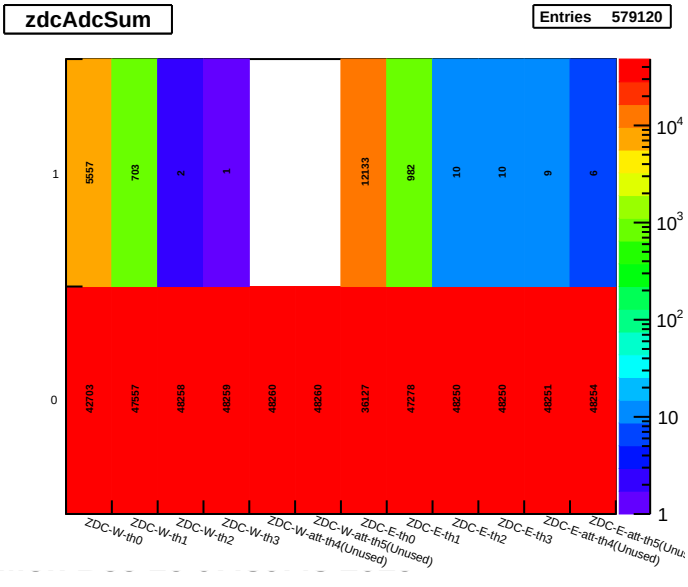

vpdWestTacMaxVsSimu
Entries 39333
Data
Simu

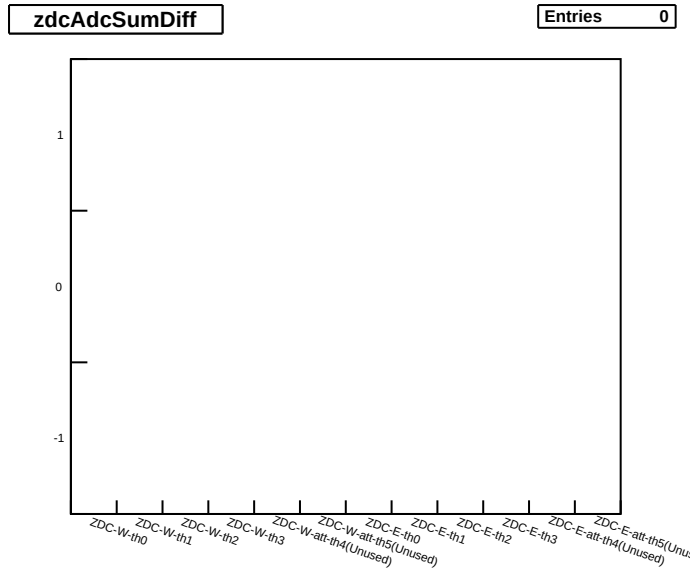

zdcEastTac
Entries 48260

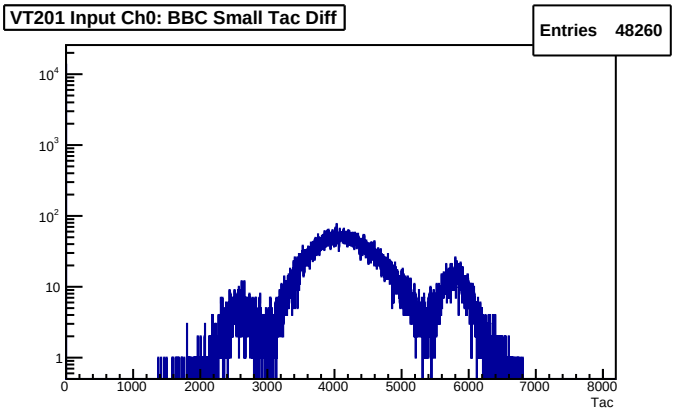

zdcEastTacVsSimu
Entries 232
Data
Simu

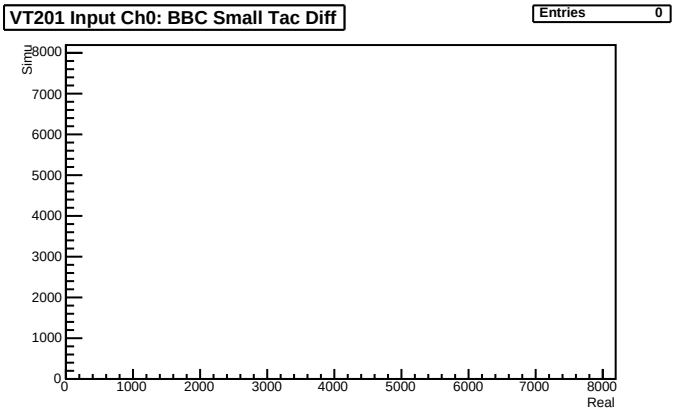

zdcWestTac
Entries 48260

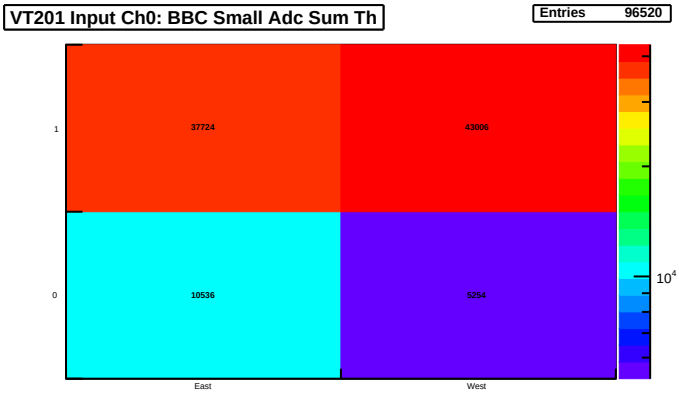

zdcWestTacVsSimu
Entries 460
Data
Simu

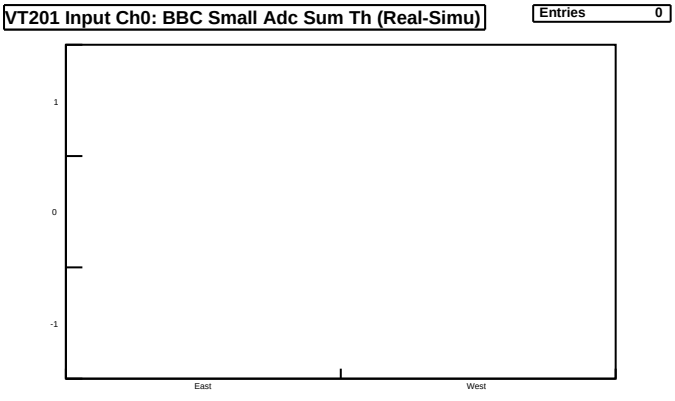

zdcAdcSum
Entries 579120
5557
703
2
1
12133
982
10
10
9
6
42703
47557
48258
48259
48260
48260
36127
47278
48250
48250
48251
48254
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)

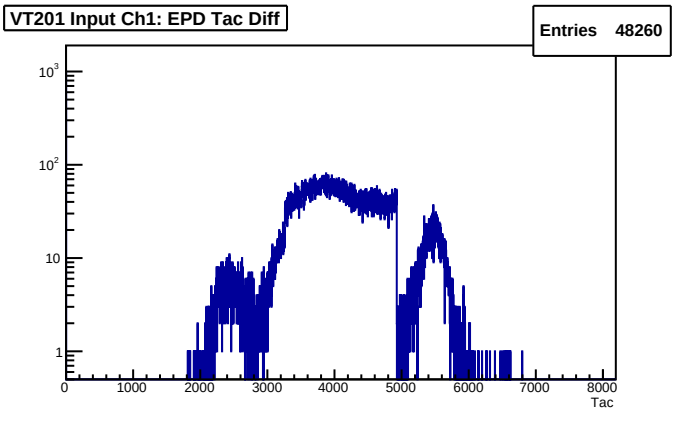

zdcAdcSumDiff
Entries 0
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)

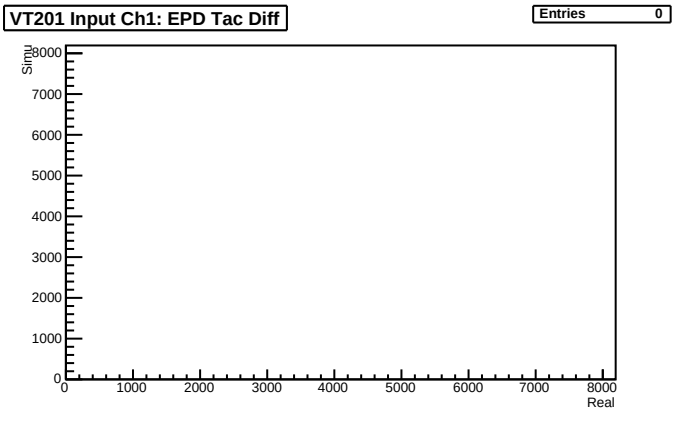

VT201 Input Ch0: BBC Small Tac Diff
Entries 48260
Tac

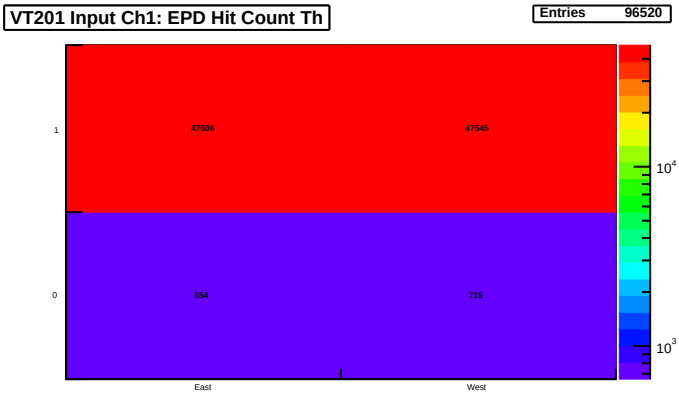

VT201 Input Ch0: BBC Small Tac Diff
Entries 0
Simu
Real

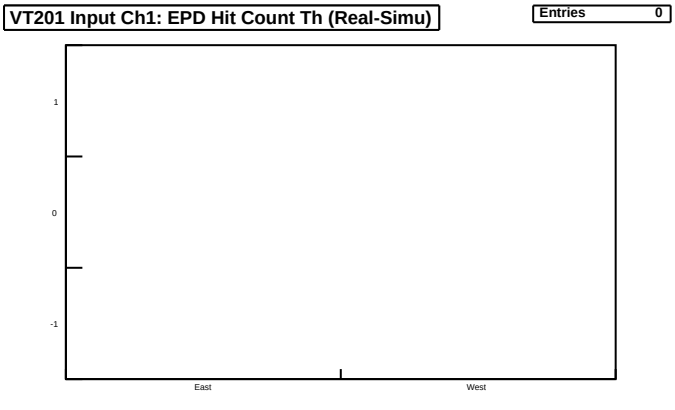

VT201 Input Ch0: BBC Small Adc Sum Th
Entries 96520
37724
43006
10536
5254
East
West

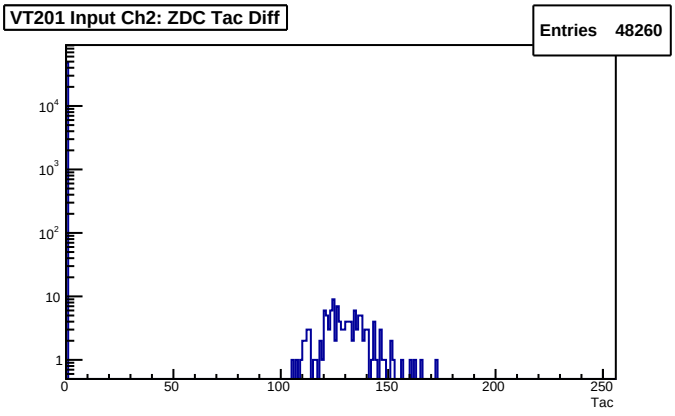

VT201 Input Ch0: BBC Small Adc Sum Th (Real-Simu)
Entries 0
East
West

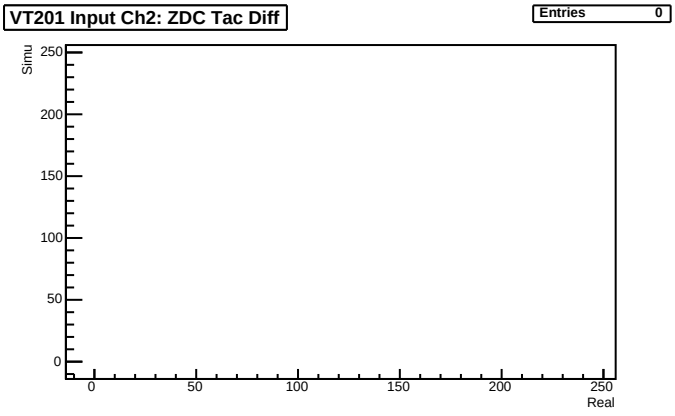

VT201 Input Ch1: EPD Tac Diff
Entries 48260
Tac

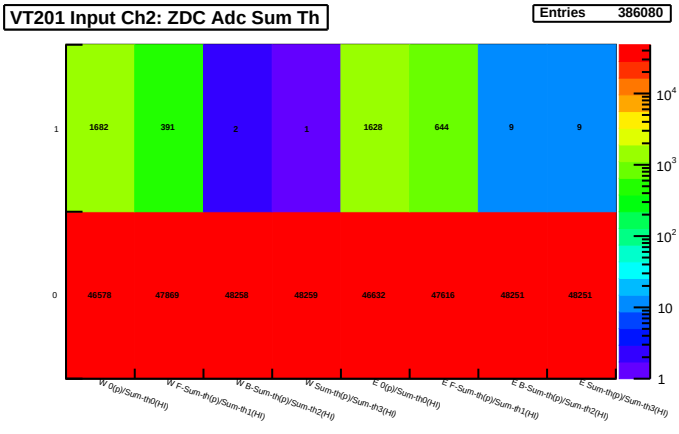

VT201 Input Ch1: EPD Tac Diff
Entries 0
Simu
Real

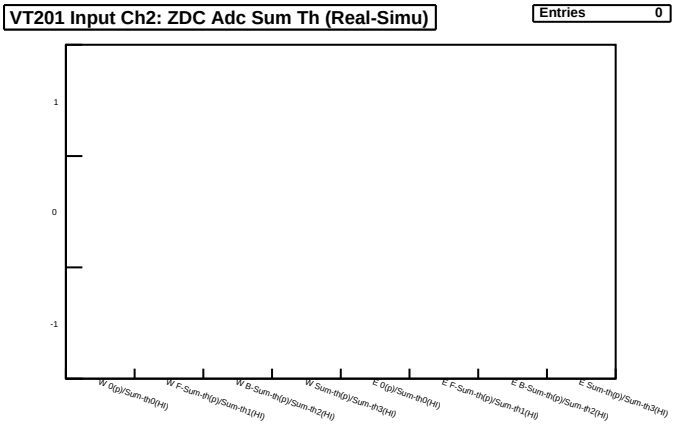

VT201 Input Ch1: EPD Hit Count Th
Entries 96520
47606
47545
654
715
East
West

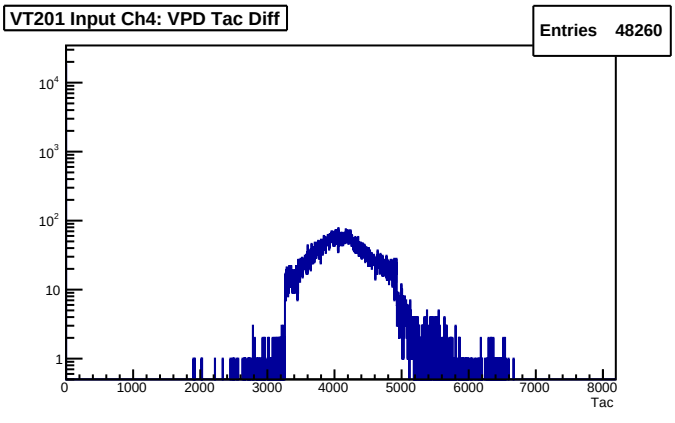

VT201 Input Ch1: EPD Hit Count Th (Real-Simu)
Entries 0
East
West

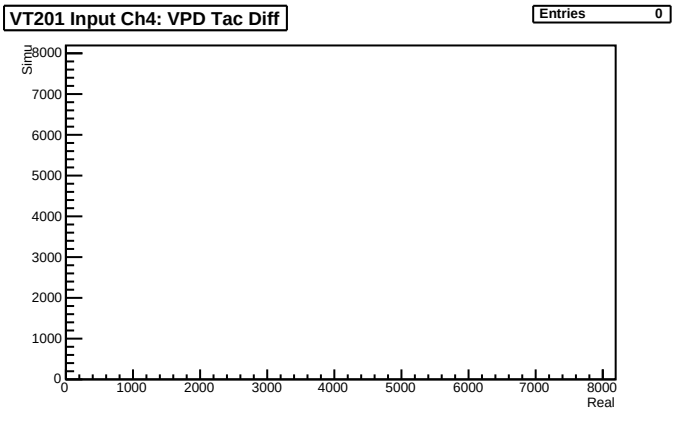

VT201 Input Ch2: ZDC Tac Diff
Entries 48260
Tac

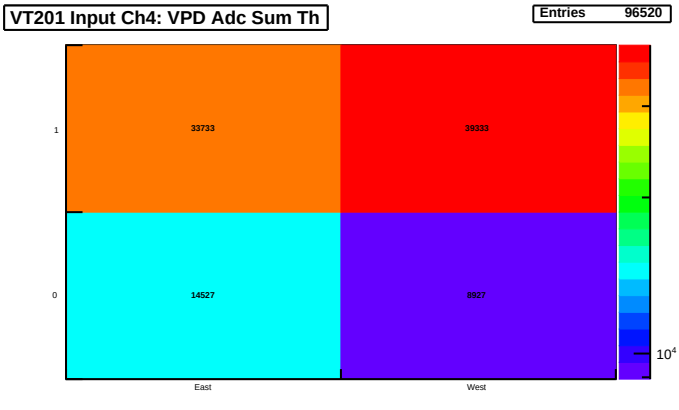

VT201 Input Ch2: ZDC Tac Diff
Entries 0
Simu
Real

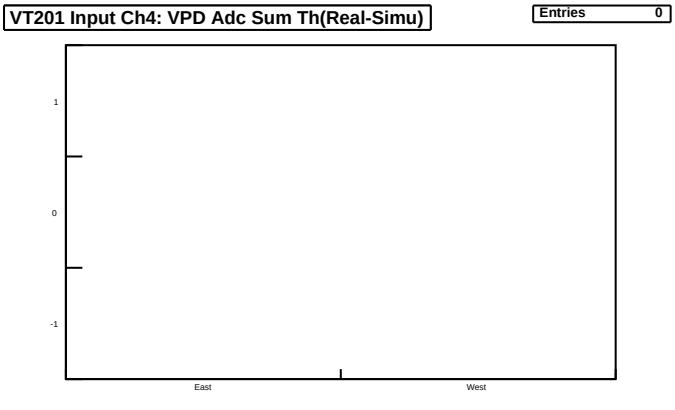

VT201 Input Ch2: ZDC Adc Sum Th
Entries 386080
1682
391
2
1
1628
644
9
9
46578
47869
48258
48259
46632
47616
48251
48251
W 0(p)/Sum-th0(Hl)
W F-Sum-th(p)/Sum-th1(Hl)
W B-Sum-th(p)/Sum-th2(Hl)
W Sum-th(p)/Sum-th3(Hl)
E 0(p)/Sum-th0(Hl)
E F-Sum-th(p)/Sum-th1(Hl)
E B-Sum-th(p)/Sum-th2(Hl)
E Sum-th(p)/Sum-th3(Hl)

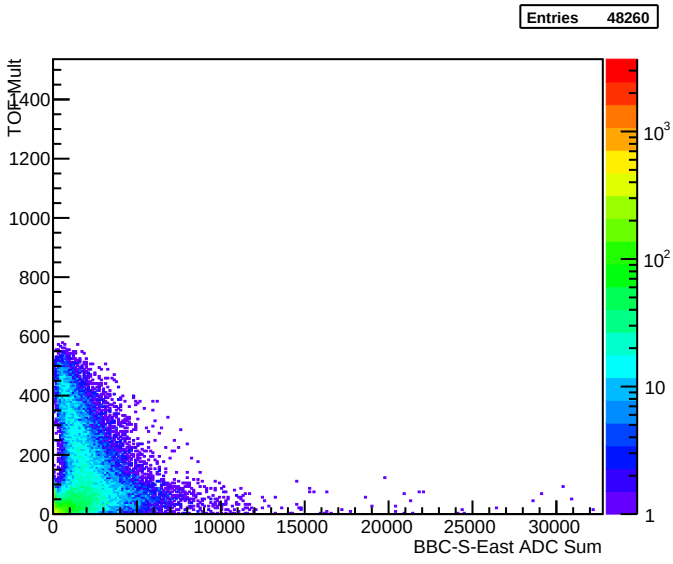

VT201 Input Ch2: ZDC Adc Sum Th (Real-Simu)
Entries 0
W 0(p)/Sum-th0(Hl)
W F-Sum-th(p)/Sum-th1(Hl)
W B-Sum-th(p)/Sum-th2(Hl)
W Sum-th(p)/Sum-th3(Hl)
E 0(p)/Sum-th0(Hl)
E F-Sum-th(p)/Sum-th1(Hl)
E B-Sum-th(p)/Sum-th2(Hl)
E Sum-th(p)/Sum-th3(Hl)

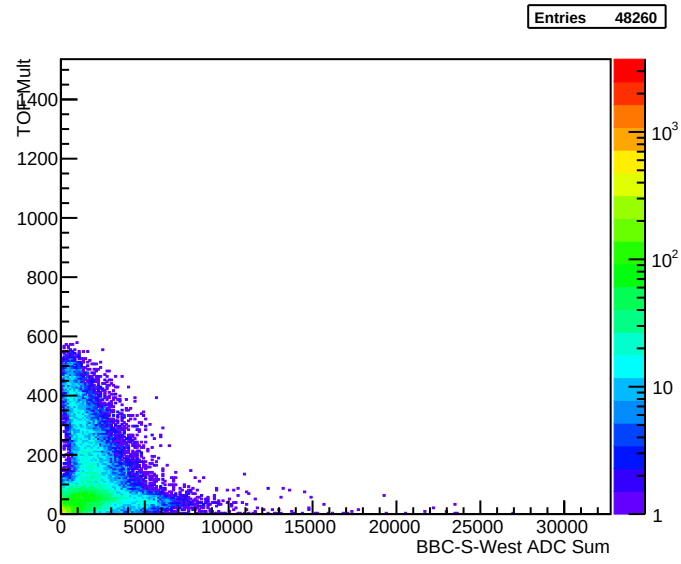

VT201 Input Ch4: VPD Tac Diff
Entries 48260
Tac

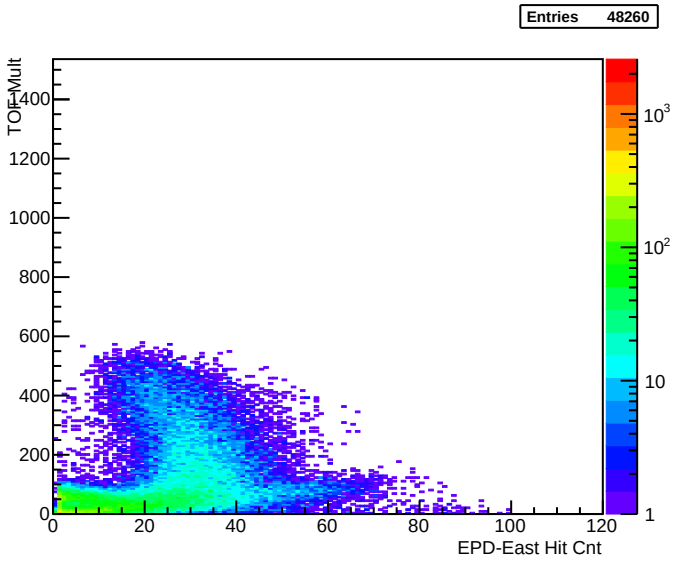

VT201 Input Ch4: VPD Tac Diff
Entries 0
Simu
Real

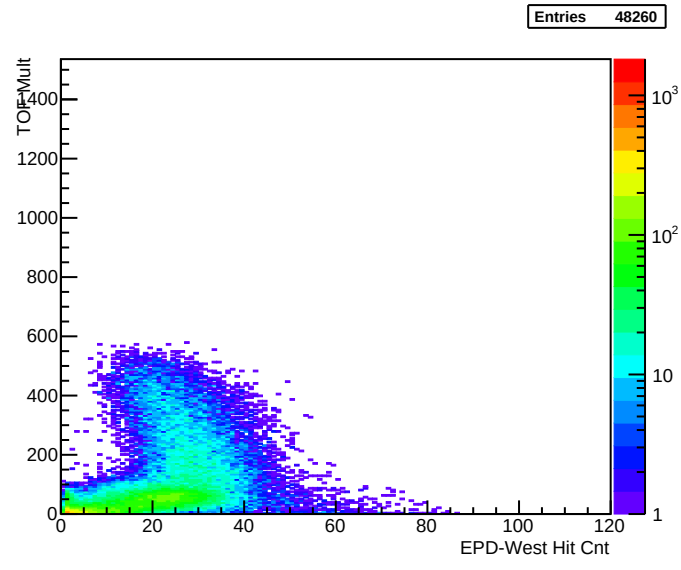

VT201 Input Ch4: VPD Adc Sum Th
Entries 96520
33733
39333
14527
8927
East
West

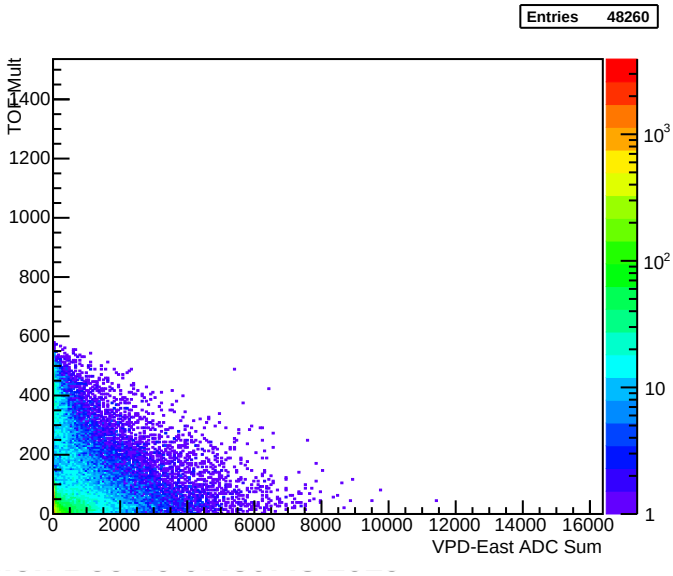

VT201 Input Ch4: VPD Adc Sum Th(Real-Simu)
Entries 0
East
West

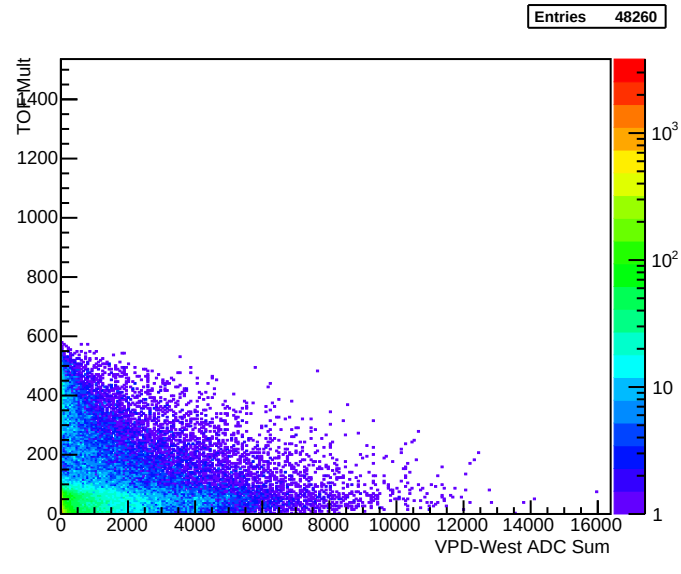

Entries 48260
TOF-Mult
BBC-S-East ADC Sum

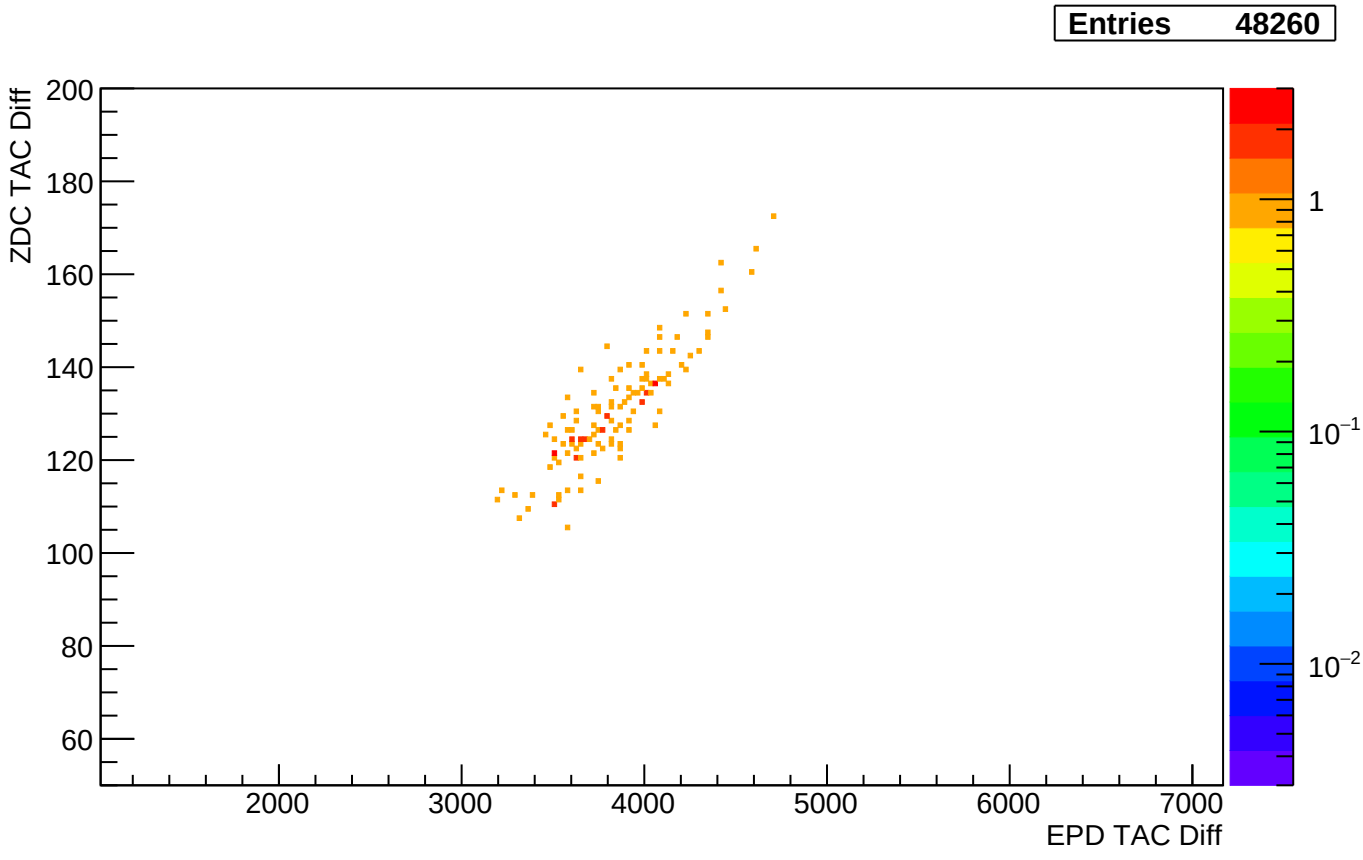

Entries 48260
TOF-Mult
BBC-S-West ADC Sum

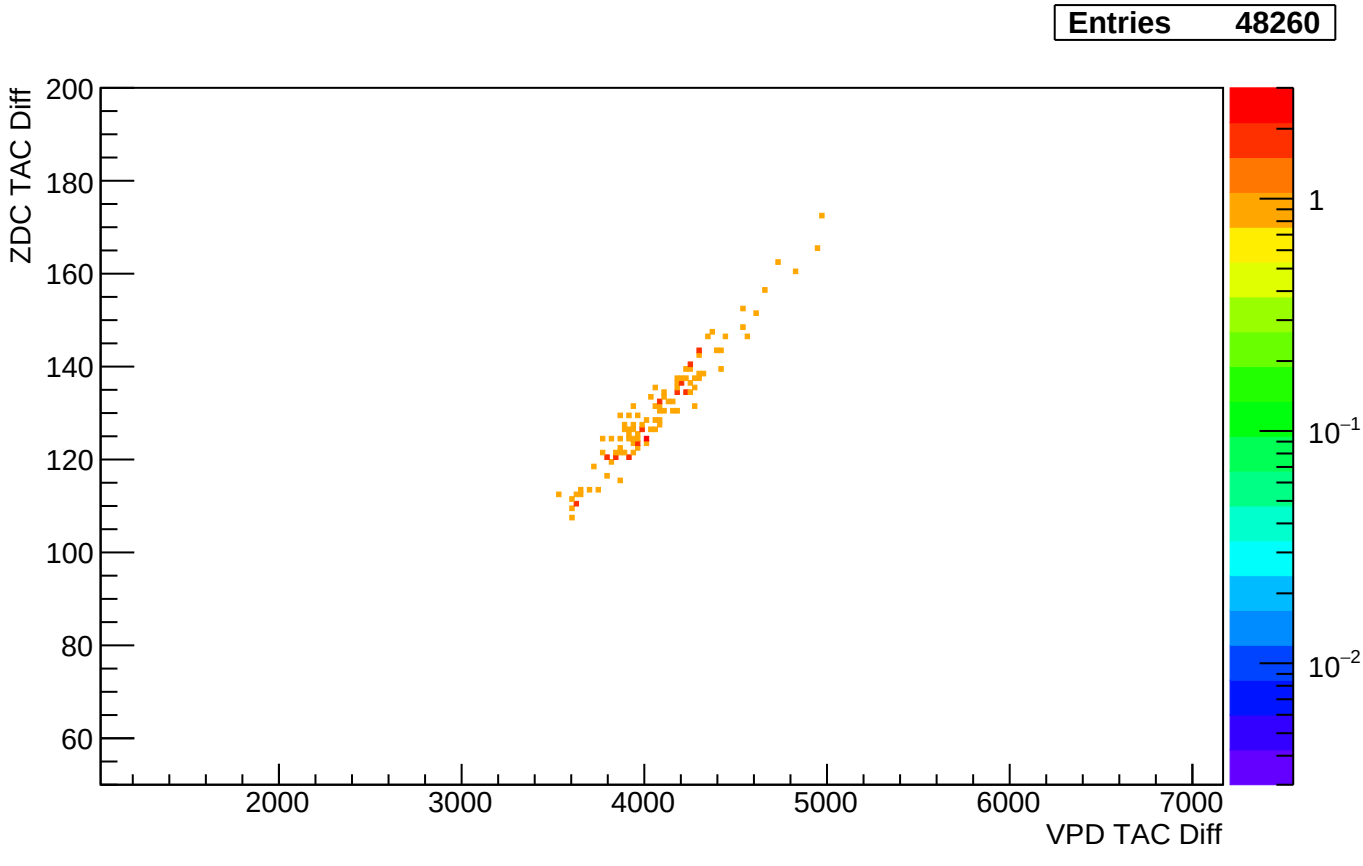

Entries 48260
TOF-Mult
EPD-East Hit Cnt

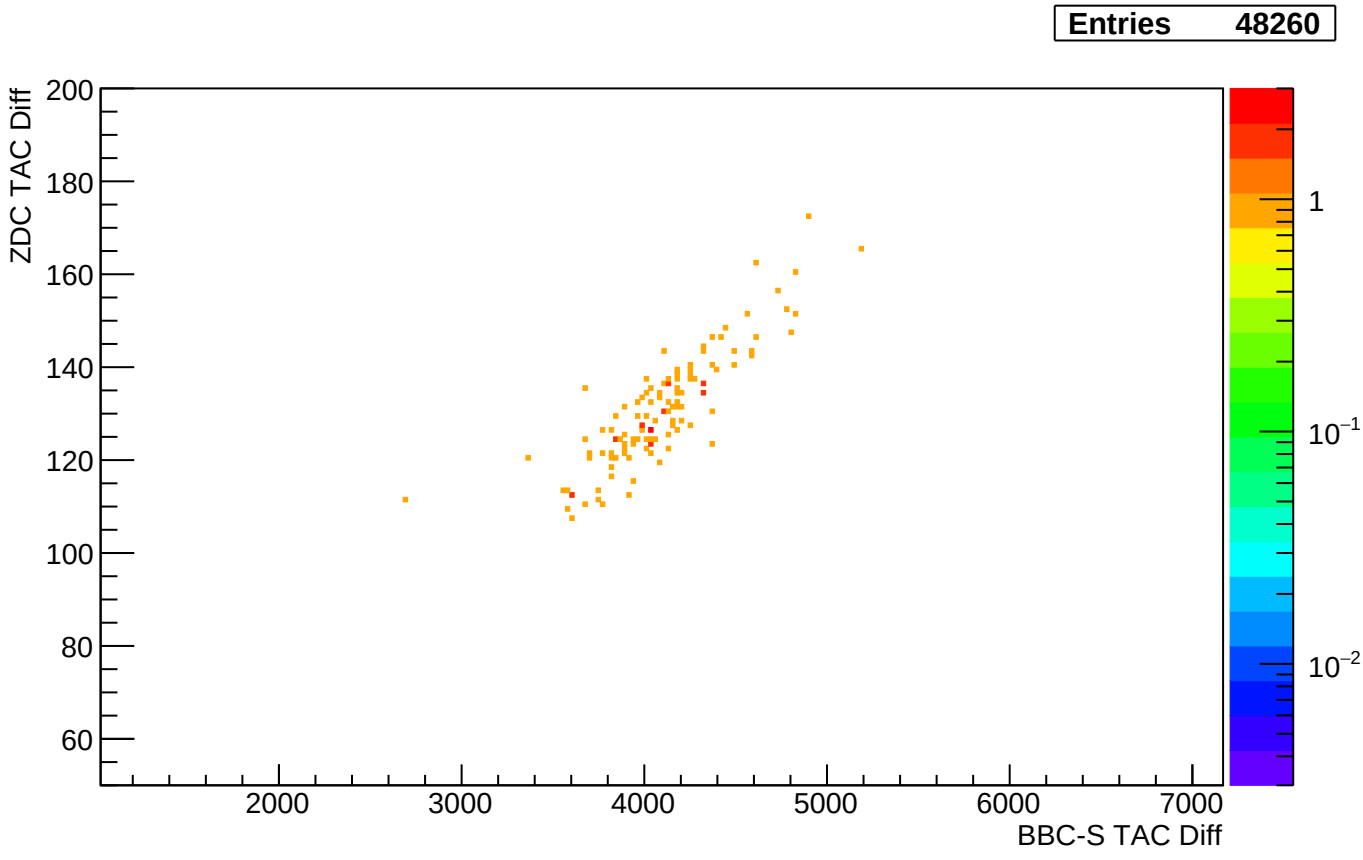

Entries 48260
TOF-Mult
EPD-West Hit Cnt

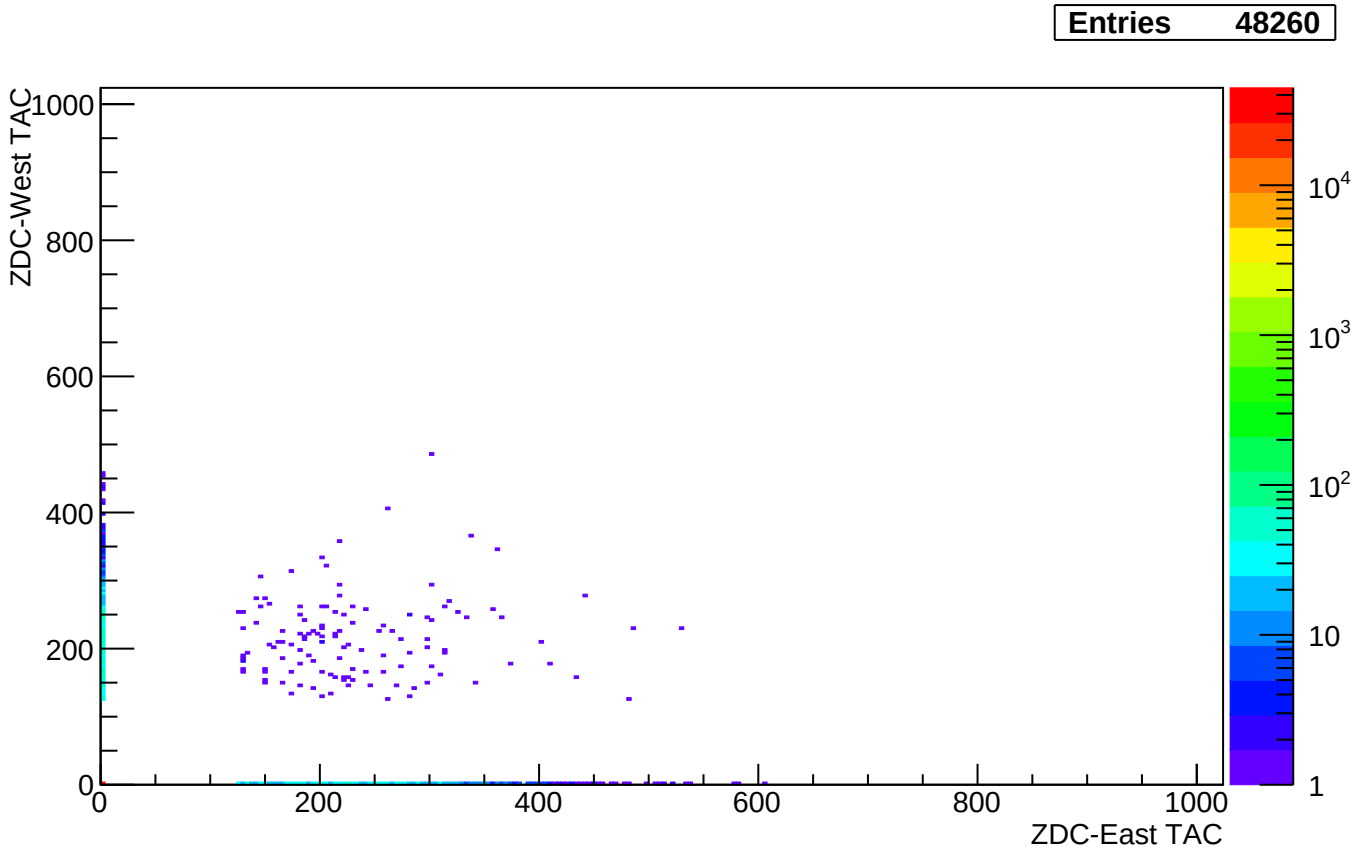

Entries 48260
TOF-Mult
VPD-East ADC Sum

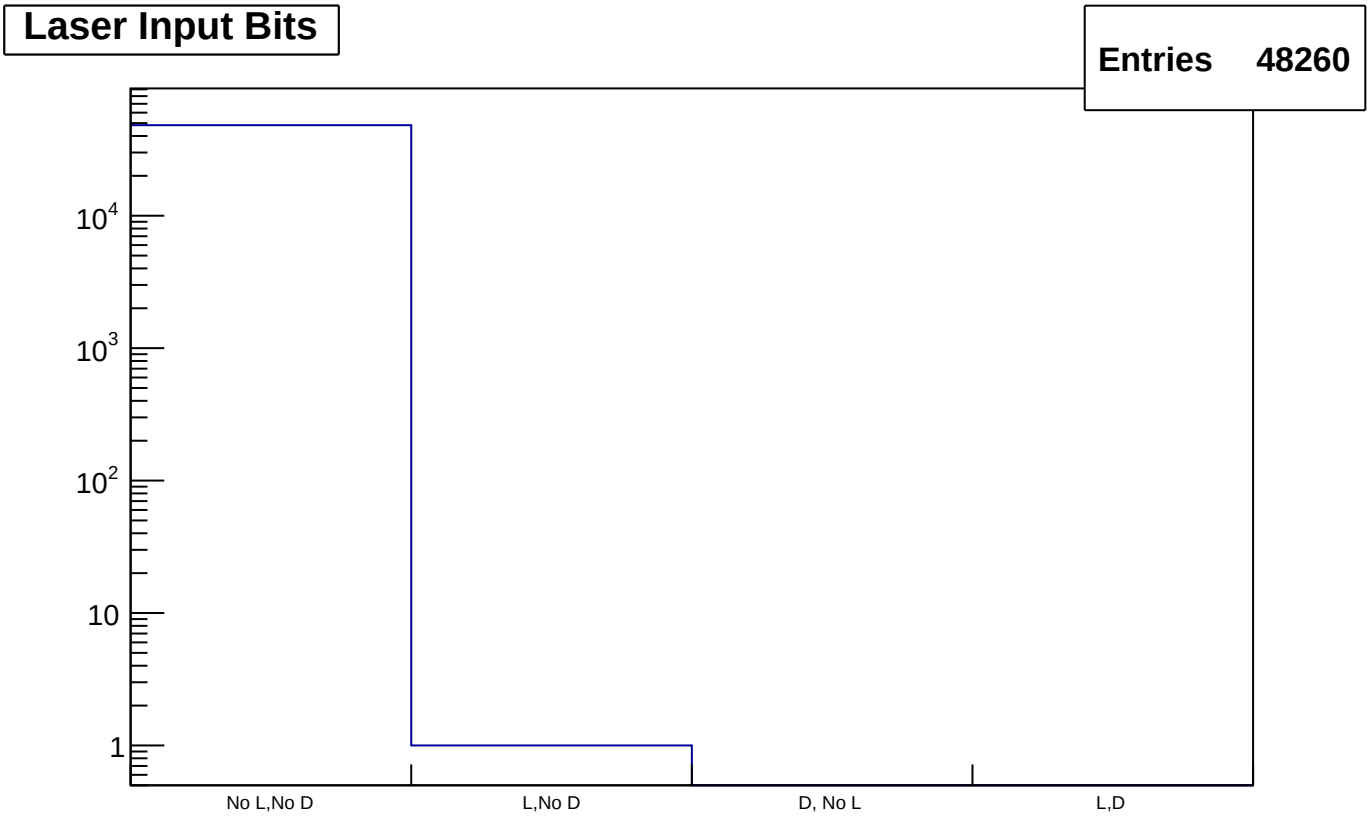

Entries 48260
TOF-Mult
VPD-West ADC Sum

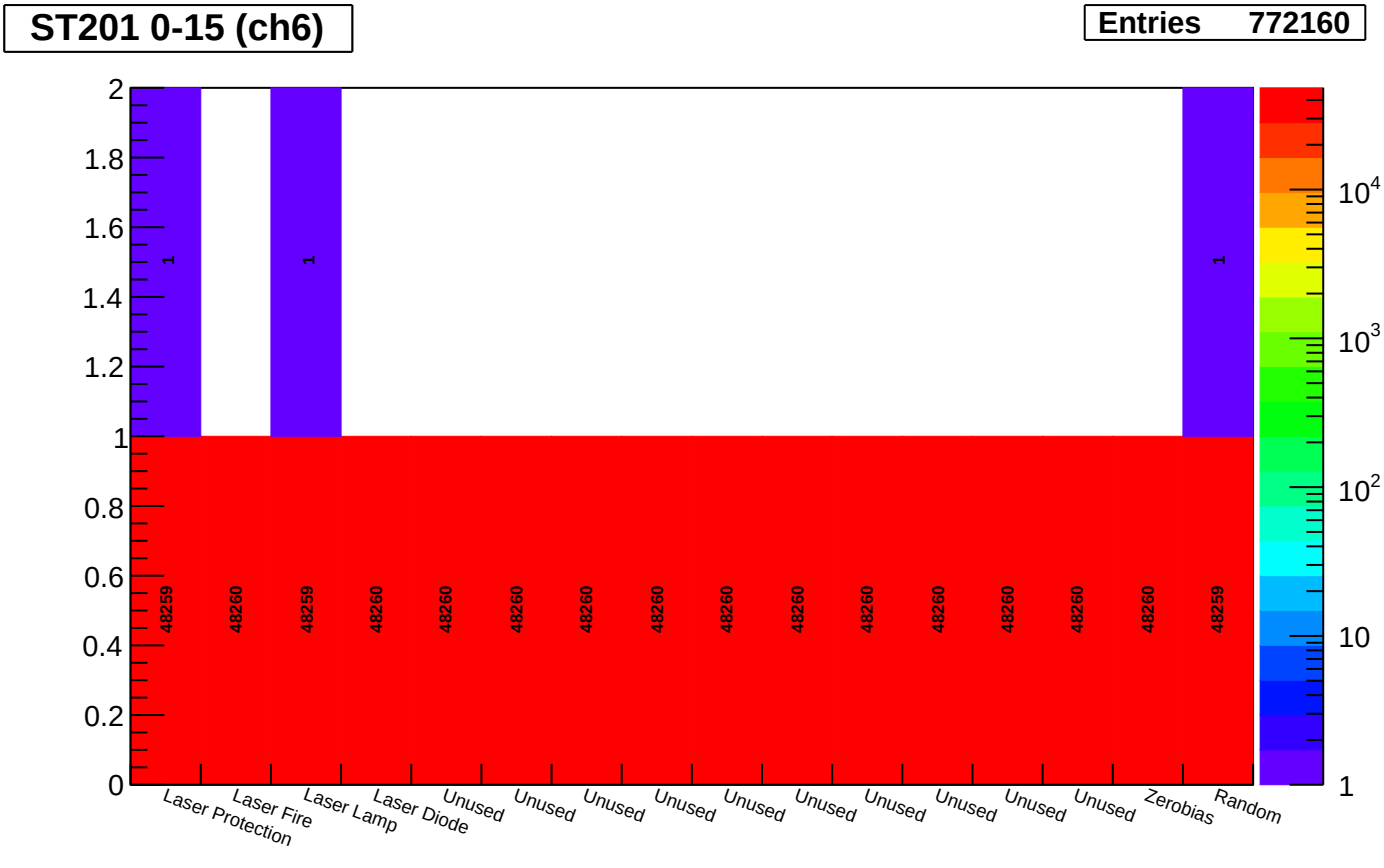

Entries 48260
ZDC TAC Diff
200
180
160
140
120
100
80
60
2000
3000
4000
5000
6000
7000
EPD TAC Diff
1
$10^{-1}$
$10^{-2}$

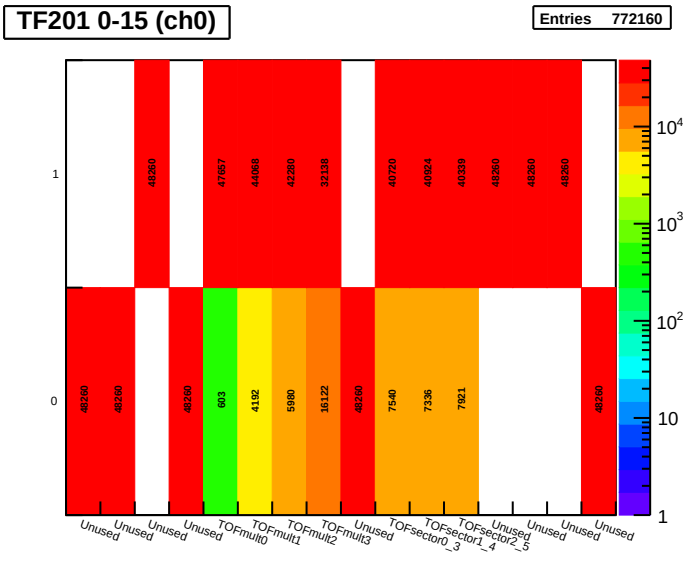

Entries 48260
ZDC TAC Diff
200
180
160
140
120
100
80
60
2000
3000
4000
5000
6000
7000
VPD TAC Diff
1
$10^{-1}$
$10^{-2}$

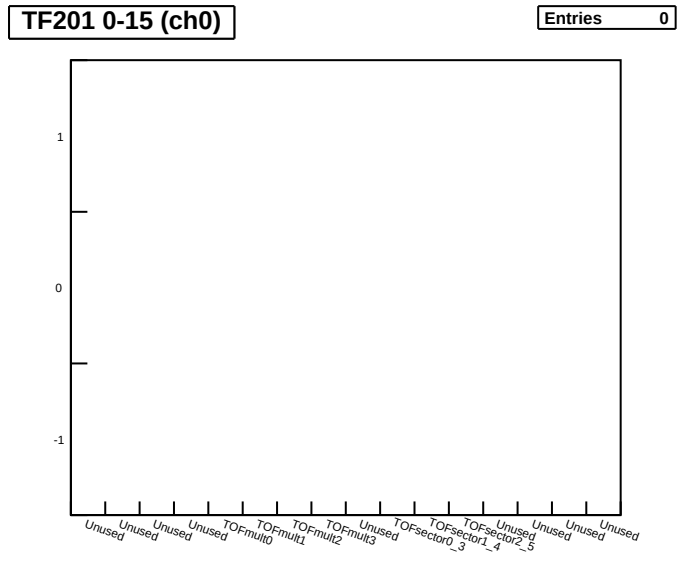

Entries 48260
ZDC TAC Diff
200
180
160
140
120
100
80
60
2000
3000
4000
5000
6000
7000
BBC-S TAC Diff
1
$10^{-1}$
$10^{-2}$

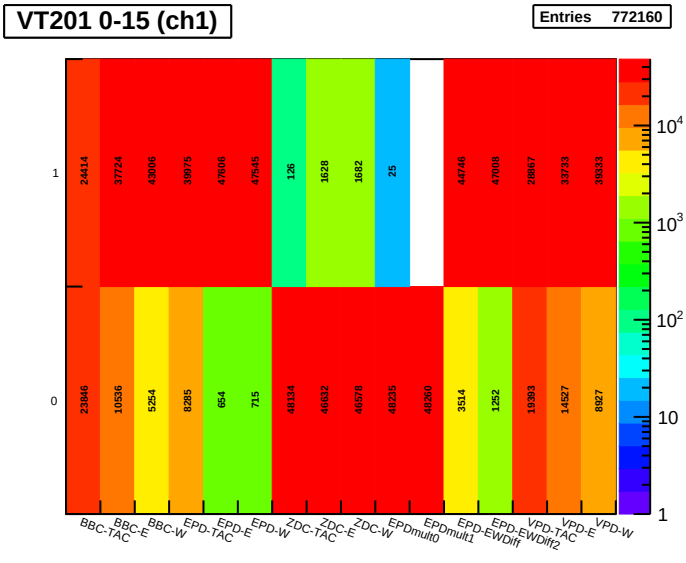

Entries 48260
ZDC-West TAC
1000
800
600
400
200
0
0
200
400
600
800
1000
ZDC-East TAC
$10^4$
$10^3$
$10^2$
10
1

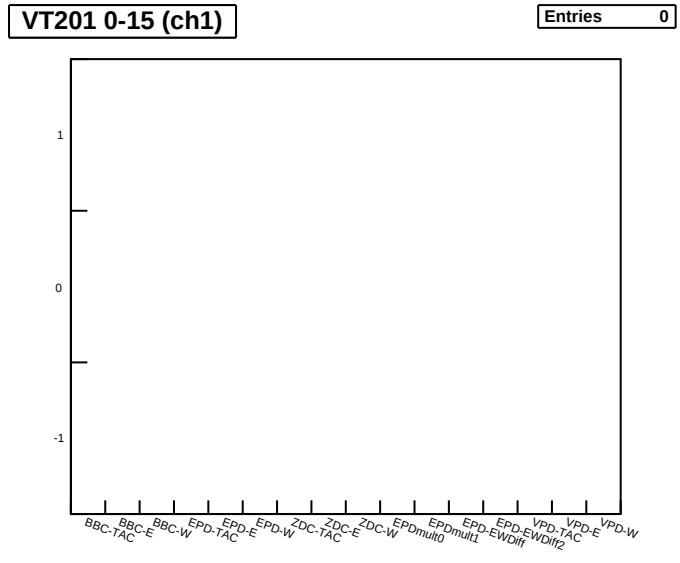

Laser Input Bits
Entries 48260
$10^4$
$10^3$
$10^2$
10
1
No L,No D
L,No D
D, No L
L,D

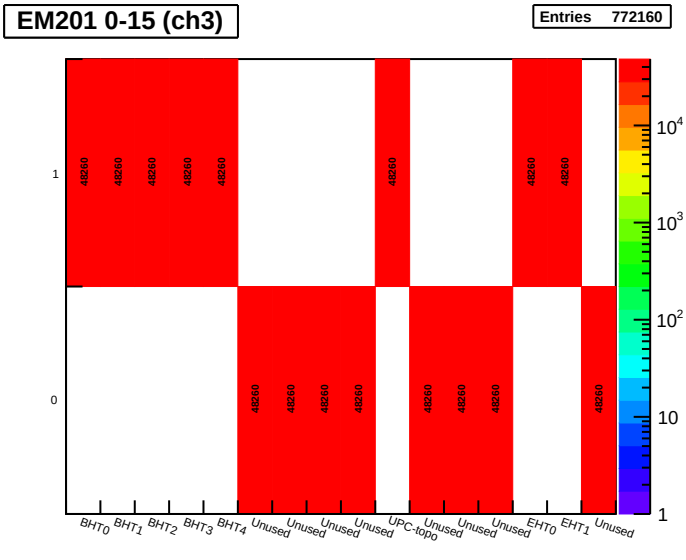

ST201 0-15 (ch6)
Entries 772160
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
1
48259
48260
Laser Protection
Laser Fire
Laser Lamp
Laser Diode
Unused
Zerobias
Random
$10^4$
$10^3$
$10^2$
10
1

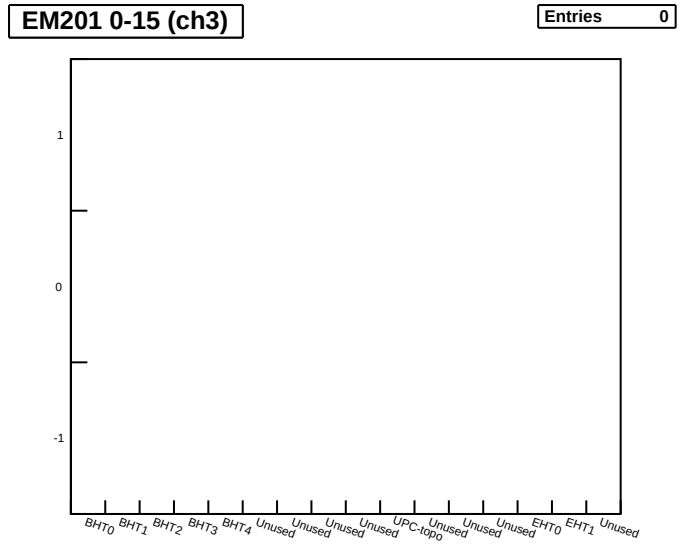

TF201 0-15 (ch0)
Entries 772160
1
0
48260
47657
44068
42280
32138
40720
40924
40339
48260
48260
48260
48260
48260
48260
603
4192
5980
16122
48260
7540
7336
7921
48260
Unused
Unused
Unused
Unused
TOFmult0
TOFmult1
TOFmult2
TOFmult3
Unused
TOFsector0_3
TOFsector1_4
TOFsector2_5
Unused
Unused
Unused
Unused
$10^4$
$10^3$
$10^2$
10
1


TF201 0-15 (ch0)
Entries 0
1
0
-1
Unused
Unused
Unused
Unused
TOFmult0
TOFmult1
TOFmult2
TOFmult3
Unused
TOFsector0_3
TOFsector1_4
TOFsector2_5
Unused
Unused
Unused


VT201 0-15 (ch1)
Entries 772160
1
0
24414
37724
43006
39975
47606
47545
126
1628
1682
25
44746
47008
28667
33733
38333
23846
10536
5254
8285
654
715
48134
46632
46578
48235
48260
3514
1252
19393
14527
8927
BBC-TAC
BBC-E
BBC-W
EPD-TAC
EPD-E
EPD-W
ZDC-TAC
ZDC-E
ZDC-W
EPDmult0
EPDmult1
EPD-EWDiff
EPD-EWDiff2
VPD-TAC
VPD-E
VPD-W
$10^4$
$10^3$
$10^2$
10
1


VT201 0-15 (ch1)
Entries 0
1
0
-1
BBC-TAC
BBC-E
BBC-W
EPD-TAC
EPD-E
EPD-W
ZDC-TAC
ZDC-E
ZDC-W
EPDmult0
EPDmult1
EPD-EWDiff
EPD-EWDiff2
VPD-TAC
VPD-E
VPD-W


EM201 0-15 (ch3)
Entries 772160
1
0
48260
48260
48260
48260
48260
48260
48260
48260
48260
48260
48260
48260
48260
48260
48260
48260
BHT0
BHT1
BHT2
BHT3
BHT4
Unused
Unused
Unused
Unused
UPC-topo
Unused
Unused
Unused
EHT0
EHT1
Unused
$10^4$
$10^3$
$10^2$
10
1


EM201 0-15 (ch3)
Entries 0
1
0
-1
BHT0
BHT1
BHT2
BHT3
BHT4
Unused
Unused
Unused
Unused
UPC-topo
Unused
Unused
Unused
EHT0
EHT1
Unused