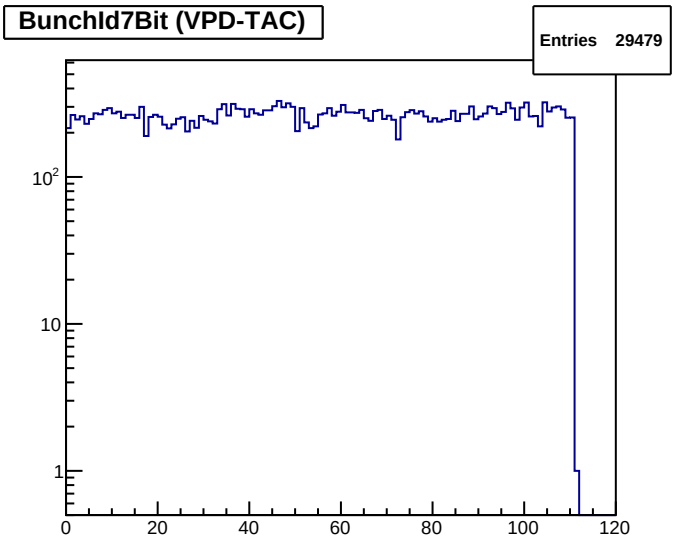
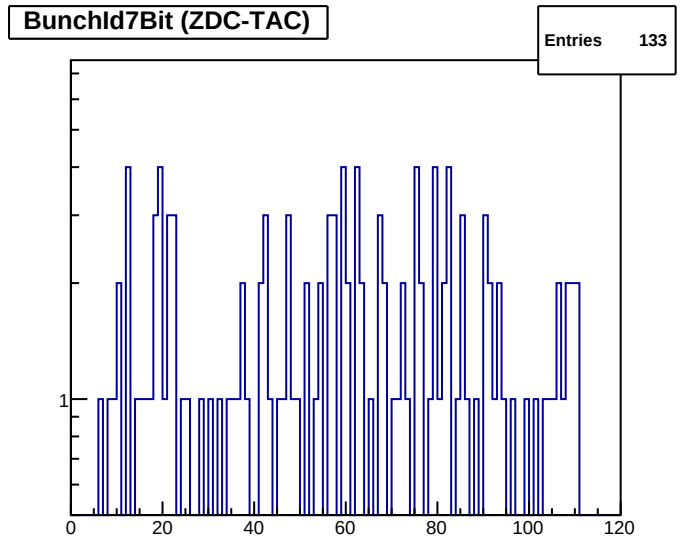
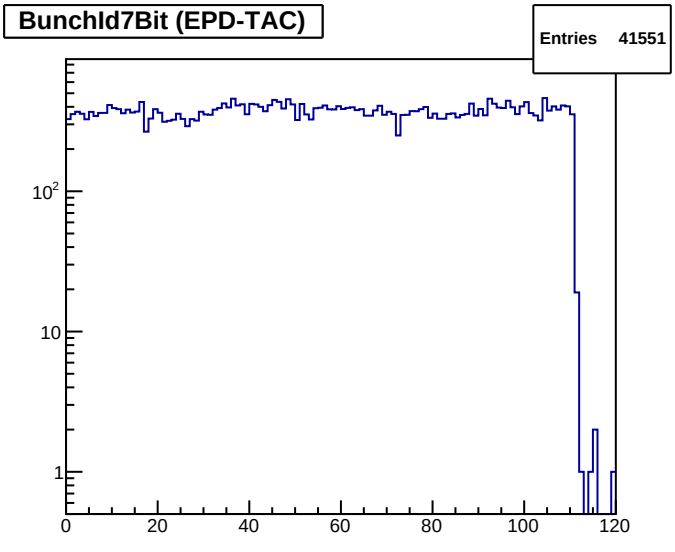
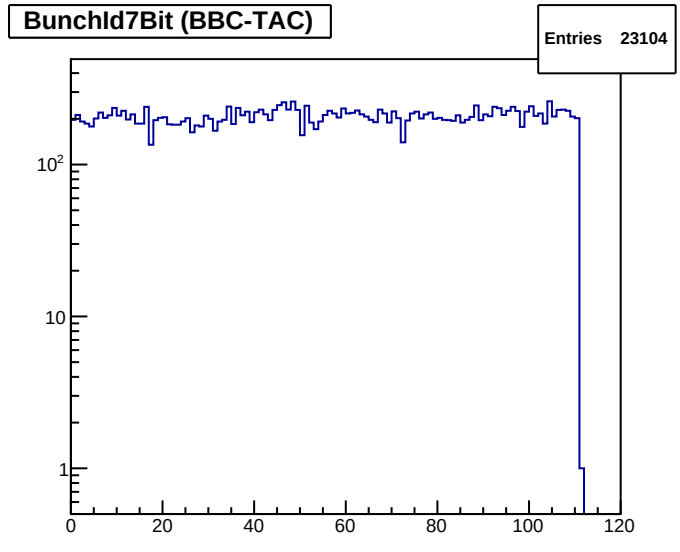
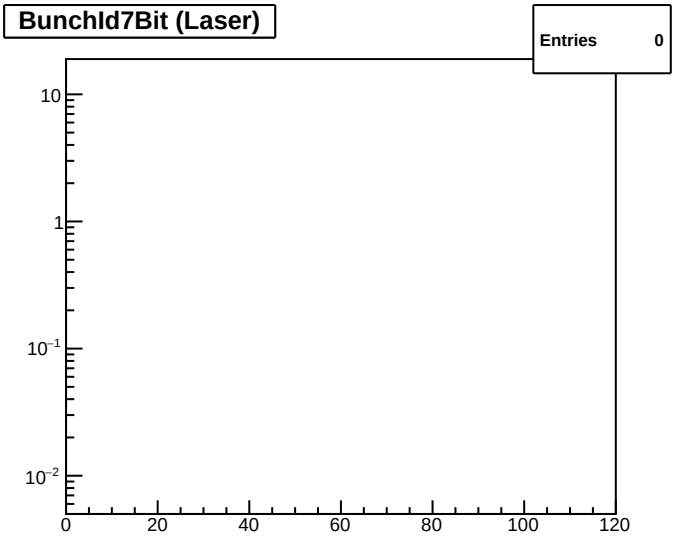
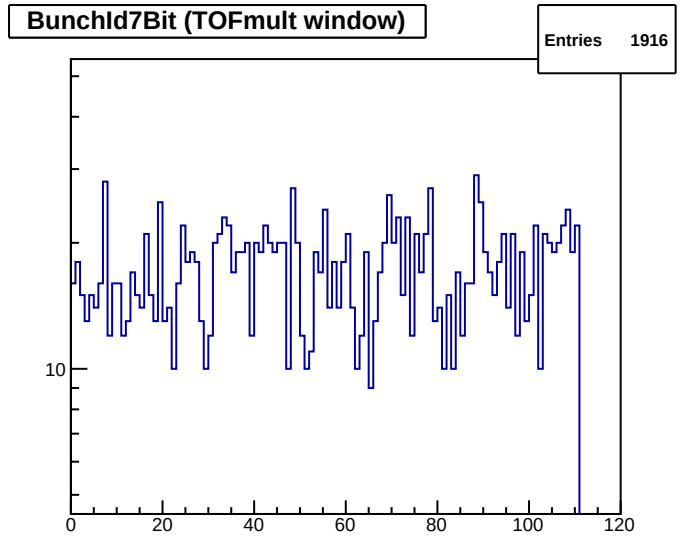

BunchId7Bit (VPD-TAC)
Entries 29479
10^2
10
1
0
20
40
60
80
100
120

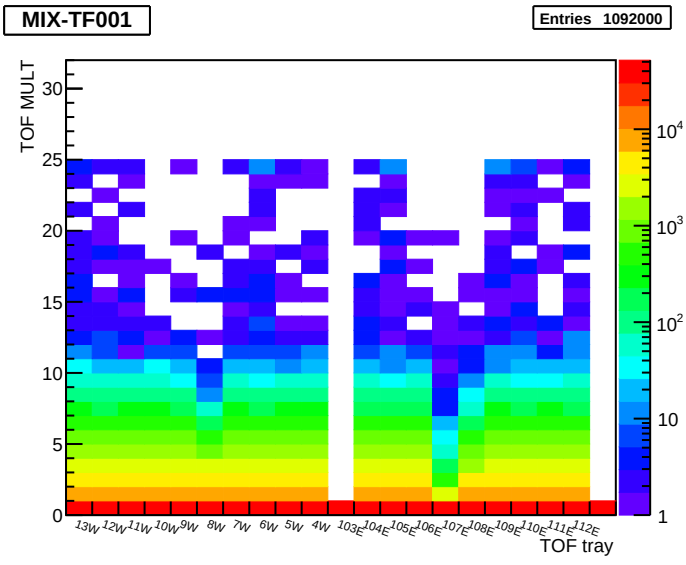

BunchId7Bit (ZDC-TAC)
Entries 133
1
0
20
40
60
80
100
120

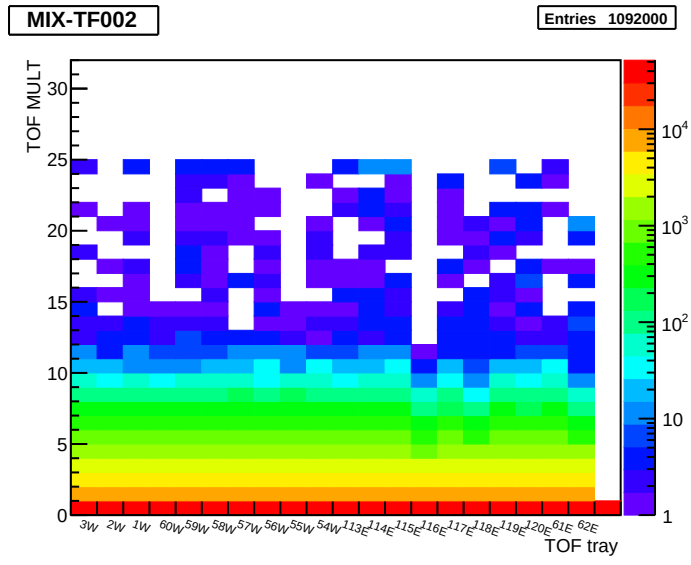

BunchId7Bit (EPD-TAC)
Entries 41551
10^2
10
1
0
20
40
60
80
100
120

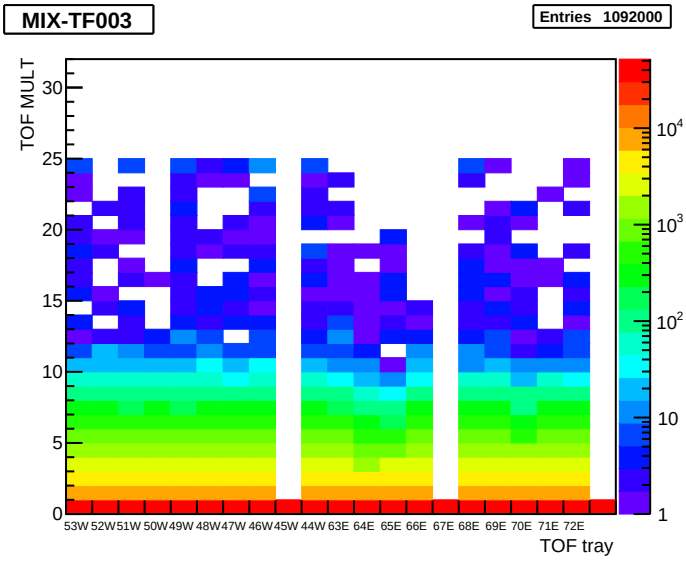

BunchId7Bit (BBC-TAC)
Entries 23104
10^2
10
1
0
20
40
60
80
100
120

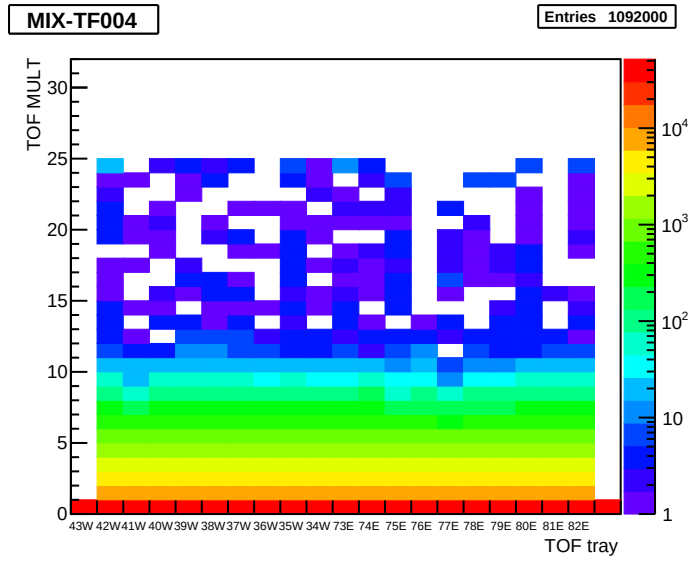

BunchId7Bit (Laser)
Entries 0
10
1
10^{-1}
10^{-2}
0
20
40
60
80
100
120

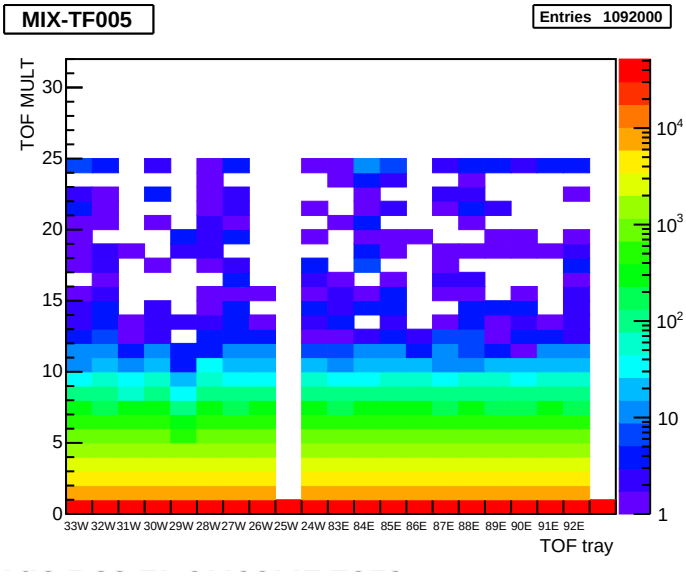

BunchId7Bit (TOFmult window)
Entries 1916
10
0
20
40
60
80
100
120

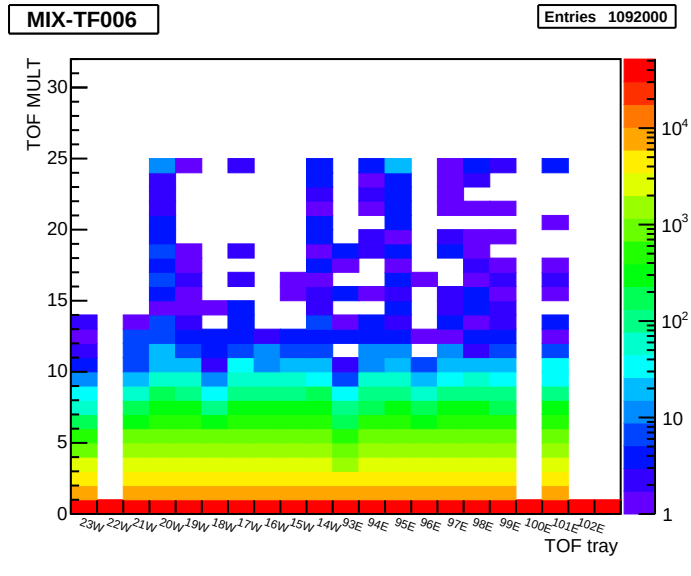

MIX-TF001
Entries 1092000
TOF MULT
TOF tray

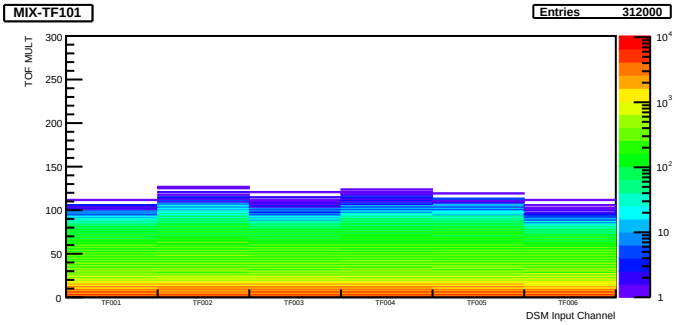

MIX-TF002
Entries 1092000
TOF MULT
TOF tray

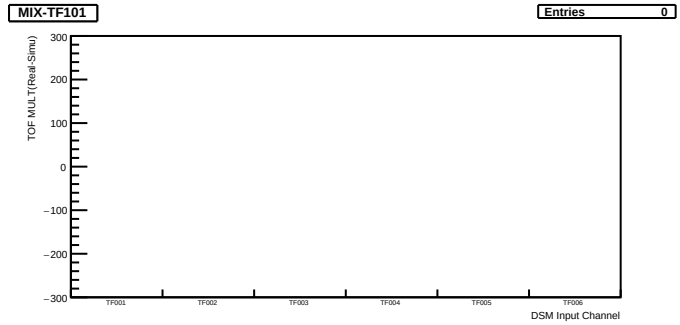

MIX-TF003
Entries 1092000
TOF MULT
TOF tray

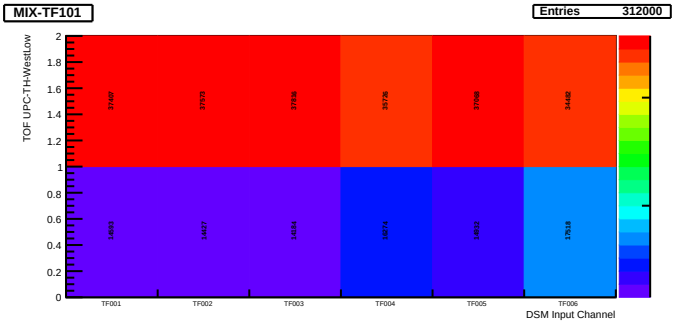

MIX-TF004
Entries 1092000
TOF MULT
TOF tray

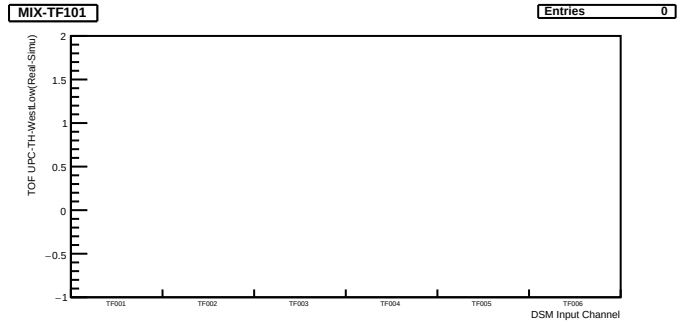

MIX-TF005
Entries 1092000
TOF MULT
TOF tray

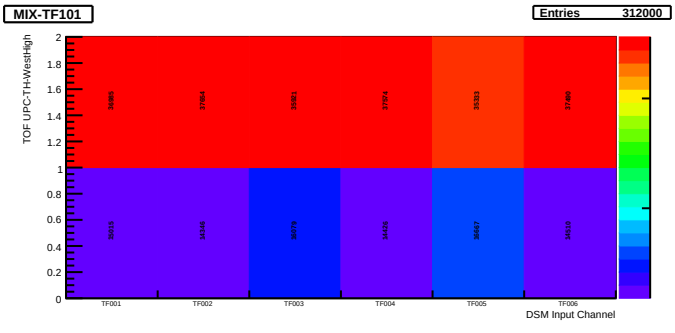

MIX-TF006
Entries 1092000
TOF MULT
TOF tray

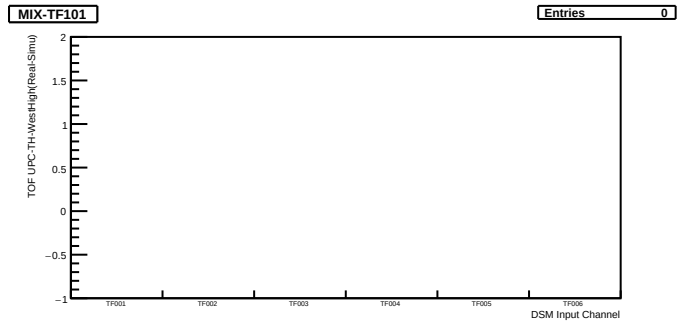

MIX-TF101
Entries 312000
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

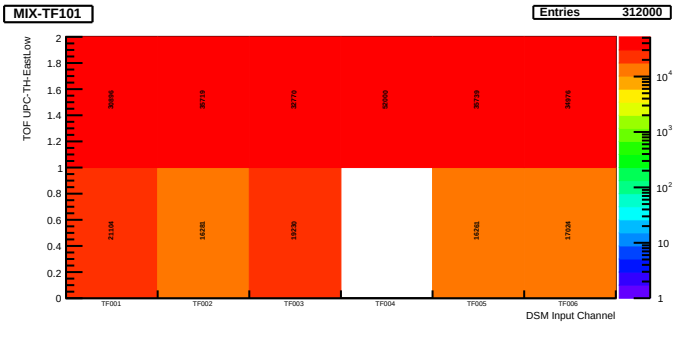

MIX-TF101
Entries 0
TOF MULT(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

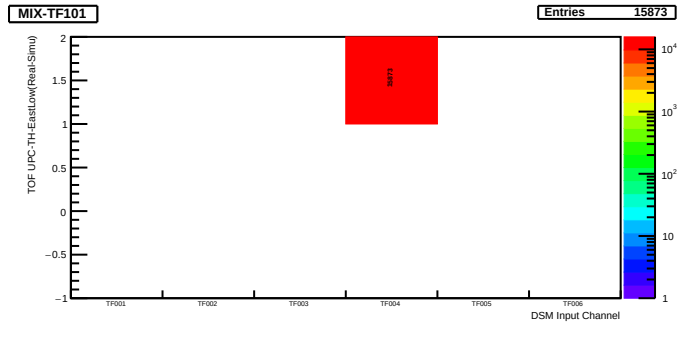

MIX-TF101
Entries 312000
TOF UPC-TH-WestLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

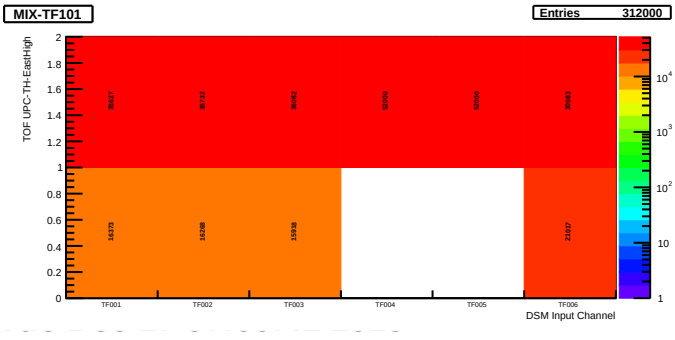

MIX-TF101
Entries 0
TOF UPC-TH-WestLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

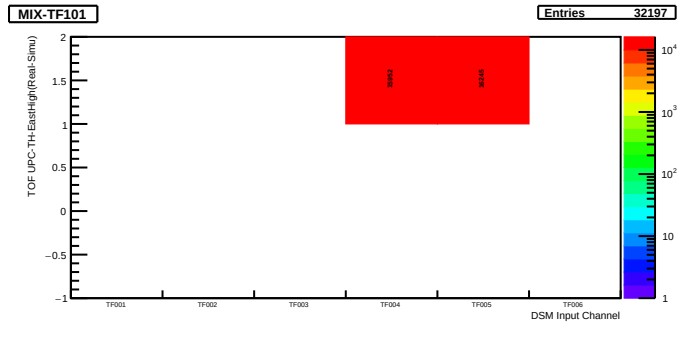

MIX-TF101
Entries 312000
TOF UPC-TH-WestHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

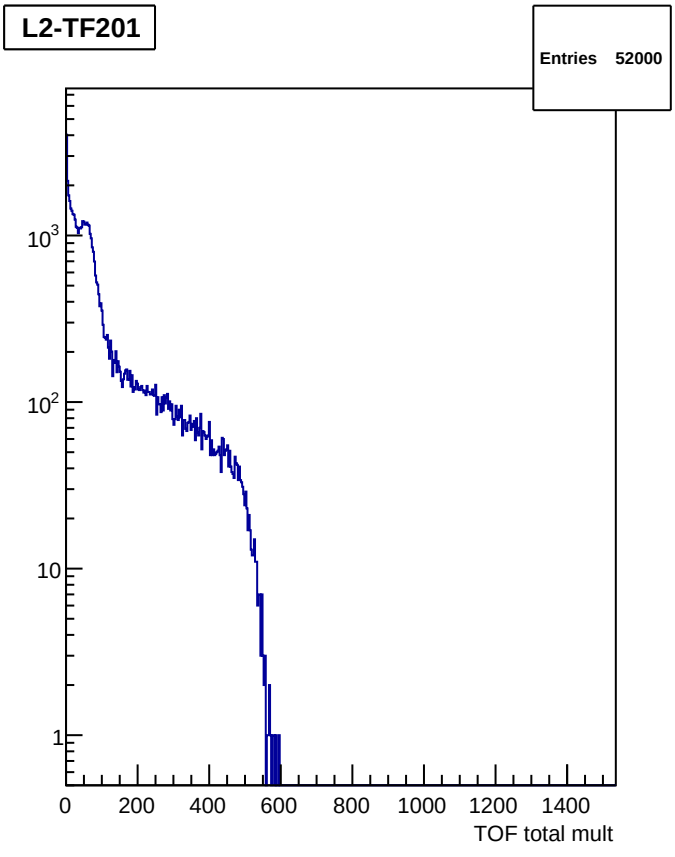

MIX-TF101
Entries 0
TOF UPC-TH-WestHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

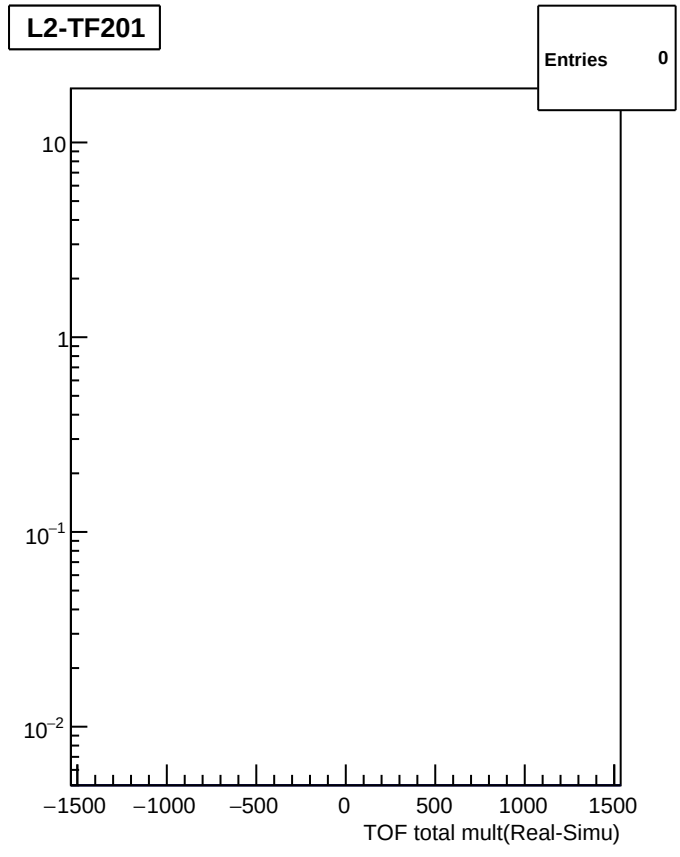

MIX-TF101
Entries 312000
TOF UPC-TH-EastLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

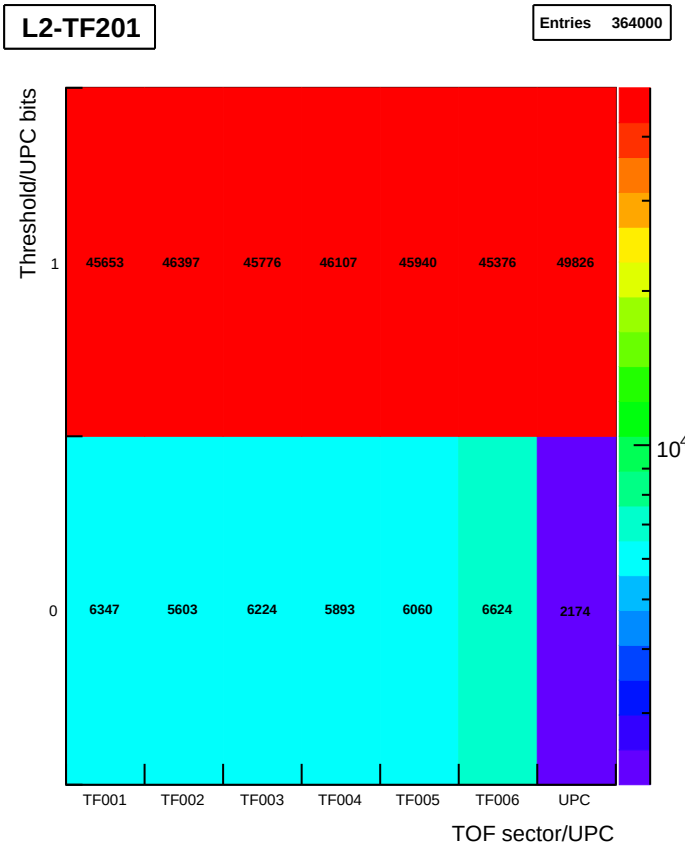

MIX-TF101
Entries 15873
TOF UPC-TH-EastLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

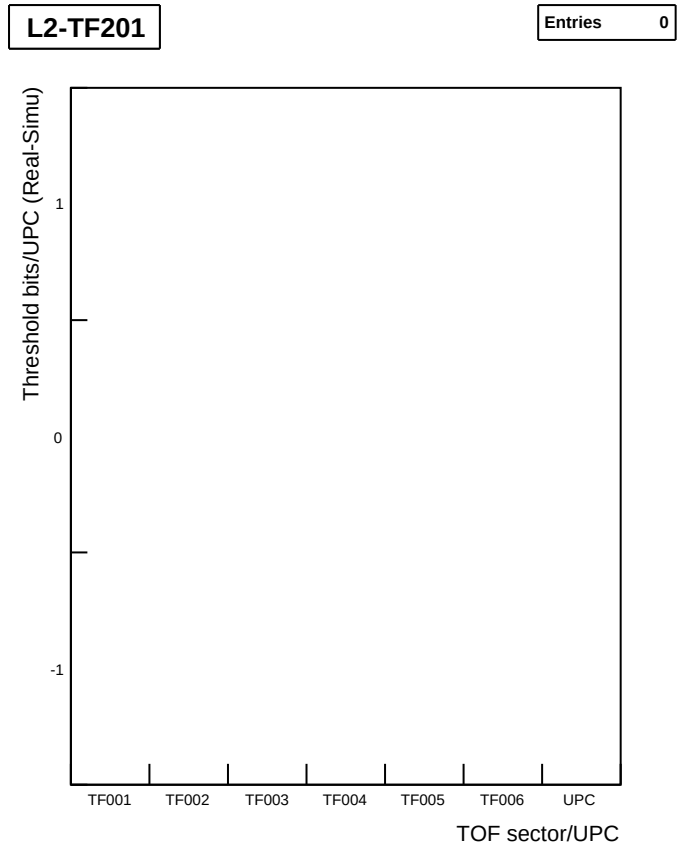

MIX-TF101
Entries 312000
TOF UPC-TH-EastHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

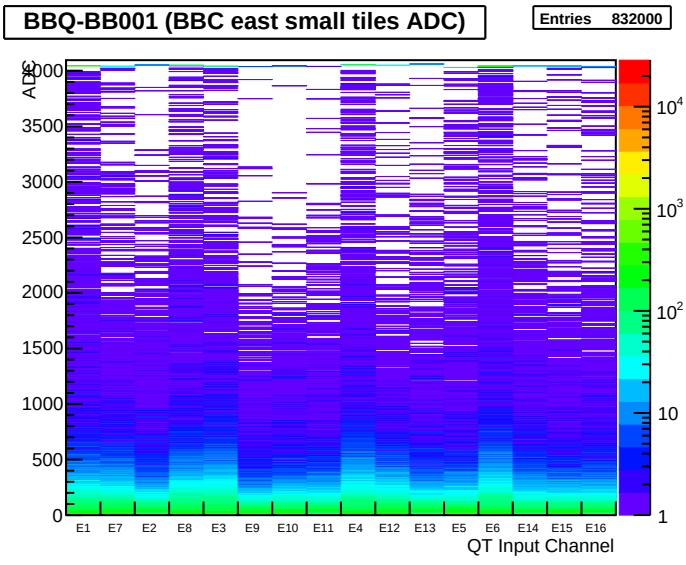

MIX-TF101
Entries 32197
TOF UPC-TH-EastHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

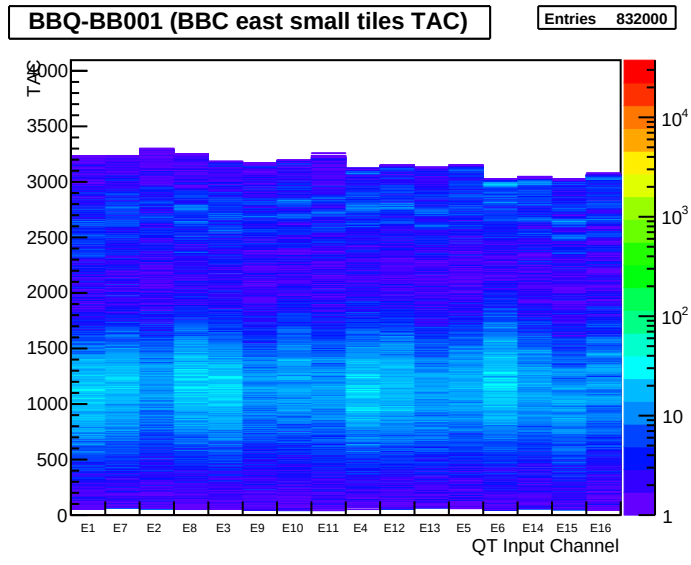

L2-TF201
Entries 52000
10^3
10^2
10
1
0
200
400
600
800
1000
1200
1400
TOF total mult

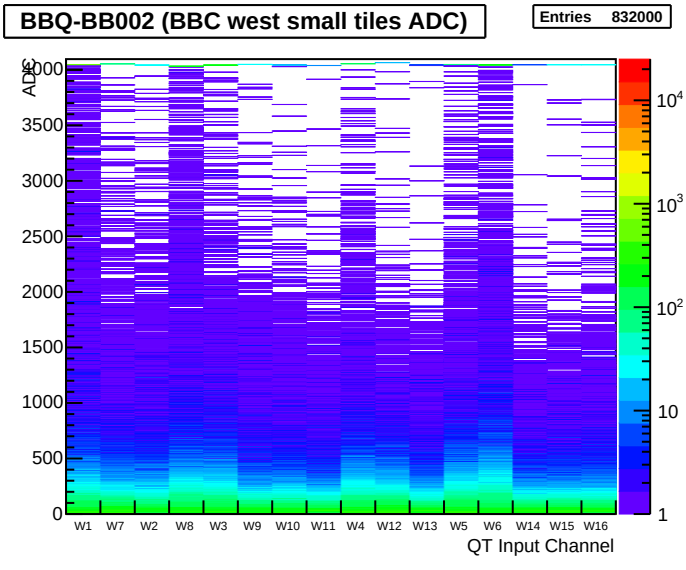

L2-TF201
Entries 0
10
1
10^{-1}
10^{-2}
−1500
−1000
−500
0
500
1000
1500
TOF total mult(Real-Simu)

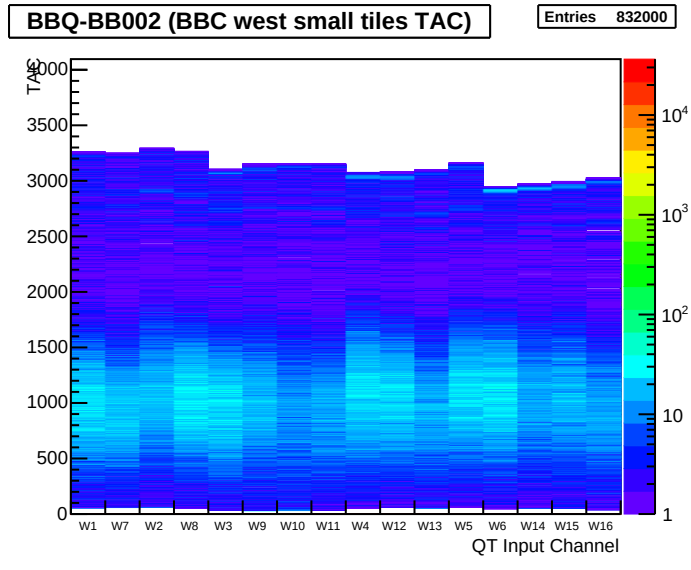

L2-TF201
Entries 364000
Threshold/UPC bits
1
0
45653
46397
45776
46107
45940
45376
49826
6347
5603
6224
5893
6060
6624
2174
TF001
TF002
TF003
TF004
TF005
TF006
UPC
TOF sector/UPC

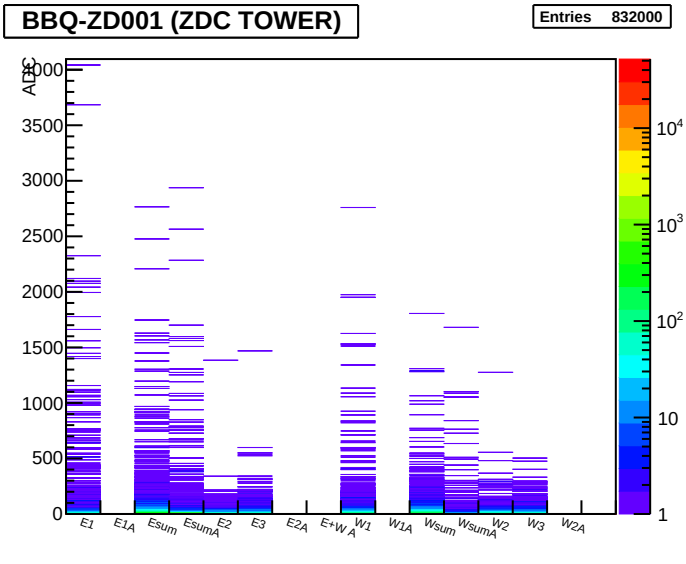

L2-TF201
Entries 0
Threshold bits/UPC (Real-Simu)
1
0
-1
TF001
TF002
TF003
TF004
TF005
TF006
UPC
TOF sector/UPC

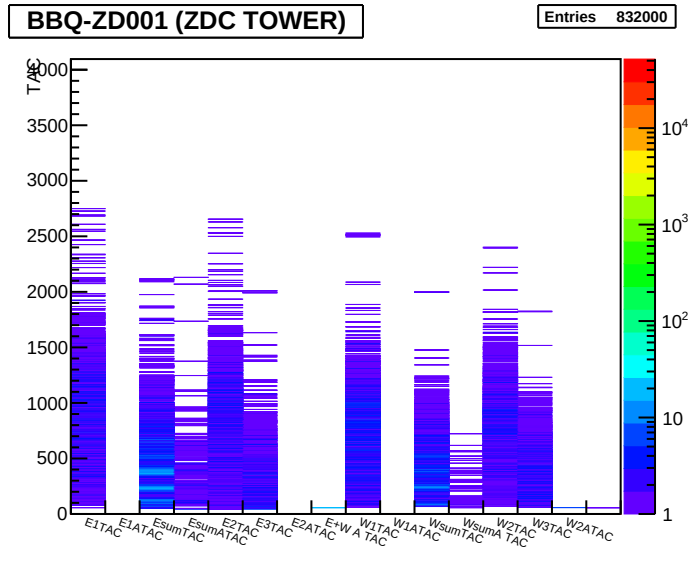

BBQ-BB001 (BBC east small tiles ADC)
Entries 832000
ADC
QT Input Channel
E1 E7 E2 E8 E3 E9 E10 E11 E4 E12 E13 E5 E6 E14 E15 E16

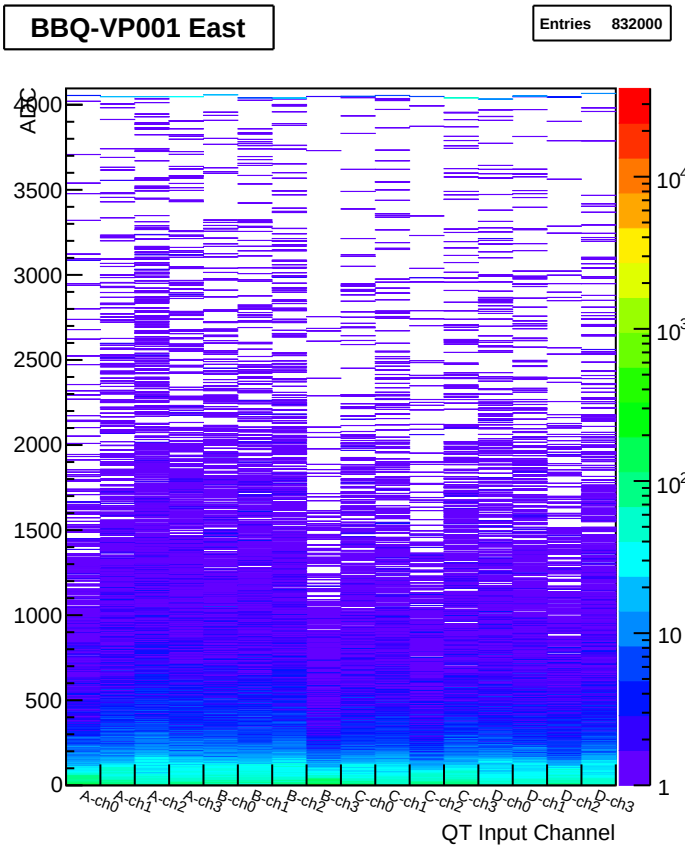

BBQ-BB001 (BBC east small tiles TAC)
Entries 832000
TAC
QT Input Channel
E1 E7 E2 E8 E3 E9 E10 E11 E4 E12 E13 E5 E6 E14 E15 E16

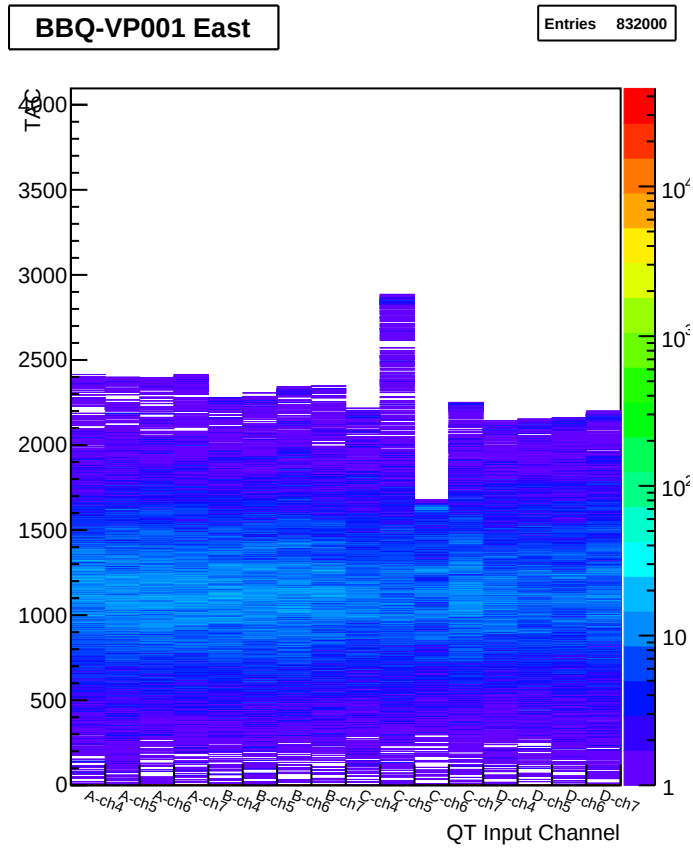

BBQ-BB002 (BBC west small tiles ADC)
Entries 832000
ADC
QT Input Channel
W1 W7 W2 W8 W3 W9 W10 W11 W4 W12 W13 W5 W6 W14 W15 W16

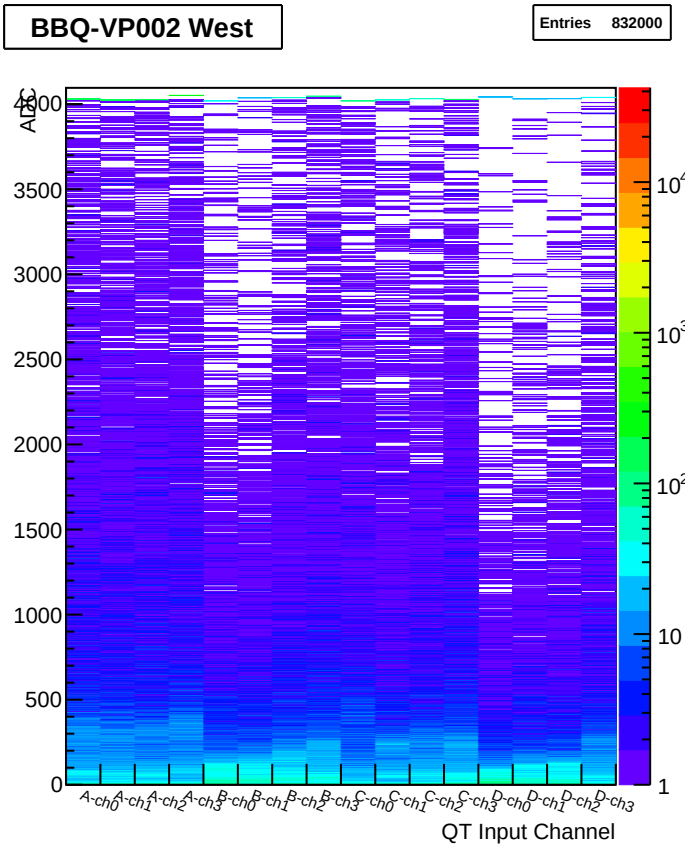

BBQ-BB002 (BBC west small tiles TAC)
Entries 832000
TAC
QT Input Channel
W1 W7 W2 W8 W3 W9 W10 W11 W4 W12 W13 W5 W6 W14 W15 W16

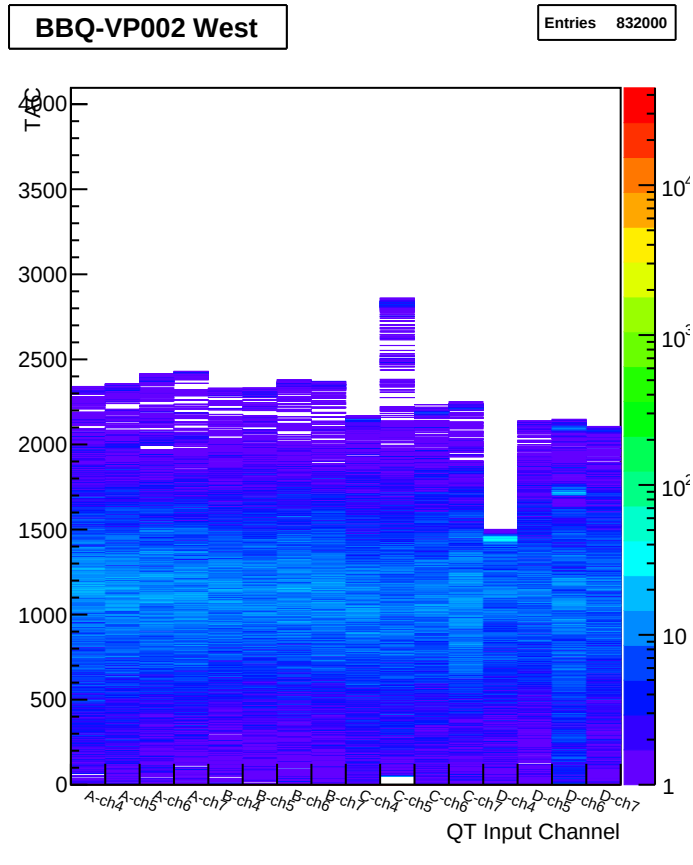

BBQ-ZD001 (ZDC TOWER)
Entries 832000
ADC
E1 E1A Esum EsumA E2 E3 E2A E+W A W1 W1A Wsum WsumA W2 W3 W2A

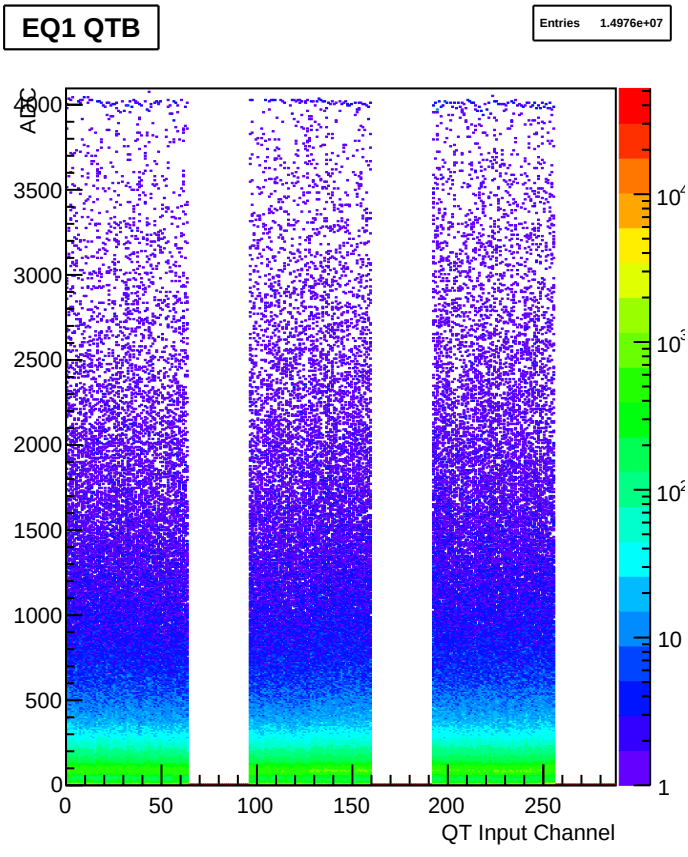

BBQ-ZD001 (ZDC TOWER)
Entries 832000
TAC
E1TAC E1ATAC EsumTAC EsumATAC E2TAC E3TAC E2ATAC E+W A TAC W1TAC W1ATAC WsumTAC WsumA TAC W2TAC W3TAC W2ATAC

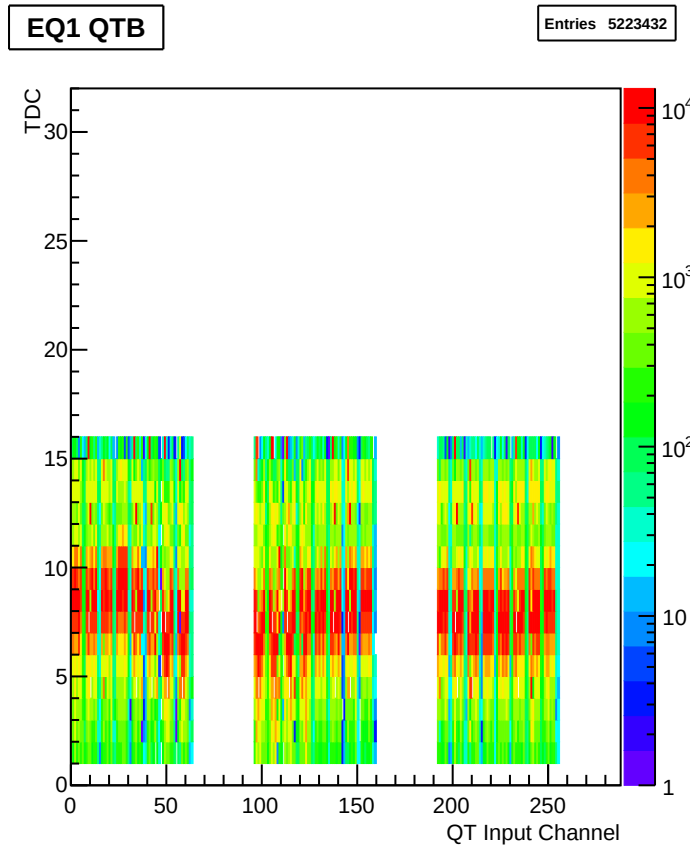

BBQ-VP001 East
Entries 832000
ADC
QT Input Channel

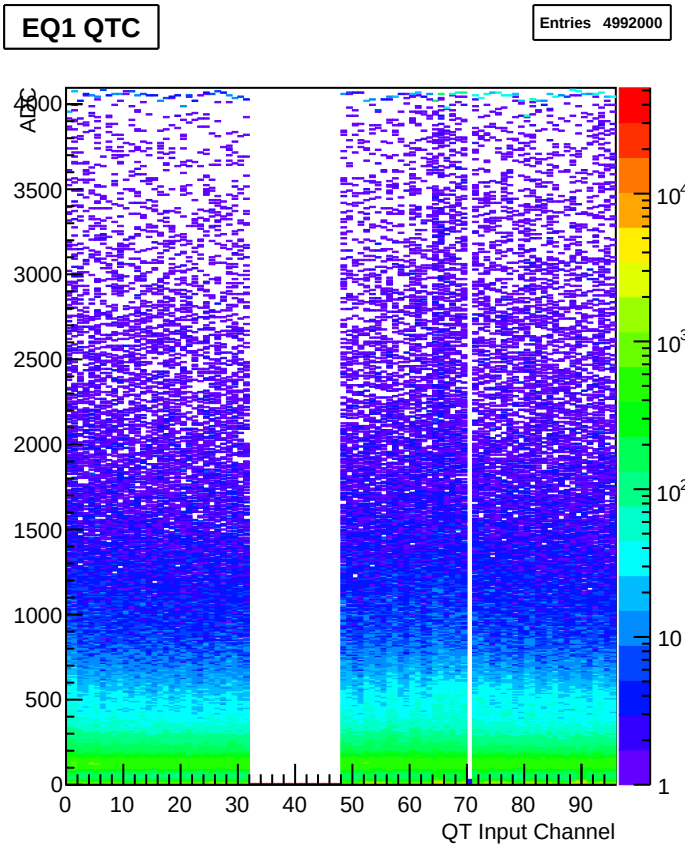

BBQ-VP001 East
Entries 832000
TAC
QT Input Channel

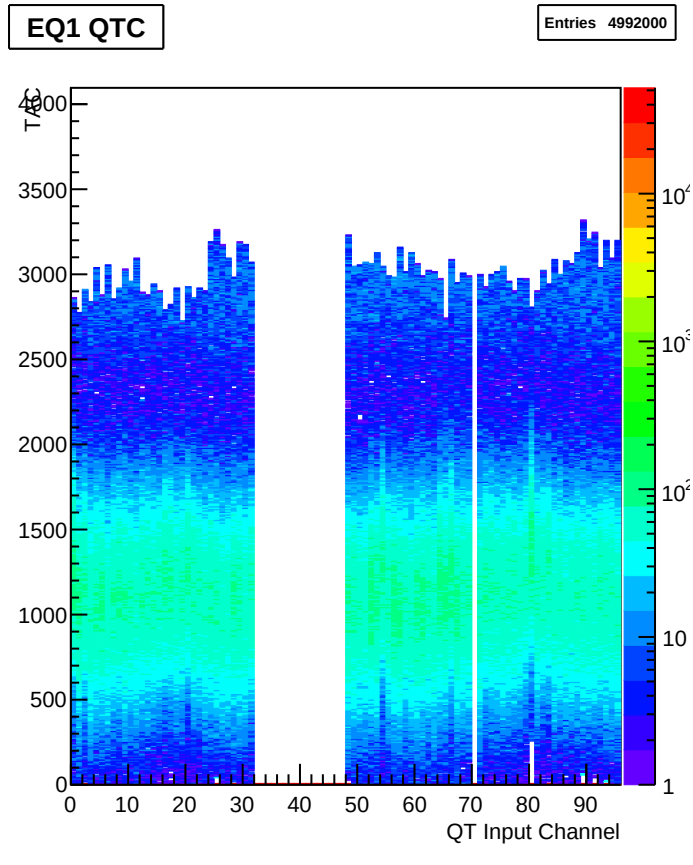

BBQ-VP002 West
Entries 832000
ADC
QT Input Channel

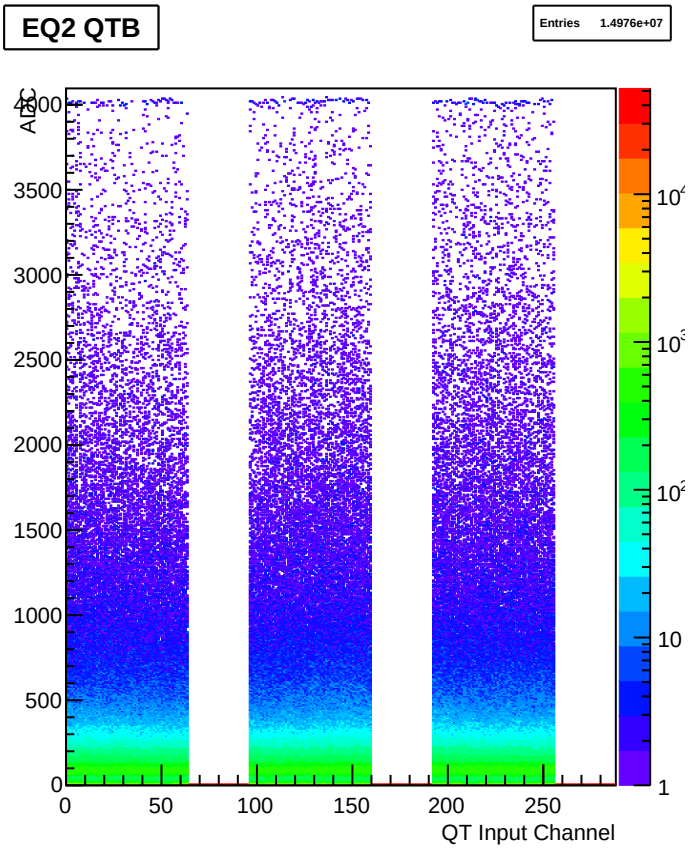

BBQ-VP002 West
Entries 832000
TAC
QT Input Channel

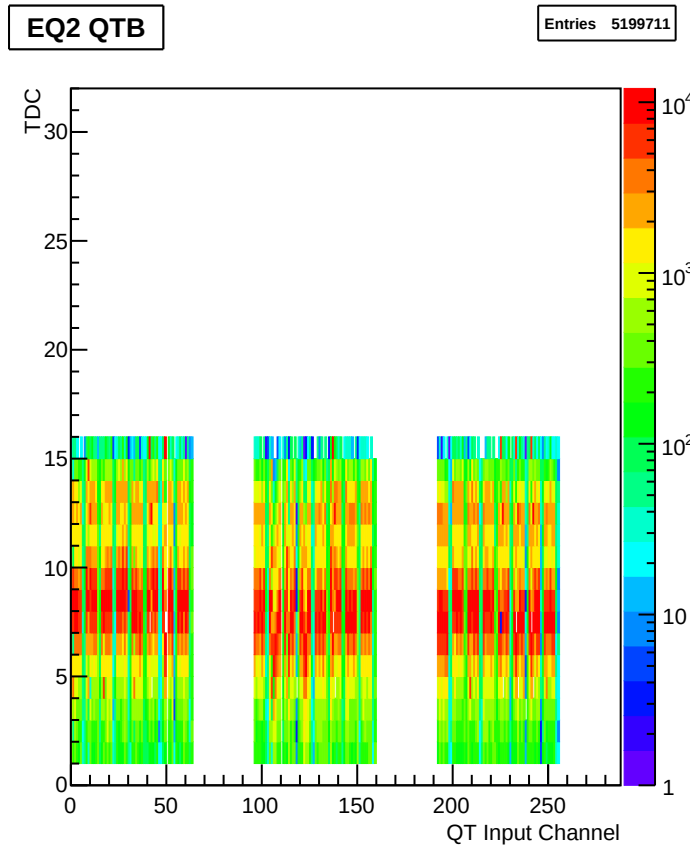

EQ1 QTB
Entries 1.4976e+07
ADC
QT Input Channel

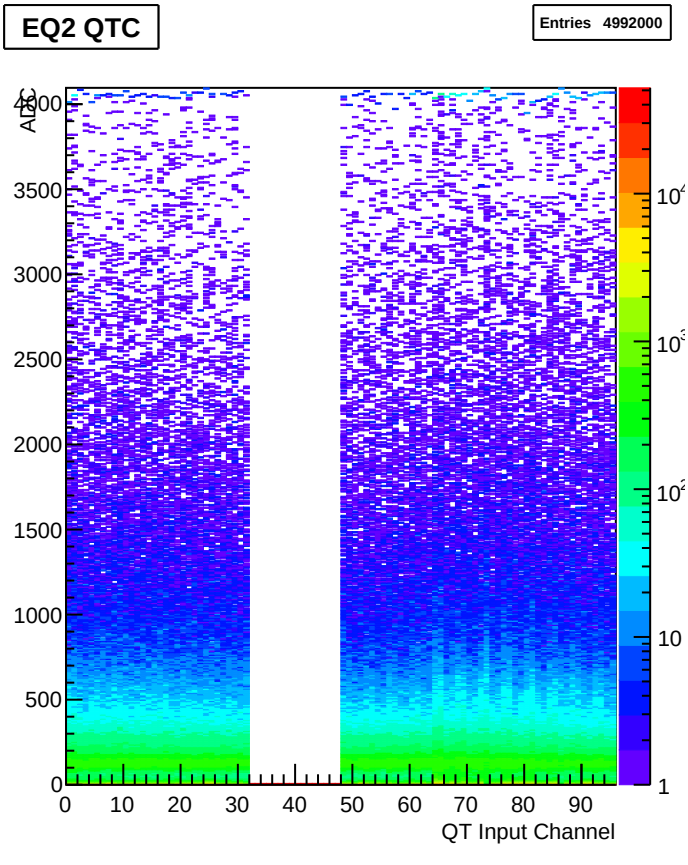

EQ1 QTB
Entries 5223432
TDC
QT Input Channel

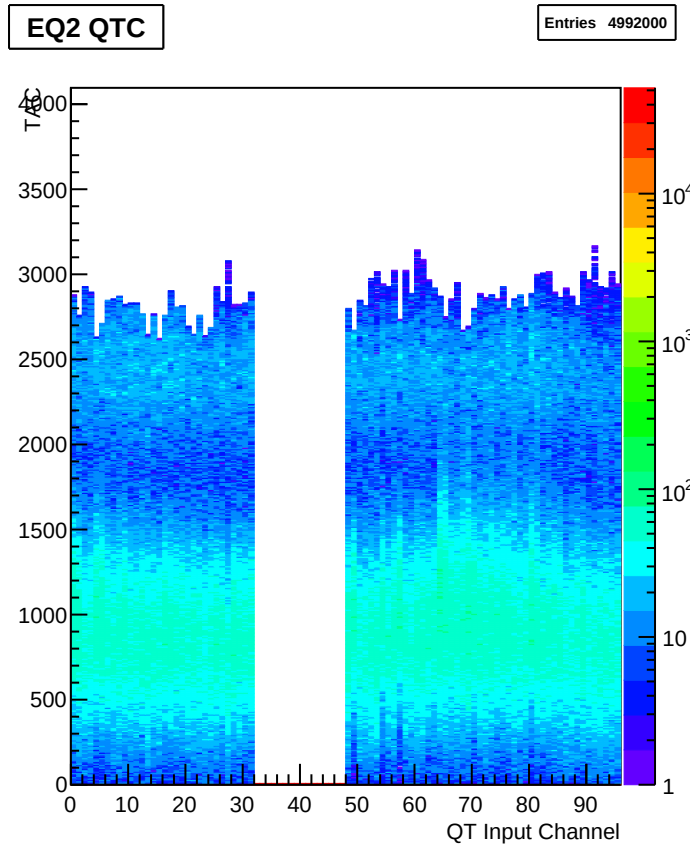

EQ1 QTC
Entries 4992000
ADC
QT Input Channel

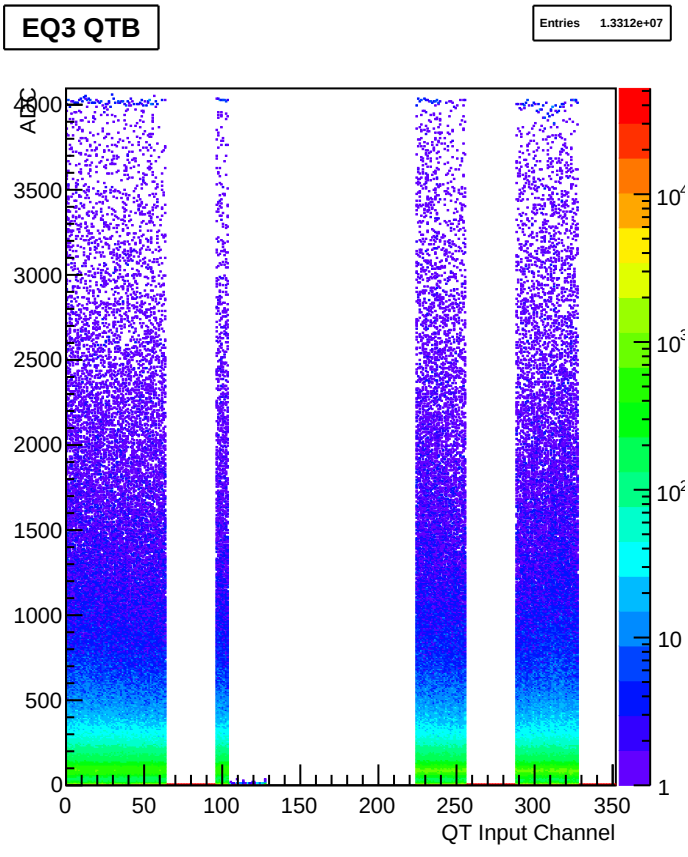

EQ1 QTC
Entries 4992000
TADC
QT Input Channel

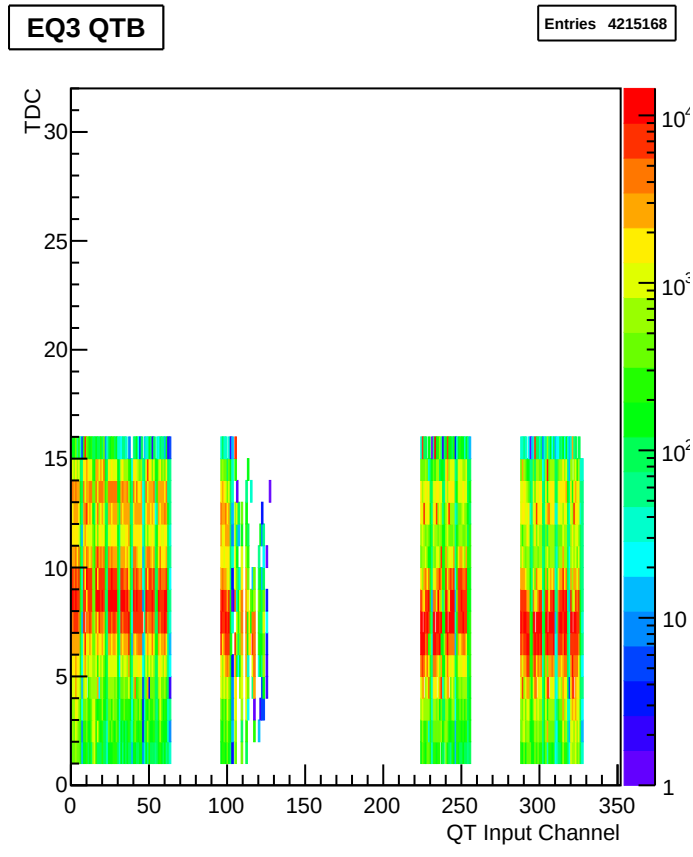

EQ2 QTB
Entries 1.4976e+07
ADC
QT Input Channel

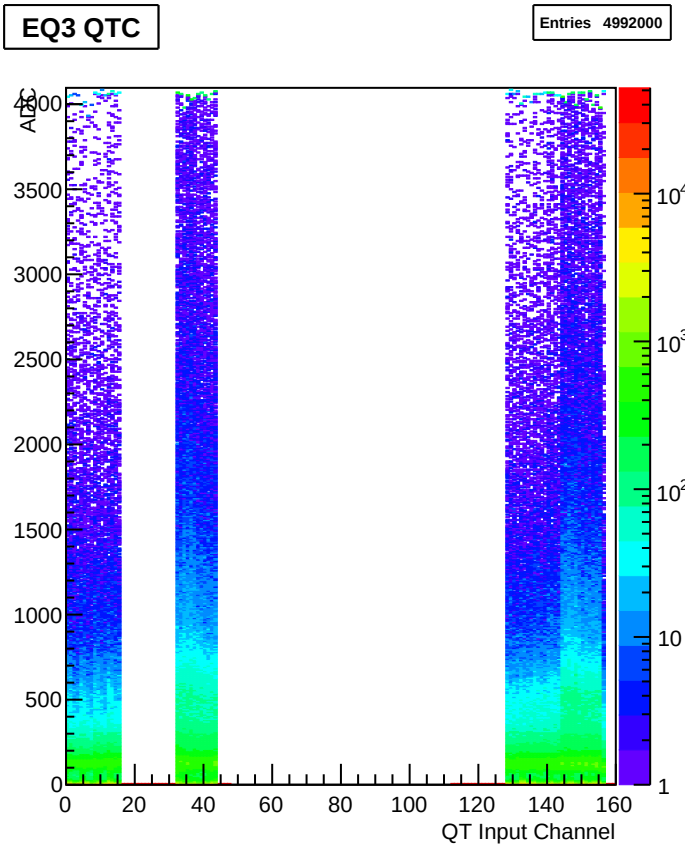

EQ2 QTB
Entries 5199711
TDC
QT Input Channel

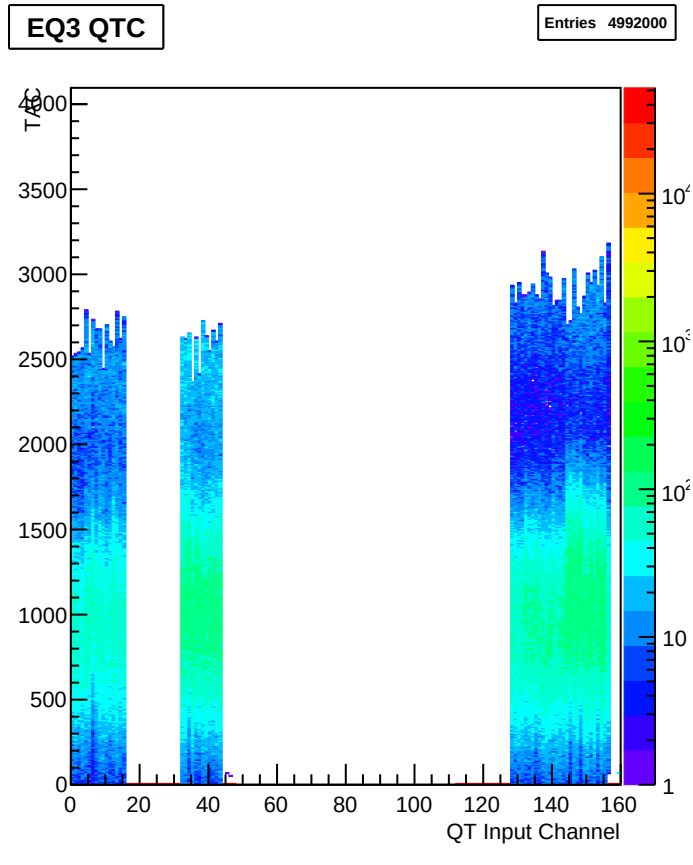

EQ2 QTC
Entries 4992000
ADC
QT Input Channel

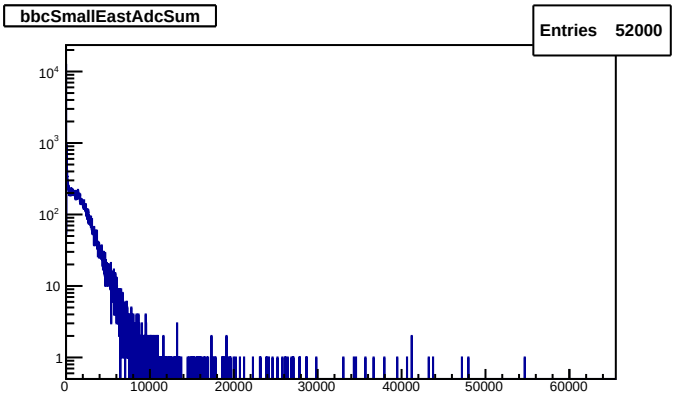

EQ2 QTC
Entries 4992000
TADC
QT Input Channel

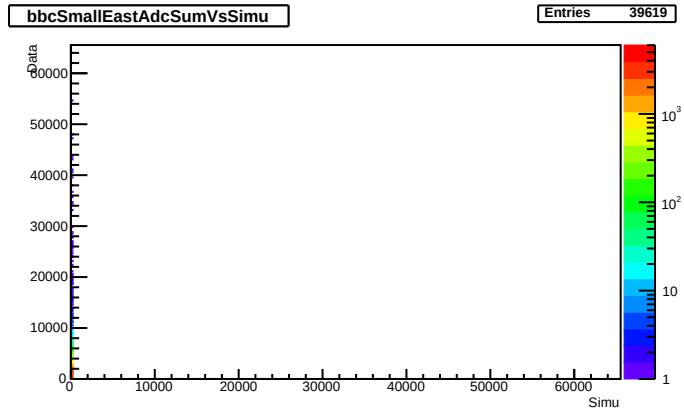

EQ3 QTB
Entries 1.3312e+07
ADC
QT Input Channel

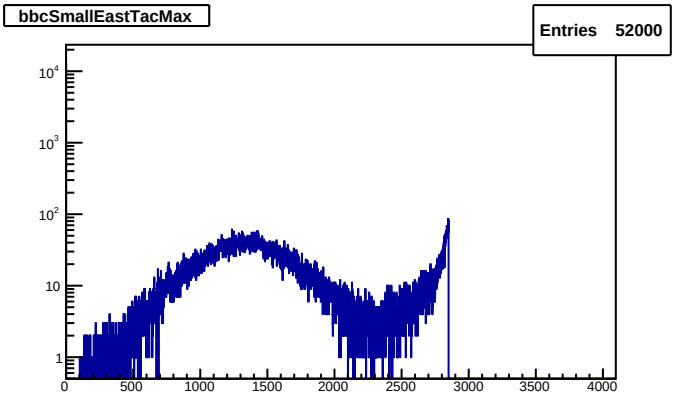

EQ3 QTB
Entries 4215168
TDC
QT Input Channel

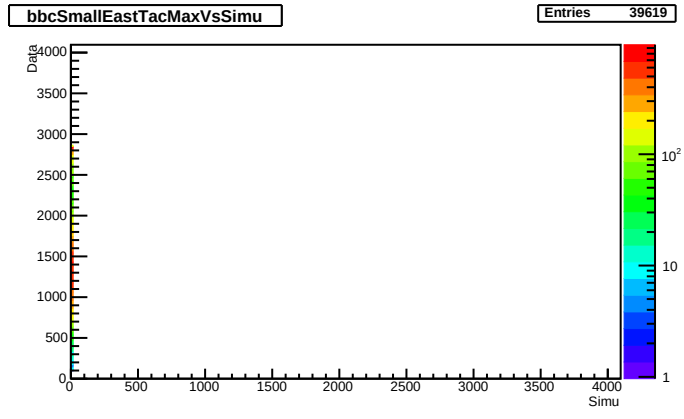

EQ3 QTC
Entries 4992000
ADC
QT Input Channel

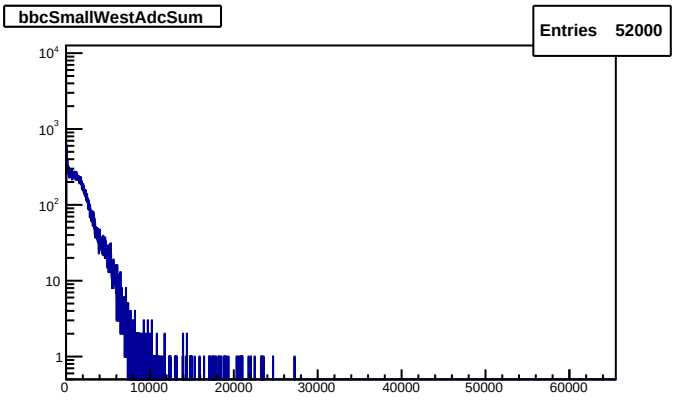

EQ3 QTC
Entries 4992000
TADC
QT Input Channel

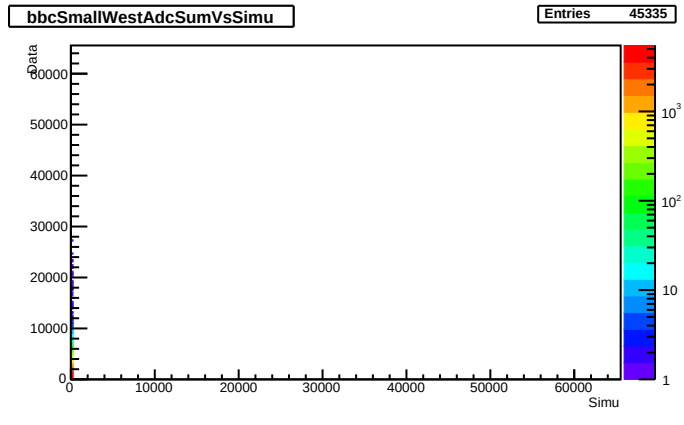

bbcSmallEastAdcSum
Entries 52000

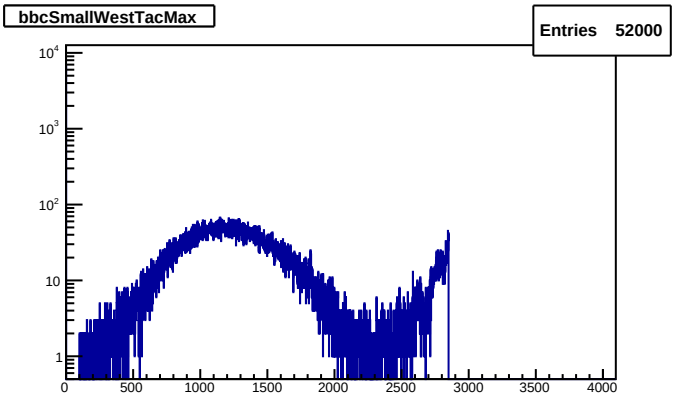

bbcSmallEastAdcSumVsSimu
Entries 39619
Data
Simu

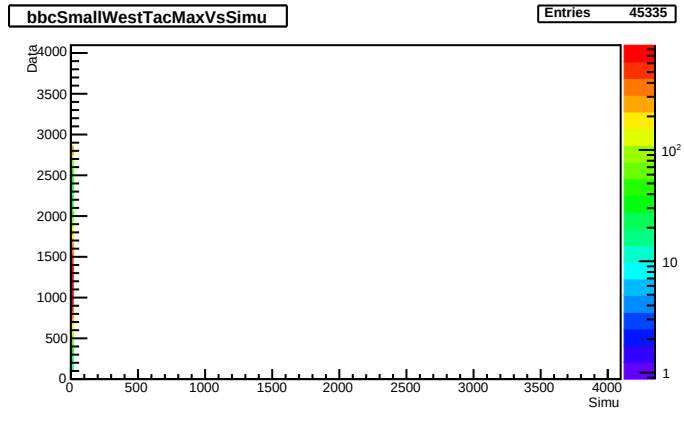

bbcSmallEastTacMax
Entries 52000


bbcSmallEastTacMaxVsSimu
Entries 39619
Data
Simu


bbcSmallWestAdcSum
Entries 52000


bbcSmallWestAdcSumVsSimu
Entries 45335
Data
Simu


bbcSmallWestTacMax
Entries 52000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Entries 45335
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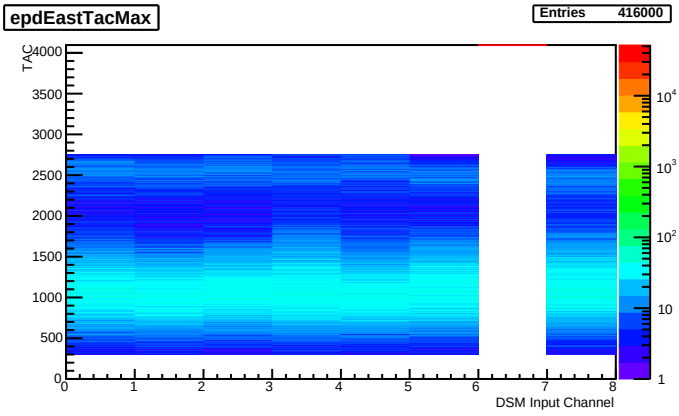
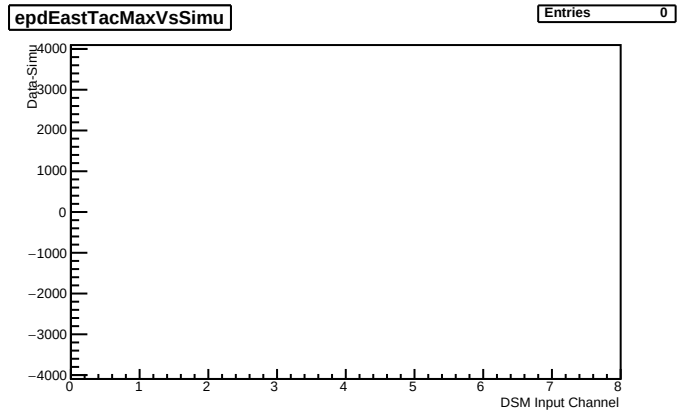
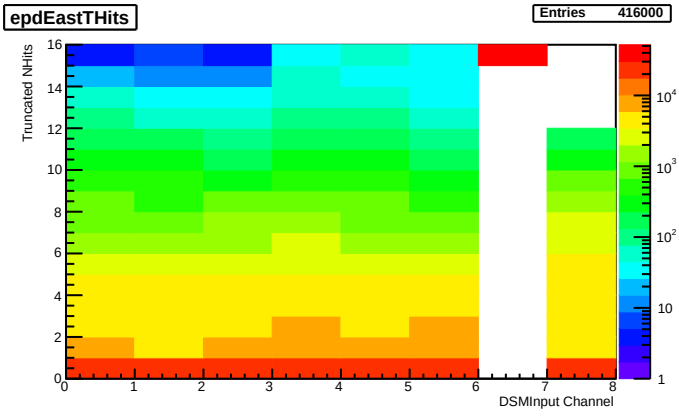
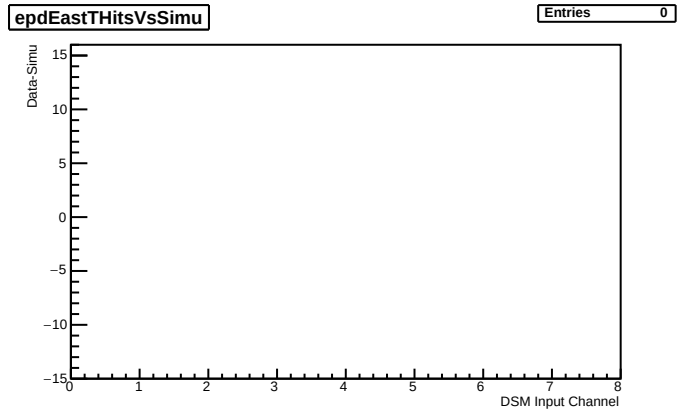
Simu


epdEastTacMax
Entries 416000
TAC
DSM Input Channel


epdEastTacMaxVsSimu
Entries 0
Data-Simu
DSM Input Channel


epdEastTHits
Entries 416000
Truncated NHits
DSMInput Channel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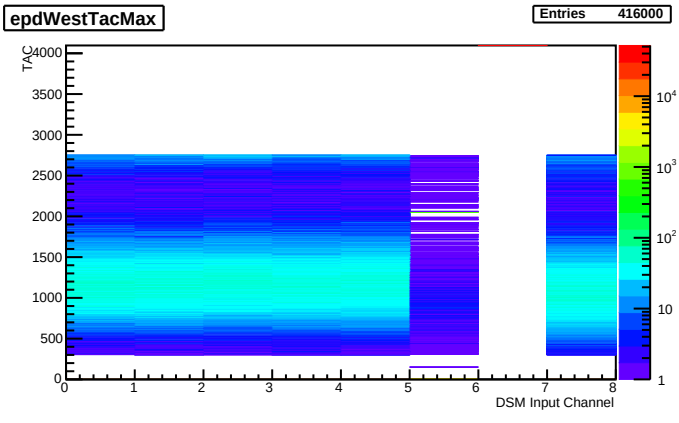
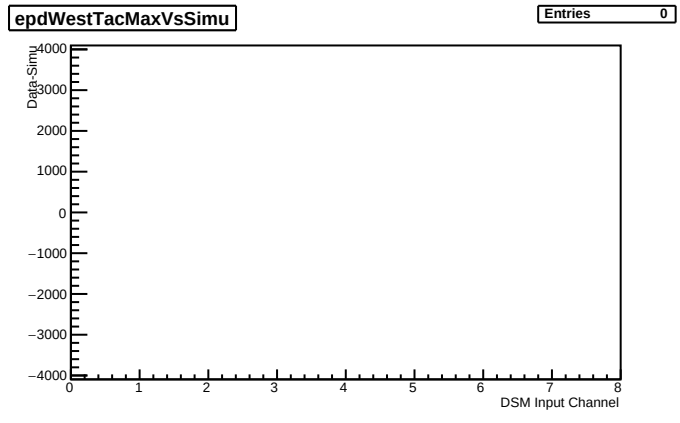
Entries 0
Data-Simu
DSM Input Channel


epdWestTacMax
Entries 416000
TAC
DSM Input Channel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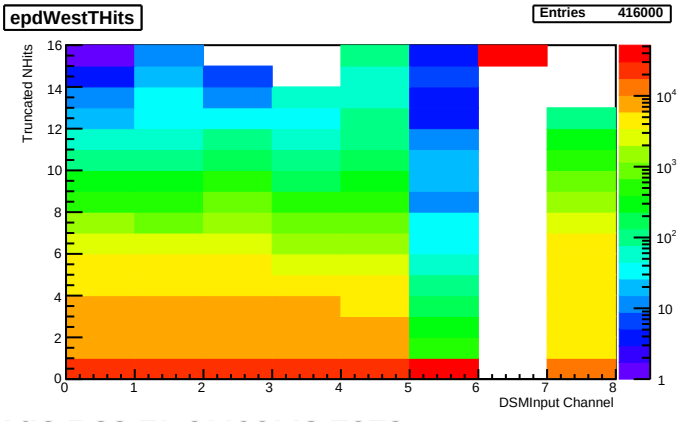
Entries 0
Data-Simu
DSM Input Channel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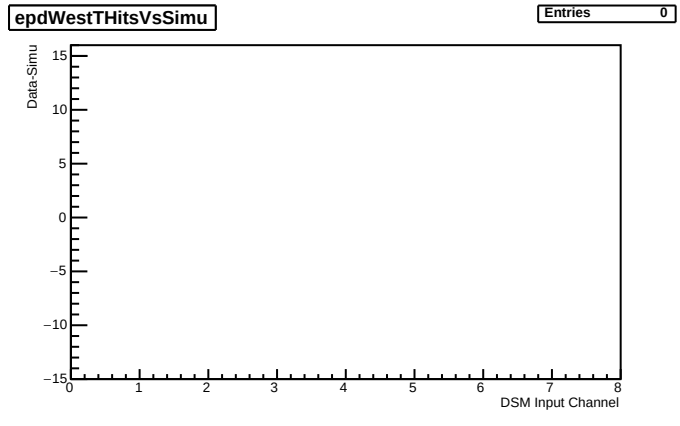
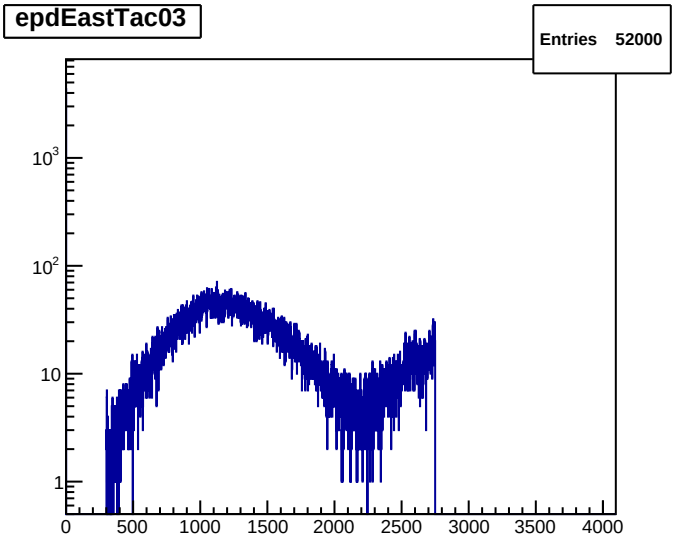
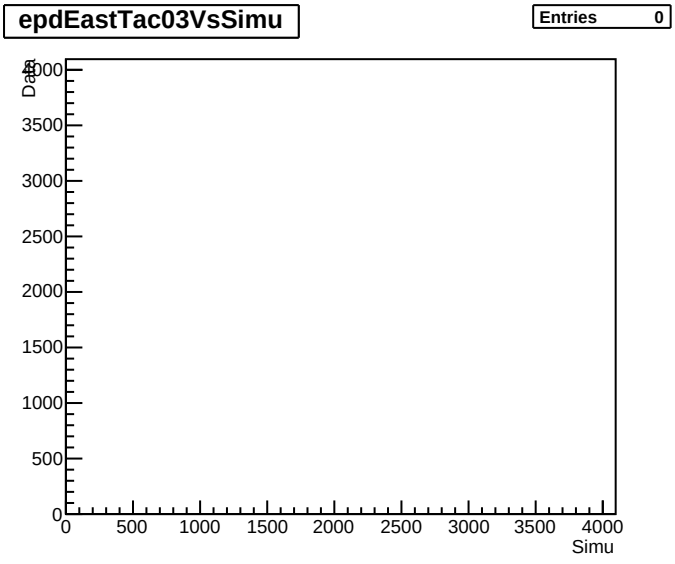
Entries 416000
Truncated NHits
DSMInput Channel


epdWestTHitsVsSimu
Entries 0
Data-Simu
DSM Input Channel


epdEastTac03
Entries 52000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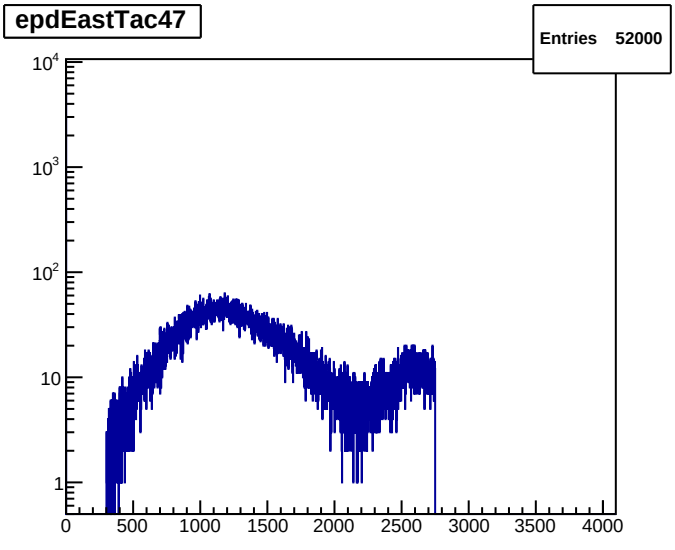
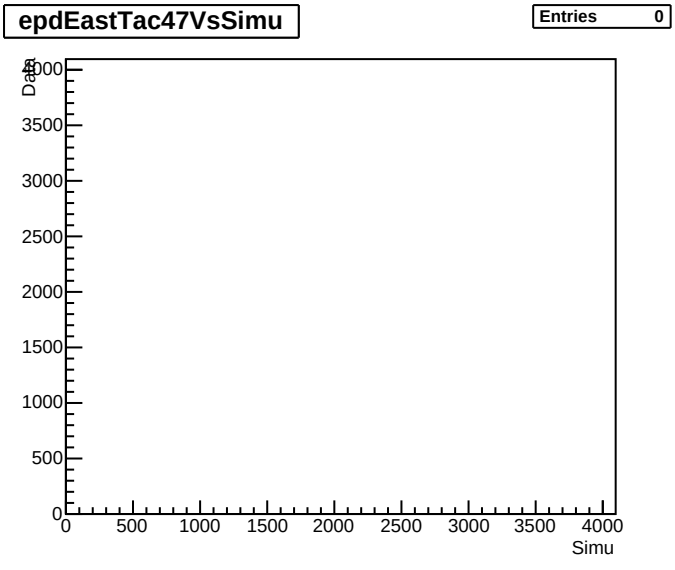
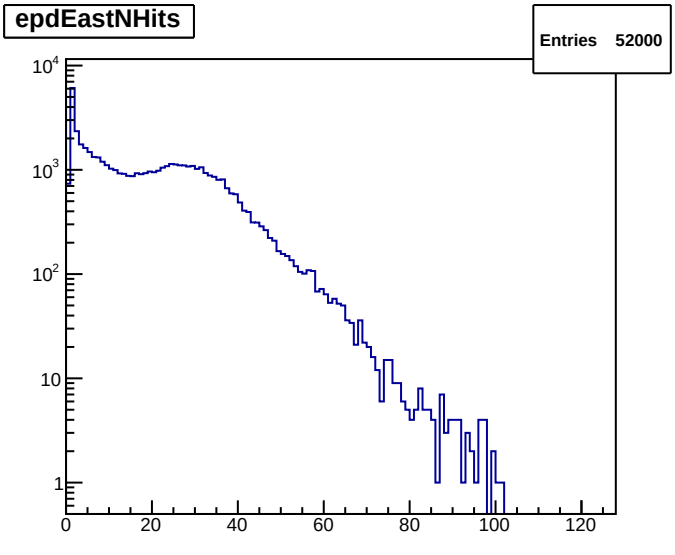
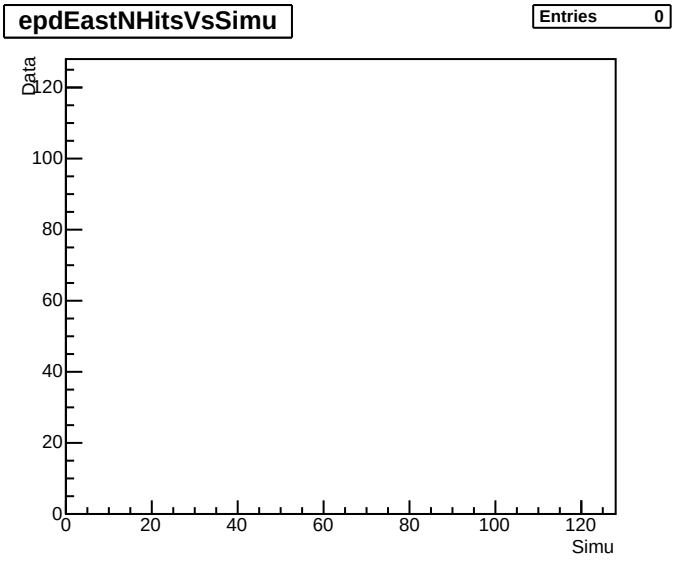
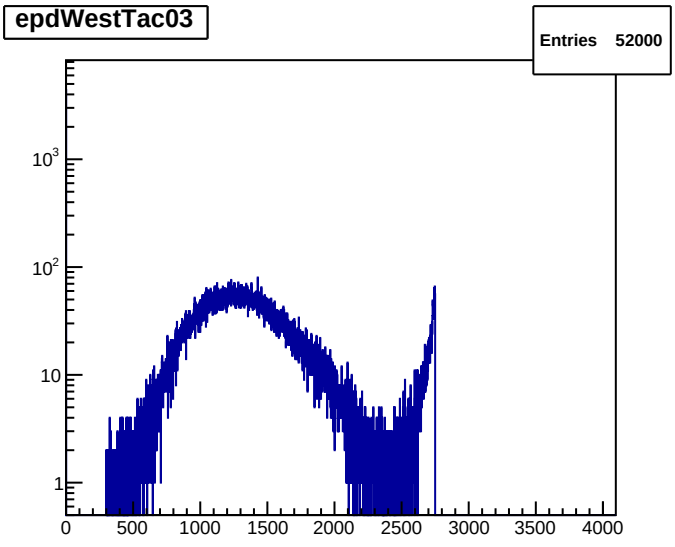
Entries 0
Data
Simu

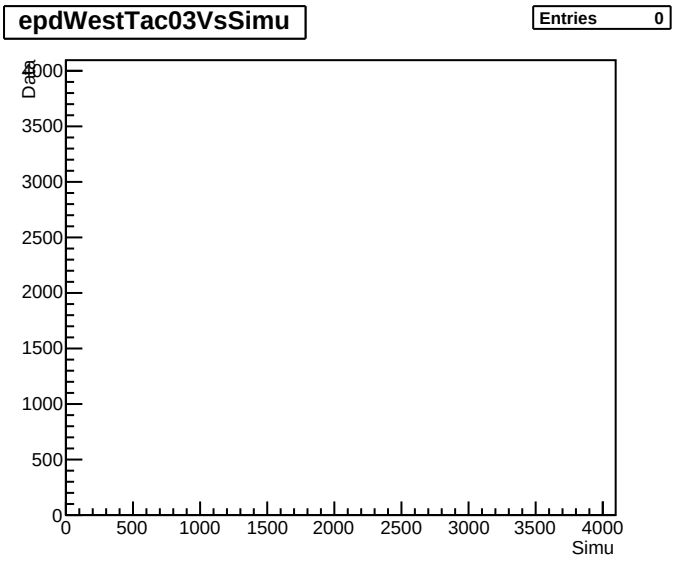

epdEastTac47
Entries 52000

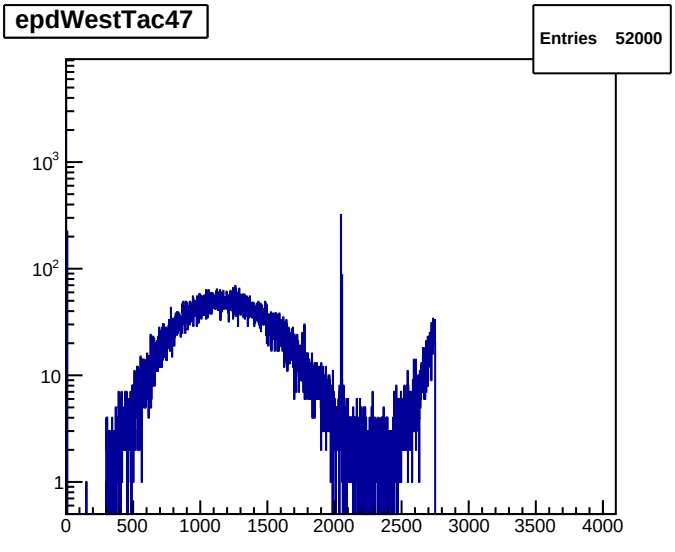

epdEastTac47VsSimu
Entries 0
Data
Simu

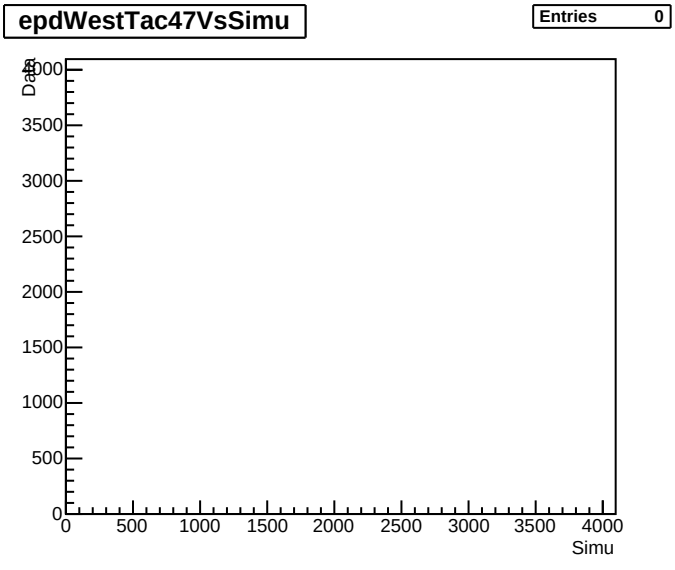

epdEastNHits
Entries 52000


epdEastNHitsVsSimu
Entries 0
Data
Simu


epdWestTac03
Entries 52000


epdWestTac03VsSimu
Entries 0
Data
Simu


epdWestTac47
Entries 52000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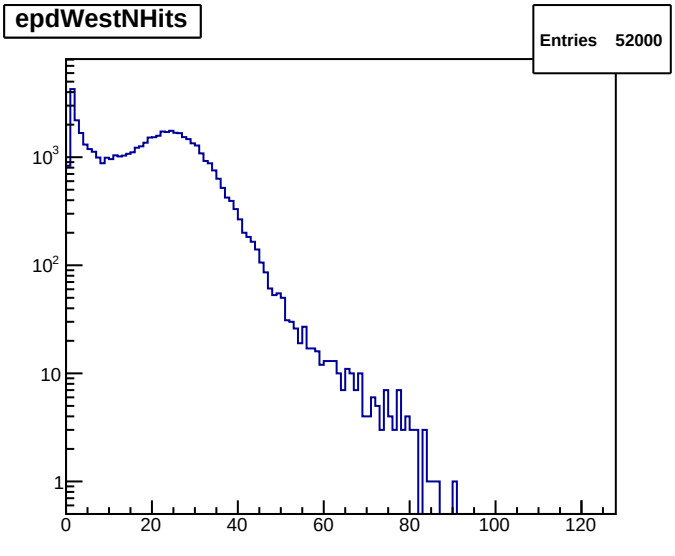
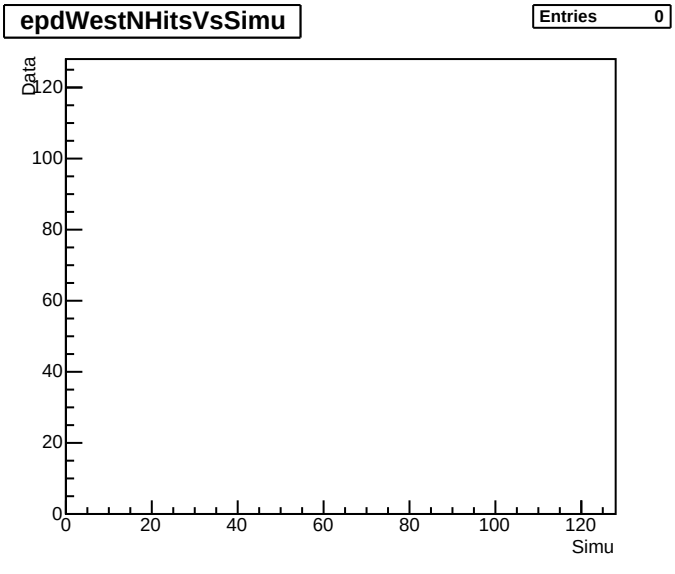
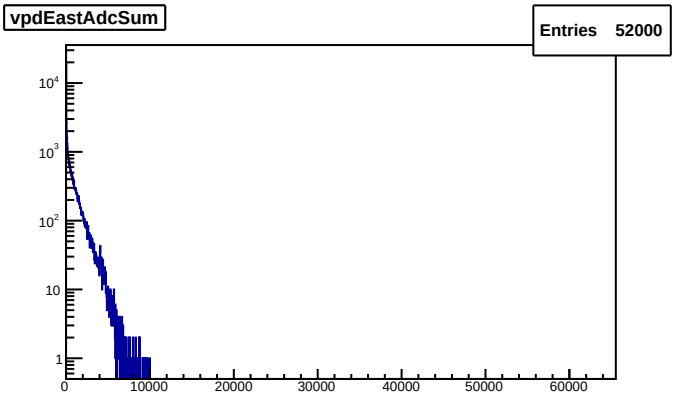
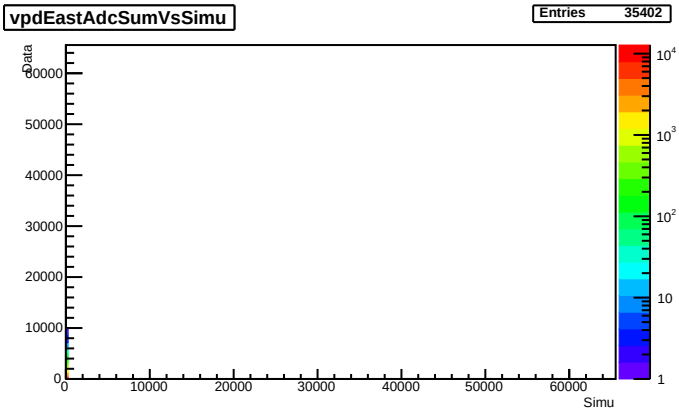
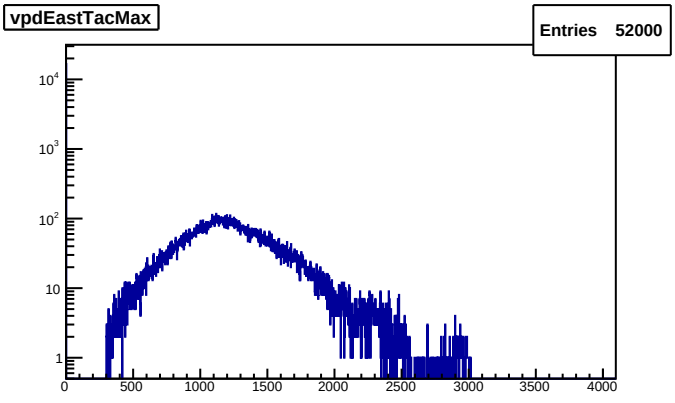
Entries 0
Data
Simu

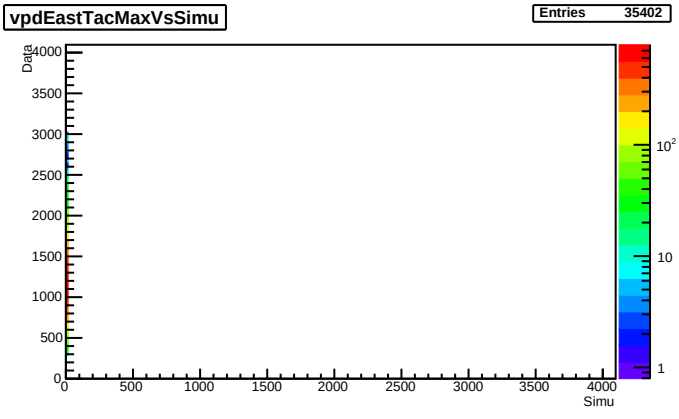

epdWestNHits
Entries 52000

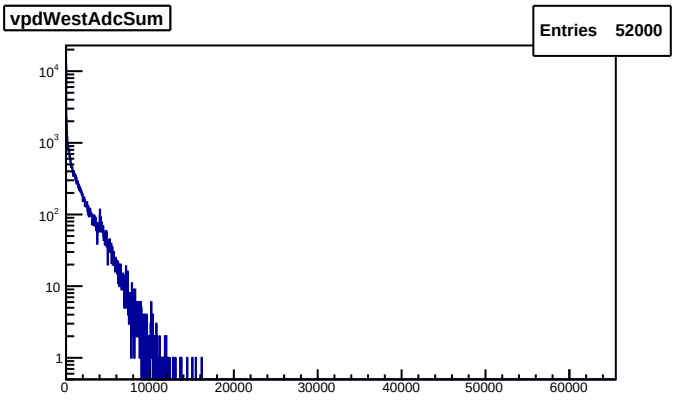

epdWestNHitsVsSimu
Entries 0
Data
Simu

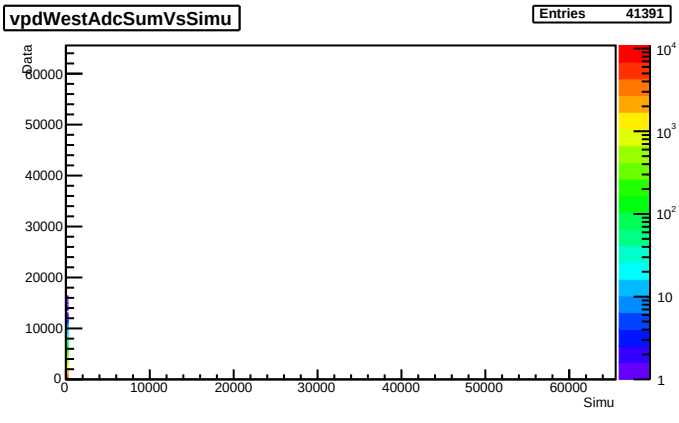

vpdEastAdcSum
Entries 52000

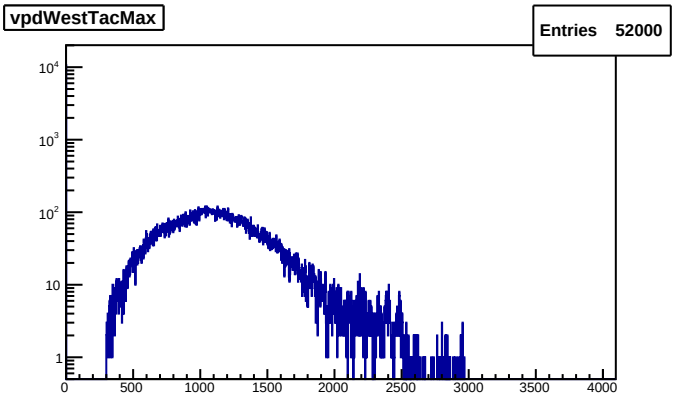

vpdEastAdcSumVsSimu
Entries 35402
Data
Simu

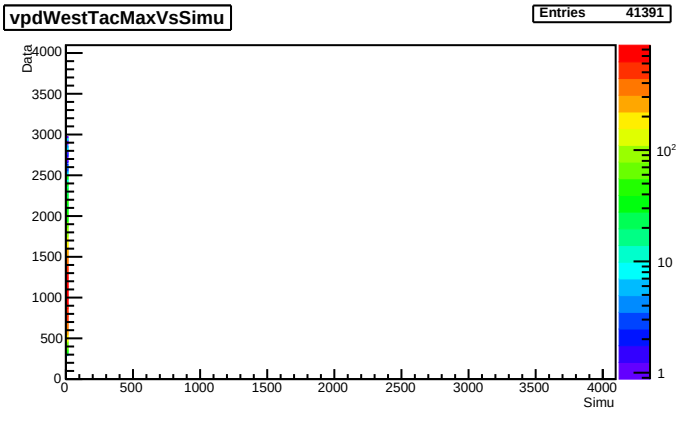

vpdEastTacMax
Entries 52000

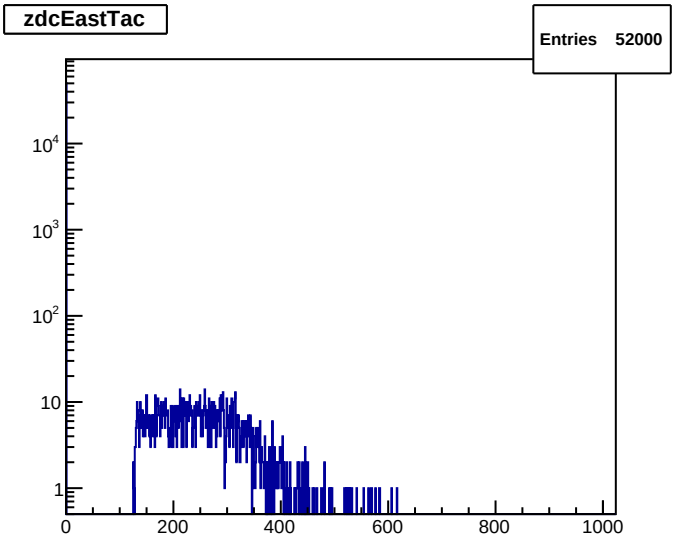

vpdEastTacMaxVsSimu
Entries 35402
Data
Simu

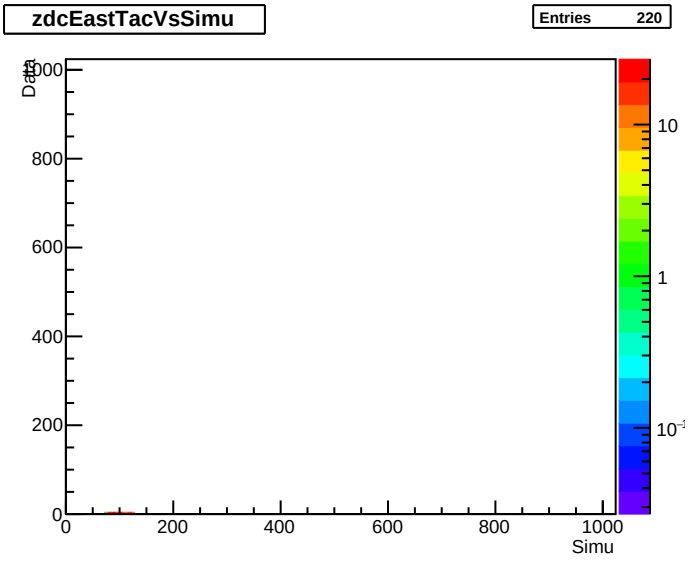

vpdWestAdcSum
Entries 52000

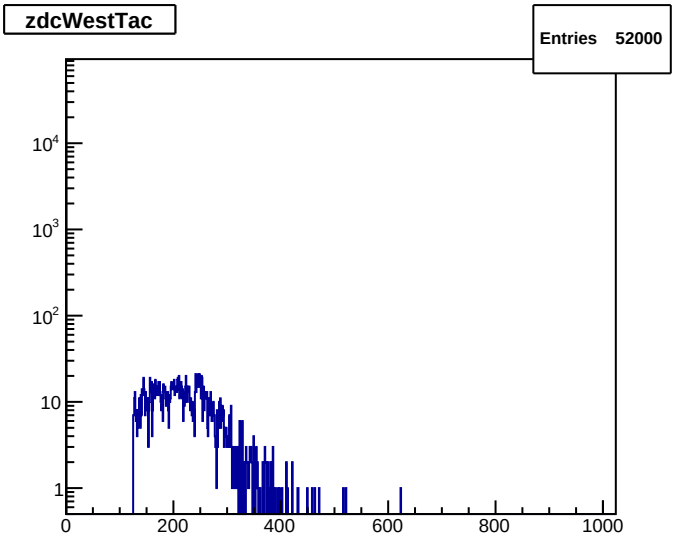

vpdWestAdcSumVsSimu
Entries 41391
Data
Simu

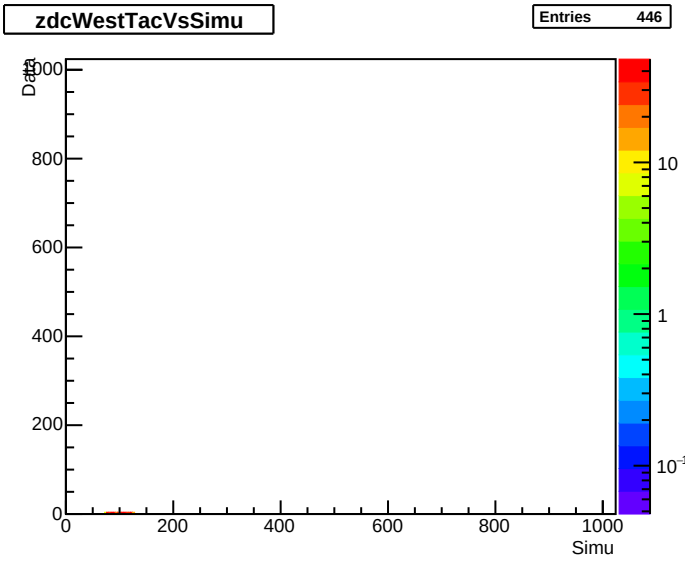

vpdWestTacMax
Entries 52000

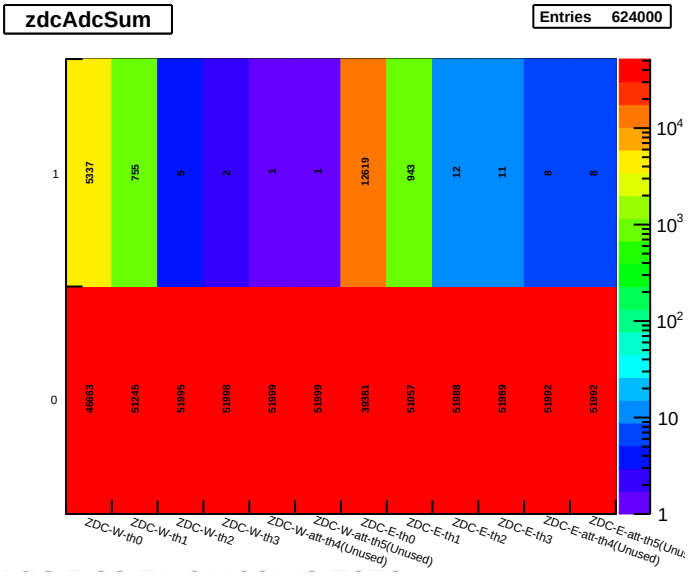

vpdWestTacMaxVsSimu
Entries 41391
Data
Simu

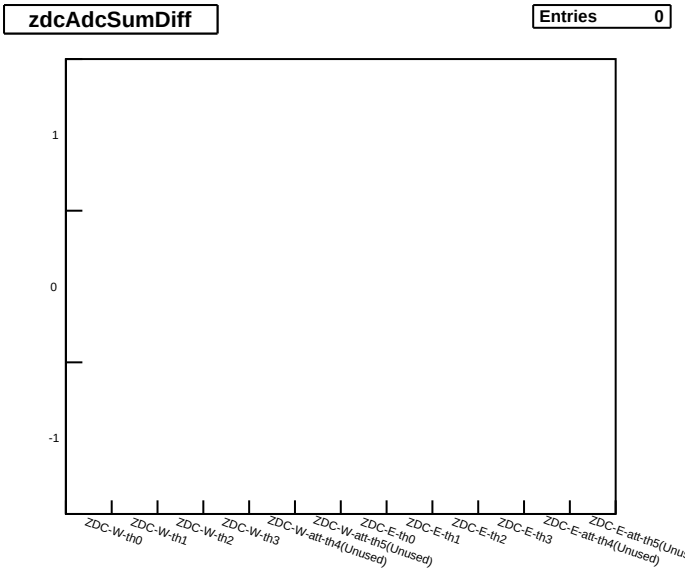

zdcEastTac
Entries 52000

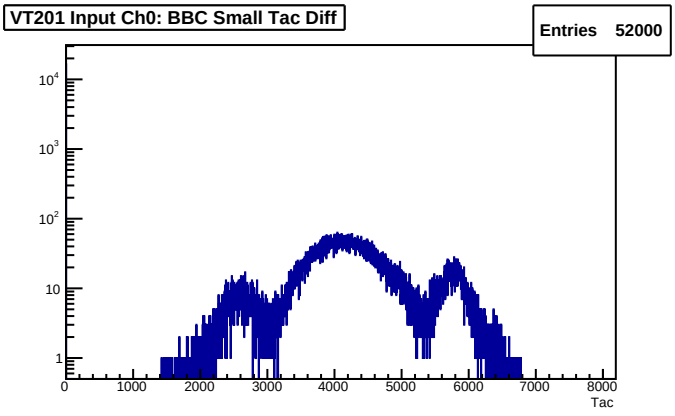

zdcEastTacVsSimu
Entries 220
Data
Simu

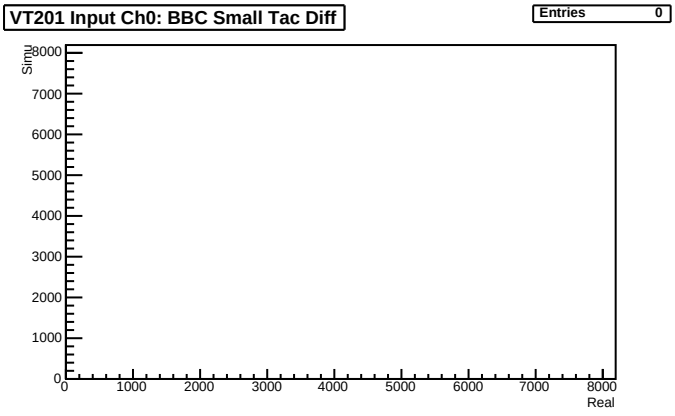

zdcWestTac
Entries 52000

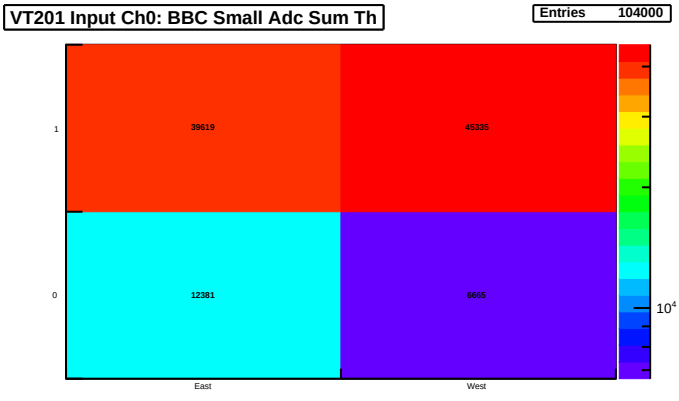

zdcWestTacVsSimu
Entries 446
Data
Simu

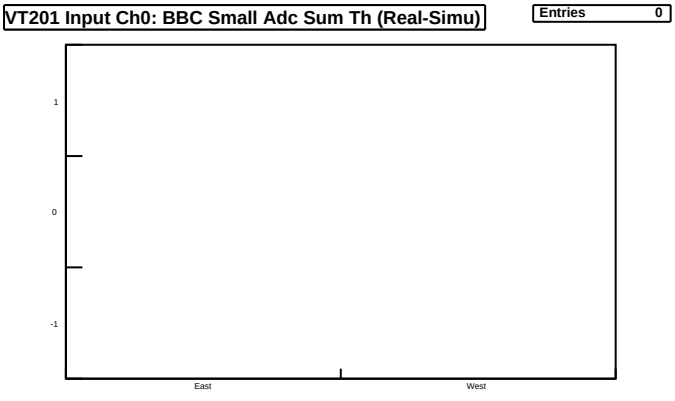

zdcAdcSum
Entries 624000
5337
755
5
2
1
1
12619
943
12
11
8
8
46663
51245
51995
51998
51999
51999
39381
51057
51988
51989
51992
51992
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)
ZDC-E-att-th5(Unu

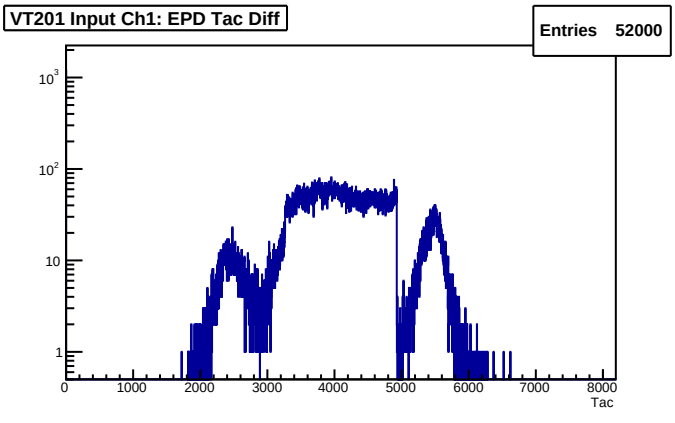

zdcAdcSumDiff
Entries 0
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)
ZDC-E-att-th5(Unu

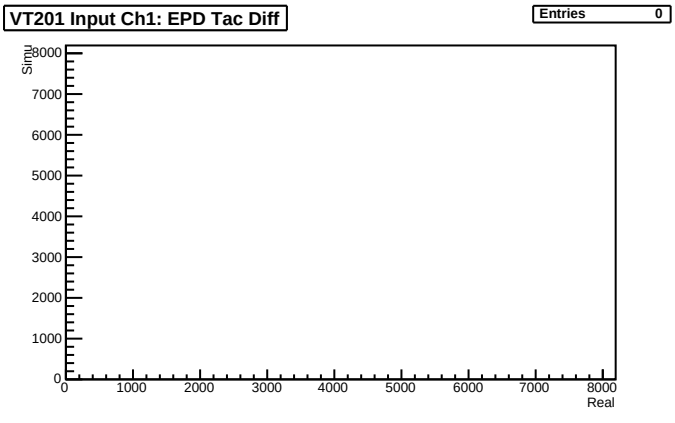

VT201 Input Ch0: BBC Small Tac Diff
Entries 52000
Tac

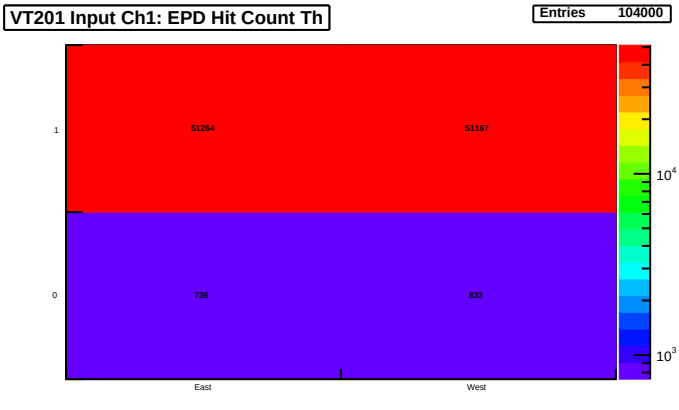

VT201 Input Ch0: BBC Small Tac Diff
Entries 0
Simu
Real

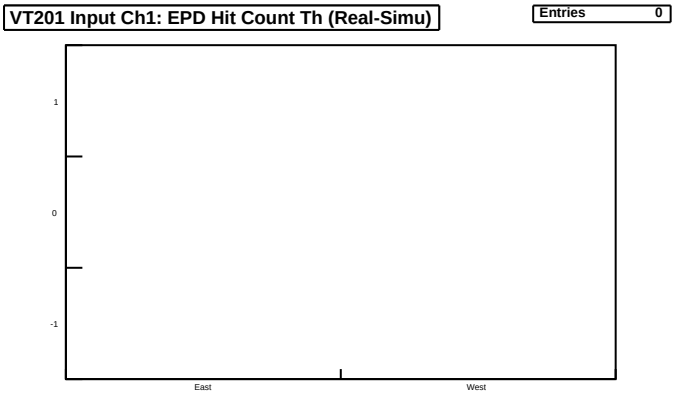

VT201 Input Ch0: BBC Small Adc Sum Th
Entries 104000
39619
45335
12381
6665
East
West

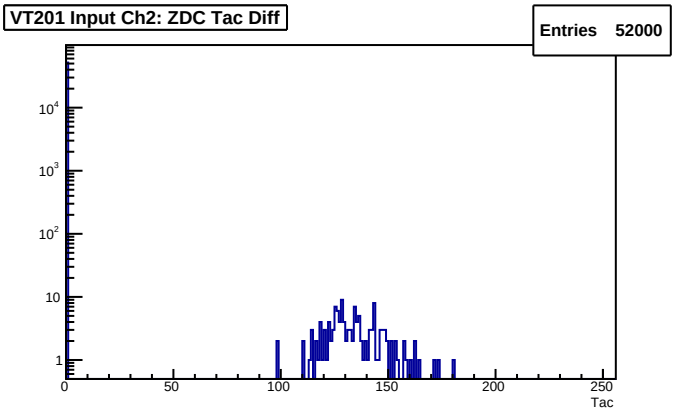

VT201 Input Ch0: BBC Small Adc Sum Th (Real-Simu)
Entries 0
East
West

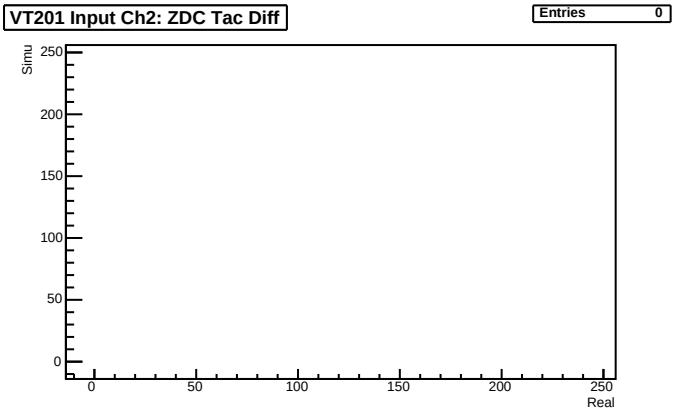

VT201 Input Ch1: EPD Tac Diff
Entries 52000
Tac

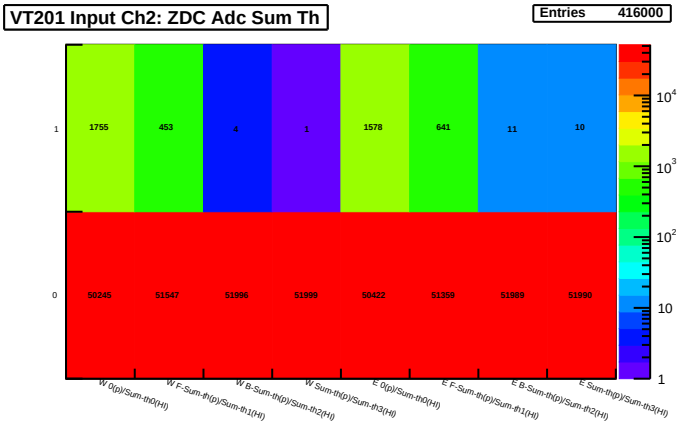

VT201 Input Ch1: EPD Tac Diff
Entries 0
Simu
Real

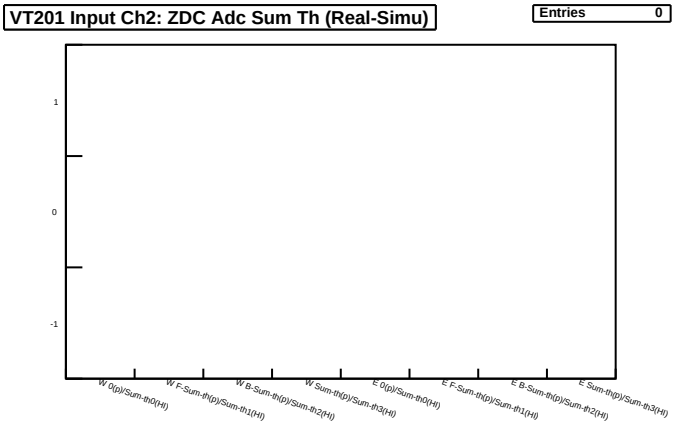

VT201 Input Ch1: EPD Hit Count Th
Entries 104000
51264
51167
736
833
East
West

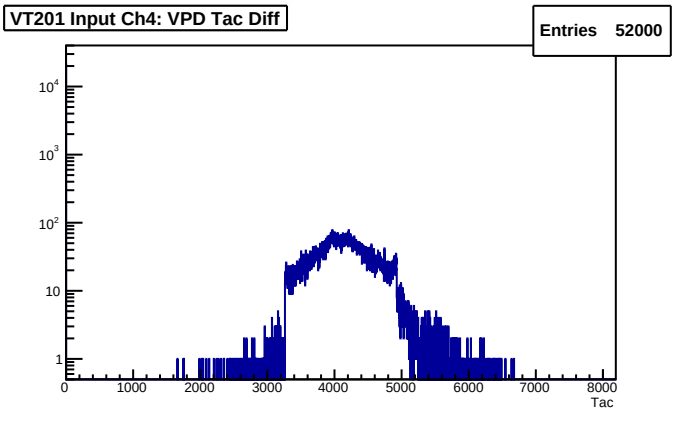

VT201 Input Ch1: EPD Hit Count Th (Real-Simu)
Entries 0
East
West

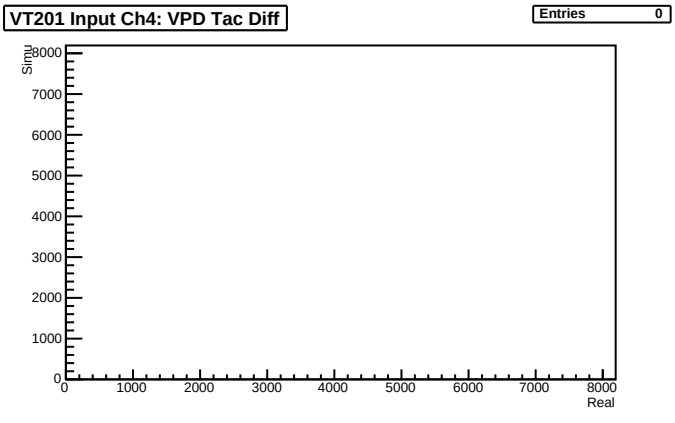

VT201 Input Ch2: ZDC Tac Diff
Entries 52000
Tac

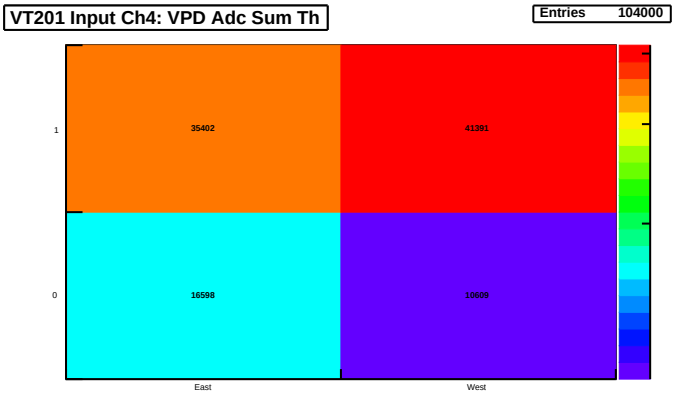

VT201 Input Ch2: ZDC Tac Diff
Entries 0
Simu
Real

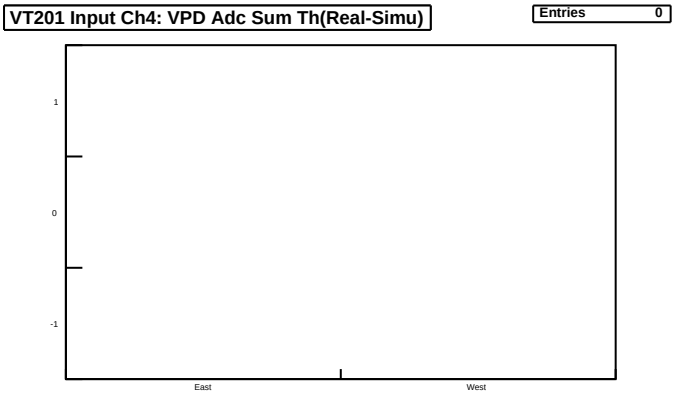

VT201 Input Ch2: ZDC Adc Sum Th
Entries 416000
1755
453
4
1
1578
641
11
10
50245
51547
51996
51999
50422
51359
51989
51990
W 0(p)/Sum-th0(Hl)
W F-Sum-th(p)/Sum-th1(Hl)
W B-Sum-th(p)/Sum-th2(Hl)
W Sum-th(p)/Sum-th3(Hl)
E 0(p)/Sum-th0(Hl)
E F-Sum-th(p)/Sum-th1(Hl)
E B-Sum-th(p)/Sum-th2(Hl)
E Sum-th(p)/Sum-th3(Hl)

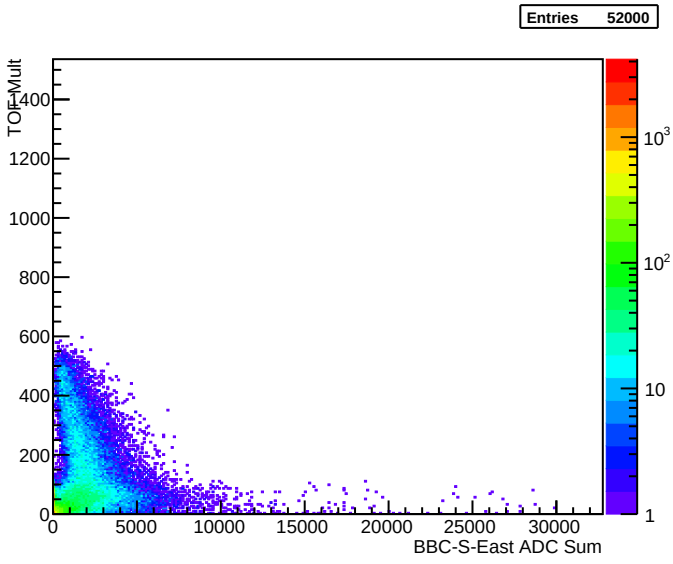

VT201 Input Ch2: ZDC Adc Sum Th (Real-Simu)
Entries 0
W 0(p)/Sum-th0(Hl)
W F-Sum-th(p)/Sum-th1(Hl)
W B-Sum-th(p)/Sum-th2(Hl)
W Sum-th(p)/Sum-th3(Hl)
E 0(p)/Sum-th0(Hl)
E F-Sum-th(p)/Sum-th1(Hl)
E B-Sum-th(p)/Sum-th2(Hl)
E Sum-th(p)/Sum-th3(Hl)

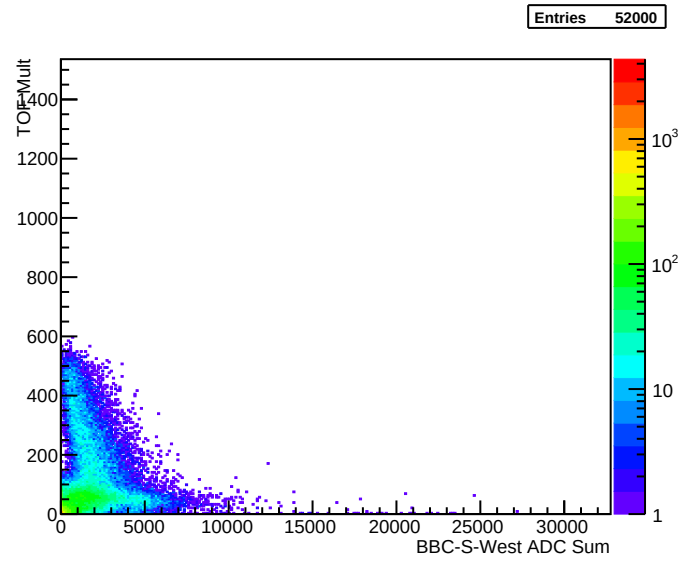

VT201 Input Ch4: VPD Tac Diff
Entries 52000
Tac

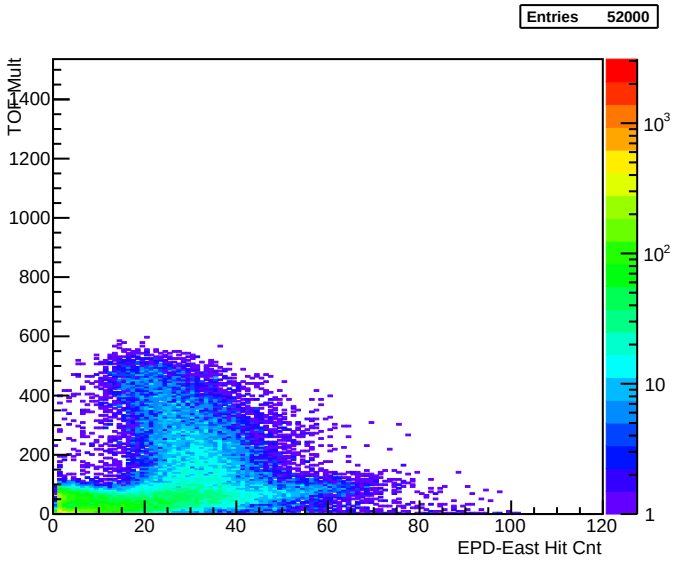

VT201 Input Ch4: VPD Tac Diff
Entries 0
Simu
Real

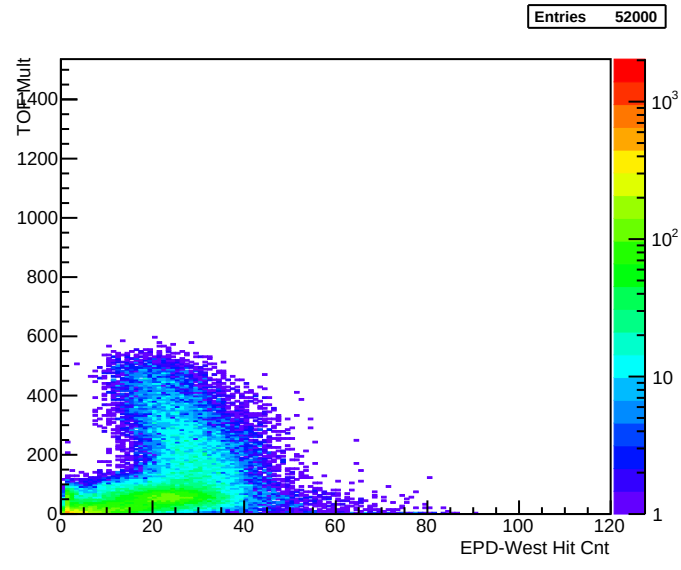

VT201 Input Ch4: VPD Adc Sum Th
Entries 104000
35402
41391
16598
10609
East
West

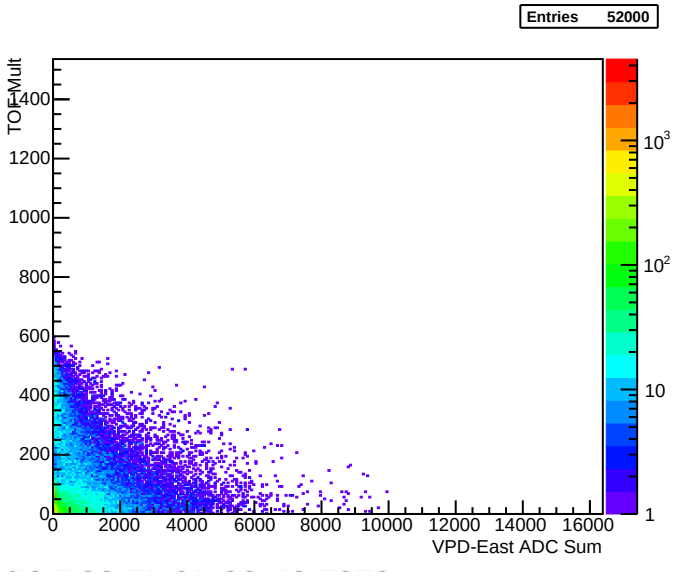

VT201 Input Ch4: VPD Adc Sum Th(Real-Simu)
Entries 0
East
West

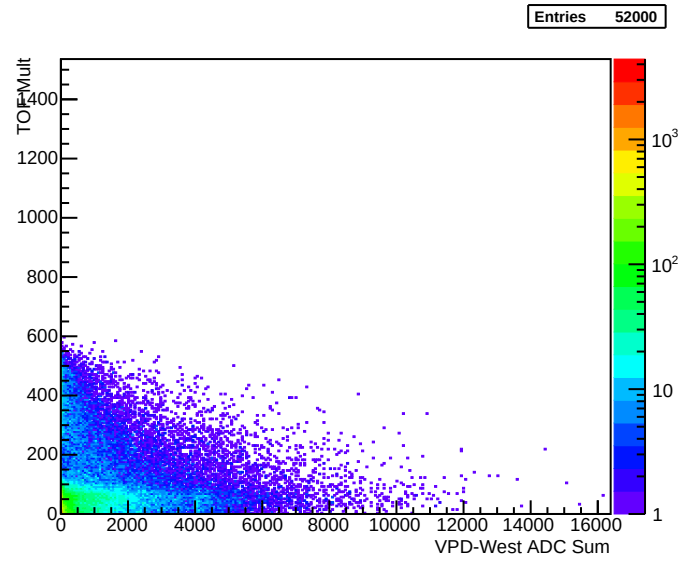

Entries 52000
TOF-Mult
BBC-S-East ADC Sum

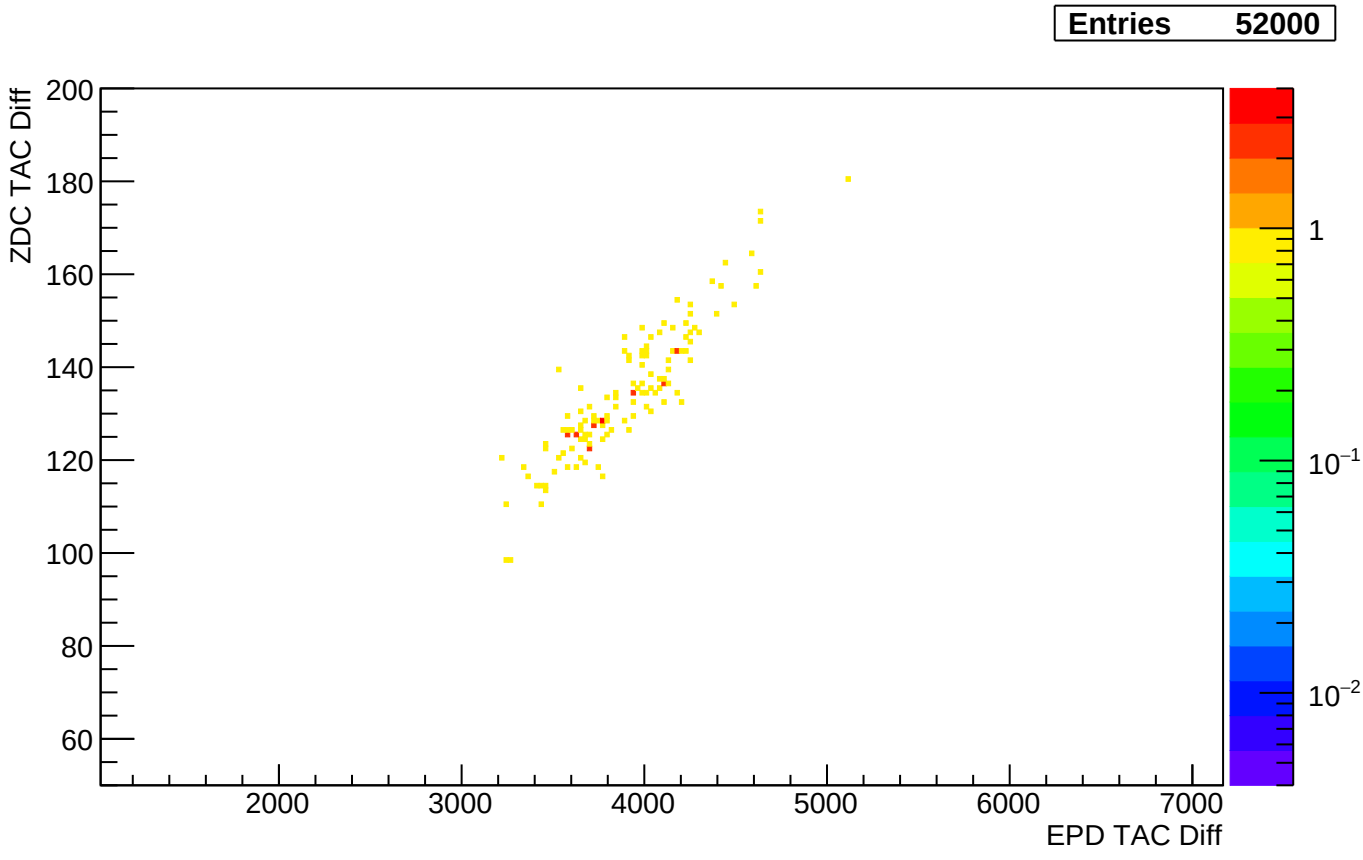

Entries 52000
TOF-Mult
BBC-S-West ADC Sum

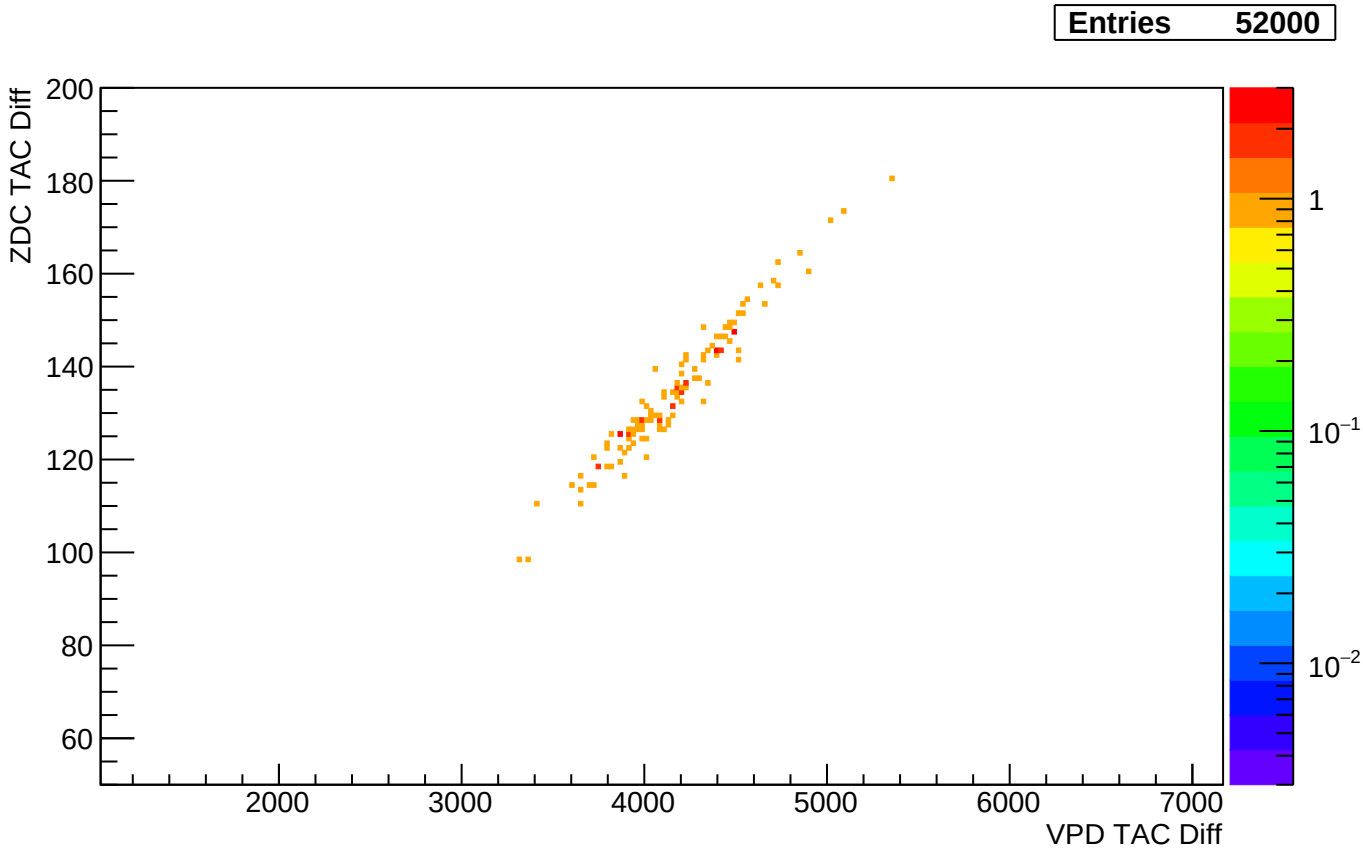

Entries 52000
TOF-Mult
EPD-East Hit Cnt

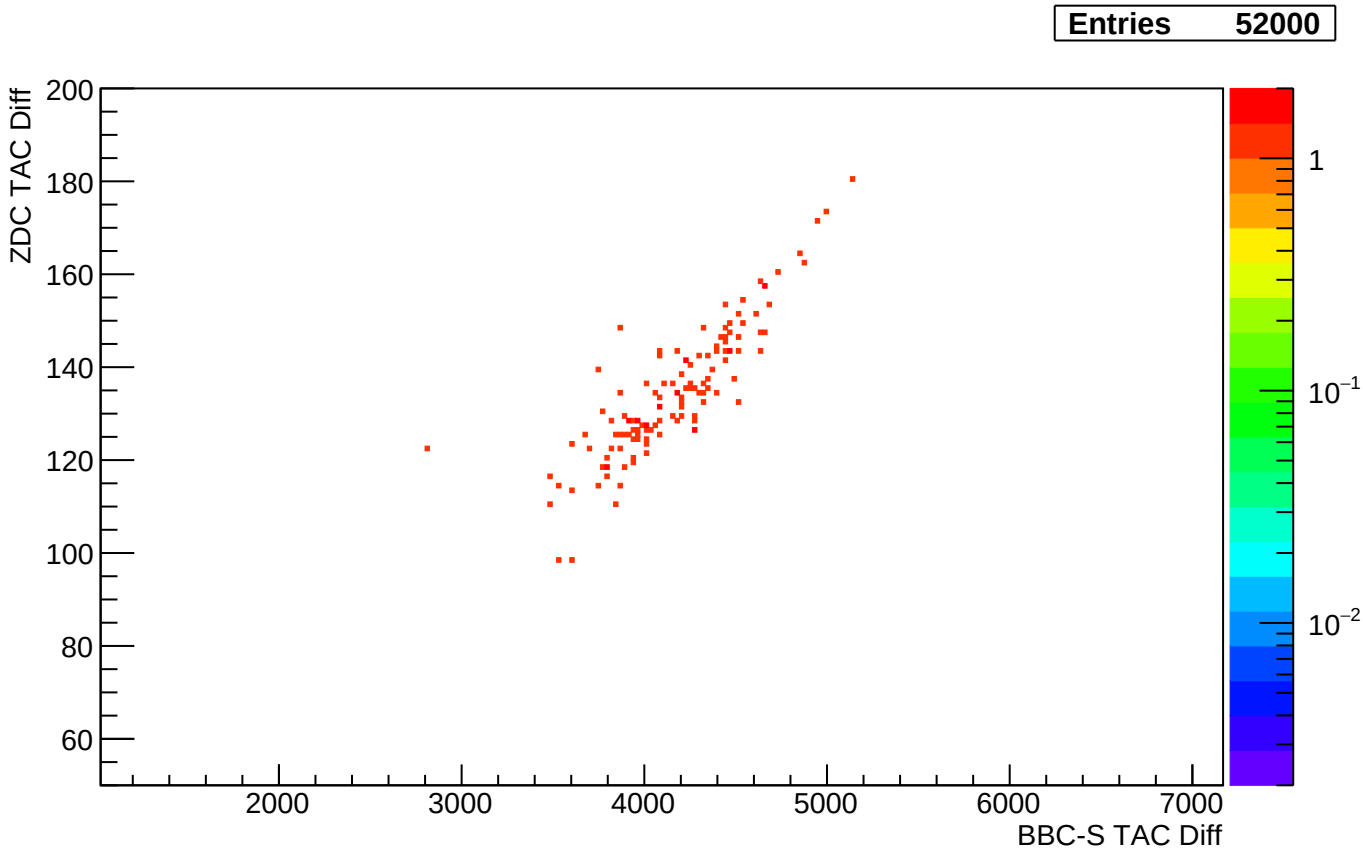

Entries 52000
TOF-Mult
EPD-West Hit Cnt

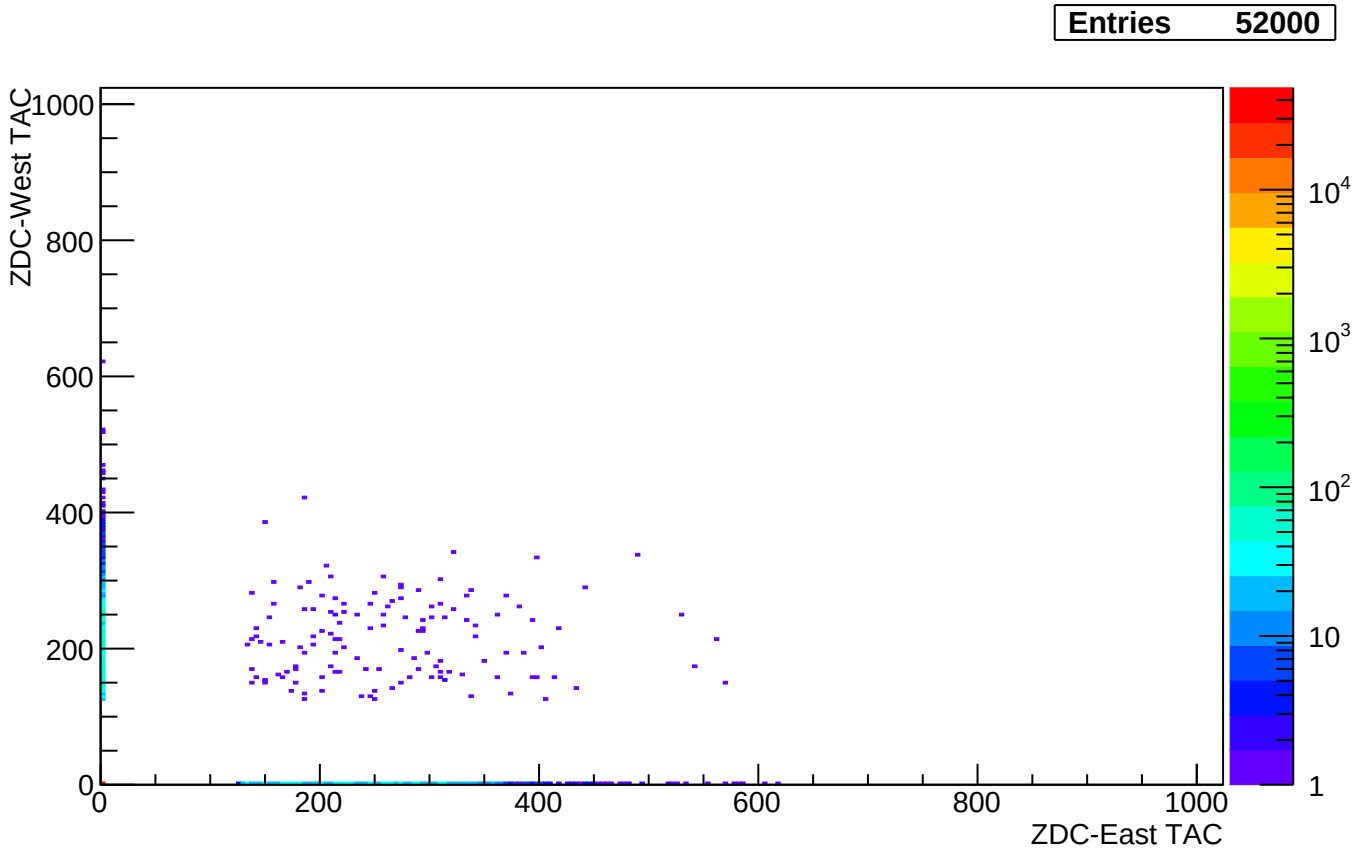

Entries 52000
TOF-Mult
VPD-East ADC Sum

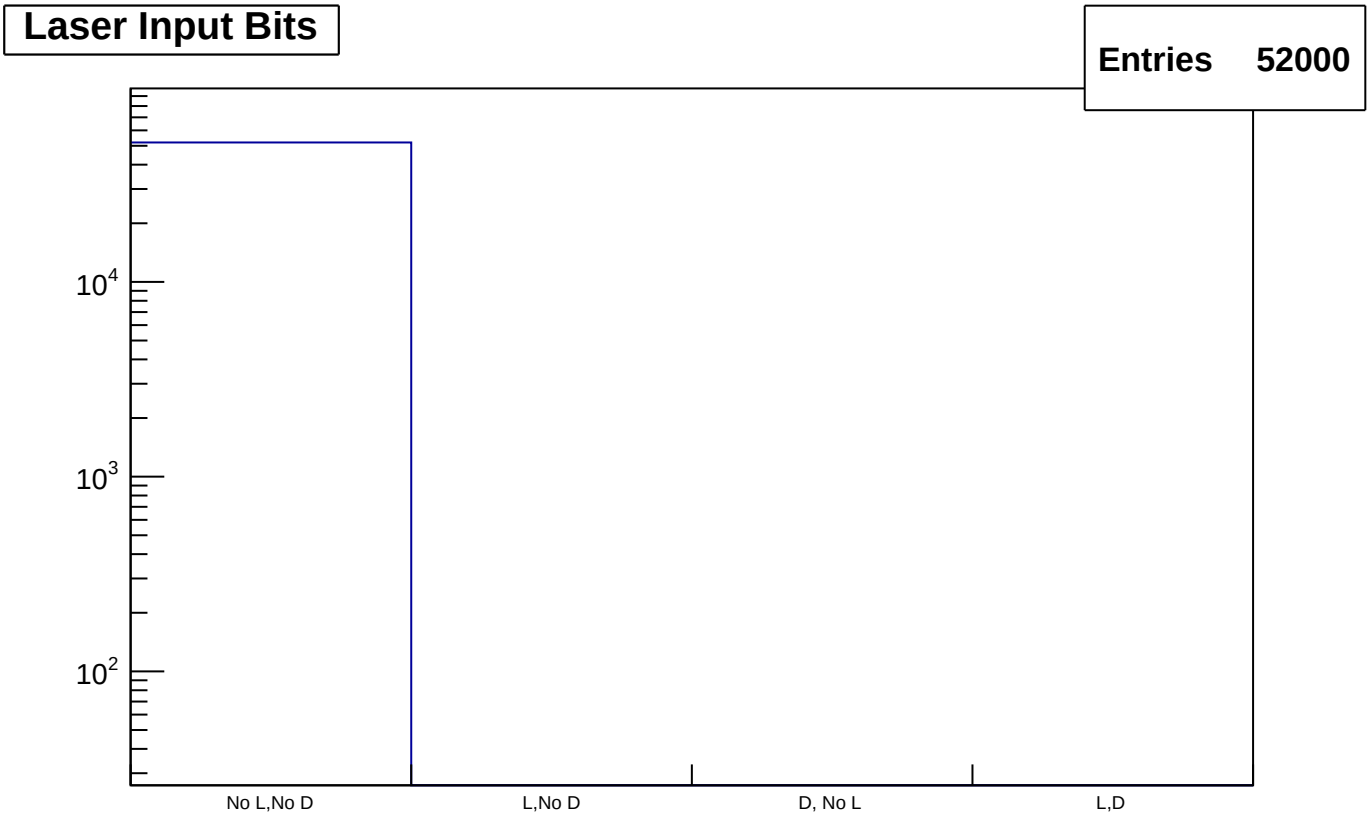

Entries 52000
TOF-Mult
VPD-West ADC Sum

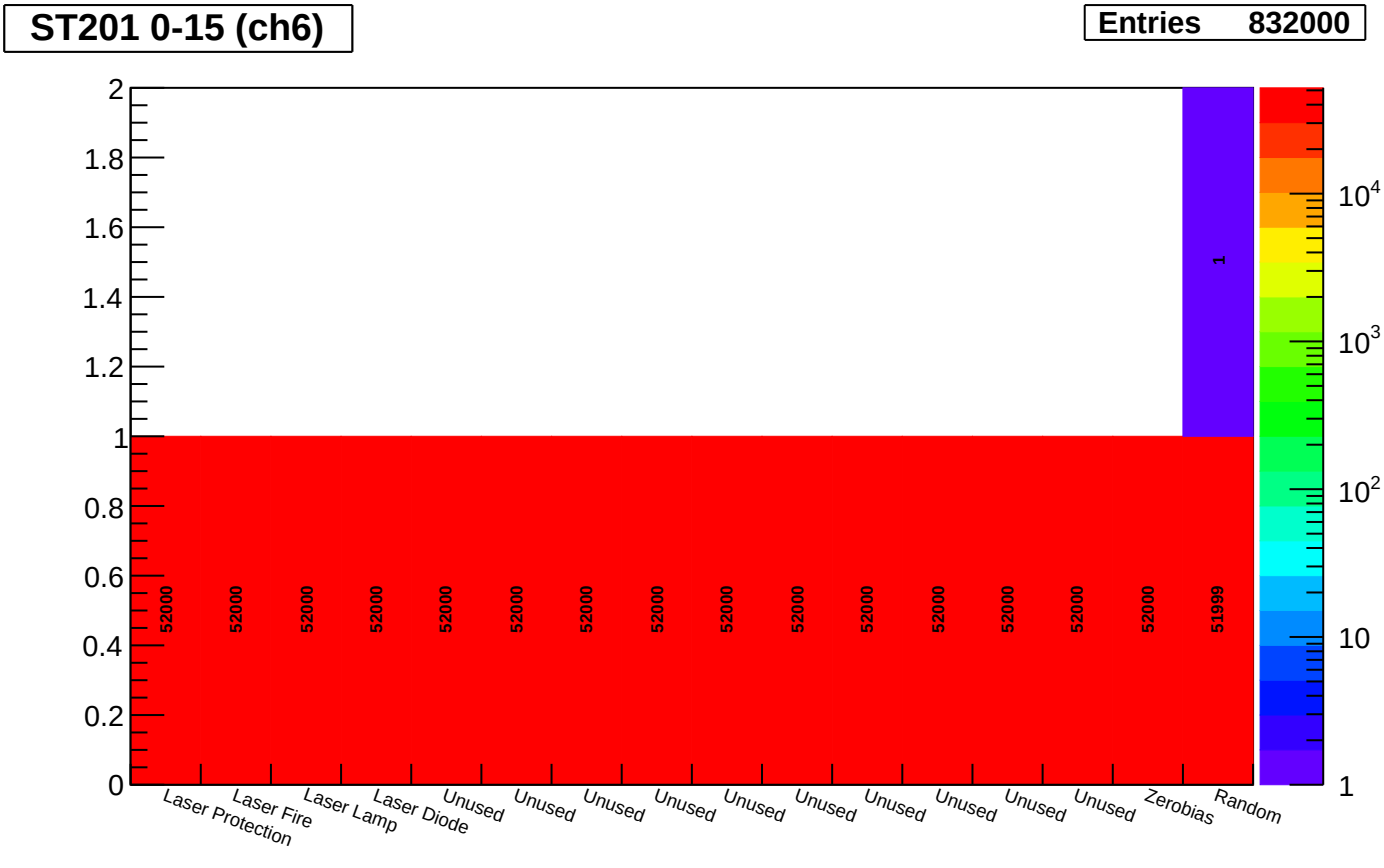

Entries 52000
ZDC TAC Diff
200
180
160
140
120
100
80
60
2000
3000
4000
5000
6000
7000
EPD TAC Diff
1
10^{-1}
10^{-2}

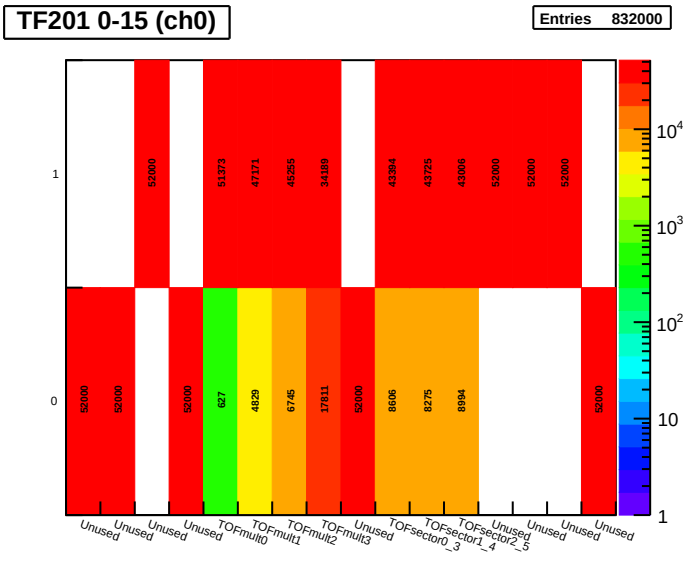

Entries 52000
ZDC TAC Diff
200
180
160
140
120
100
80
60
2000
3000
4000
5000
6000
7000
VPD TAC Diff
1
10^{-1}
10^{-2}

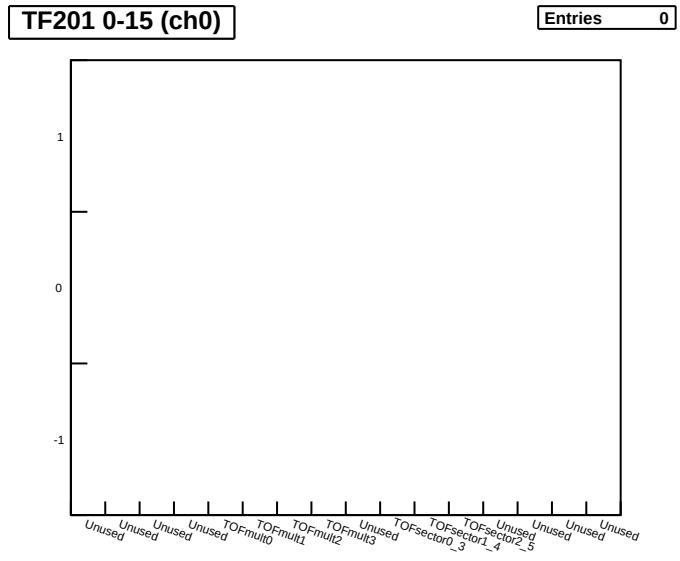

Entries 52000
ZDC TAC Diff
200
180
160
140
120
100
80
60
2000
3000
4000
5000
6000
7000
BBC-S TAC Diff
1
10^{-1}
10^{-2}

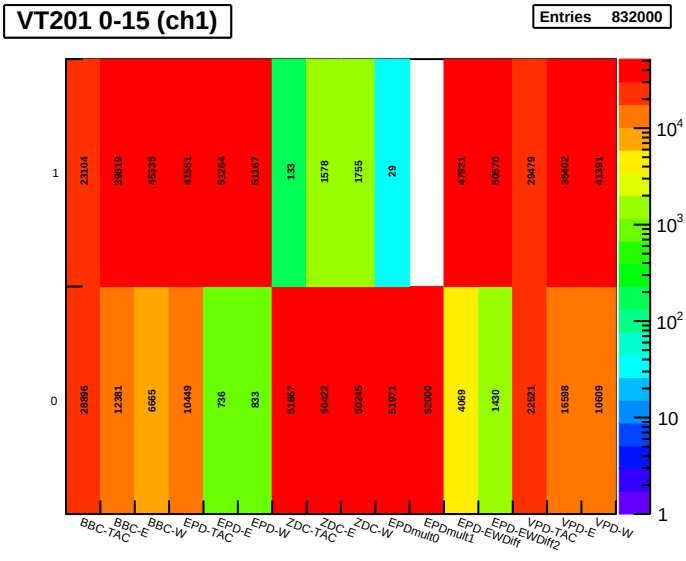

Entries 52000
ZDC-West TAC
1000
800
600
400
200
0
0
200
400
600
800
1000
ZDC-East TAC
10^4
10^3
10^2
10
1

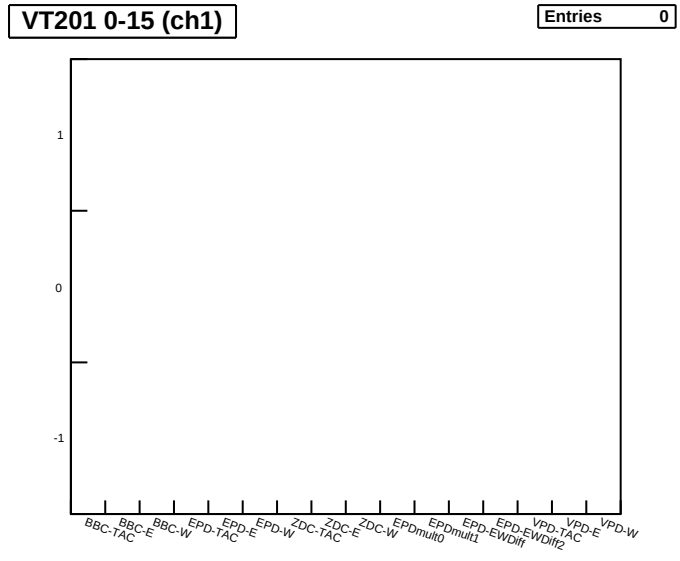

Laser Input Bits
Entries 52000
10^4
10^3
10^2
No L,No D
L,No D
D, No L
L,D

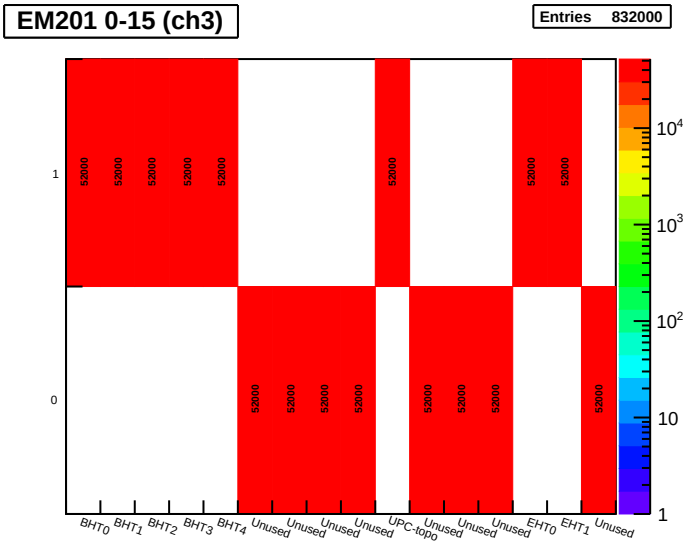

ST201 0-15 (ch6)
Entries 832000
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
52000
52000
52000
52000
52000
52000
52000
52000
52000
52000
52000
52000
52000
52000
52000
51999
1
10^4
10^3
10^2
10
1
Laser Protection
Laser Fire
Laser Lamp
Laser Diode
Unused
Unused
Unused
Unused
Unused
Unused
Unused
Unused
Unused
Unused
Zerobias
Random

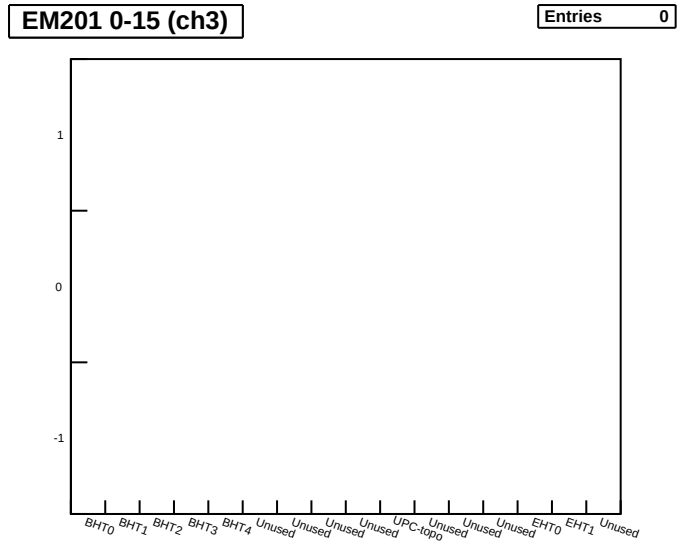

TF201 0-15 (ch0)
Entries 832000
1
0
52000
51373
47171
45255
34189
43394
43725
43006
52000
52000
52000
52000
52000
52000
52000
627
4829
6745
17811
52000
8606
8275
8994
52000
Unused
TOFmult0
TOFmult1
TOFmult2
TOFmult3
TOFsector0_3
TOFsector1_4
TOFsector2_5
10^4
10^3
10^2
10
1


TF201 0-15 (ch0)
Entries 0
1
0
-1
Unused
TOFmult0
TOFmult1
TOFmult2
TOFmult3
TOFsector0_3
TOFsector1_4
TOFsector2_5


VT201 0-15 (ch1)
Entries 832000
1
0
23104
39619
45335
41551
51264
51167
133
1578
1755
29
47931
50570
29479
35402
41391
28896
12381
6665
10449
736
833
51867
50422
50245
51971
52000
4069
1430
22521
16598
10609
BBC-TAC
BBC-E
BBC-W
EPD-TAC
EPD-E
EPD-W
ZDC-TAC
ZDC-E
ZDC-W
EPDmult0
EPDmult1
EPD-EWDiff
EPD-EWDiff2
VPD-TAC
VPD-E
VPD-W
10^4
10^3
10^2
10
1


VT201 0-15 (ch1)
Entries 0
1
0
-1
BBC-TAC
BBC-E
BBC-W
EPD-TAC
EPD-E
EPD-W
ZDC-TAC
ZDC-E
ZDC-W
EPDmult0
EPDmult1
EPD-EWDiff
EPD-EWDiff2
VPD-TAC
VPD-E
VPD-W


EM201 0-15 (ch3)
Entries 832000
1
0
52000
52000
52000
52000
52000
52000
52000
52000
52000
52000
52000
52000
52000
52000
52000
52000
52000
BHT0
BHT1
BHT2
BHT3
BHT4
Unused
UPC-topo
EHT0
EHT1
10^4
10^3
10^2
10
1


EM201 0-15 (ch3)
Entries 0
1
0
-1
BHT0
BHT1
BHT2
BHT3
BHT4
Unused
UPC-topo
EHT0
EHT1