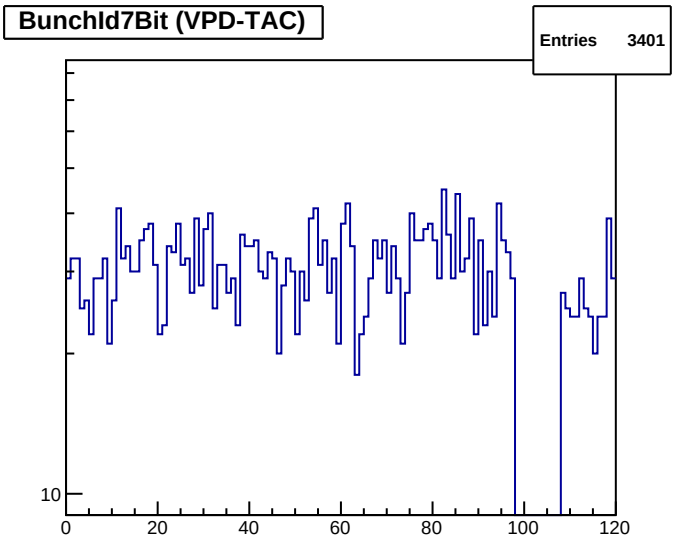
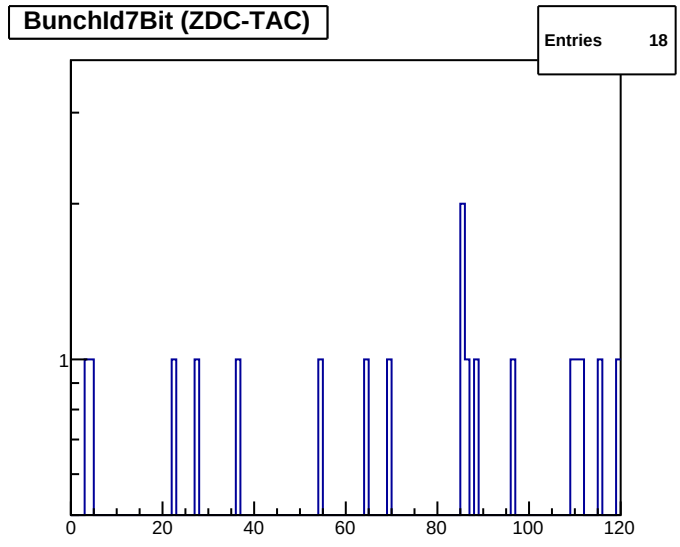
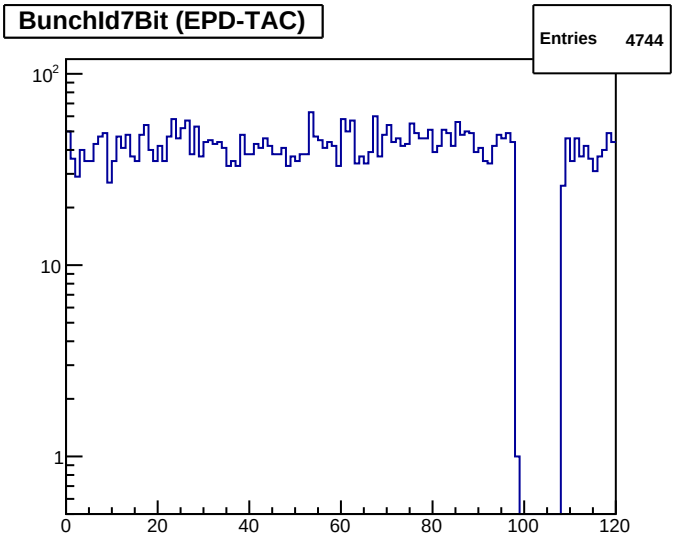
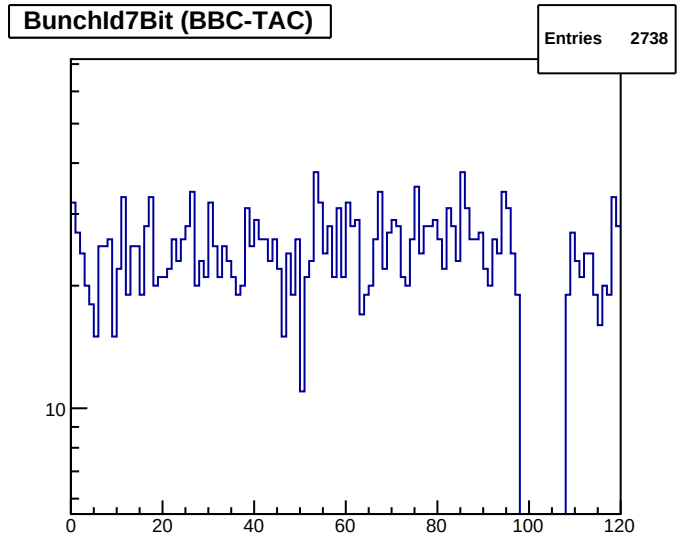
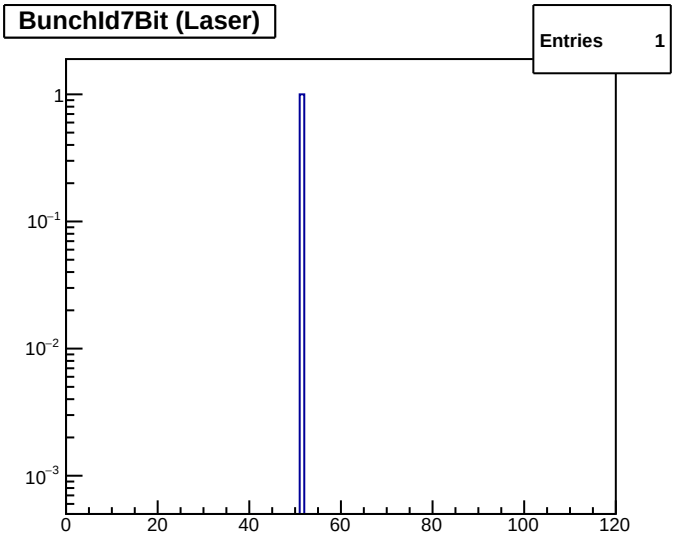
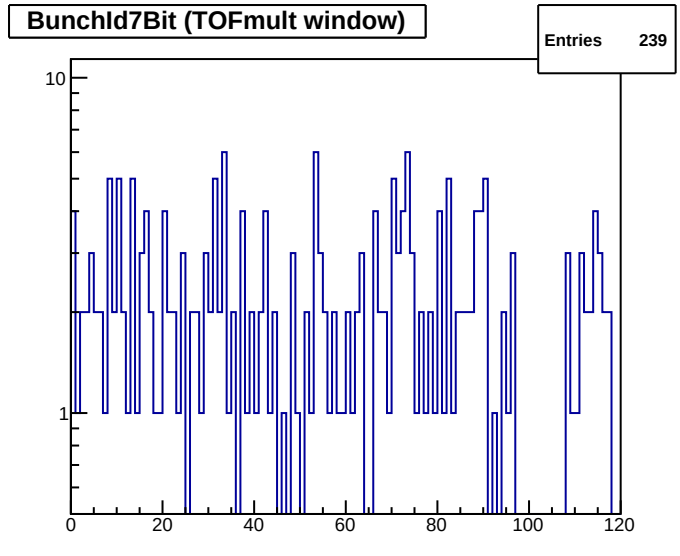

BunchId7Bit (VPD-TAC)
Entries 3401
10
0 20 40 60 80 100 120

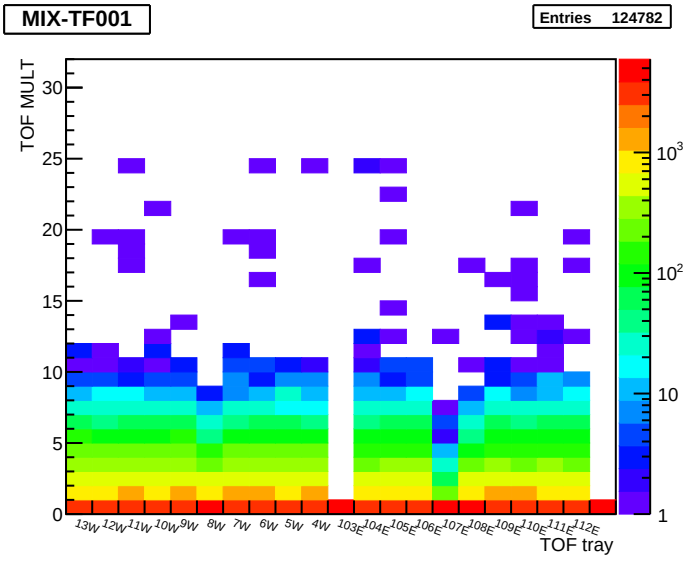

BunchId7Bit (ZDC-TAC)
Entries 18
1
0 20 40 60 80 100 120

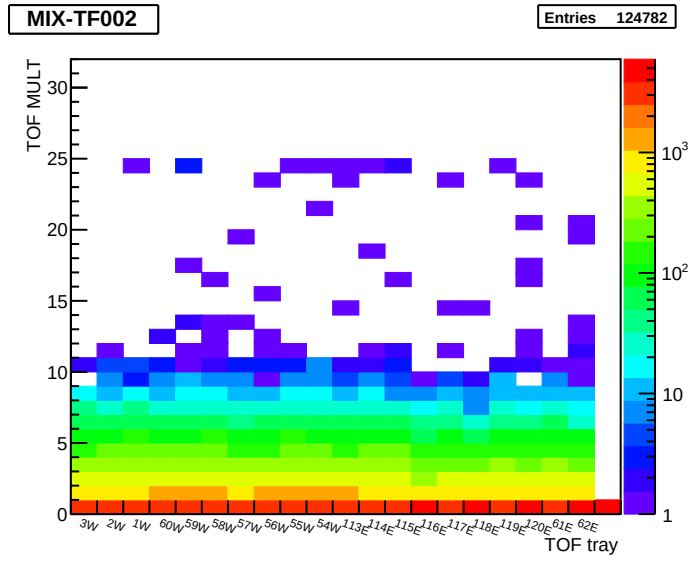

BunchId7Bit (EPD-TAC)
Entries 4744
10^2
10
1
0 20 40 60 80 100 120

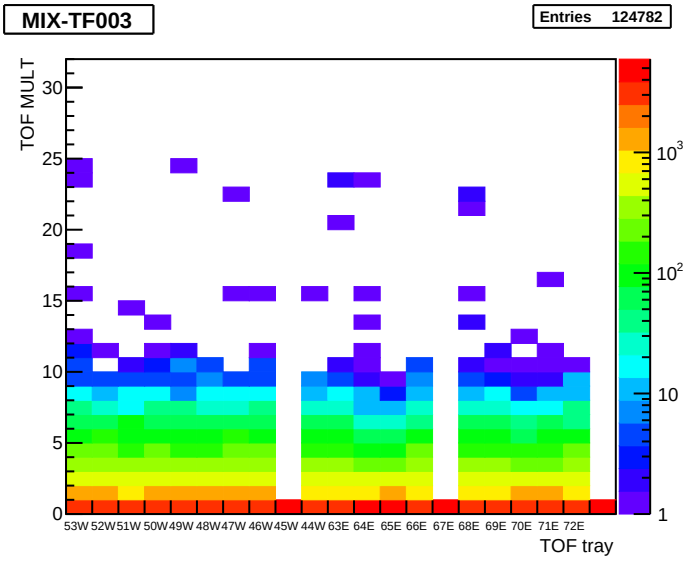

BunchId7Bit (BBC-TAC)
Entries 2738
10
0 20 40 60 80 100 120

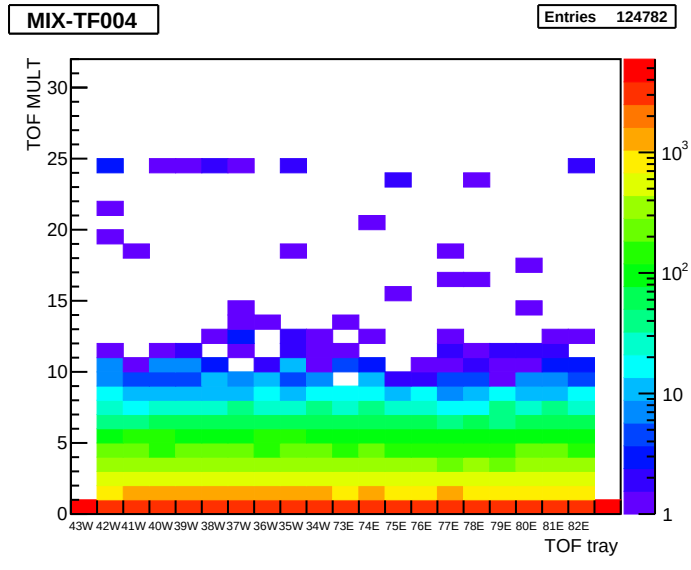

BunchId7Bit (Laser)
Entries 1
1
10^{-1}
10^{-2}
10^{-3}
0 20 40 60 80 100 120

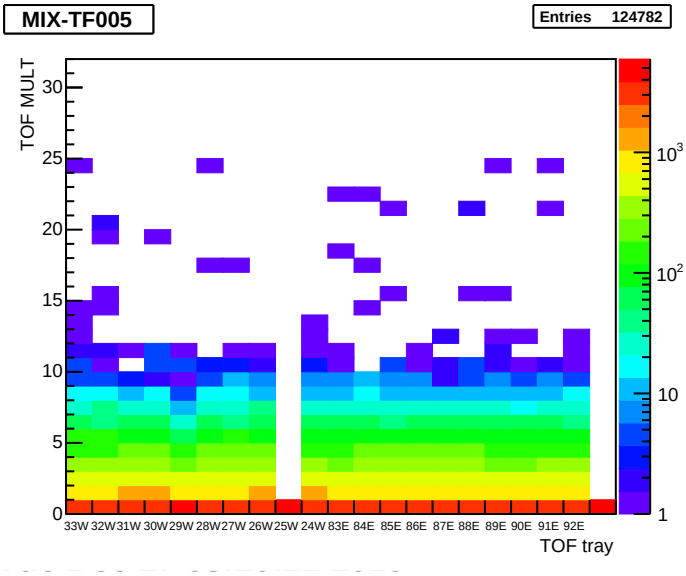

BunchId7Bit (TOFmult window)
Entries 239
10
1
0 20 40 60 80 100 120

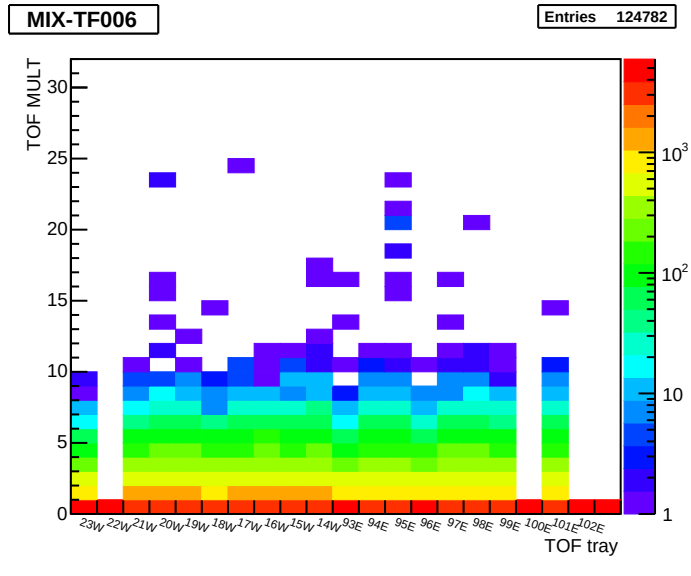

MIX-TF001
Entries 124782
TOF MULT
TOF tray

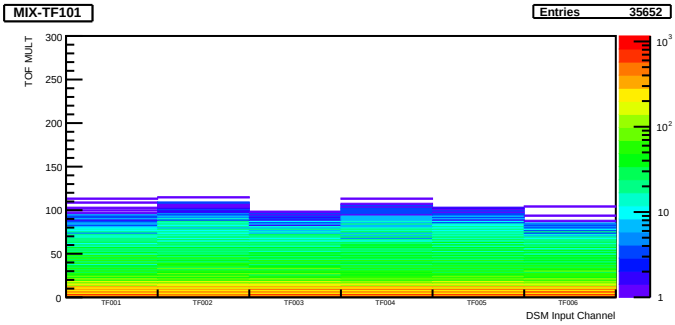

MIX-TF002
Entries 124782
TOF MULT
TOF tray

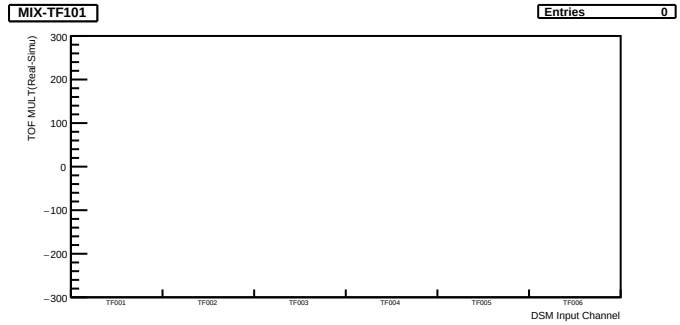

MIX-TF003
Entries 124782
TOF MULT
TOF tray

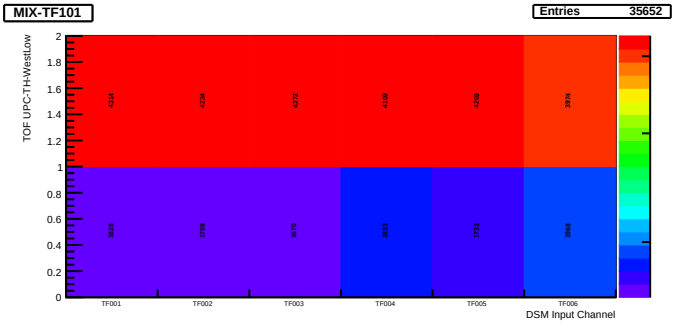

MIX-TF004
Entries 124782
TOF MULT
TOF tray

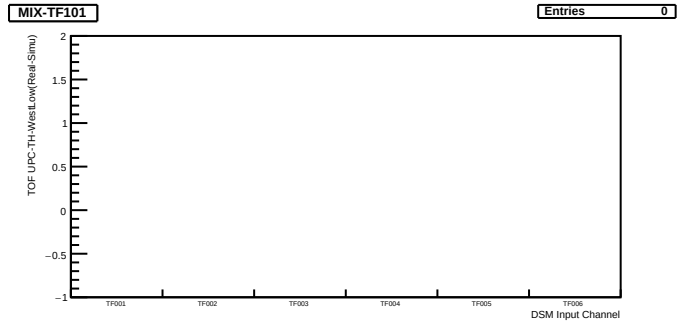

MIX-TF005
Entries 124782
TOF MULT
TOF tray

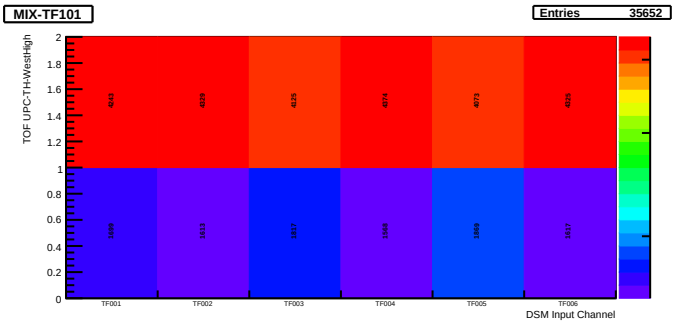

MIX-TF006
Entries 124782
TOF MULT
TOF tray

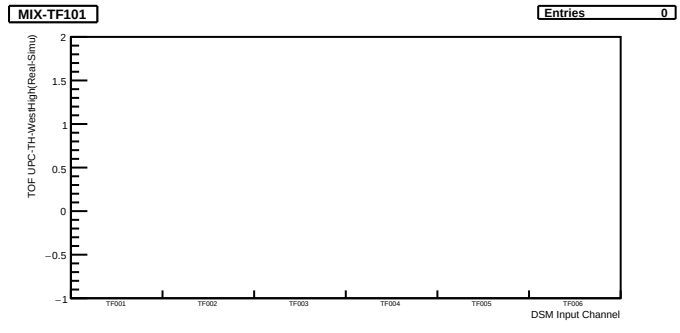

MIX-TF101
Entries 35652
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

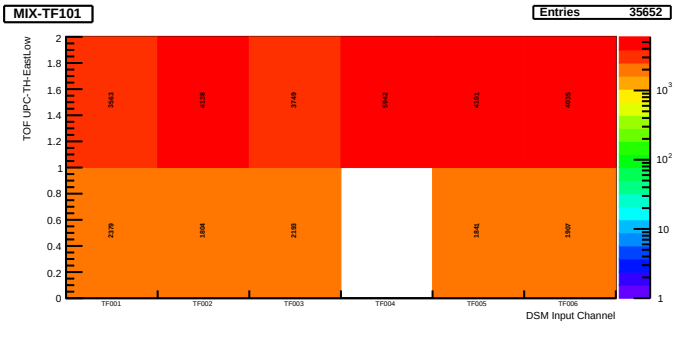

MIX-TF101
Entries 0
TOF MULT(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

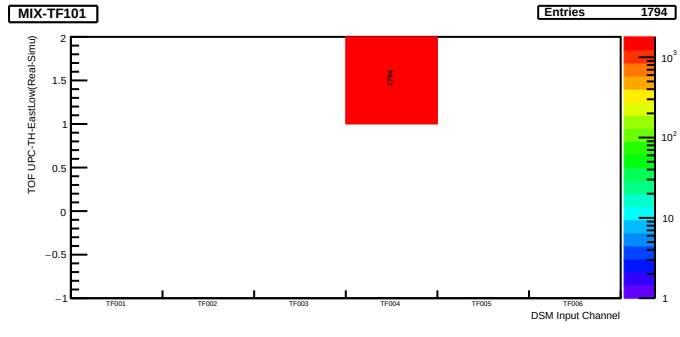

MIX-TF101
Entries 35652
TOF UPC-TH-WestLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

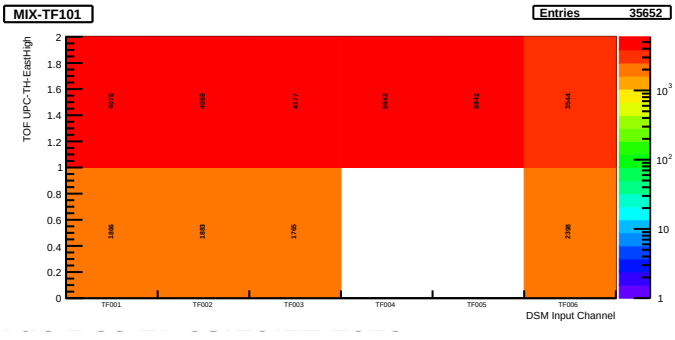

MIX-TF101
Entries 0
TOF UPC-TH-WestLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

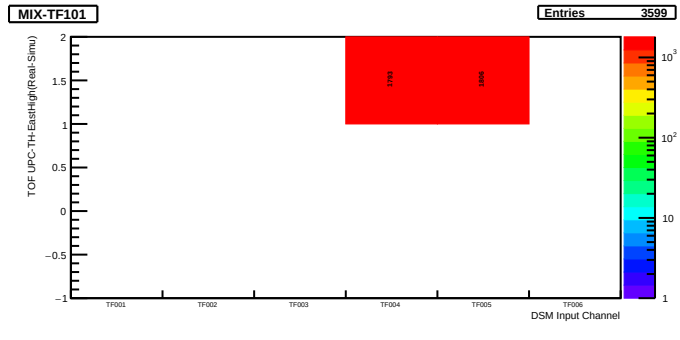

MIX-TF101
Entries 35652
TOF UPC-TH-WestHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

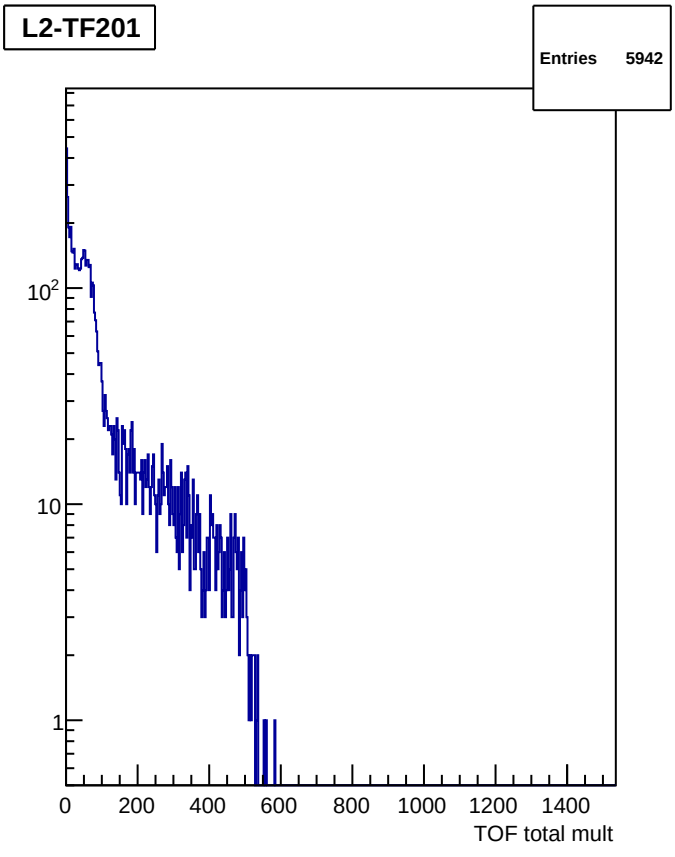

MIX-TF101
Entries 0
TOF UPC-TH-WestHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

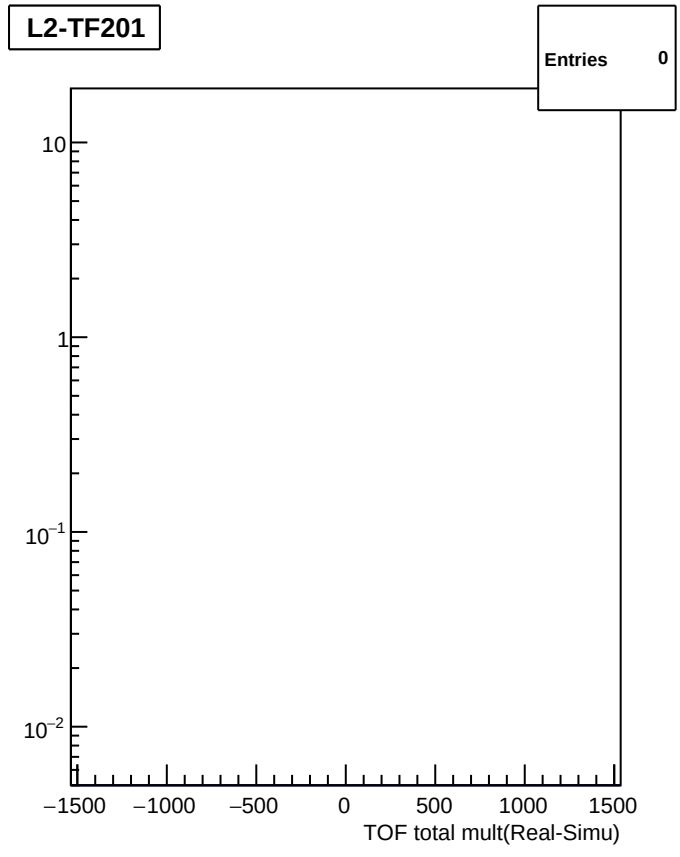

MIX-TF101
Entries 35652
TOF UPC-TH-EastLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

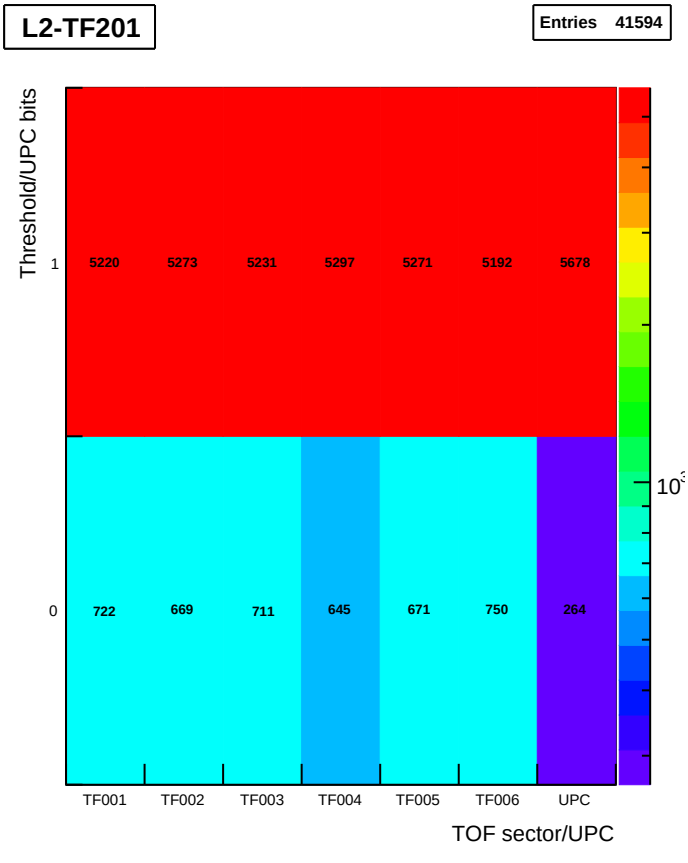

MIX-TF101
Entries 1794
TOF UPC-TH-EastLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

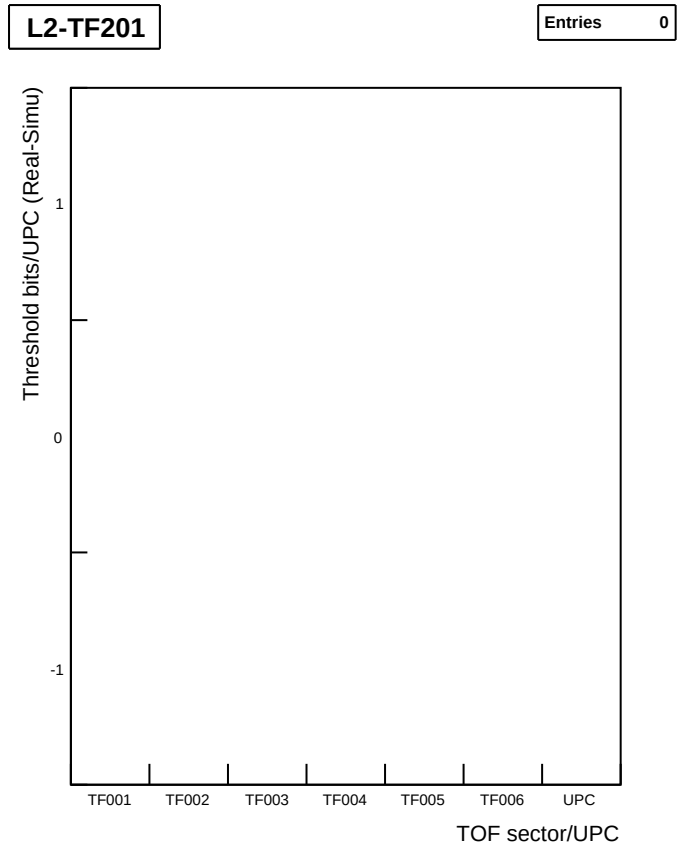

MIX-TF101
Entries 35652
TOF UPC-TH-EastHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

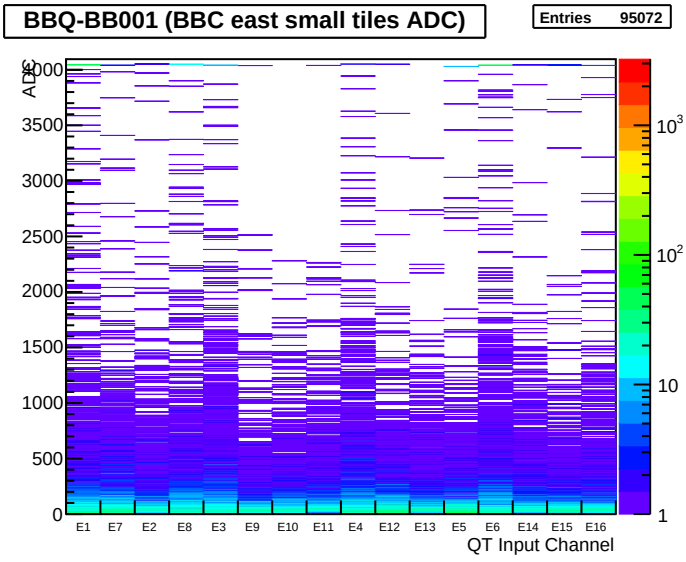

MIX-TF101
Entries 3599
TOF UPC-TH-EastHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

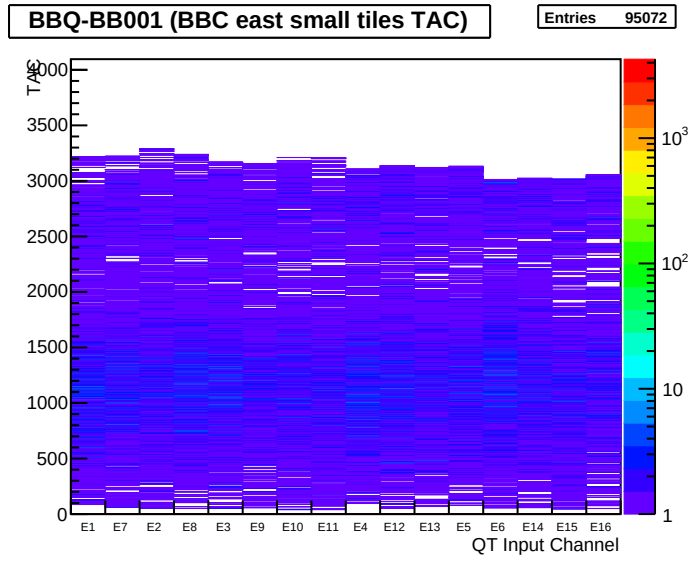

L2-TF201
Entries 5942
10^2
10
1
0
200
400
600
800
1000
1200
1400
TOF total mult

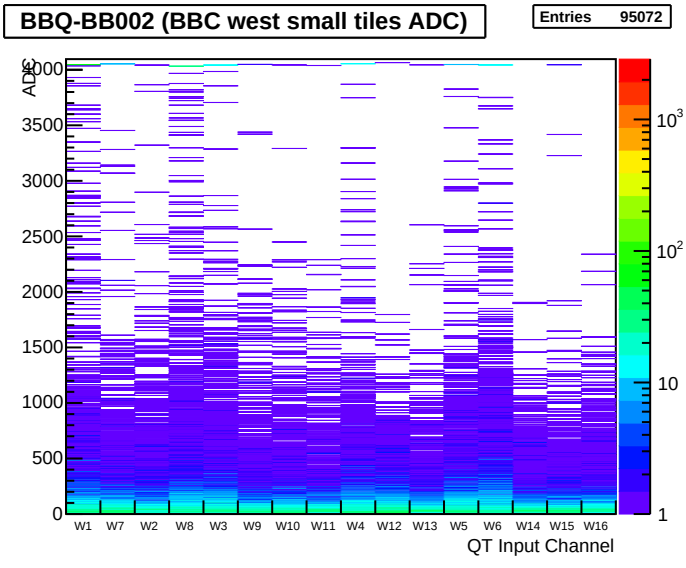

L2-TF201
Entries 0
10
1
10^{-1}
10^{-2}
−1500
−1000
−500
0
500
1000
1500
TOF total mult(Real-Simu)

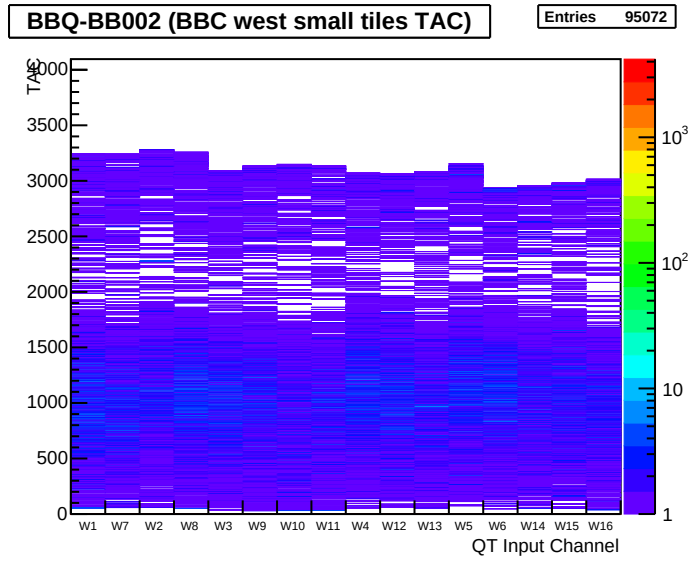

L2-TF201
Entries 41594
Threshold/UPC bits
1
0
5220
5273
5231
5297
5271
5192
5678
722
669
711
645
671
750
264
TF001
TF002
TF003
TF004
TF005
TF006
UPC
TOF sector/UPC

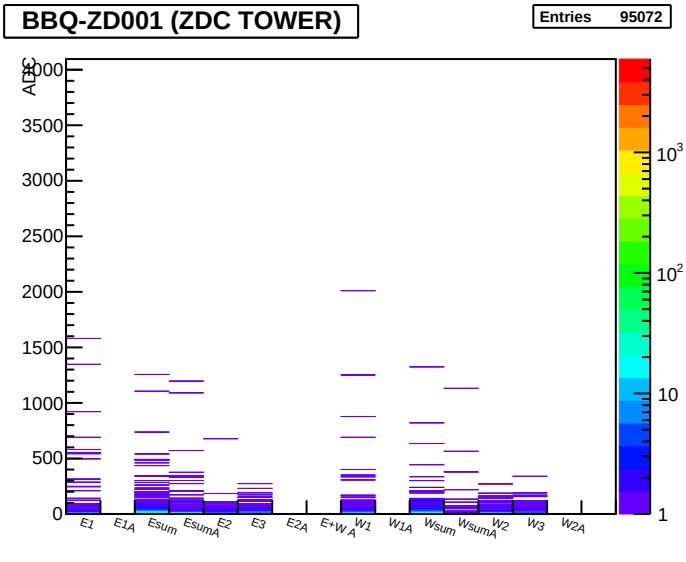

L2-TF201
Entries 0
Threshold bits/UPC (Real-Simu)
1
0
-1
TF001
TF002
TF003
TF004
TF005
TF006
UPC
TOF sector/UPC

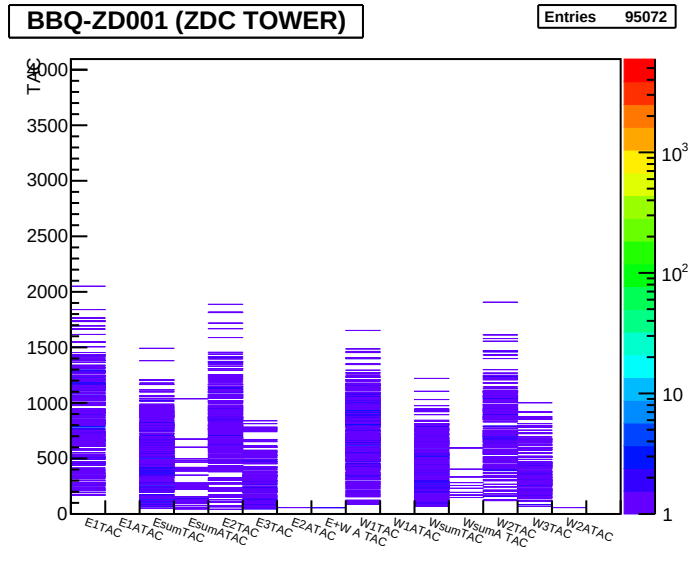

BBQ-BB001 (BBC east small tiles ADC)
Entries 95072
ADC
QT Input Channel
E1 E7 E2 E8 E3 E9 E10 E11 E4 E12 E13 E5 E6 E14 E15 E16

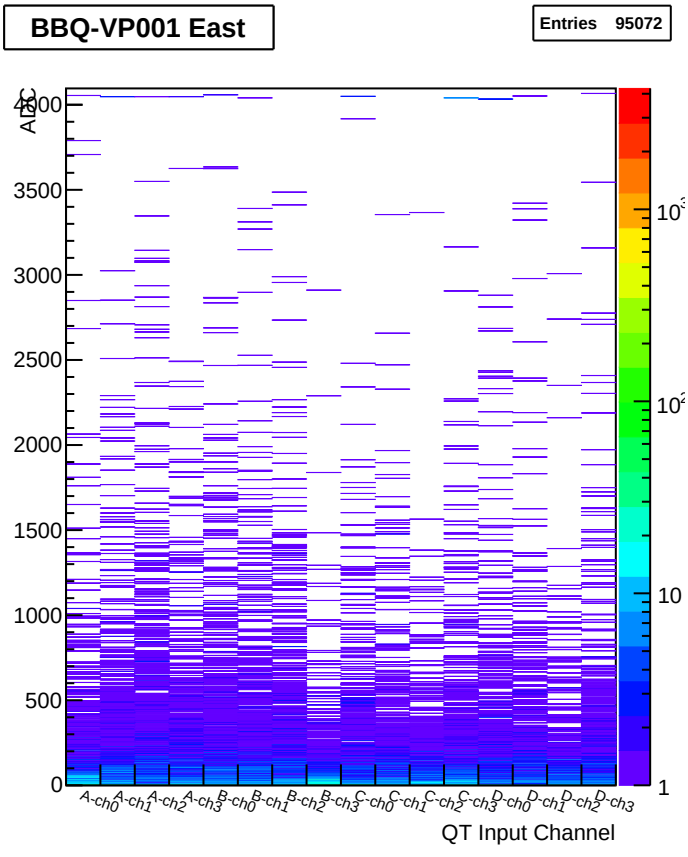

BBQ-BB001 (BBC east small tiles TAC)
Entries 95072
TAC
QT Input Channel
E1 E7 E2 E8 E3 E9 E10 E11 E4 E12 E13 E5 E6 E14 E15 E16

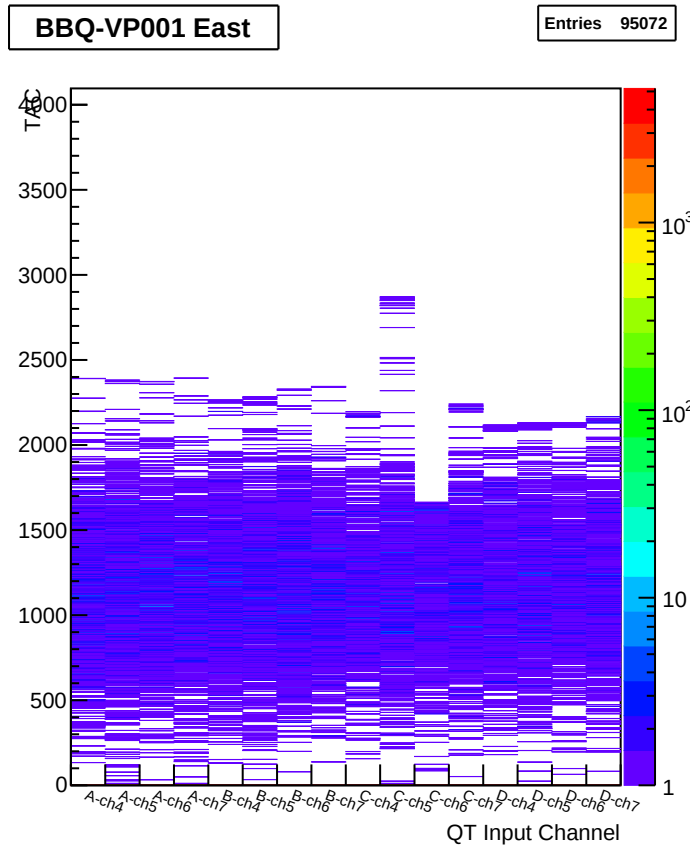

BBQ-BB002 (BBC west small tiles ADC)
Entries 95072
ADC
QT Input Channel
W1 W7 W2 W8 W3 W9 W10 W11 W4 W12 W13 W5 W6 W14 W15 W16

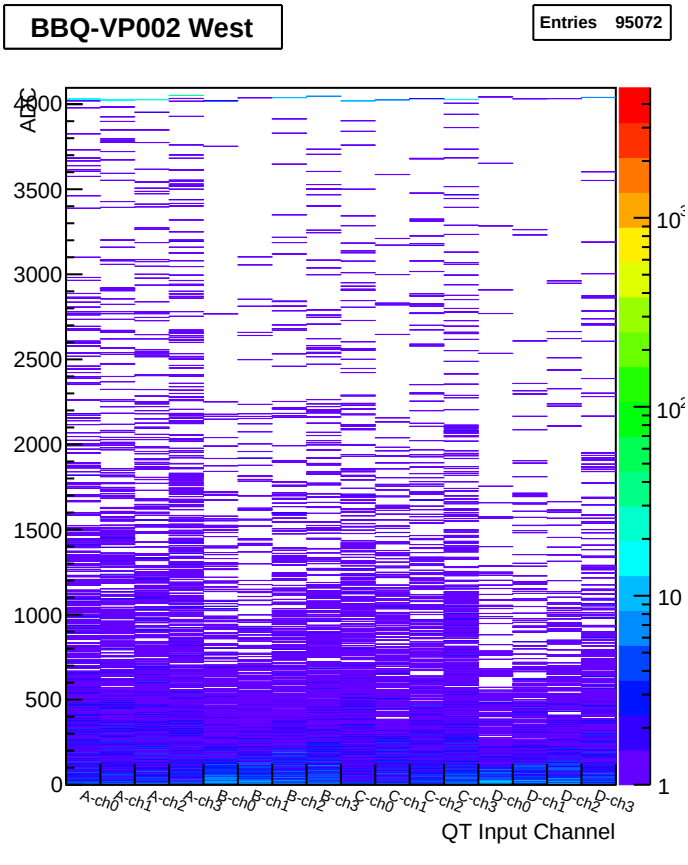

BBQ-BB002 (BBC west small tiles TAC)
Entries 95072
TAC
QT Input Channel
W1 W7 W2 W8 W3 W9 W10 W11 W4 W12 W13 W5 W6 W14 W15 W16

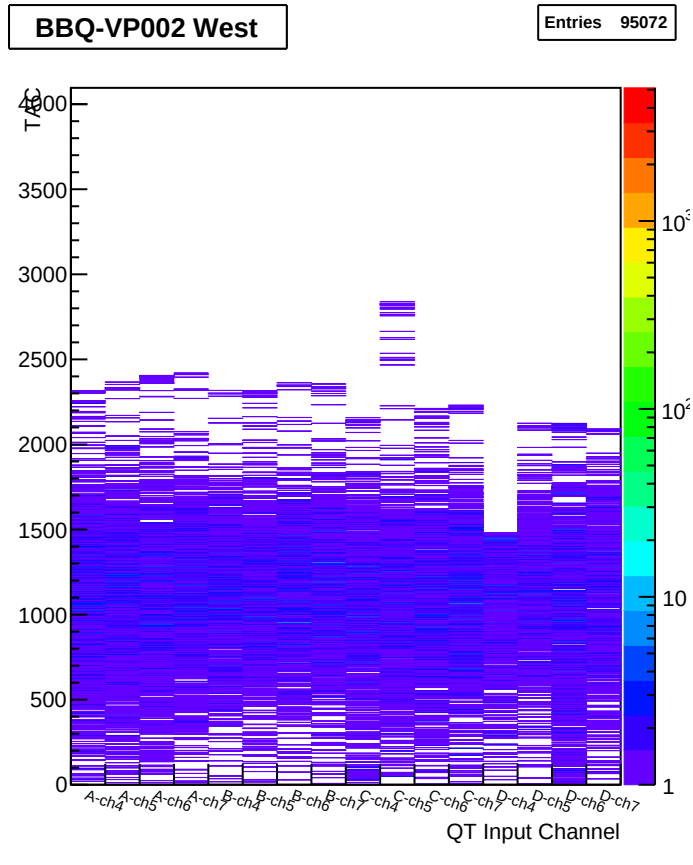

BBQ-ZD001 (ZDC TOWER)
Entries 95072
ADC
E1 E1A Esum EsumA E2 E3 E2A E+W A W1 W1A Wsum WsumA W2 W3 W2A

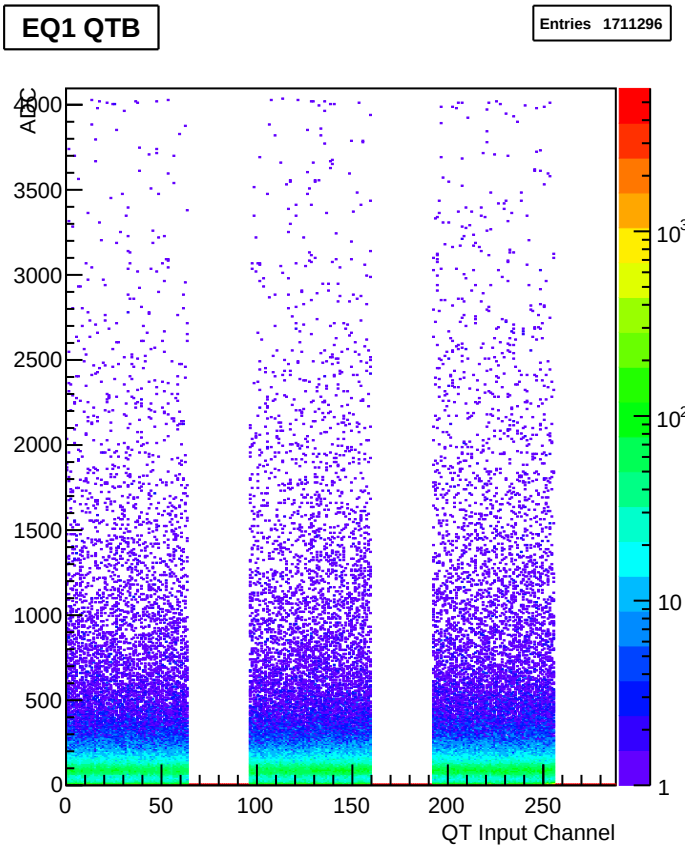

BBQ-ZD001 (ZDC TOWER)
Entries 95072
TAC
E1TAC E1ATAC EsumTAC EsumATAC E2TAC E3TAC E2ATAC E+W A TAC W1TAC W1ATAC WsumTAC WsumA TAC W2TAC W3TAC W2ATAC

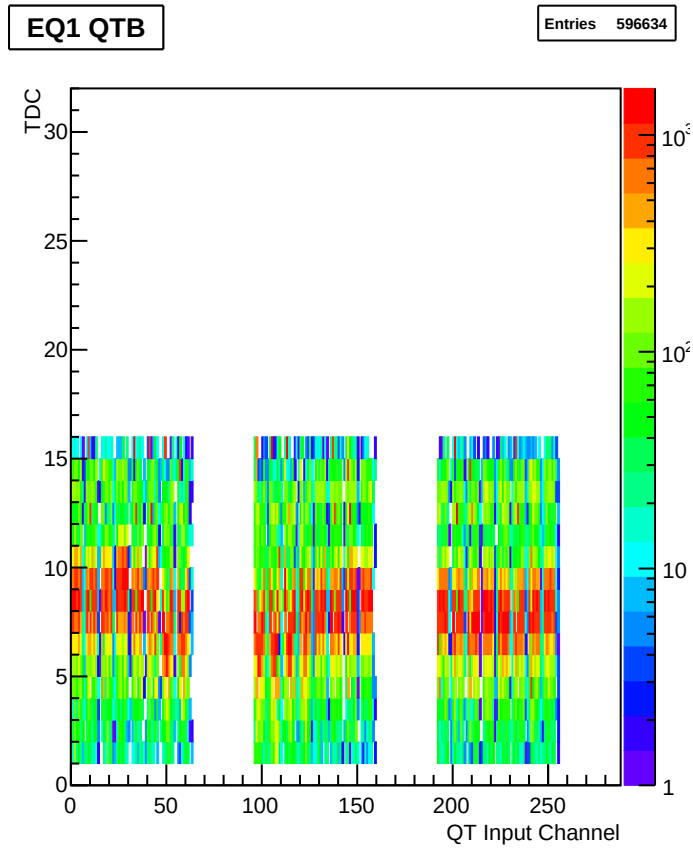

BBQ-VP001 East
Entries 95072
ADC
QT Input Channel

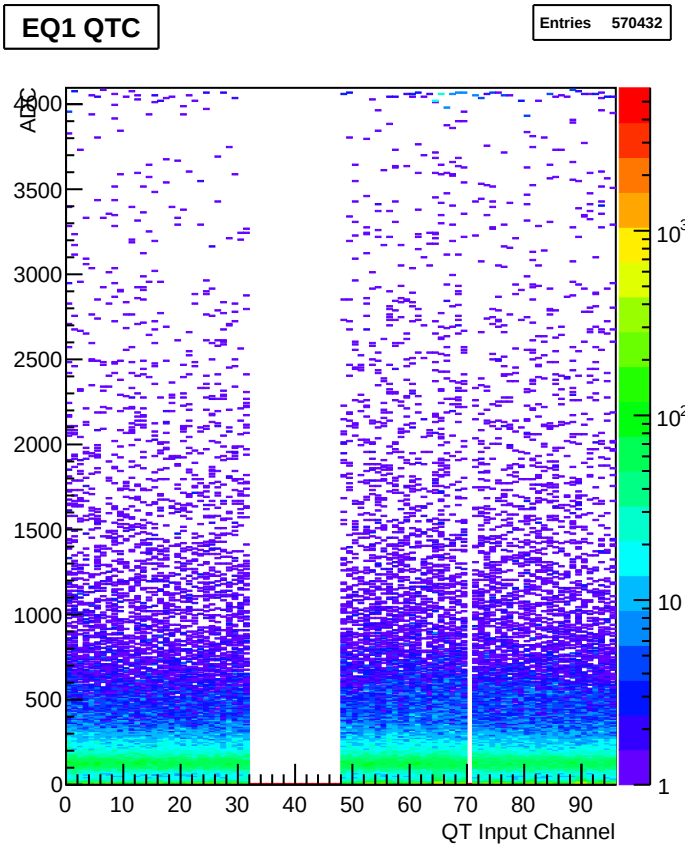

BBQ-VP001 East
Entries 95072
TADC
QT Input Channel

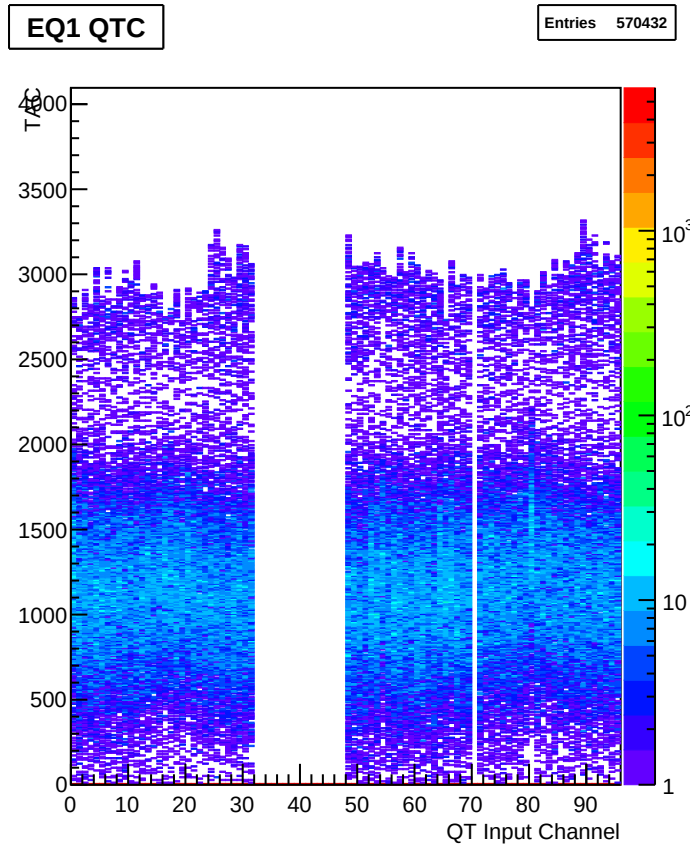

BBQ-VP002 West
Entries 95072
ADC
QT Input Channel

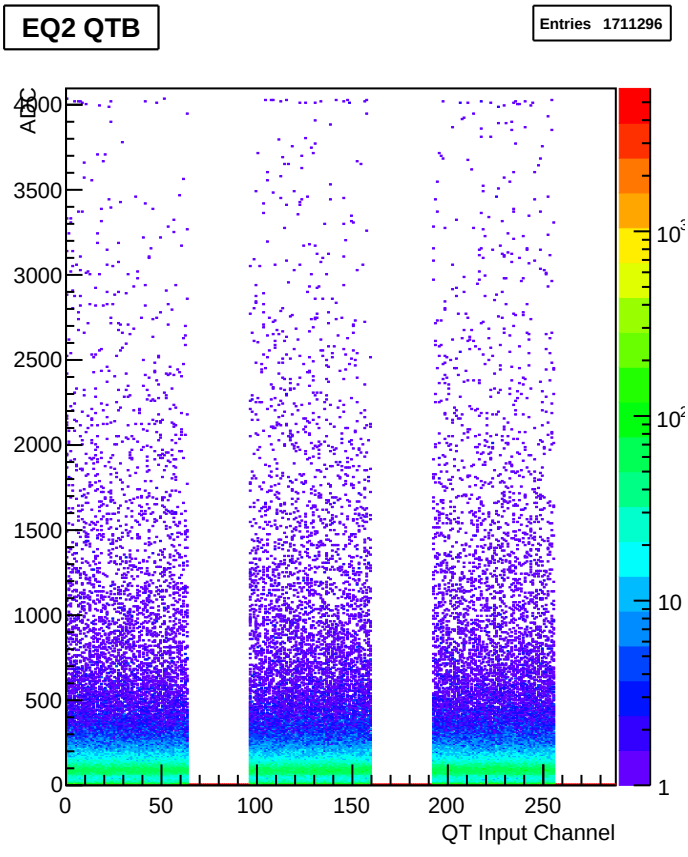

BBQ-VP002 West
Entries 95072
TADC
QT Input Channel

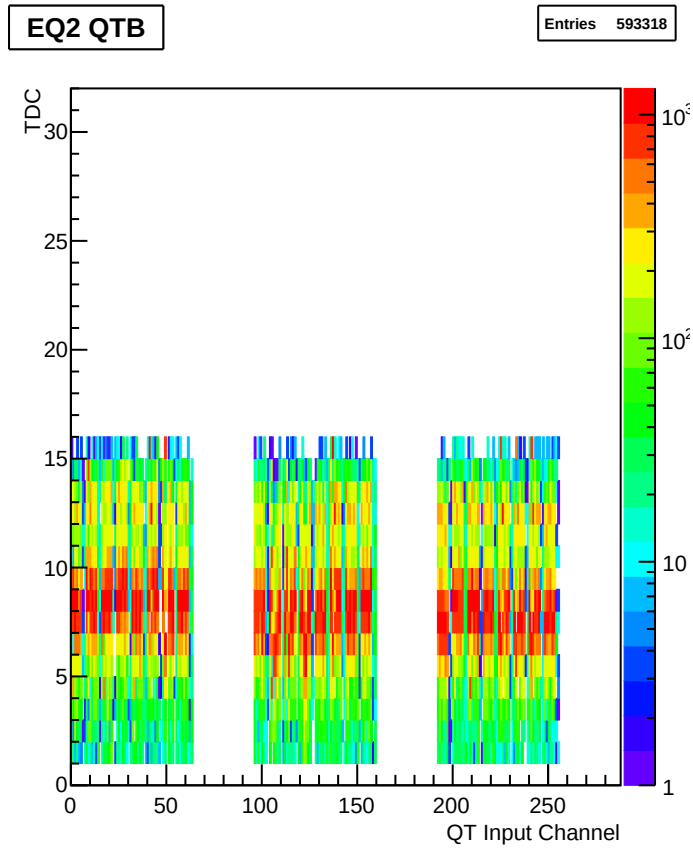

EQ1 QTB
Entries 1711296
ADC
QT Input Channel

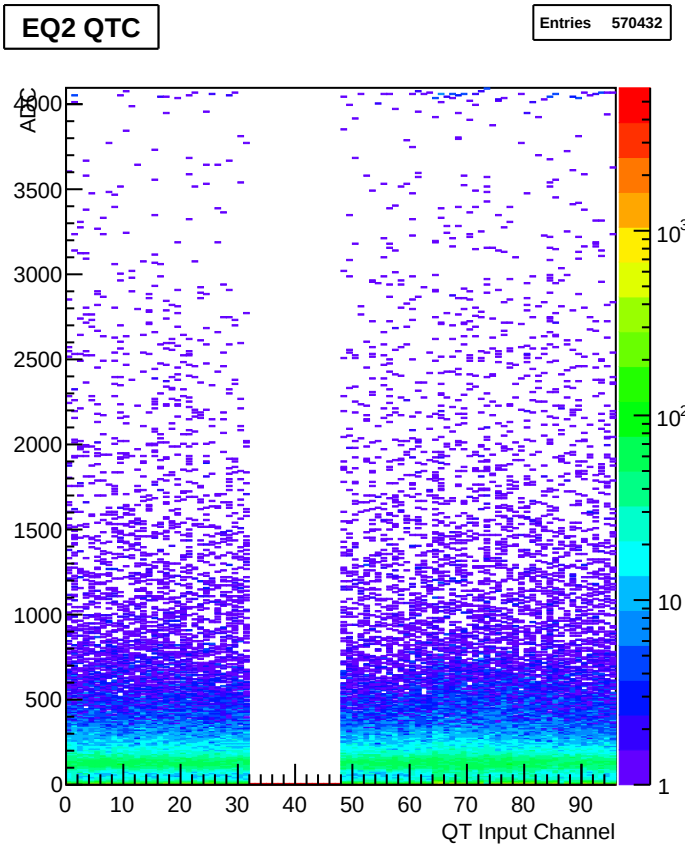

EQ1 QTB
Entries 596634
TDC
QT Input Channel

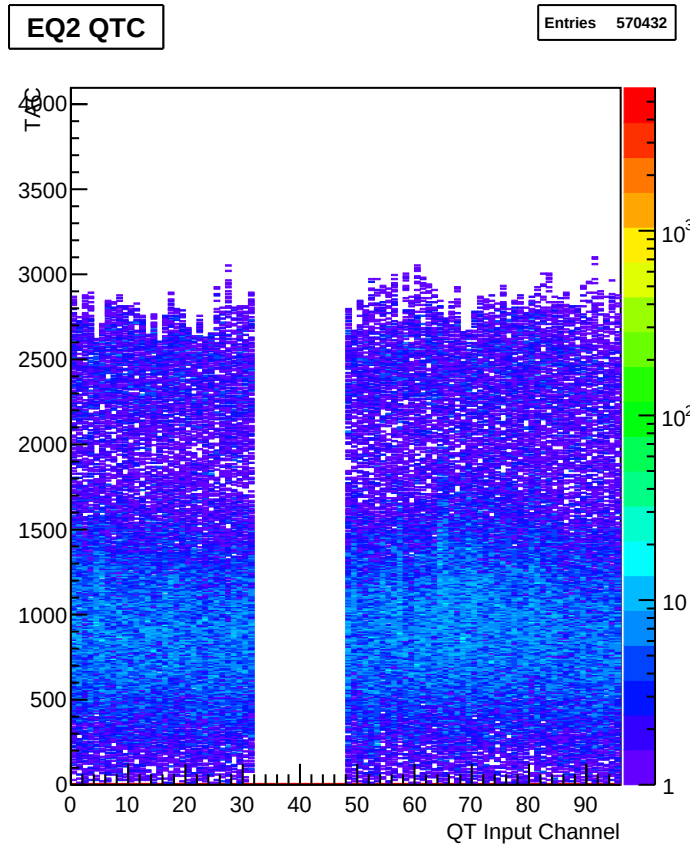

EQ1 QTC
Entries 570432
ADC
QT Input Channel

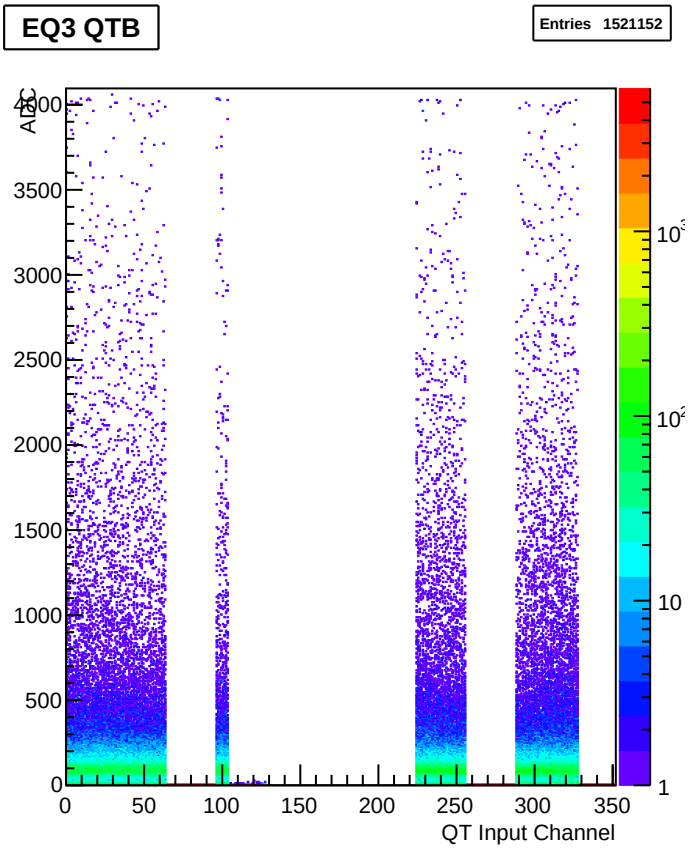

EQ1 QTC
Entries 570432
TADC
QT Input Channel

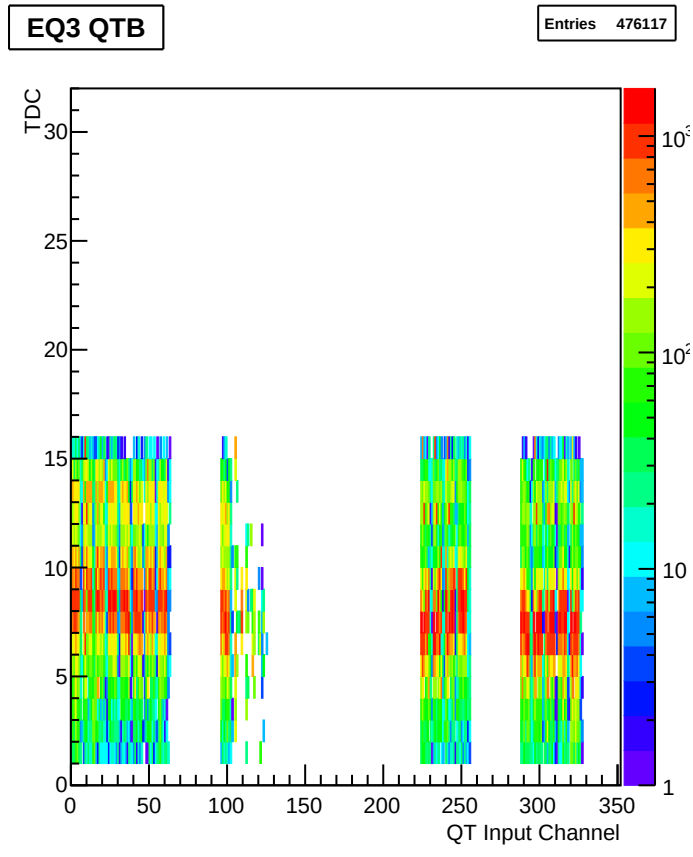

EQ2 QTB
Entries 1711296
ADC
QT Input Channel

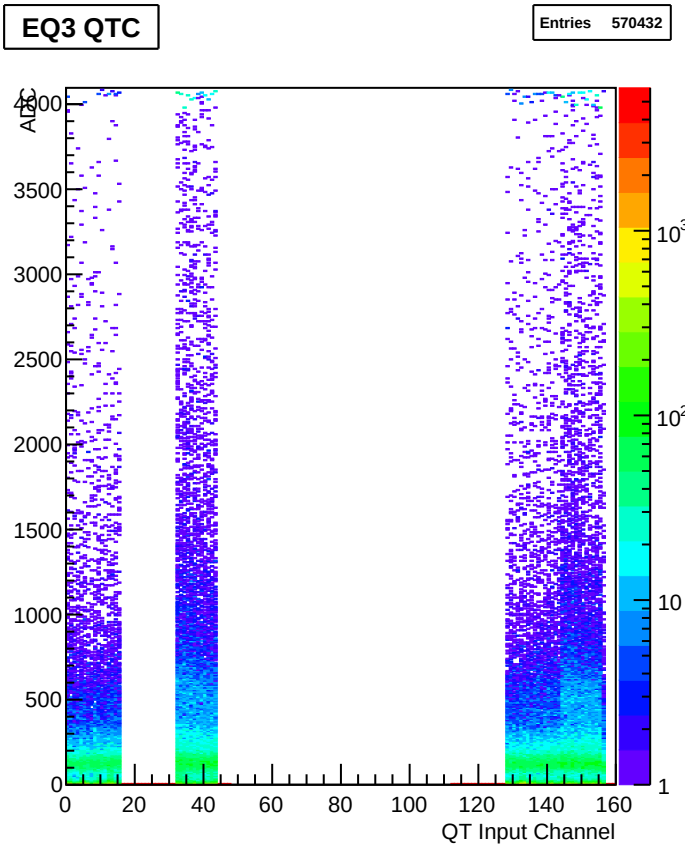

EQ2 QTB
Entries 593318
TDC
QT Input Channel

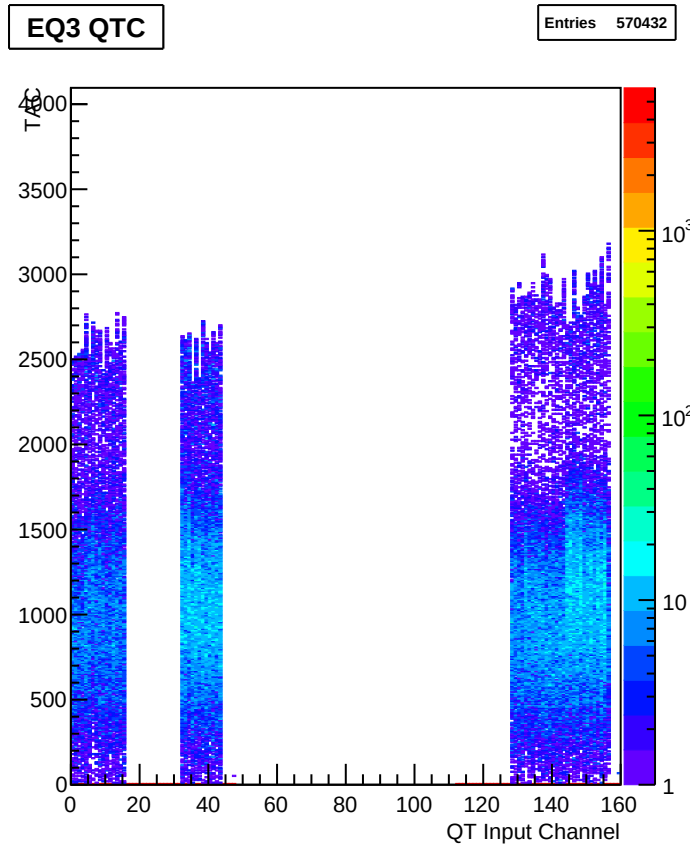

EQ2 QTC
Entries 570432
ADC
QT Input Channel

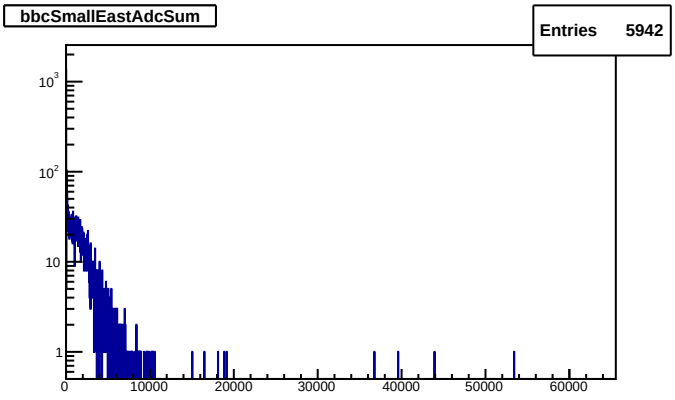

EQ2 QTC
Entries 570432
TADC
QT Input Channel

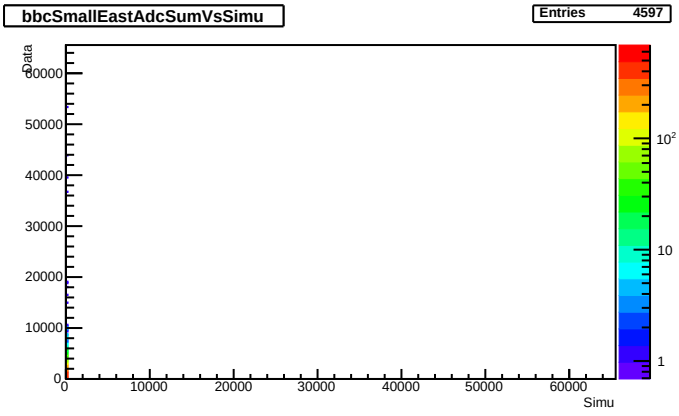

EQ3 QTB
Entries 1521152
ADC
QT Input Channel

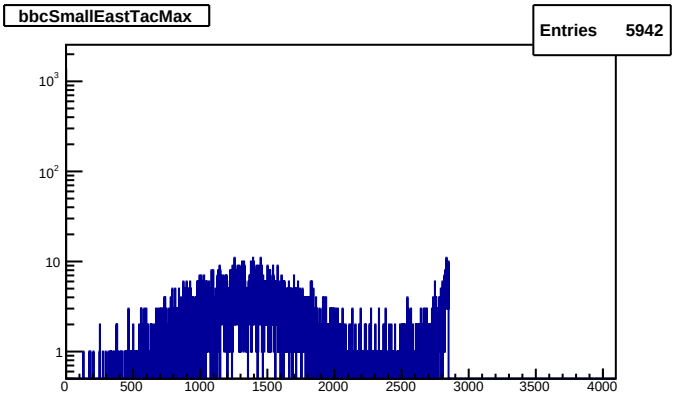

EQ3 QTB
Entries 476117
TDC
QT Input Channel

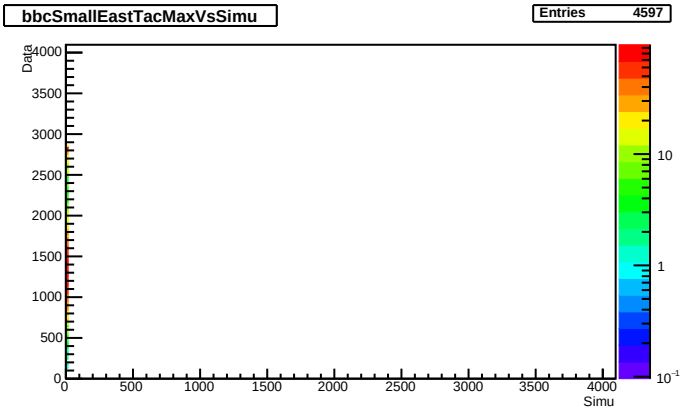

EQ3 QTC
Entries 570432
ADC
QT Input Channel

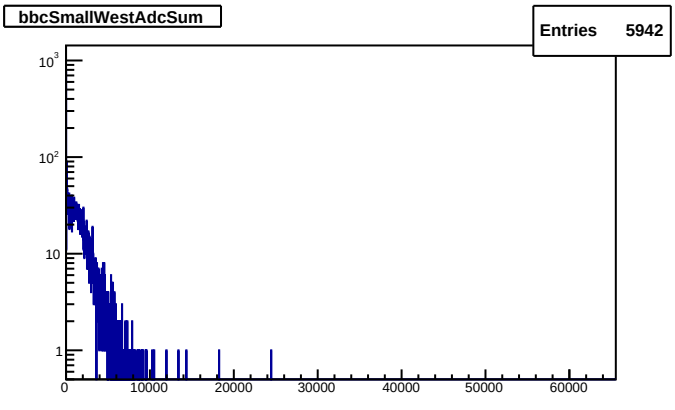

EQ3 QTC
Entries 570432
TADC
QT Input Channel

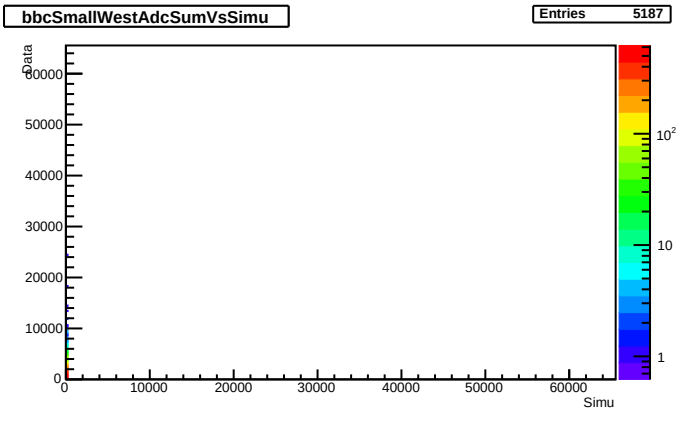

bbcSmallEastAdcSum
Entries 5942

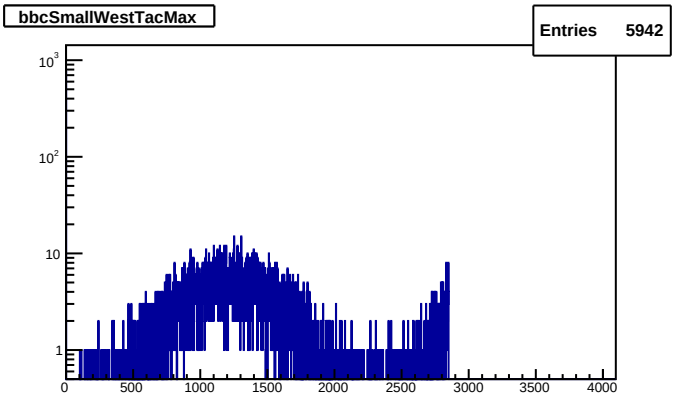

bbcSmallEastAdcSumVsSimu
Entries 4597
Data
Simu

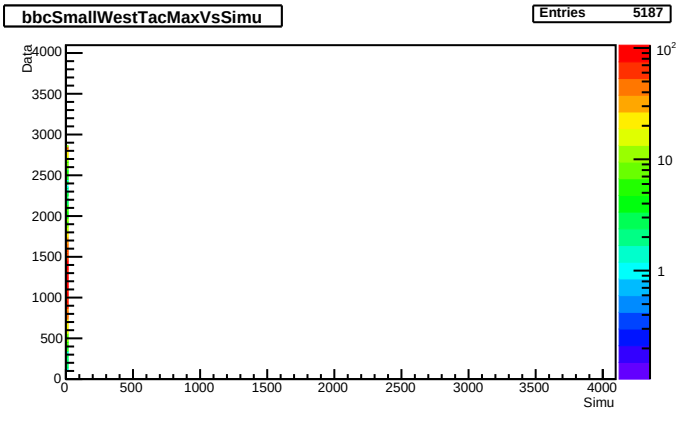

bbcSmallEastTacMax
Entries 5942

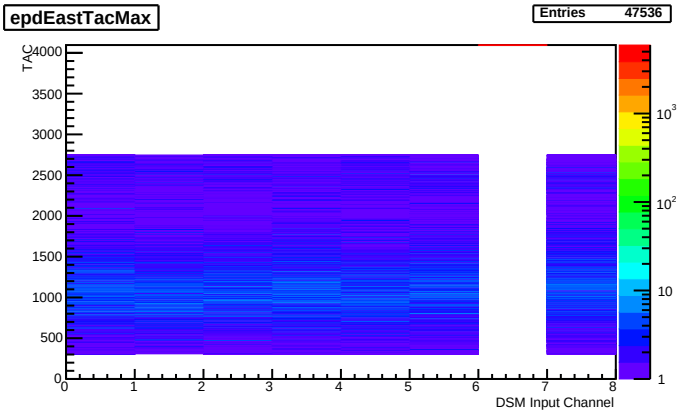

bbcSmallEastTacMaxVsSimu
Entries 4597
Data
Simu

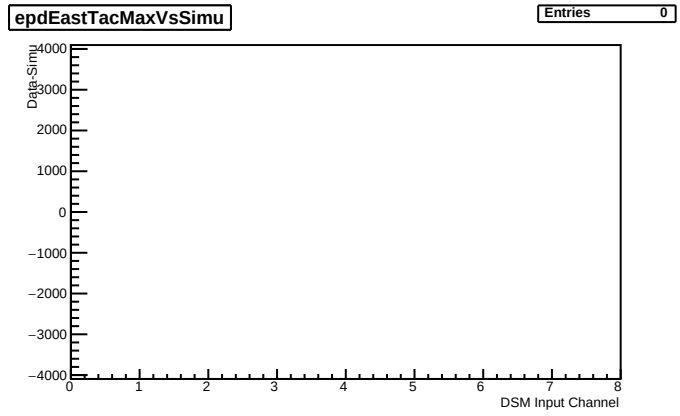

bbcSmallWestAdcSum
Entries 5942

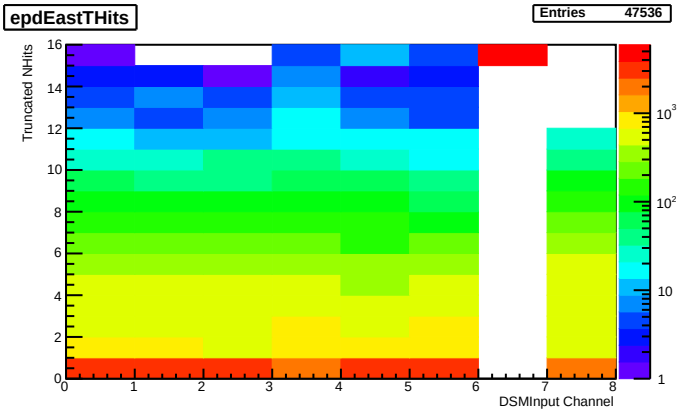

bbcSmallWestAdcSumVsSimu
Entries 5187
Data
Simu

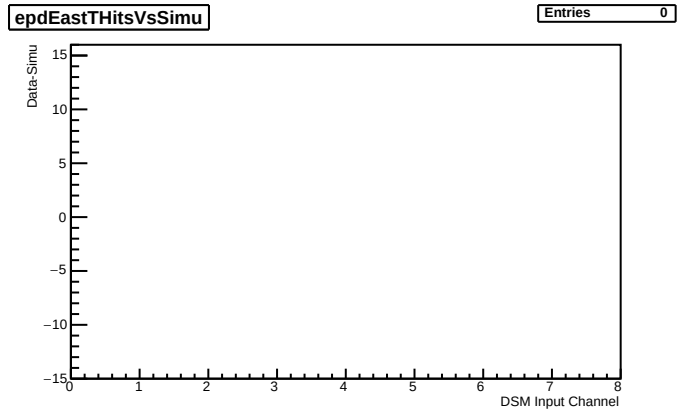

bbcSmallWestTacMax
Entries 5942

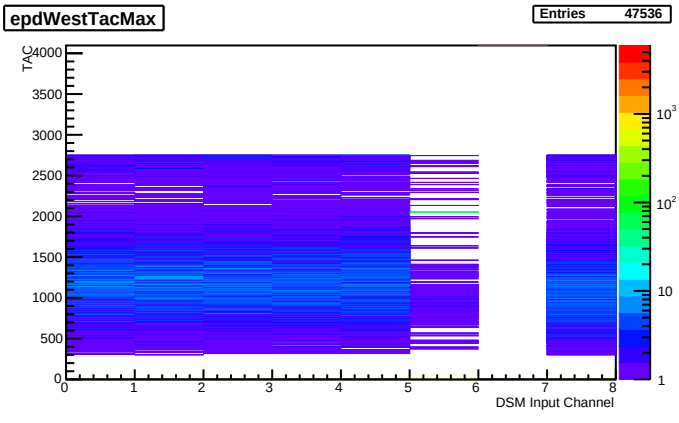

bbcSmallWestTacMaxVsSimu
Entries 5187
Data
Simu

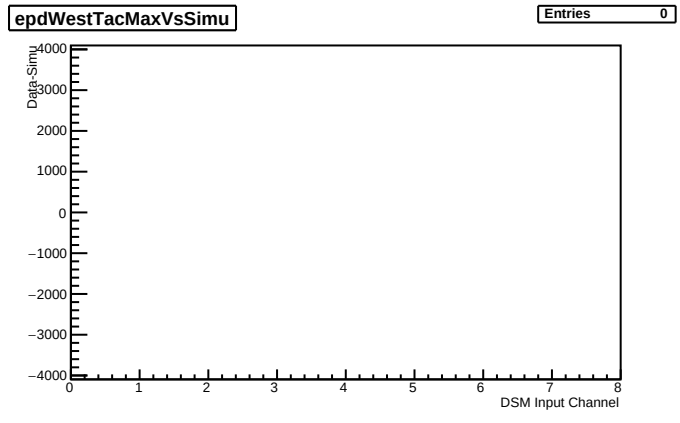

epdEastTacMax
Entries 47536
TAC
DSM Input Channel

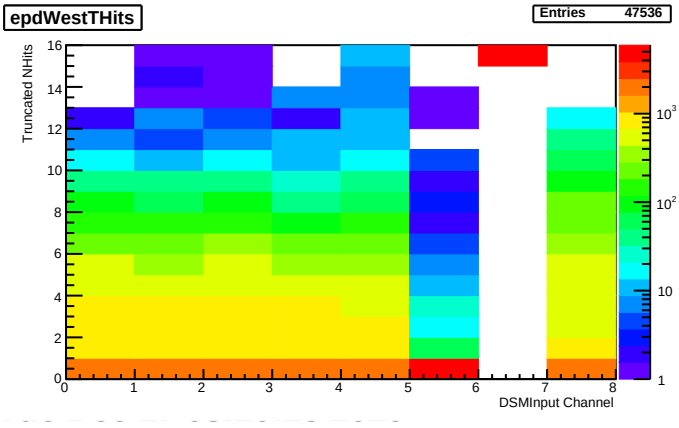

epdEastTacMaxVsSimu
Entries 0
Data-Simu
DSM Input Channel

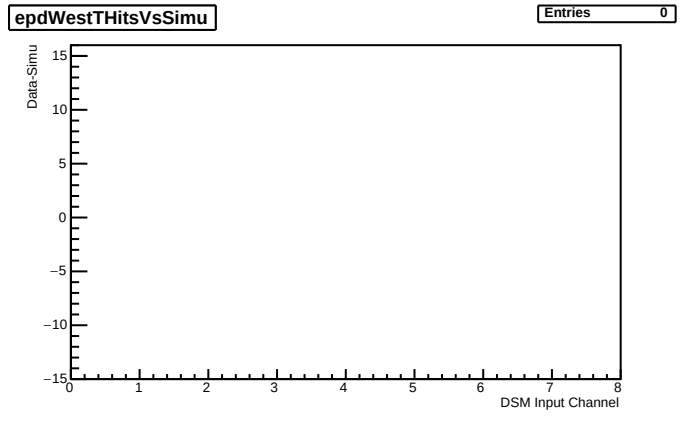

epdEastTHits
Entries 47536
Truncated NHits
DSMInput Channel

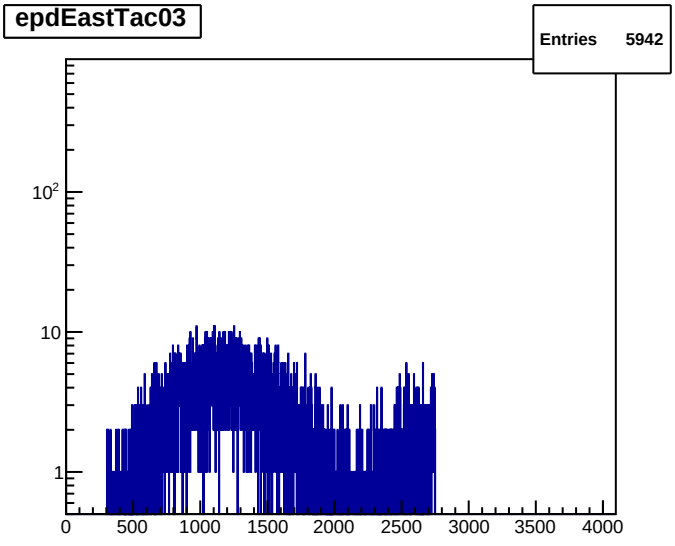

epdEastTHitsVsSimu
Entries 0
Data-Simu
DSM Input Channel

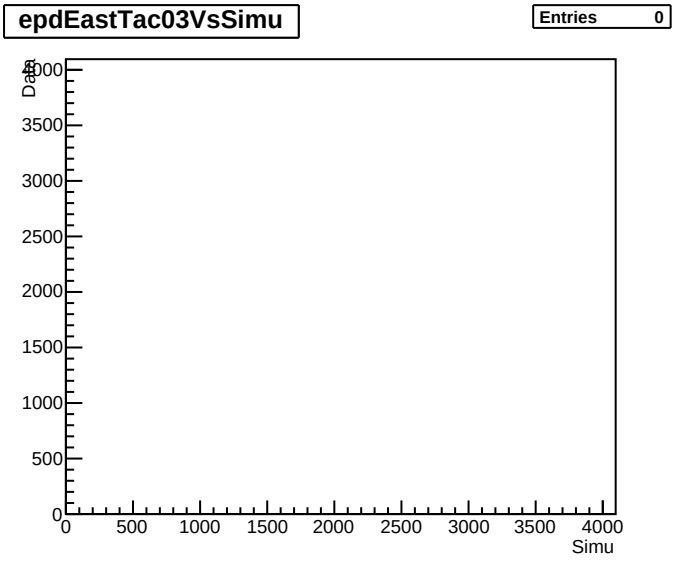

epdWestTacMax
Entries 47536
TAC
DSM Input Channel

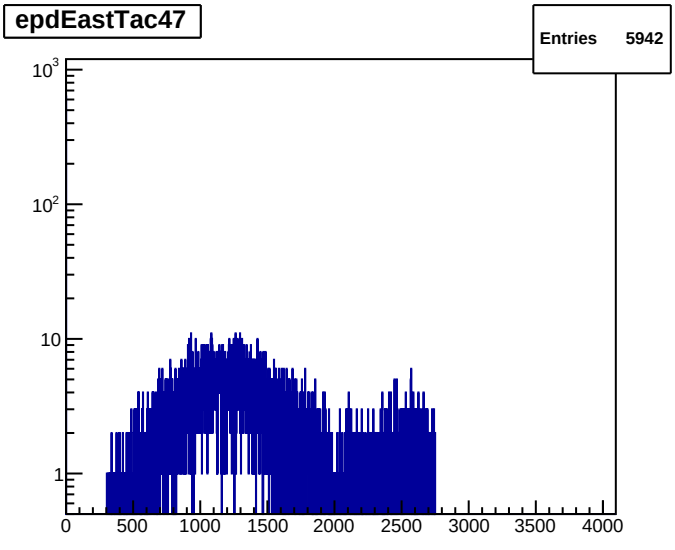

epdWestTacMaxVsSimu
Entries 0
Data-Simu
DSM Input Channel

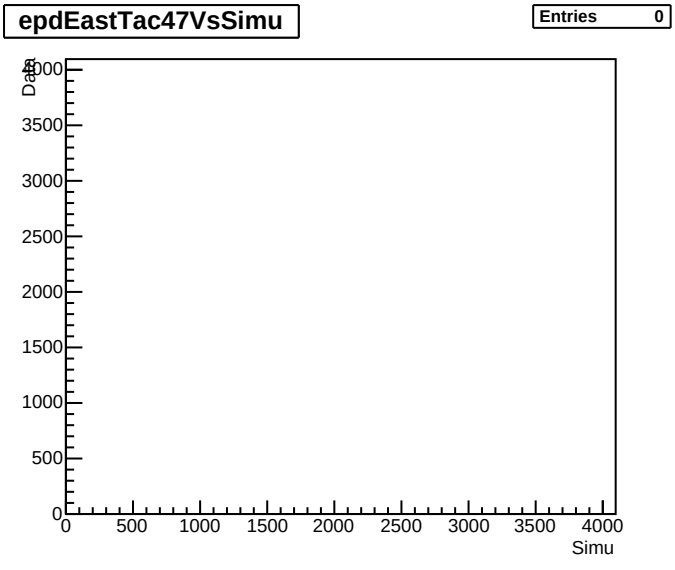

epdWestTHits
Entries 47536
Truncated NHits
DSMInput Channel

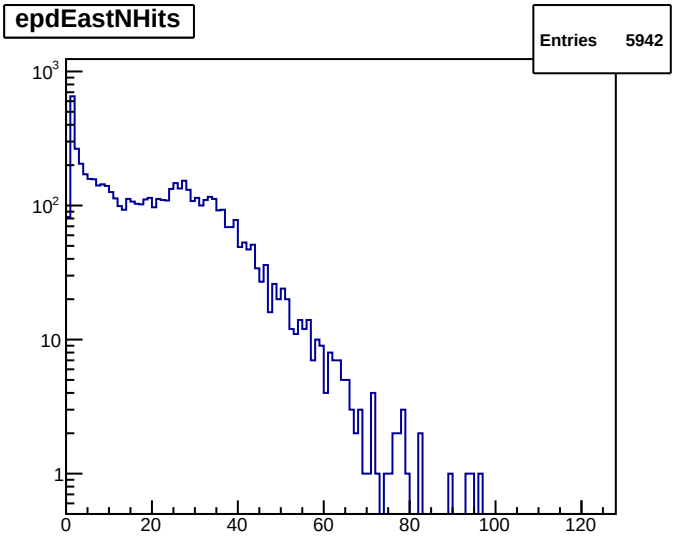

epdWestTHitsVsSimu
Entries 0
Data-Simu
DSM Input Channel

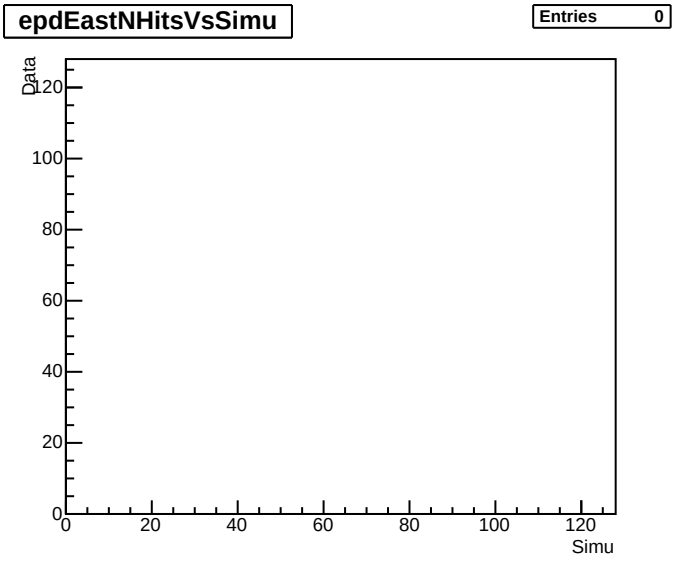

epdEastTac03
Entries 5942


epdEastTac03VsSimu
Entries 0
Data
Simu


epdEastTac47
Entries 5942


epdEastTac47VsSimu
Entries 0
Data
Simu


epdEastNHits
Entries 5942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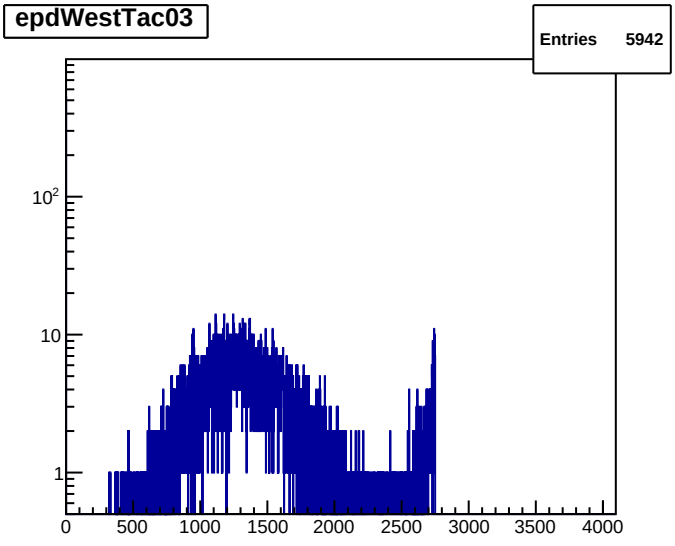
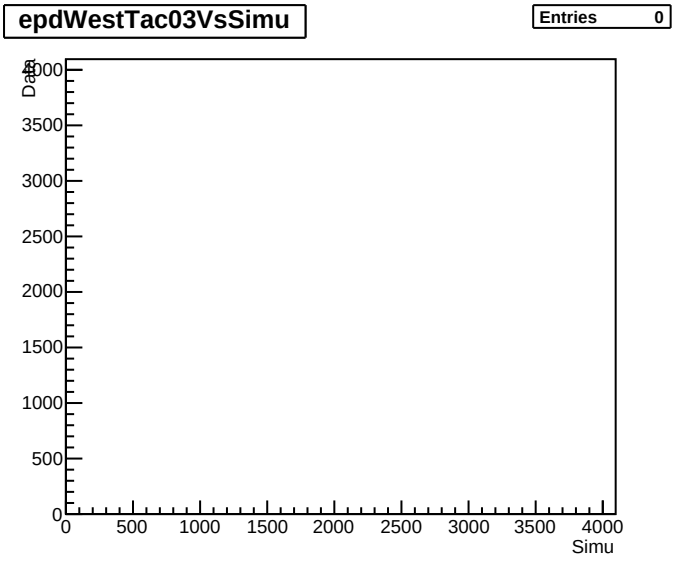
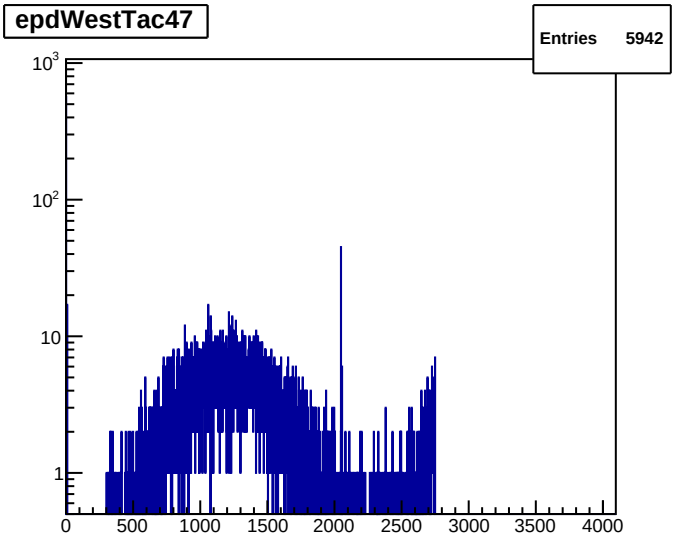
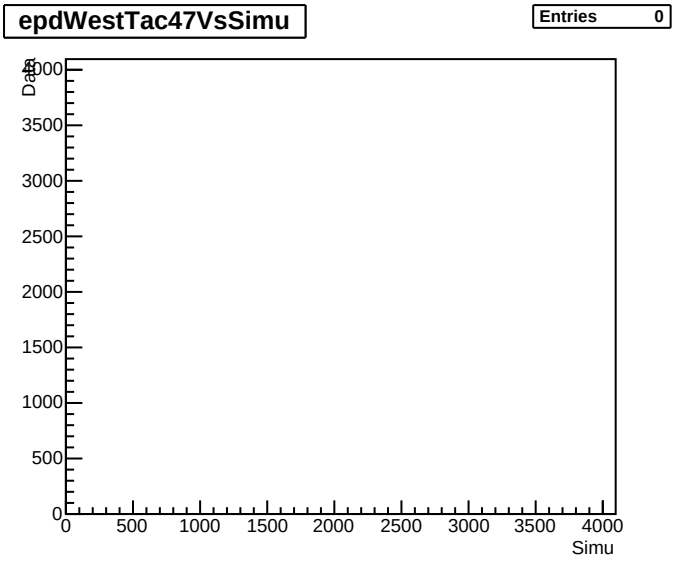
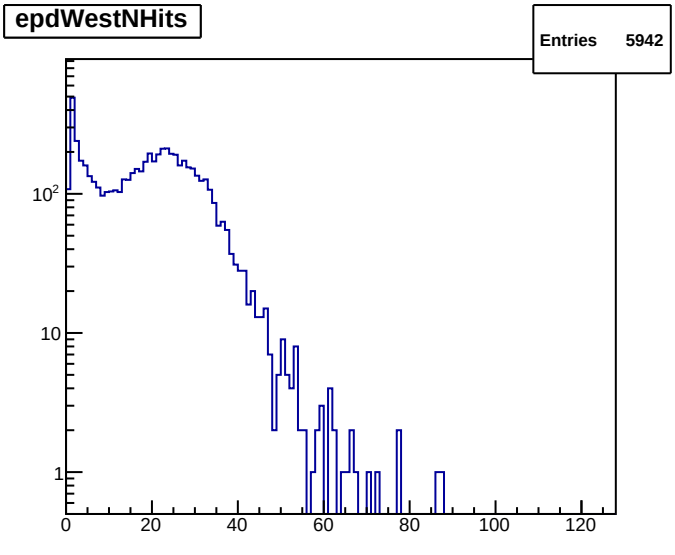
Entries 0
Data
Simu

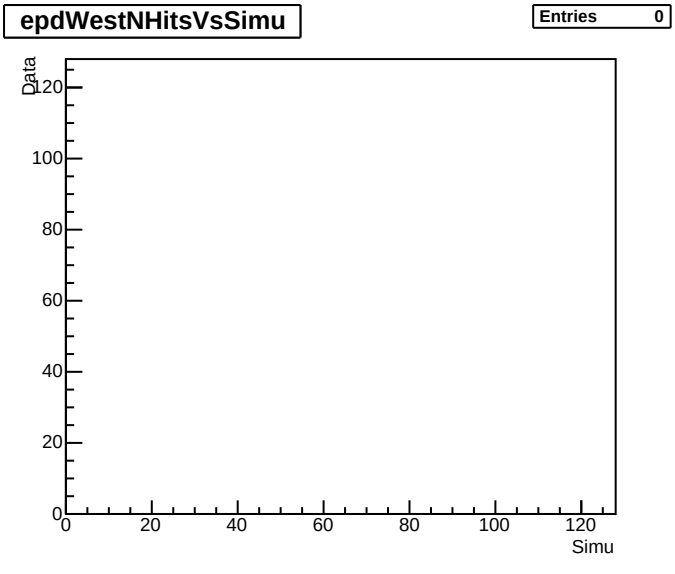

epdWestTac03
Entries 5942

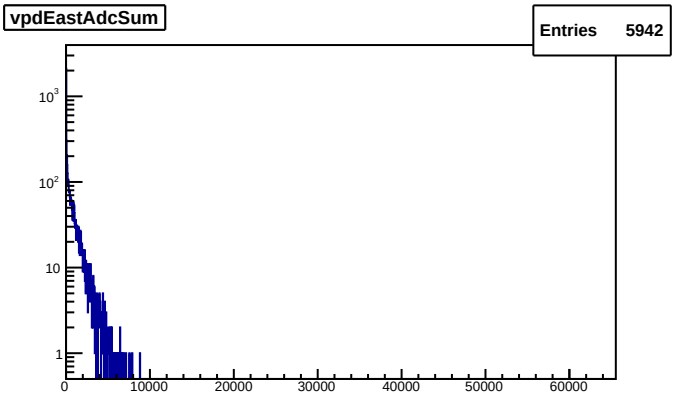

epdWestTac03VsSimu
Entries 0
Data
Simu

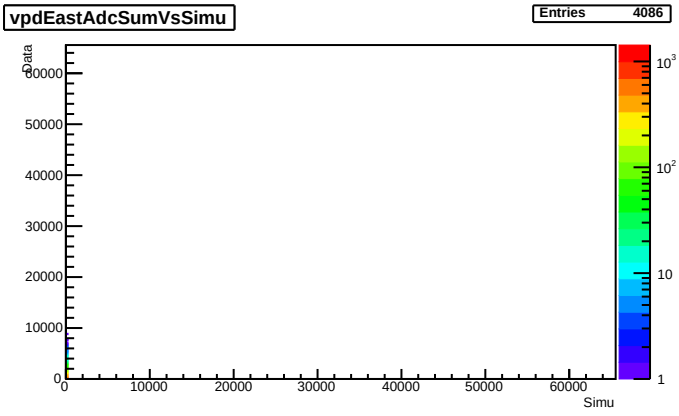

epdWestTac47
Entries 5942

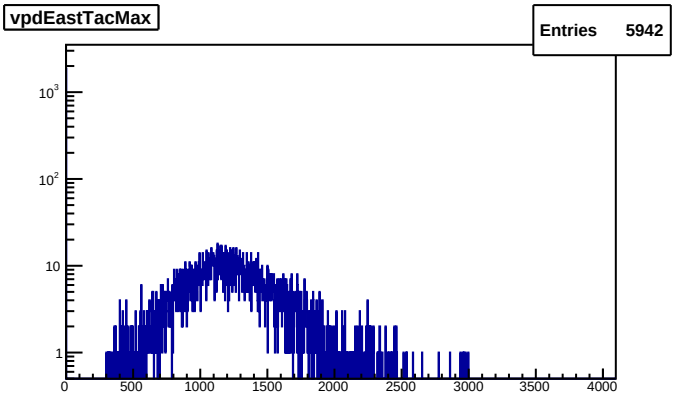

epdWestTac47VsSimu
Entries 0
Data
Simu

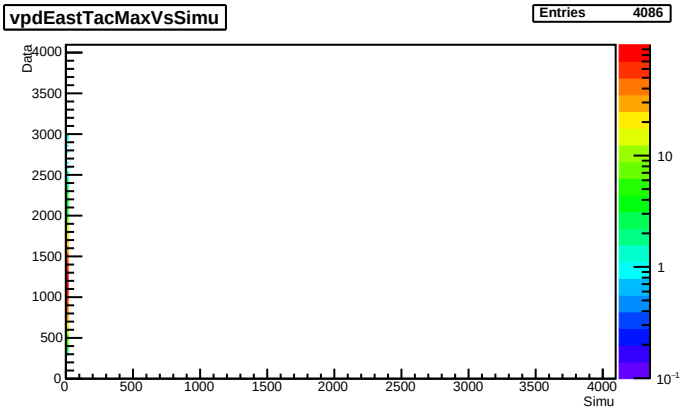

epdWestNHits
Entries 5942

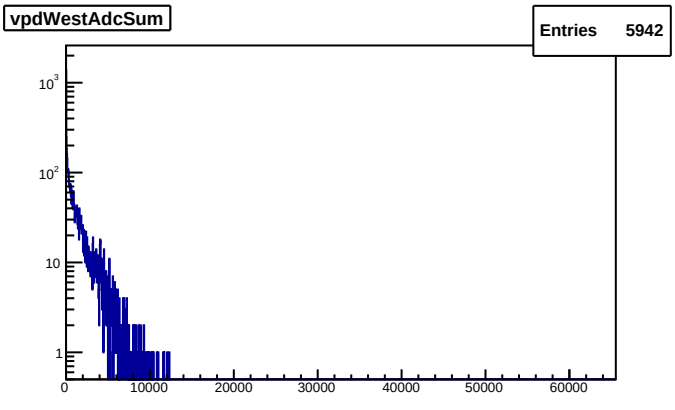

epdWestNHitsVsSimu
Entries 0
Data
Simu

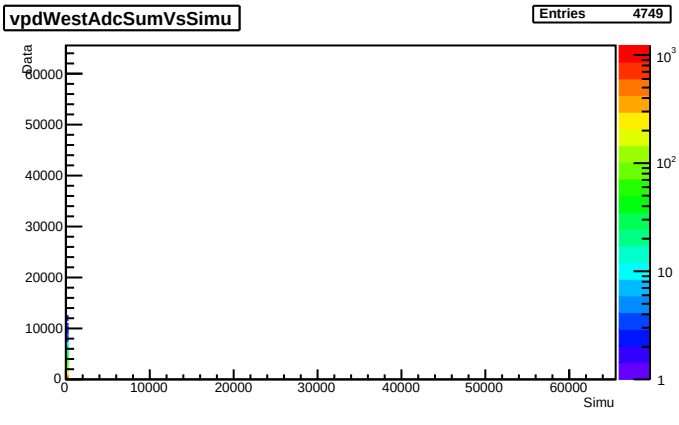

vpdEastAdcSum
Entries 5942

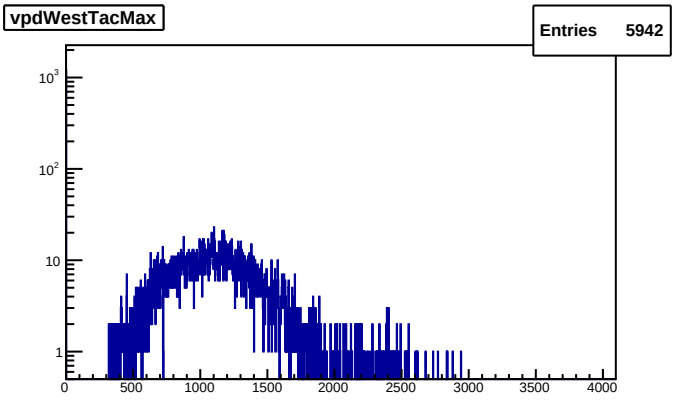

vpdEastAdcSumVsSimu
Entries 4086
Data
Simu

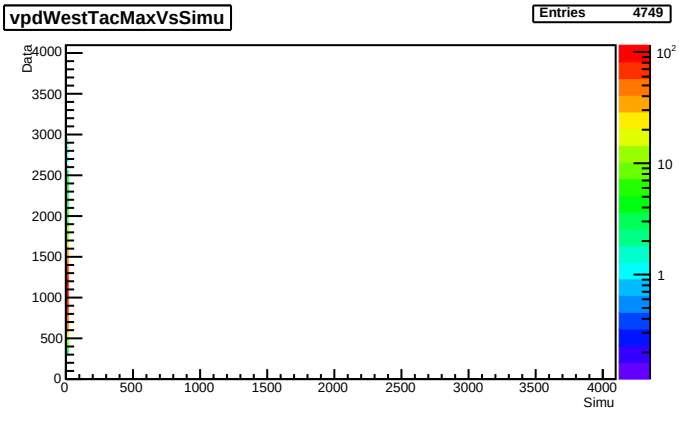

vpdEastTacMax
Entries 5942

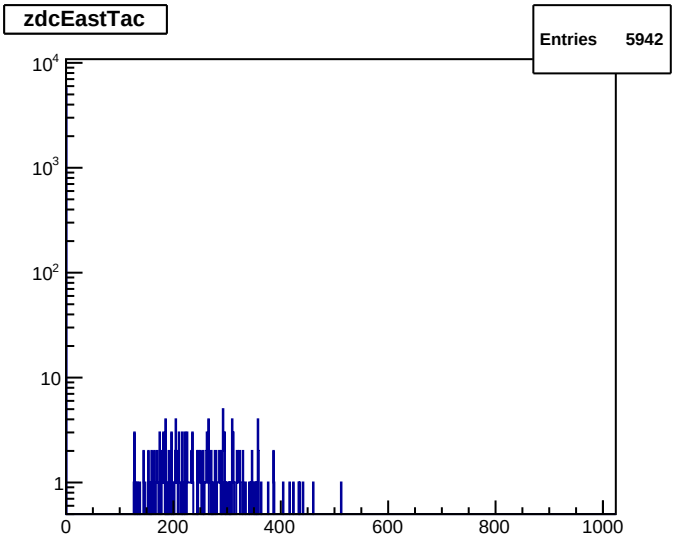

vpdEastTacMaxVsSimu
Entries 4086
Data
Simu

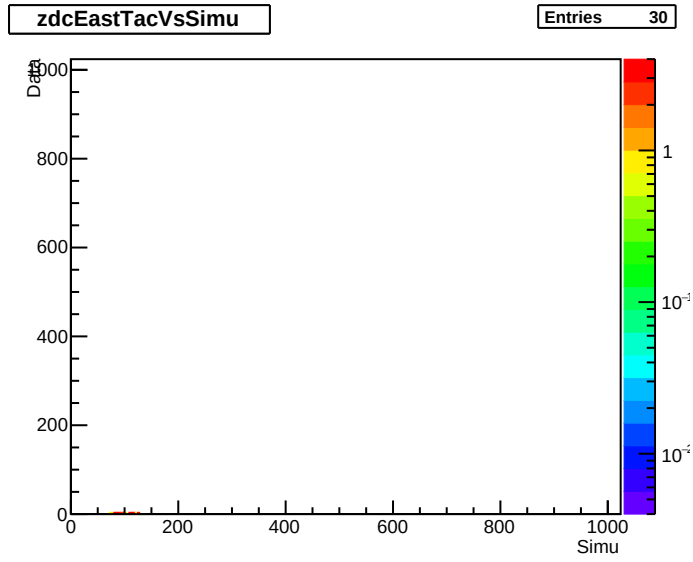

vpdWestAdcSum
Entries 5942

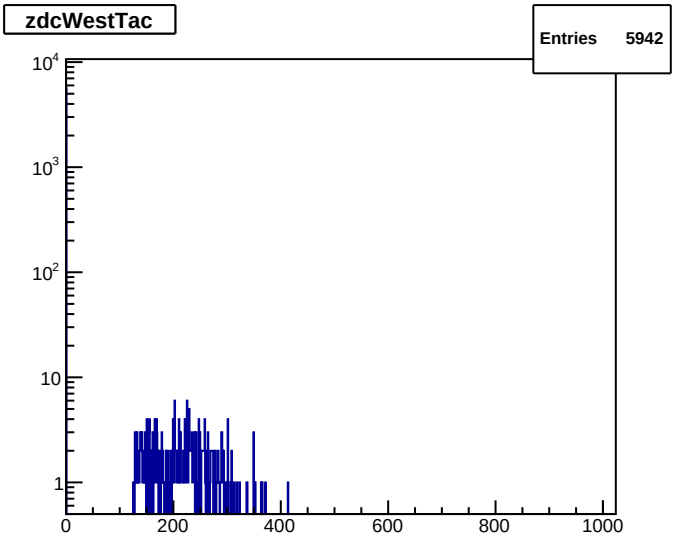

vpdWestAdcSumVsSimu
Entries 4749
Data
Simu

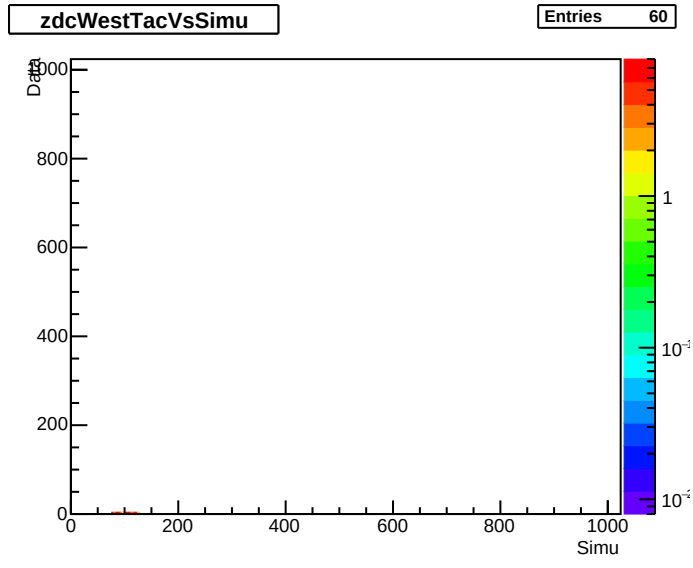

vpdWestTacMax
Entries 5942

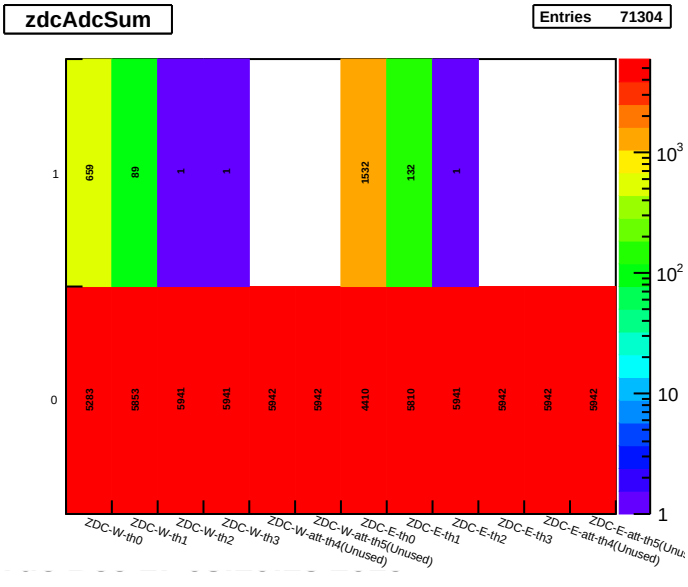

vpdWestTacMaxVsSimu
Entries 4749
Data
Simu

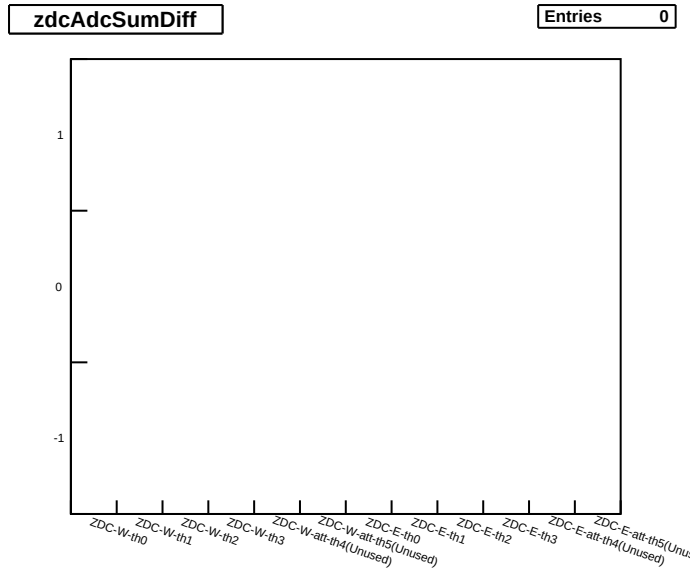

zdcEastTac
Entries 5942

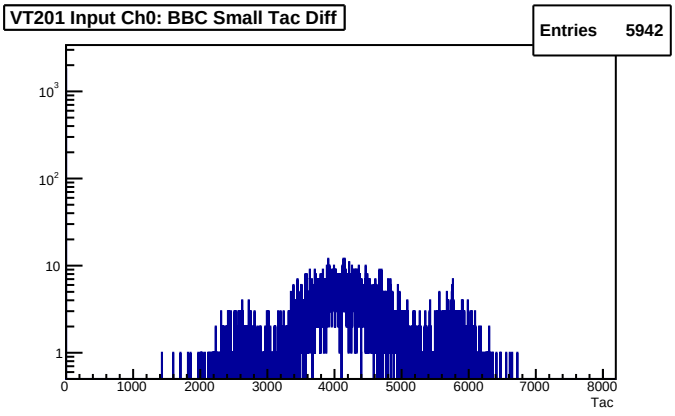

zdcEastTacVsSimu
Entries 30
Data
Simu

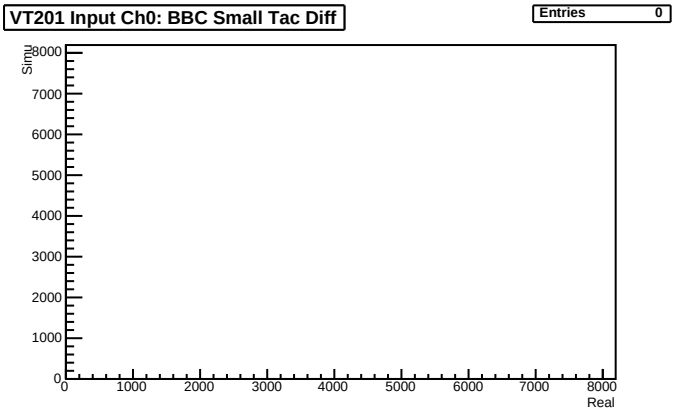

zdcWestTac
Entries 5942

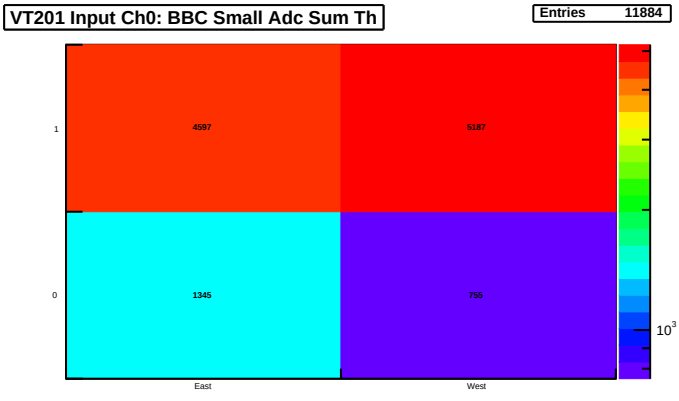

zdcWestTacVsSimu
Entries 60
Data
Simu

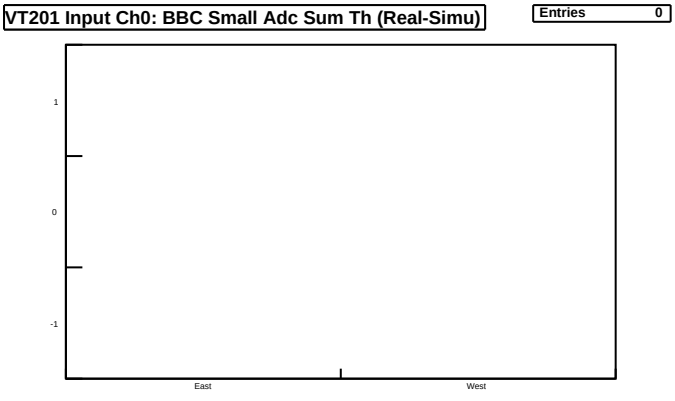

zdcAdcSum
Entries 71304
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)
ZDC-E-att-th5(Unu

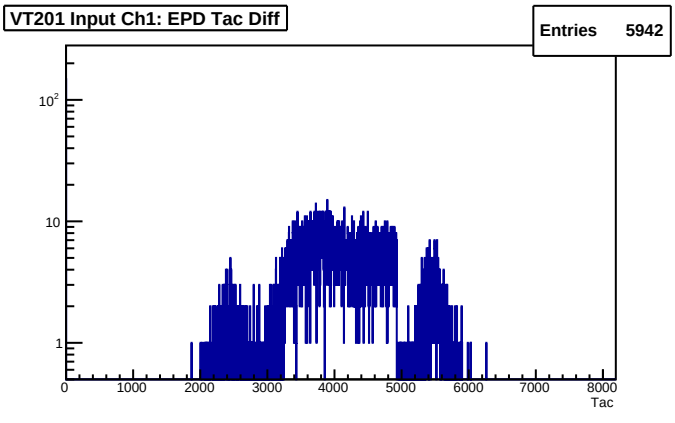

zdcAdcSumDiff
Entries 0
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)
ZDC-E-att-th5(Unu

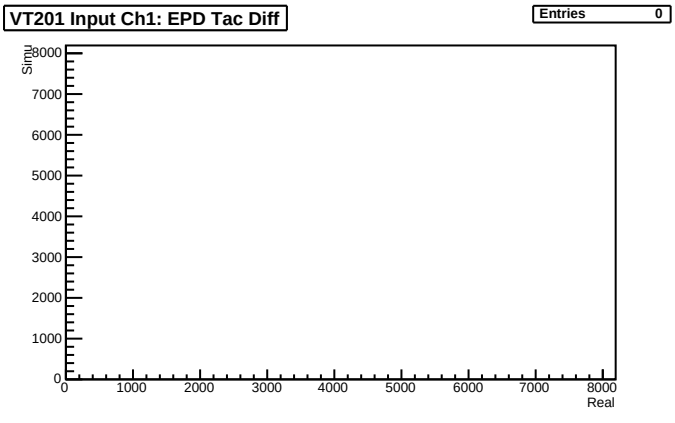

VT201 Input Ch0: BBC Small Tac Diff
Entries 5942
Tac

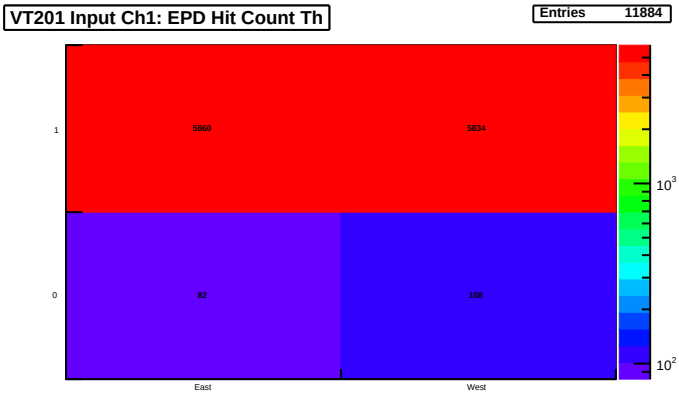

VT201 Input Ch0: BBC Small Tac Diff
Entries 0
Simu
Real

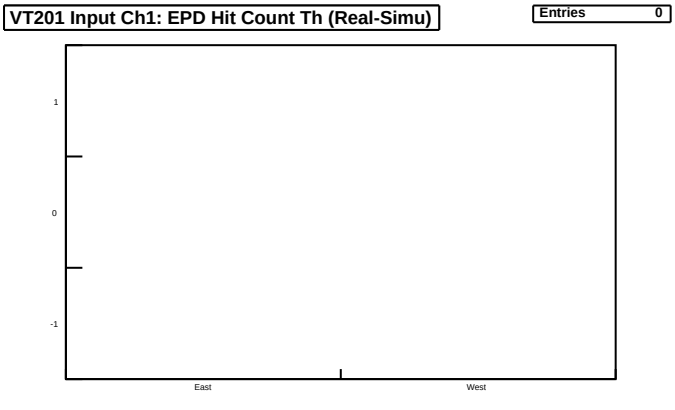

VT201 Input Ch0: BBC Small Adc Sum Th
Entries 11884
4597
5187
1345
755
East
West

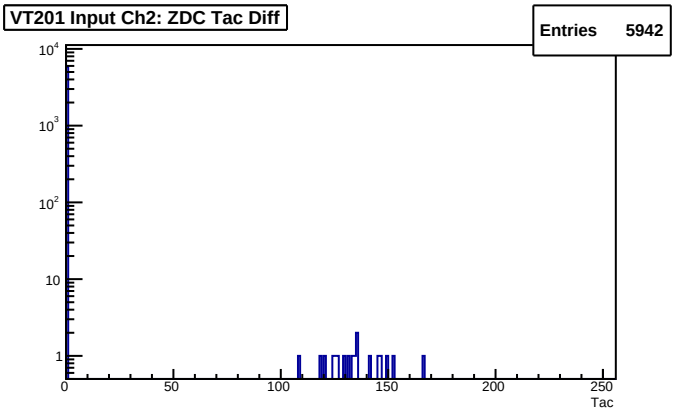

VT201 Input Ch0: BBC Small Adc Sum Th (Real-Simu)
Entries 0
East
West

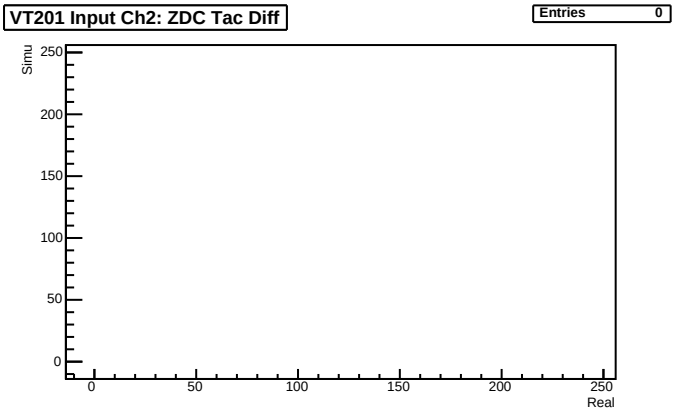

VT201 Input Ch1: EPD Tac Diff
Entries 5942
Tac

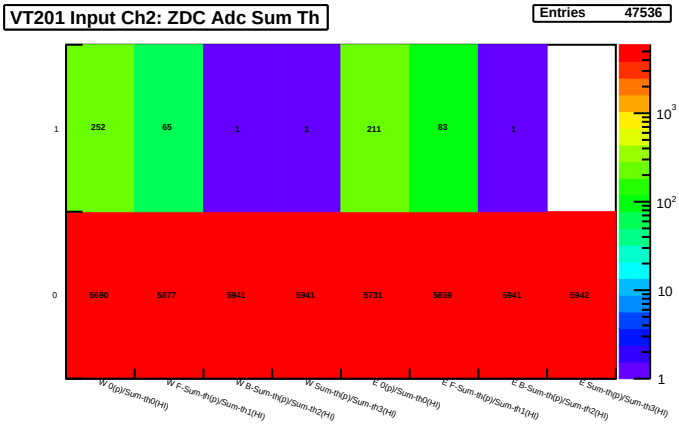

VT201 Input Ch1: EPD Tac Diff
Entries 0
Simu
Real

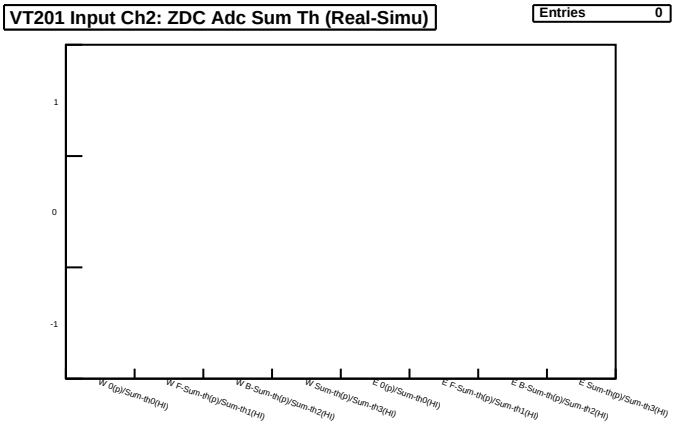

VT201 Input Ch1: EPD Hit Count Th
Entries 11884
5860
5834
82
108
East
West

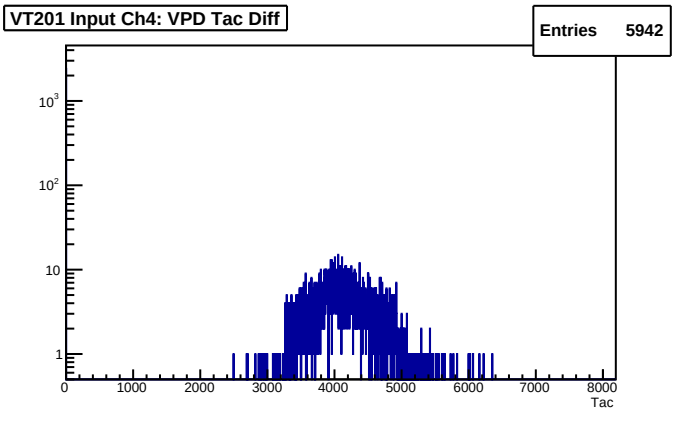

VT201 Input Ch1: EPD Hit Count Th (Real-Simu)
Entries 0
East
West

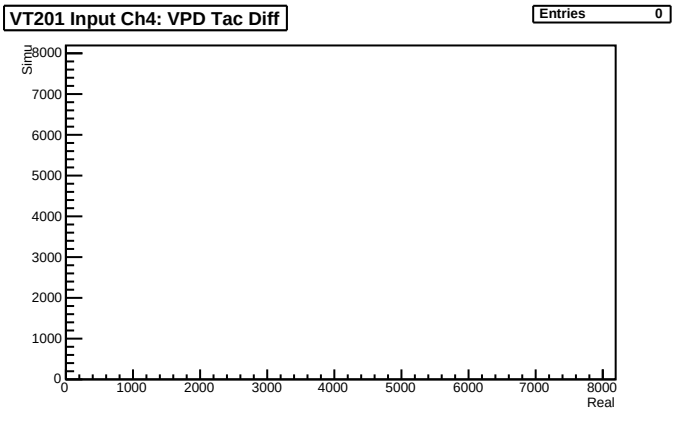

VT201 Input Ch2: ZDC Tac Diff
Entries 5942
Tac

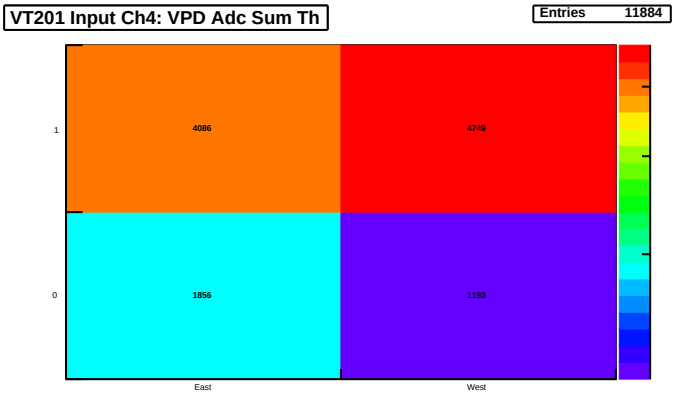

VT201 Input Ch2: ZDC Tac Diff
Entries 0
Simu
Real

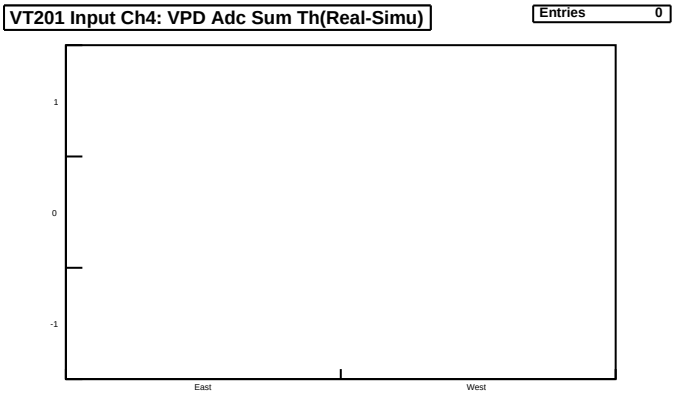

VT201 Input Ch2: ZDC Adc Sum Th
Entries 47536
252
65
1
1
211
83
1
5690
5877
5941
5941
5731
5859
5941
5942
W 0(p)/Sum-th0(H)
W F-Sum-th(p)/Sum-th1(H)
W B-Sum-th(p)/Sum-th2(H)
W Sum-th(p)/Sum-th3(H)
E 0(p)/Sum-th0(H)
E F-Sum-th(p)/Sum-th1(H)
E B-Sum-th(p)/Sum-th2(H)
E Sum-th(p)/Sum-th3(H)

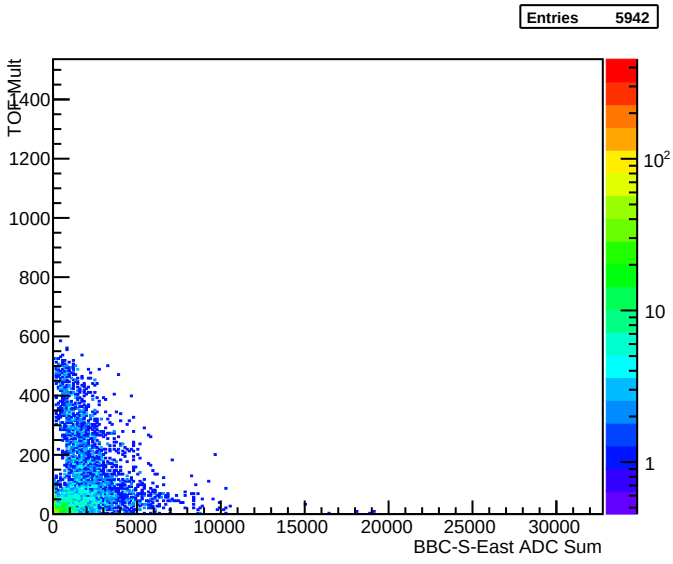

VT201 Input Ch2: ZDC Adc Sum Th (Real-Simu)
Entries 0
W 0(p)/Sum-th0(H)
W F-Sum-th(p)/Sum-th1(H)
W B-Sum-th(p)/Sum-th2(H)
W Sum-th(p)/Sum-th3(H)
E 0(p)/Sum-th0(H)
E F-Sum-th(p)/Sum-th1(H)
E B-Sum-th(p)/Sum-th2(H)
E Sum-th(p)/Sum-th3(H)

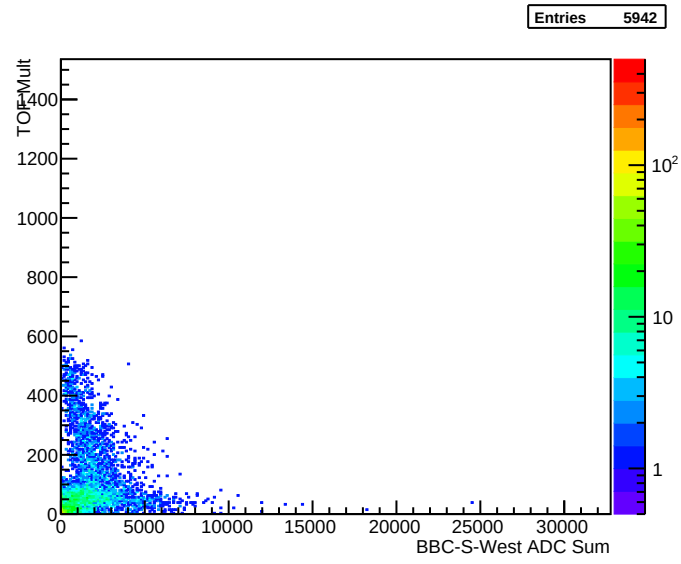

VT201 Input Ch4: VPD Tac Diff
Entries 5942
Tac

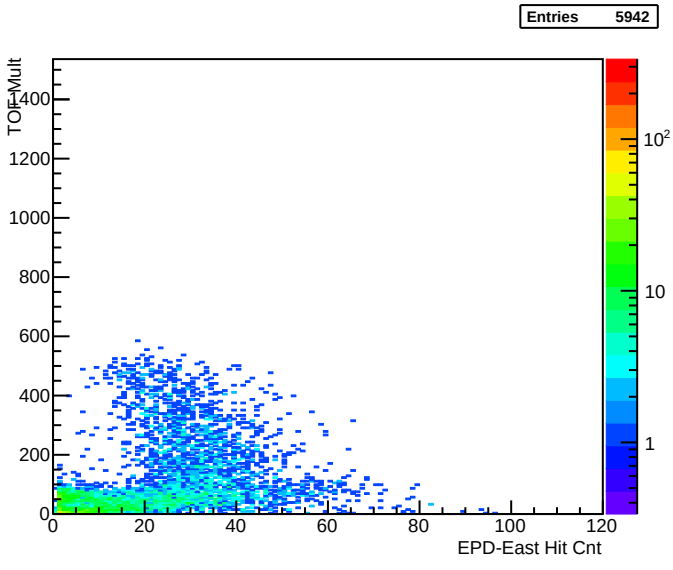

VT201 Input Ch4: VPD Tac Diff
Entries 0
Simu
Real

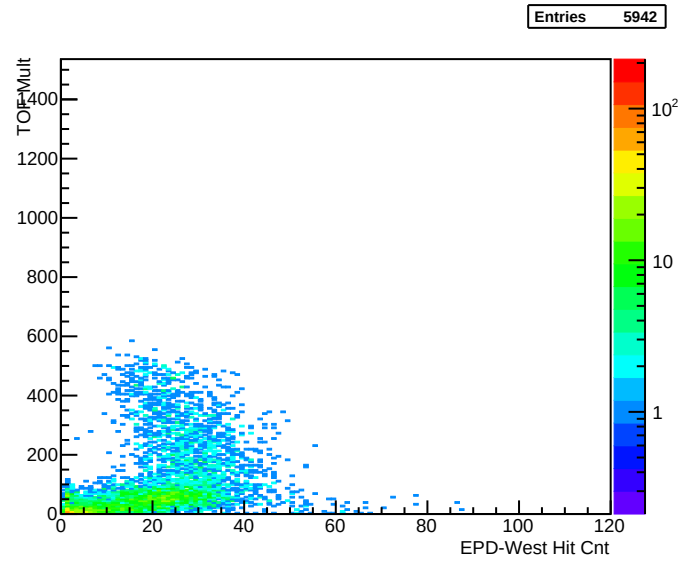

VT201 Input Ch4: VPD Adc Sum Th
Entries 11884
4086
4749
1856
1193
East
West

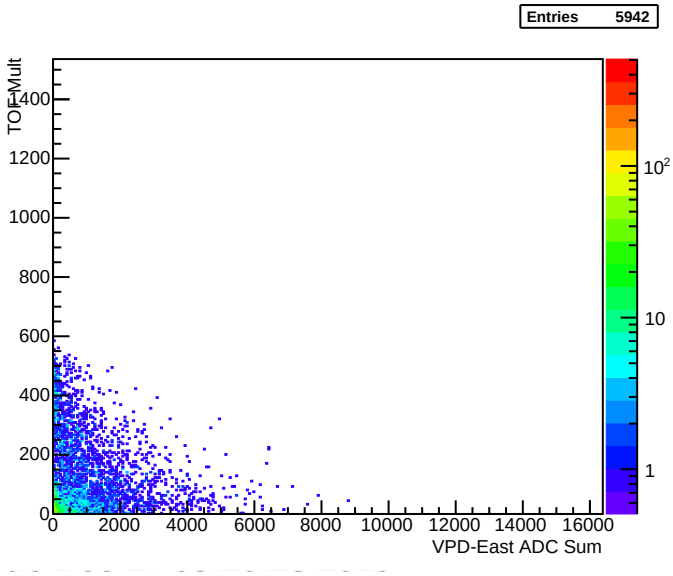

VT201 Input Ch4: VPD Adc Sum Th(Real-Simu)
Entries 0
East
West

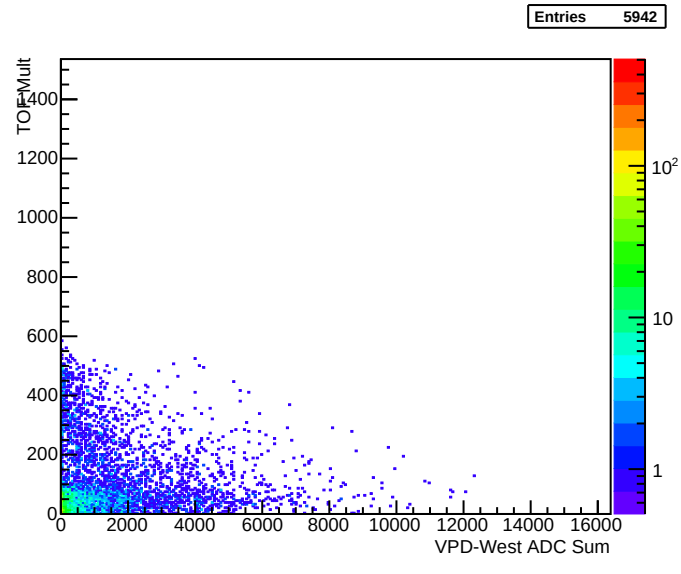

Entries 5942
TOF-Mult
BBC-S-East ADC Sum

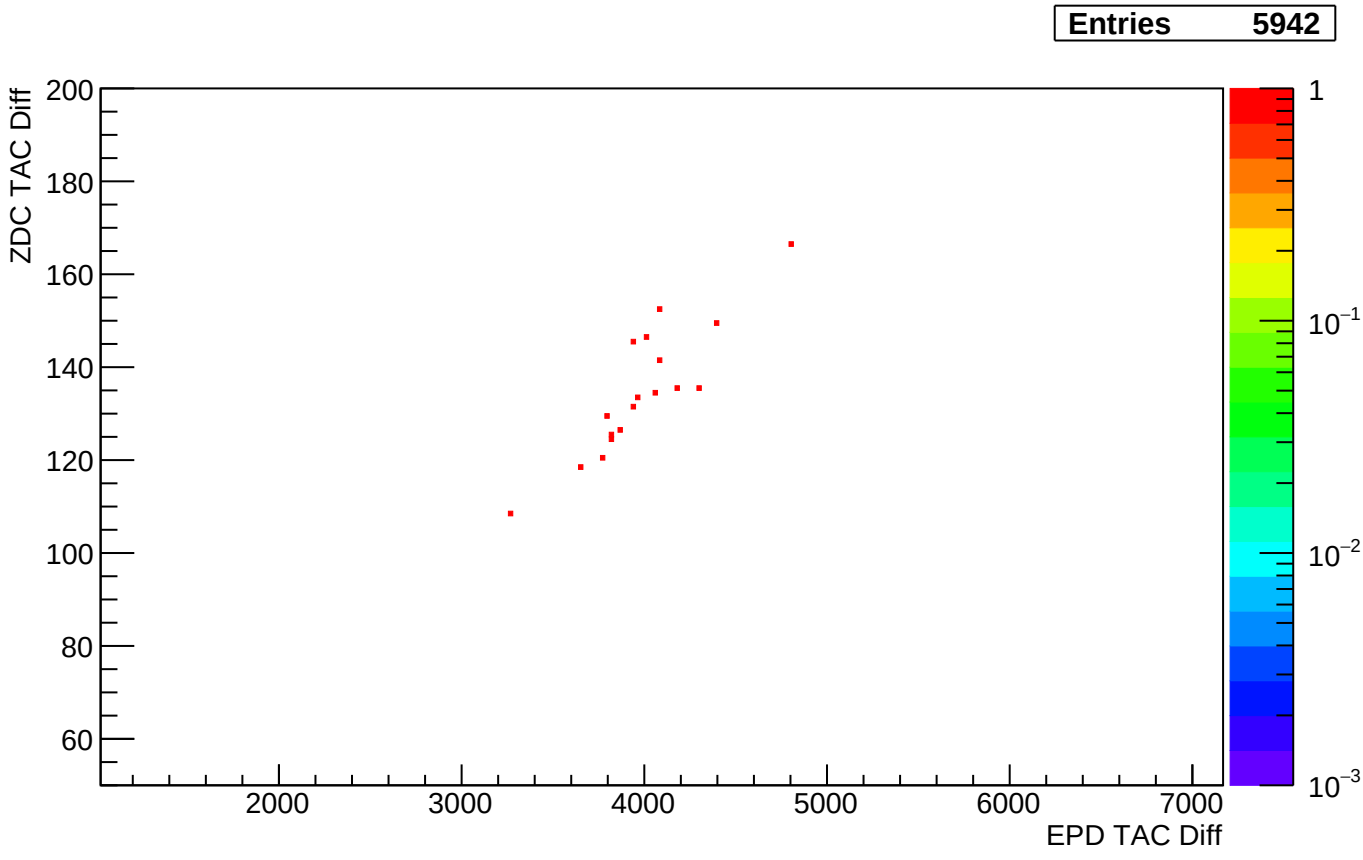

Entries 5942
TOF-Mult
BBC-S-West ADC Sum

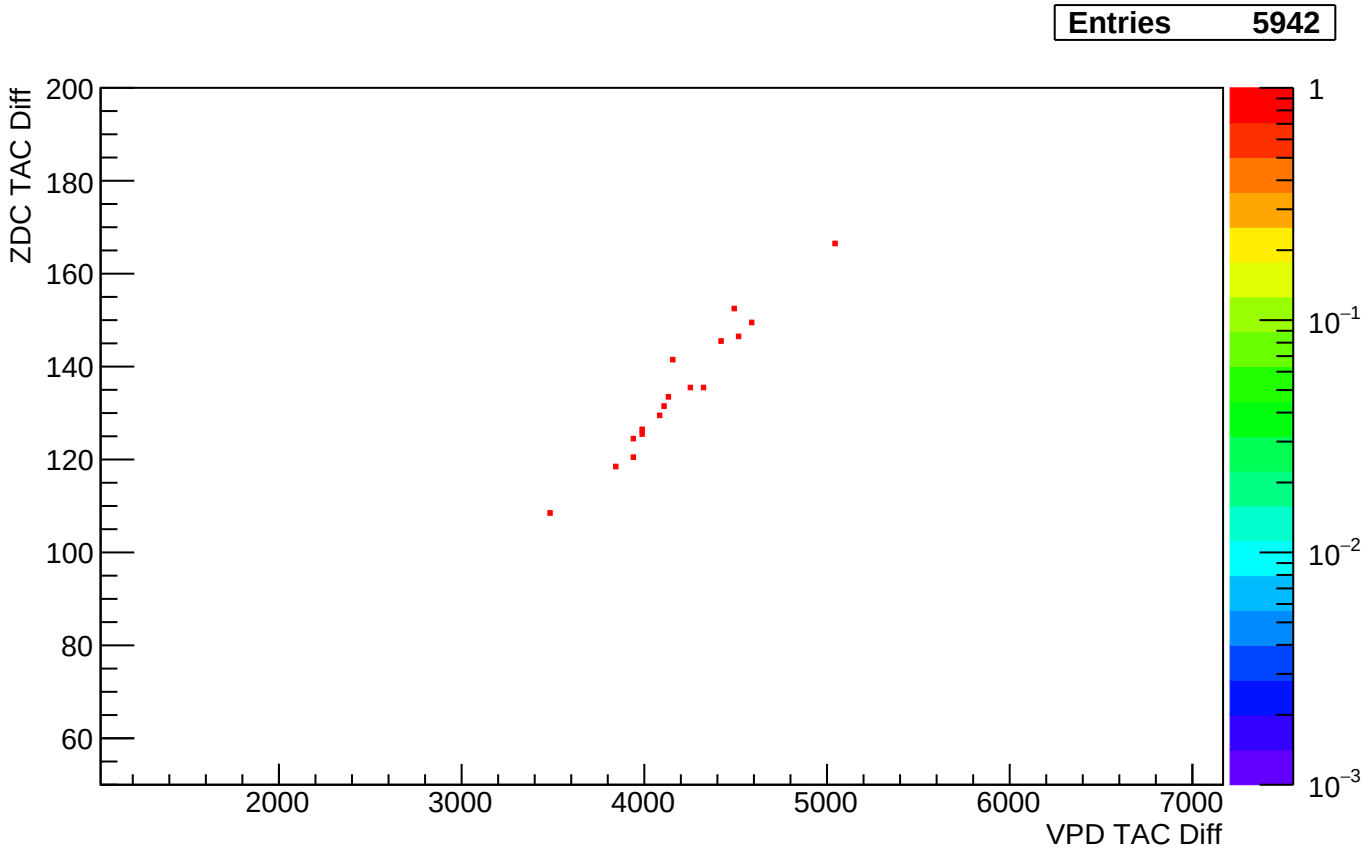

Entries 5942
TOF-Mult
EPD-East Hit Cnt

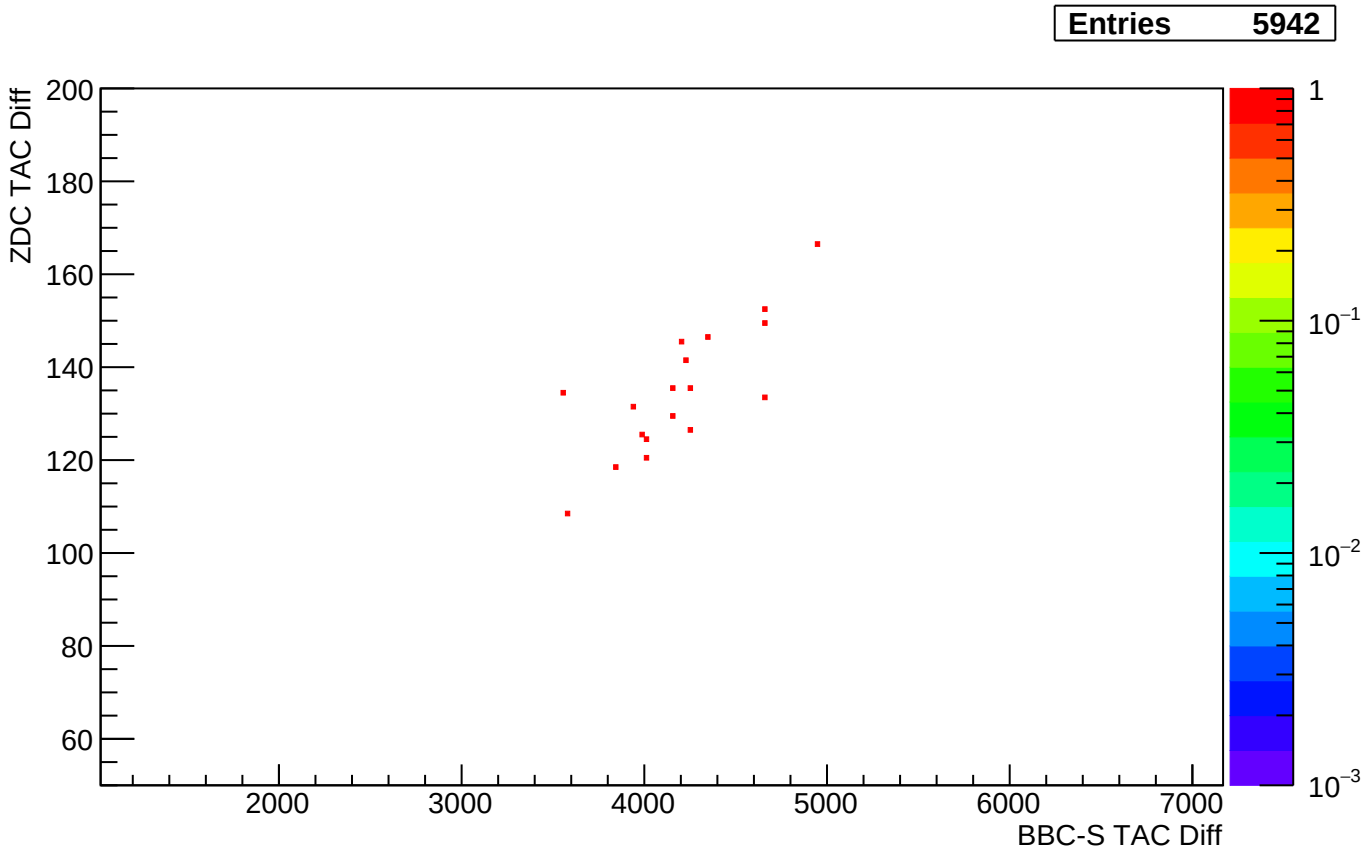

Entries 5942
TOF-Mult
EPD-West Hit Cnt

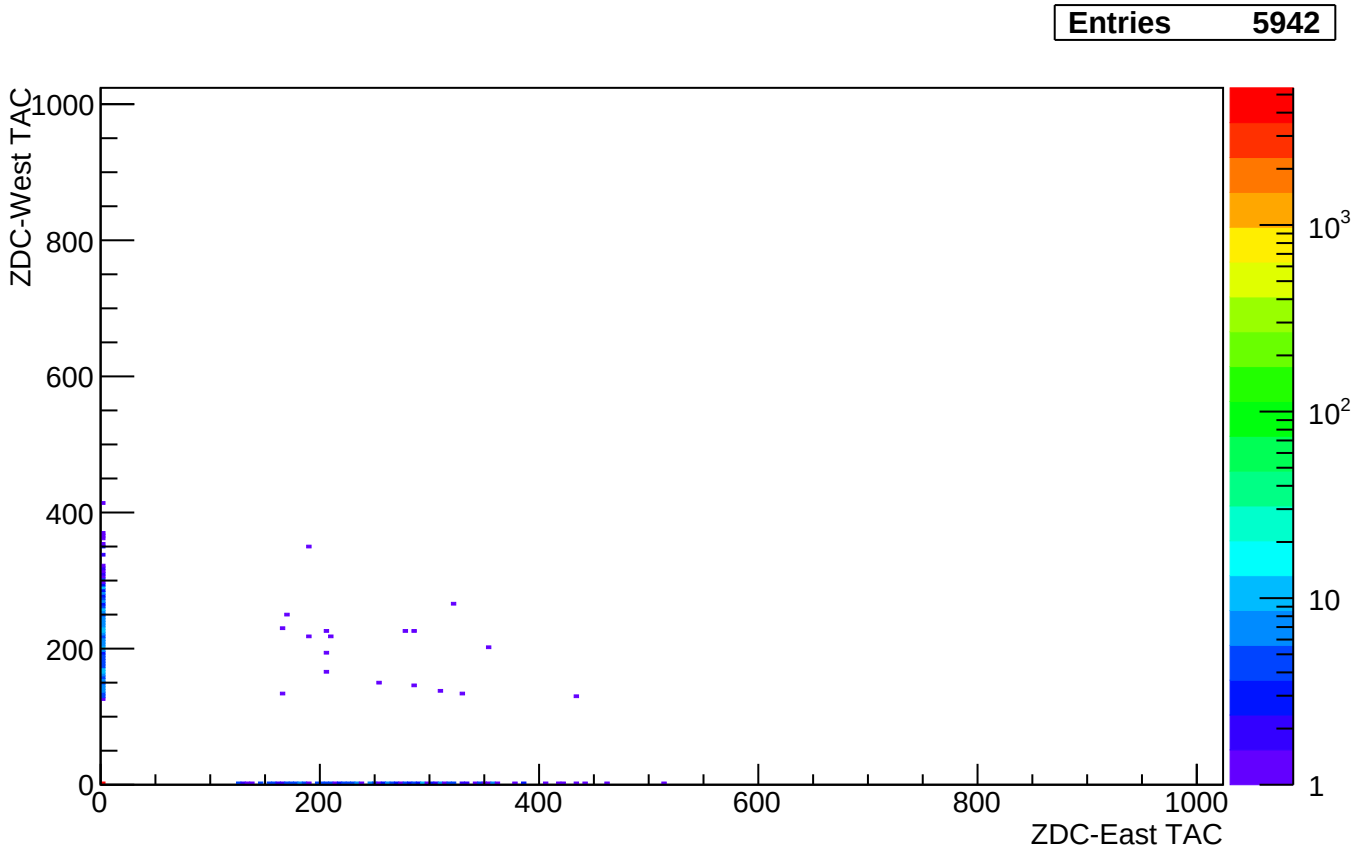

Entries 5942
TOF-Mult
VPD-East ADC Sum

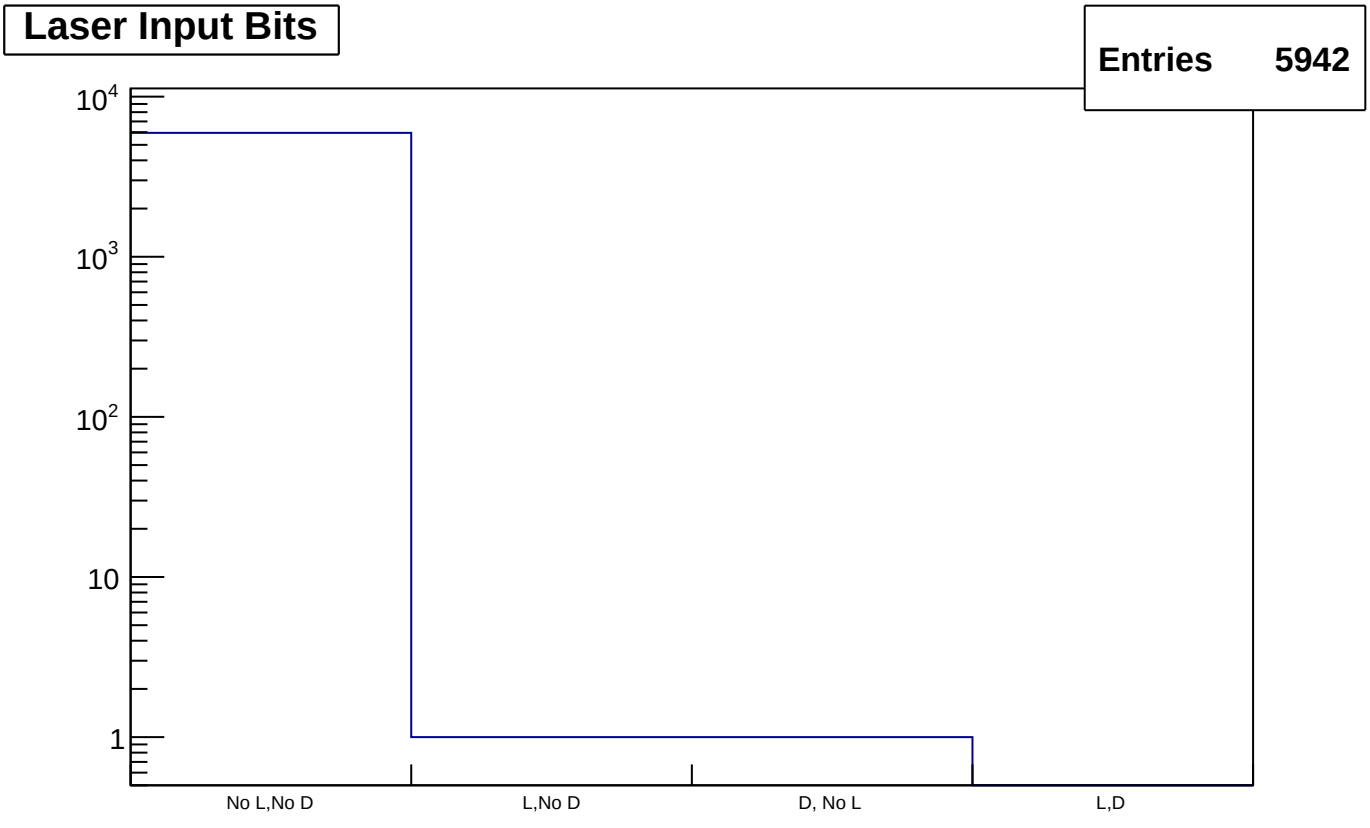

Entries 5942
TOF-Mult
VPD-West ADC Sum

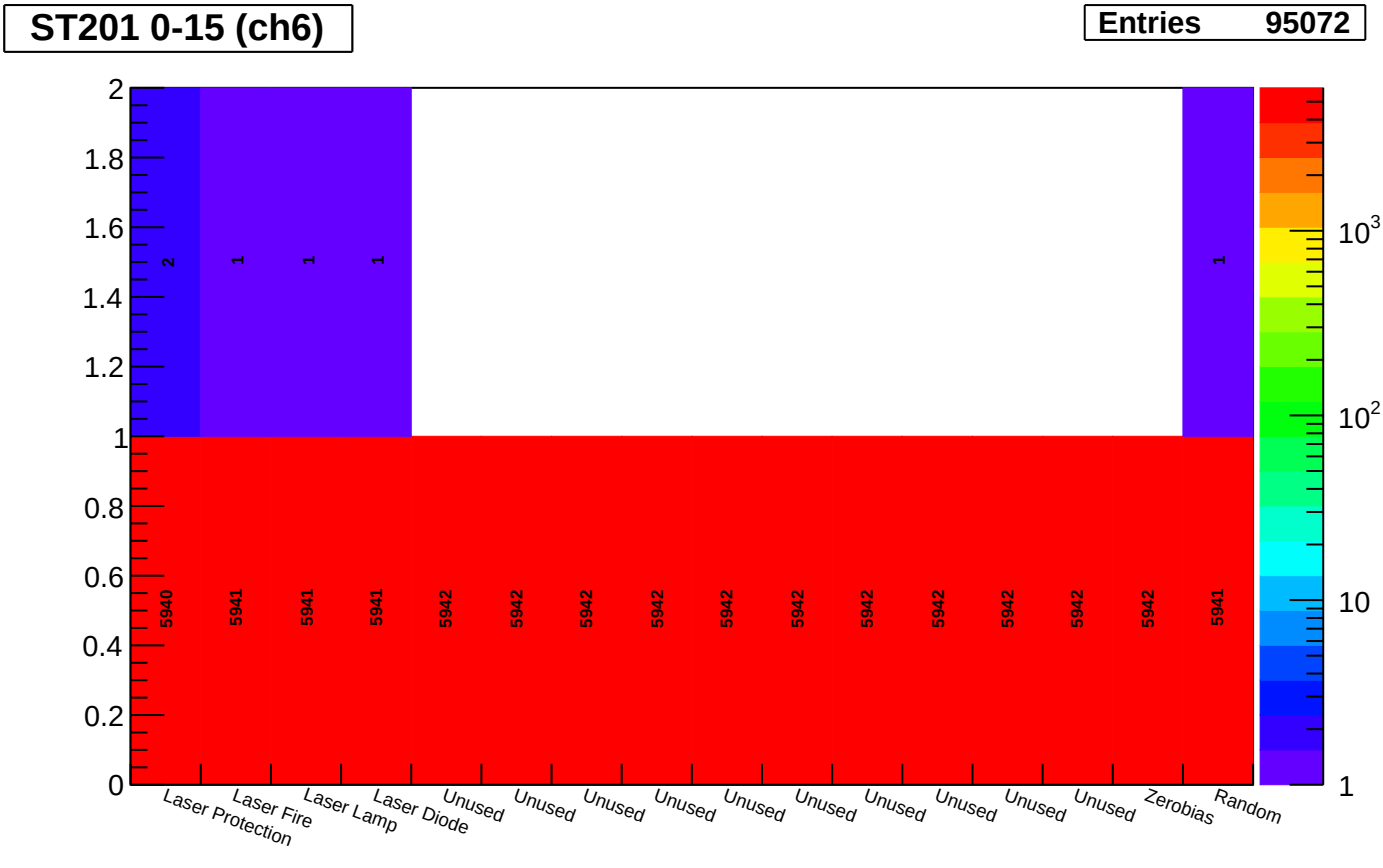

Entries 5942
ZDC TAC Diff
200
180
160
140
120
100
80
60
2000
3000
4000
5000
6000
7000
EPD TAC Diff
1
10^{-1}
10^{-2}
10^{-3}

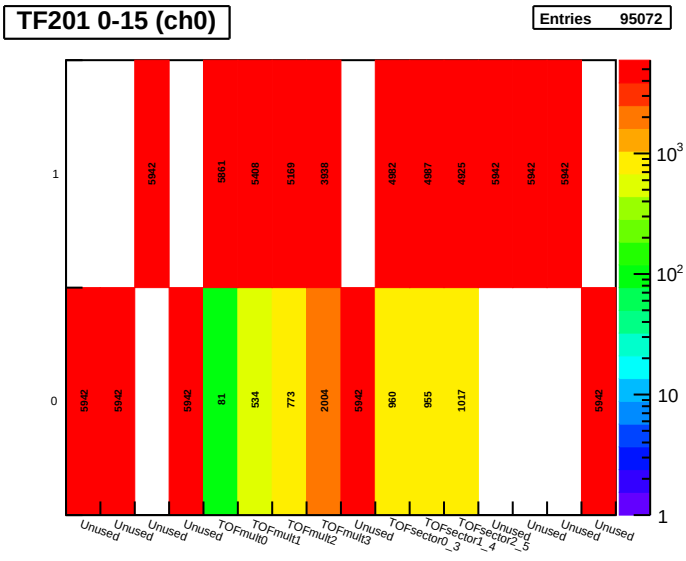

Entries 5942
ZDC TAC Diff
200
180
160
140
120
100
80
60
2000
3000
4000
5000
6000
7000
VPD TAC Diff
1
10^{-1}
10^{-2}
10^{-3}

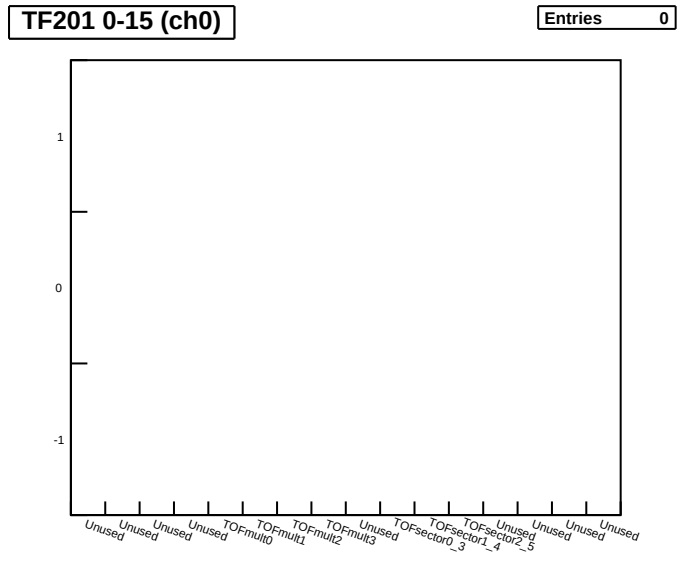

Entries 5942
ZDC TAC Diff
BBC-S TAC Diff
200
180
160
140
120
100
80
60
2000
3000
4000
5000
6000
7000
1
10^{-1}
10^{-2}
10^{-3}

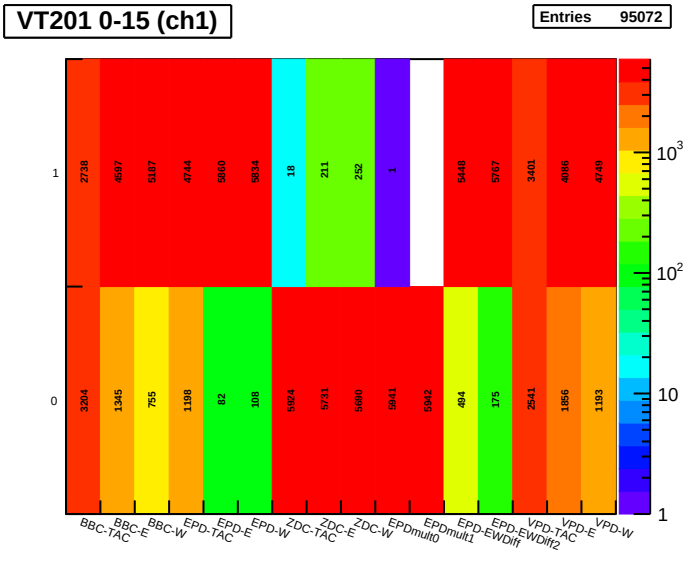

Entries 5942
ZDC-West TAC
ZDC-East TAC
1000
800
600
400
200
0
0
200
400
600
800
1000
10^3
10^2
10
1

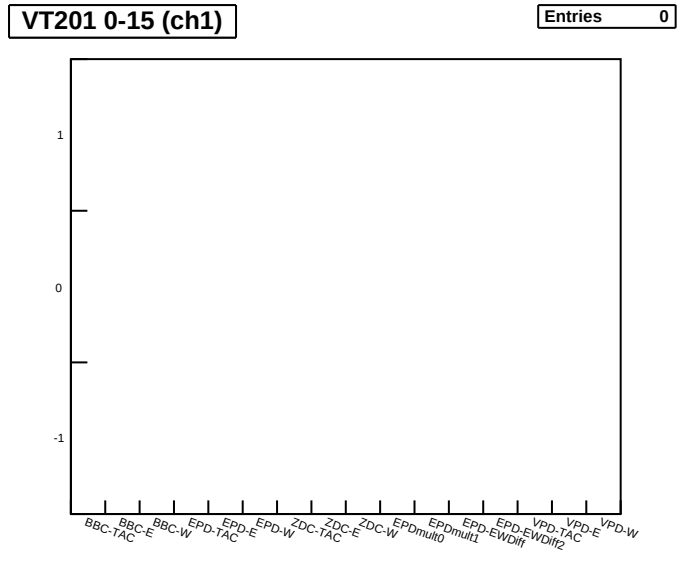

Laser Input Bits
Entries 5942
10^4
10^3
10^2
10
1
No L,No D
L,No D
D, No L
L,D

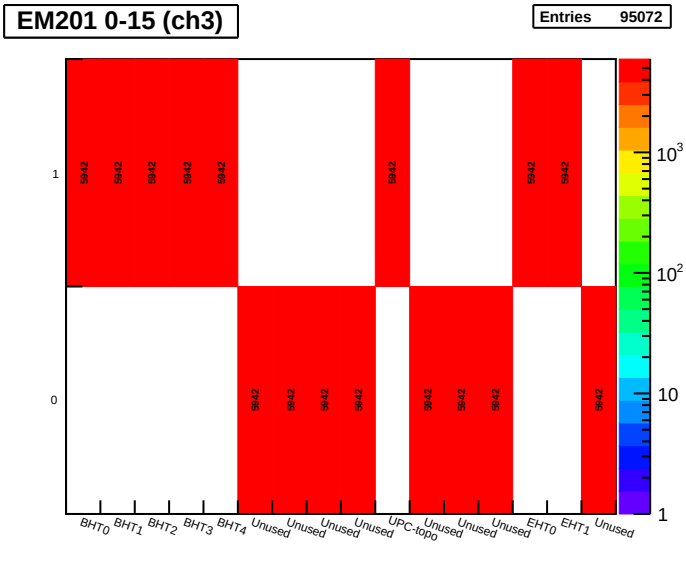

ST201 0-15 (ch6)
Entries 95072
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
2
1
1
1
1
5940
5941
5941
5941
5942
5942
5942
5942
5942
5942
5942
5942
5942
5942
5942
5941
Laser Protection
Laser Fire
Laser Lamp
Laser Diode
Unused
Unused
Unused
Unused
Unused
Unused
Unused
Unused
Unused
Unused
Zerobias
Random
10^3
10^2
10
1

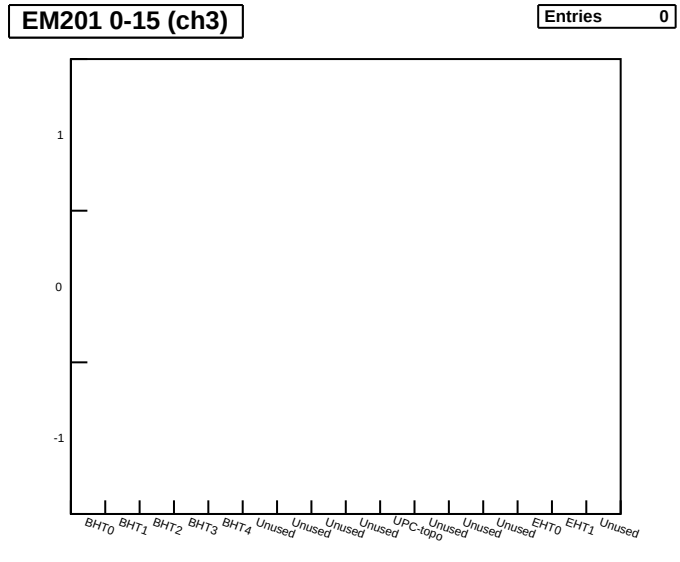

TF201 0-15 (ch0)
Entries 95072
1
0
5942 5942 5942 5942 5942 5942 5942 5942 5942 5942 5942 5942 5942 5942 5942 5942
81 534 773 2004 960 965 1017
Unused Unused Unused Unused TOFmult0 TOFmult1 TOFmult2 TOFmult3 Unused TOFsector0_3 TOFsector1_4 TOFsector2_5 Unused Unused Unused Unused
10^3
10^2
10
1


TF201 0-15 (ch0)
Entries 0
1
0
-1
Unused Unused Unused Unused TOFmult0 TOFmult1 TOFmult2 TOFmult3 Unused TOFsector0_3 TOFsector1_4 TOFsector2_5 Unused Unused Unused Unused


VT201 0-15 (ch1)
Entries 95072
1
0
2738 4597 5167 4744 5860 5834 18 211 252 1 5448 5767 3401 4086 4749
3204 1345 755 1198 82 108 5924 5731 5600 5941 5942 494 175 2541 1856 1193
BBC-TAC BBC-E BBC-W EPD-TAC EPD-E EPD-W ZDC-TAC ZDC-E ZDC-W EPDmult0 EPDmult1 EPD-EWDiff EPD-EWDiff2 VPD-TAC VPD-E VPD-W
10^3
10^2
10
1


VT201 0-15 (ch1)
Entries 0
1
0
-1
BBC-TAC BBC-E BBC-W EPD-TAC EPD-E EPD-W ZDC-TAC ZDC-E ZDC-W EPDmult0 EPDmult1 EPD-EWDiff EPD-EWDiff2 VPD-TAC VPD-E VPD-W


EM201 0-15 (ch3)
Entries 95072
1
0
5942 5942 5942 5942 5942 5942 5942 5942 5942 5942 5942 5942 5942 5942 5942 5942
BHT0 BHT1 BHT2 BHT3 BHT4 Unused Unused Unused Unused UPC-topo Unused Unused Unused EHT0 EHT1 Unused
10^3
10^2
10
1


EM201 0-15 (ch3)
Entries 0
1
0
-1
BHT0 BHT1 BHT2 BHT3 BHT4 Unused Unused Unused Unused UPC-topo Unused Unused Unused EHT0 EHT1 Unused