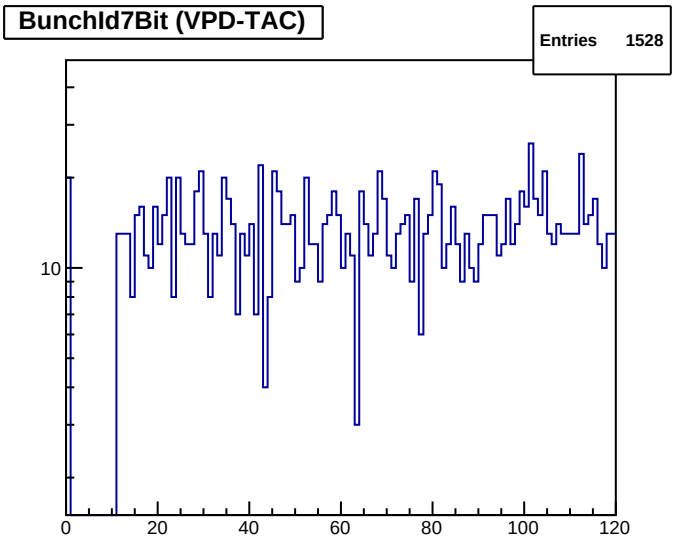
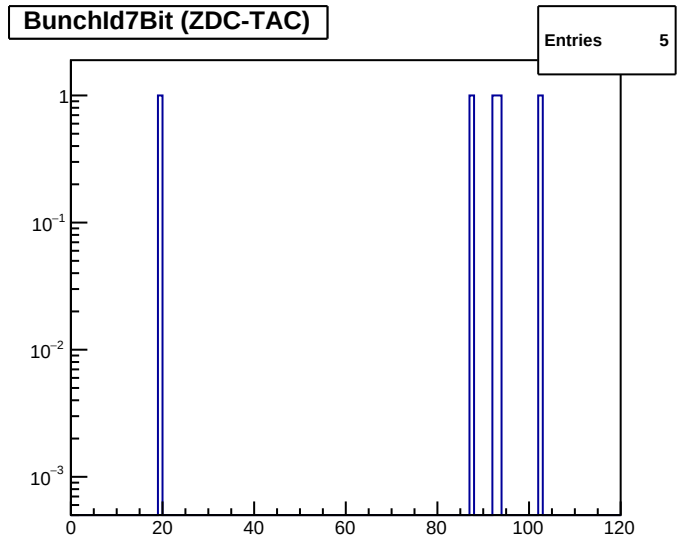
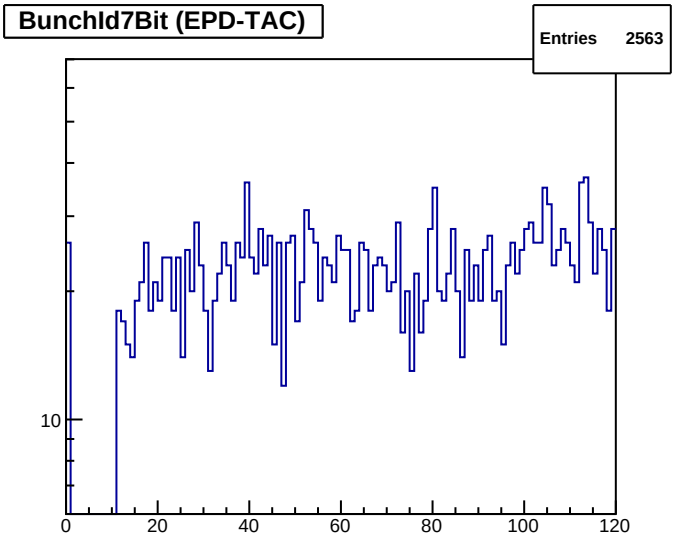
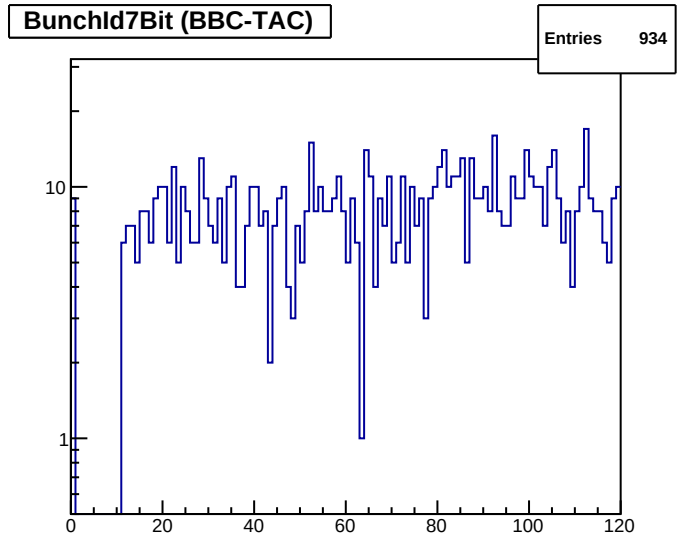
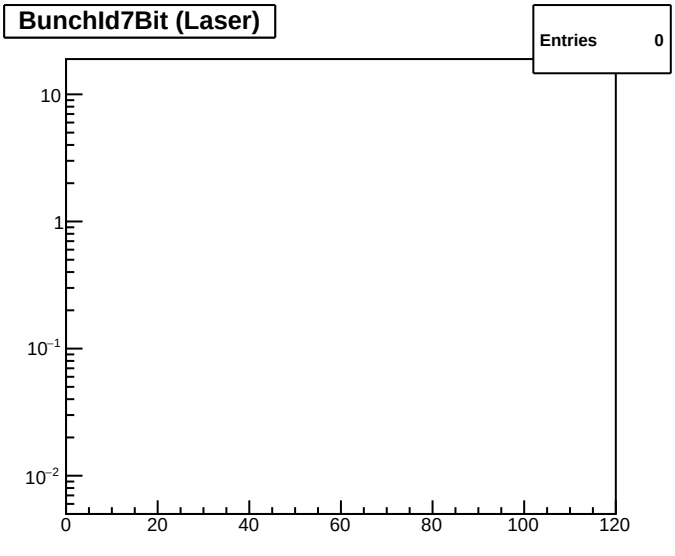
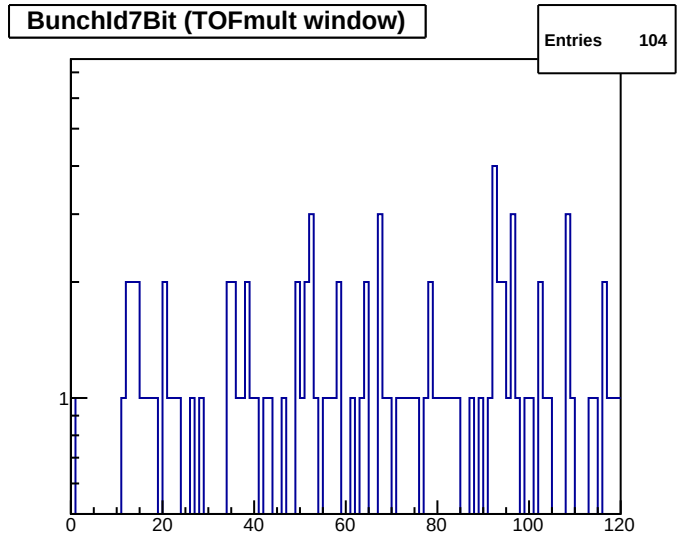

BunchId7Bit (VPD-TAC)
Entries 1528
10
0 20 40 60 80 100 120

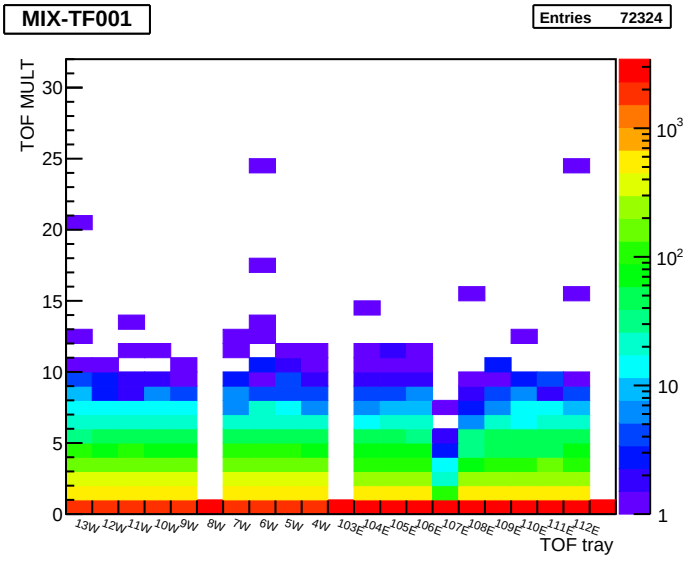

BunchId7Bit (ZDC-TAC)
Entries 5
1
10^{-1}
10^{-2}
10^{-3}
0 20 40 60 80 100 120

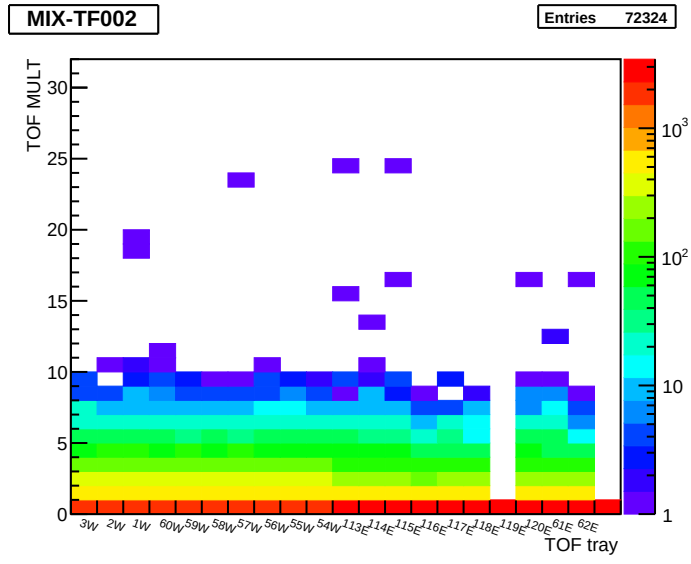

BunchId7Bit (EPD-TAC)
Entries 2563
10
0 20 40 60 80 100 120

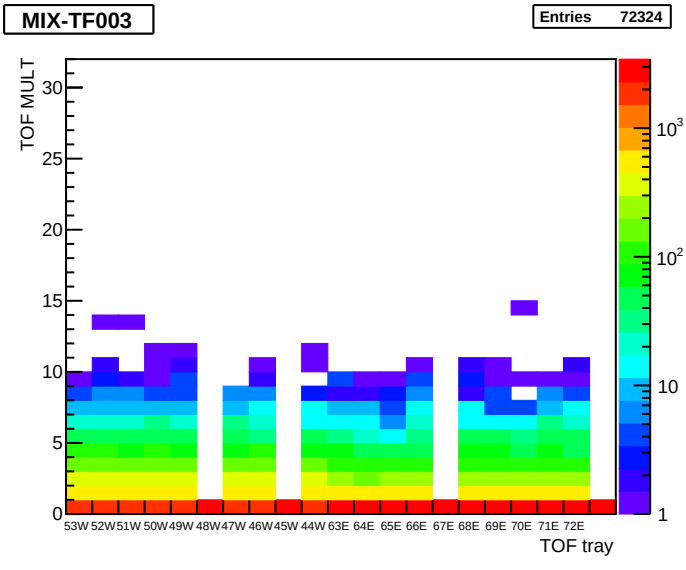

BunchId7Bit (BBC-TAC)
Entries 934
10
1
0 20 40 60 80 100 120

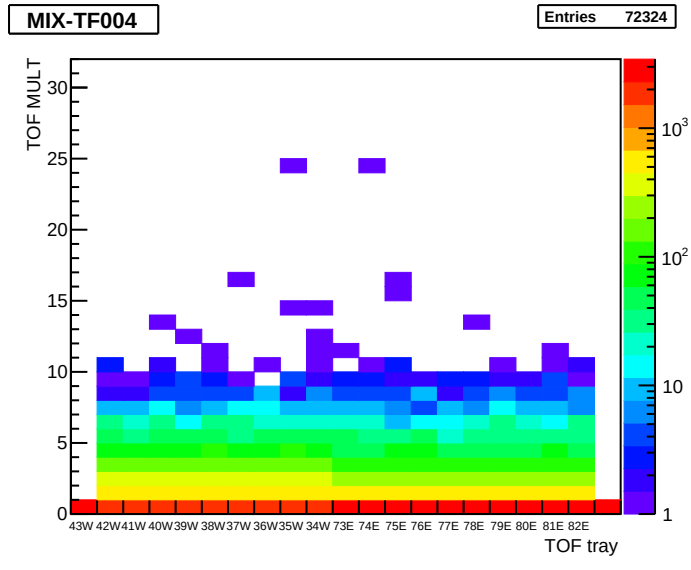

BunchId7Bit (Laser)
Entries 0
10
1
10^{-1}
10^{-2}
0 20 40 60 80 100 120

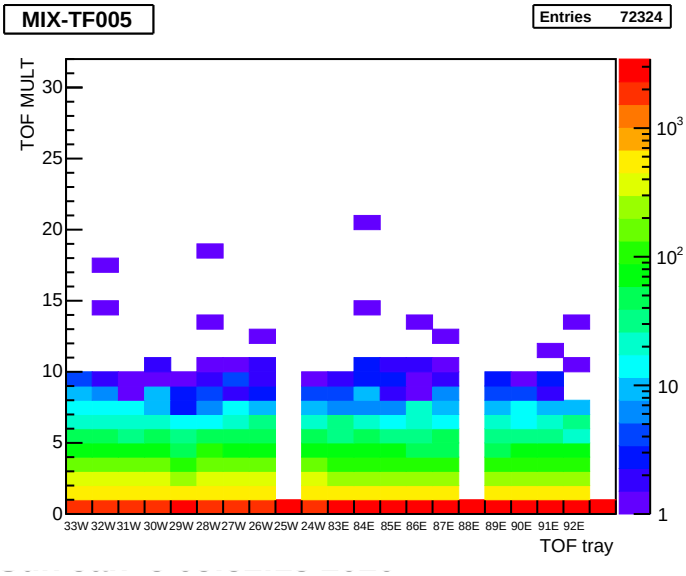

BunchId7Bit (TOFmult window)
Entries 104
1
0 20 40 60 80 100 120

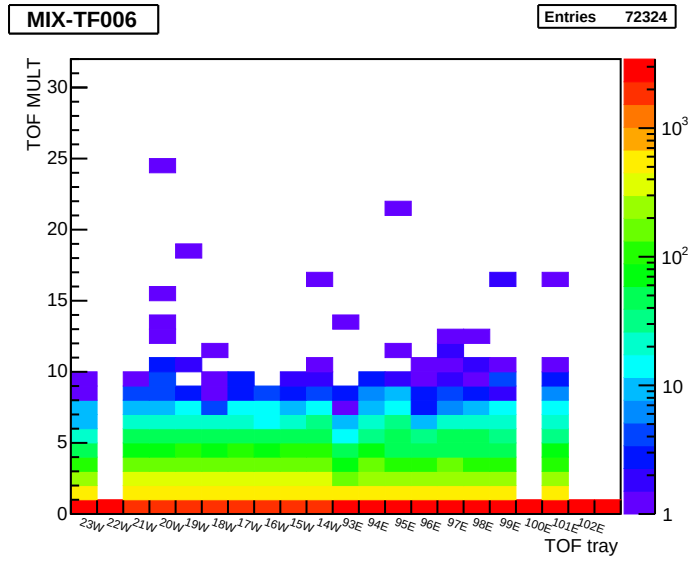

MIX-TF001
Entries 72324
TOF MULT
TOF tray
13W 12W 11W 10W 9W 8W 7W 6W 5W 4W 103E 104E 105E 106E 107E 108E 109E 110E 111E 112E

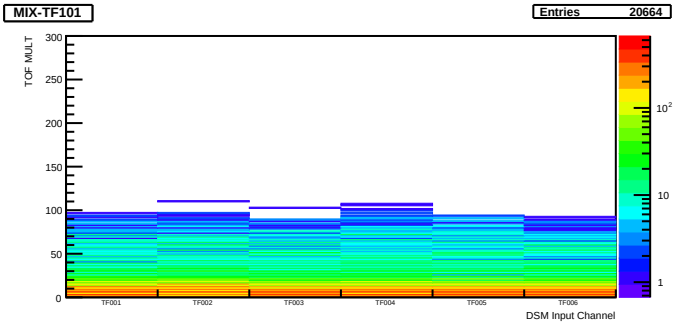

MIX-TF002
Entries 72324
TOF MULT
TOF tray
3W 2W 1W 60W 59W 58W 57W 56W 55W 54W 113E 114E 115E 116E 117E 118E 119E 120E 61E 62E

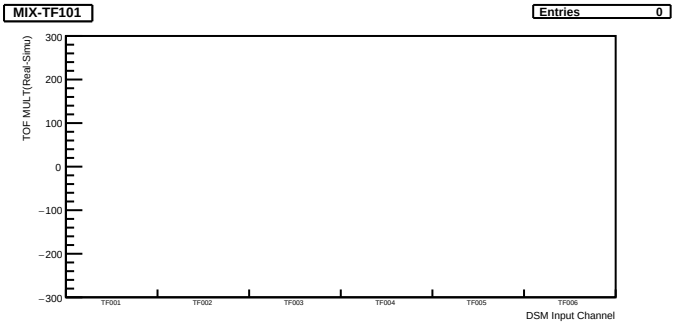

MIX-TF003
Entries 72324
TOF MULT
TOF tray
53W 52W 51W 50W 49W 48W 47W 46W 45W 44W 63E 64E 65E 66E 67E 68E 69E 70E 71E 72E

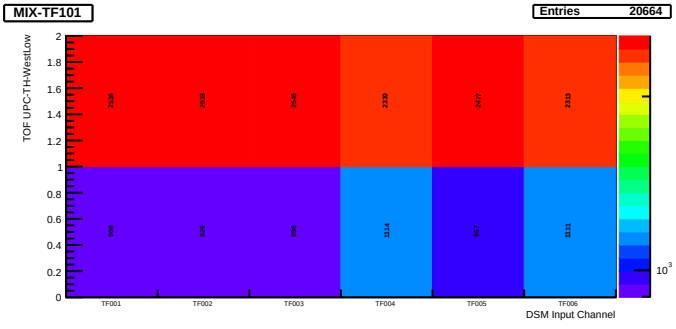

MIX-TF004
Entries 72324
TOF MULT
TOF tray
43W 42W 41W 40W 39W 38W 37W 36W 35W 34W 73E 74E 75E 76E 77E 78E 79E 80E 81E 82E

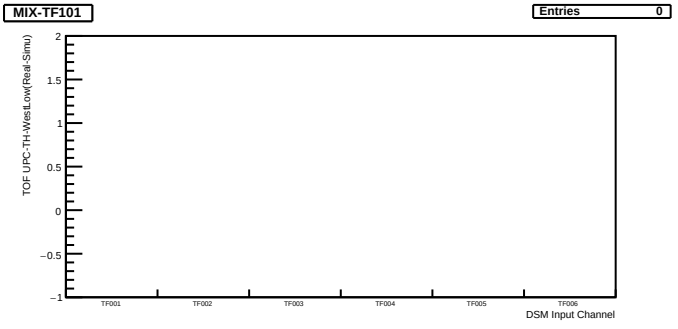

MIX-TF005
Entries 72324
TOF MULT
TOF tray
33W 32W 31W 30W 29W 28W 27W 26W 25W 24W 83E 84E 85E 86E 87E 88E 89E 90E 91E 92E

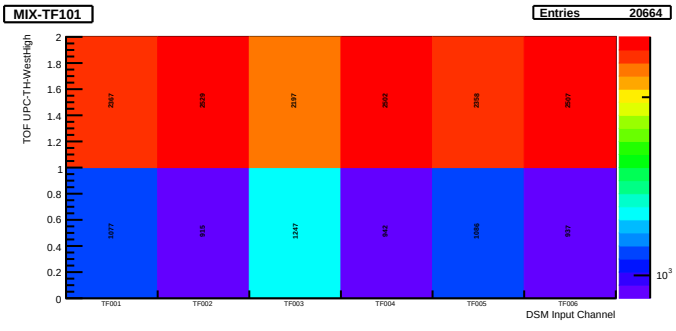

MIX-TF006
Entries 72324
TOF MULT
TOF tray
23W 22W 21W 20W 19W 18W 17W 16W 15W 14W 93E 94E 95E 96E 97E 98E 99E 100E 101E 102E

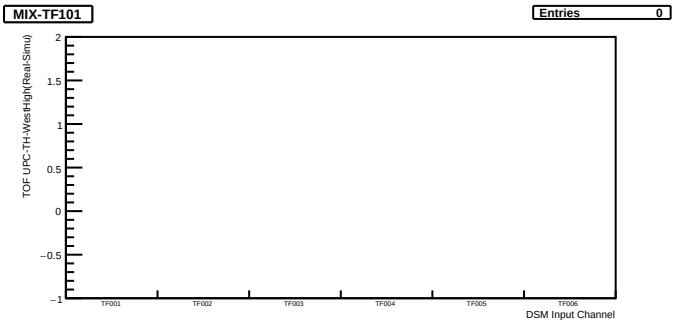

MIX-TF101
Entries 20664
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

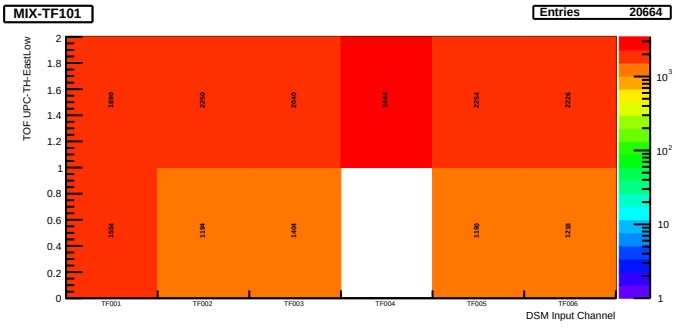

MIX-TF101
Entries 0
TOF MULT(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

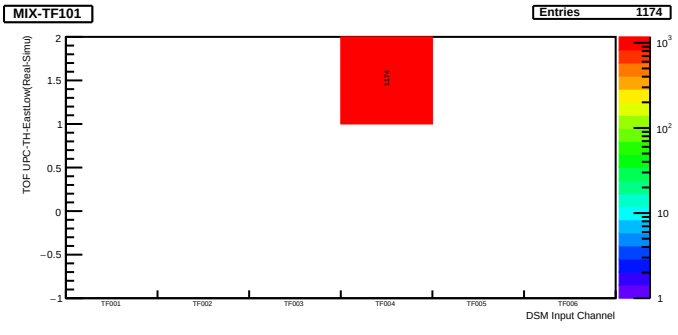

MIX-TF101
Entries 20664
TOF UPC-TH-WestLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

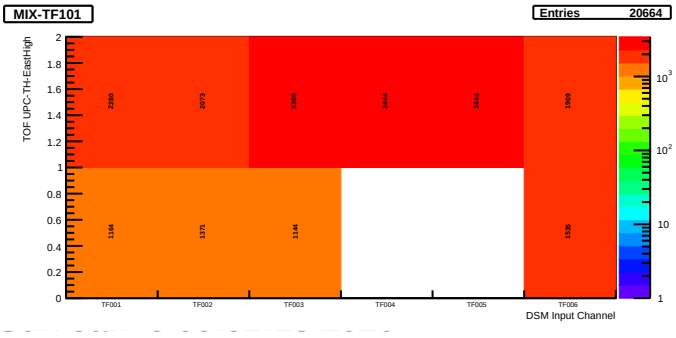

MIX-TF101
Entries 0
TOF UPC-TH-WestLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

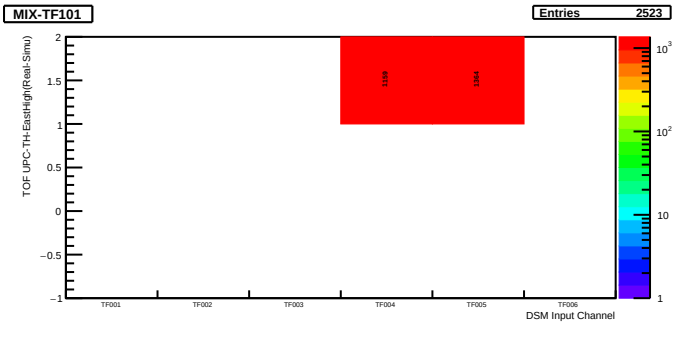

MIX-TF101
Entries 20664
TOF UPC-TH-WestHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

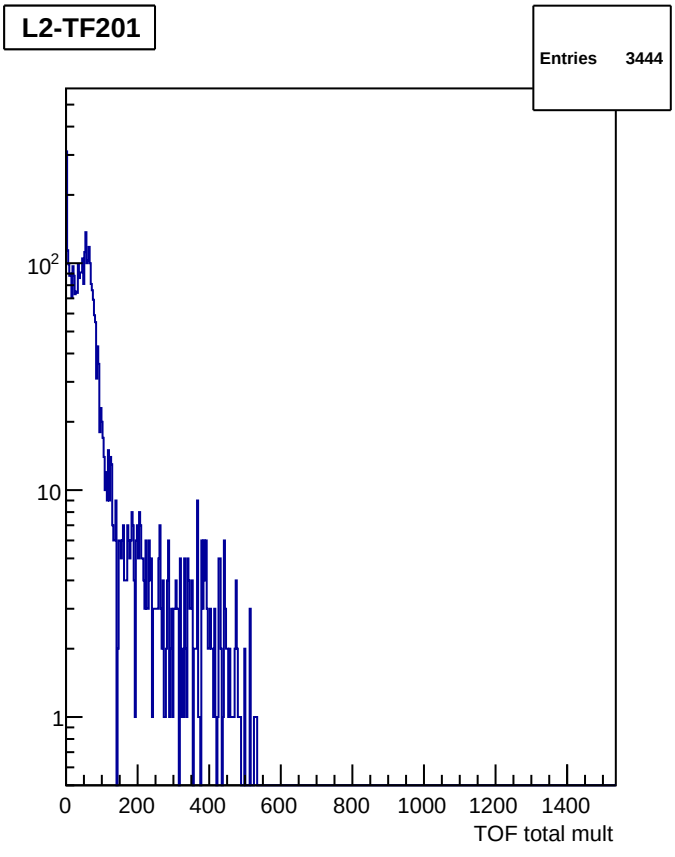

MIX-TF101
Entries 0
TOF UPC-TH-WestHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

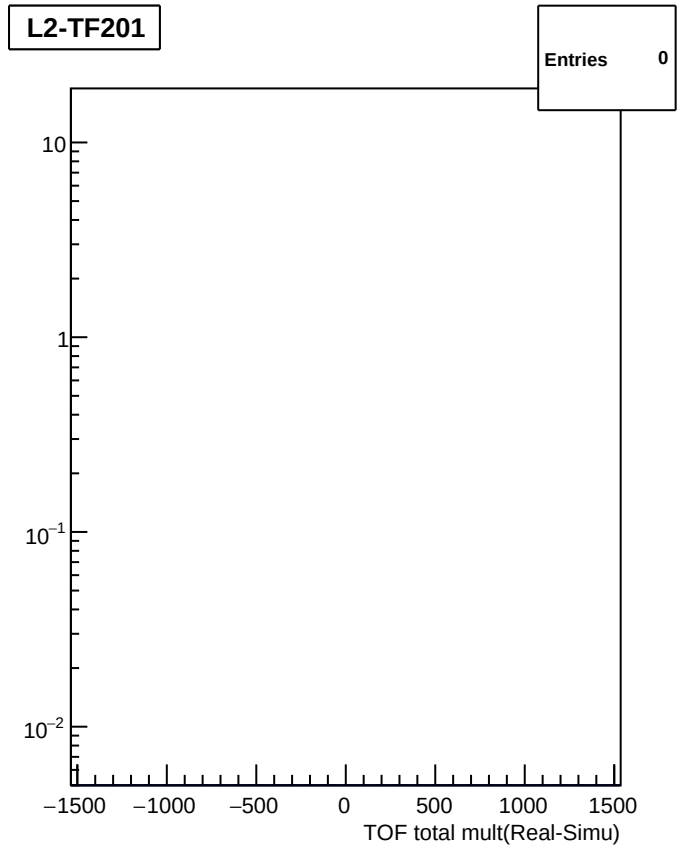

MIX-TF101
Entries 20664
TOF UPC-TH-EastLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

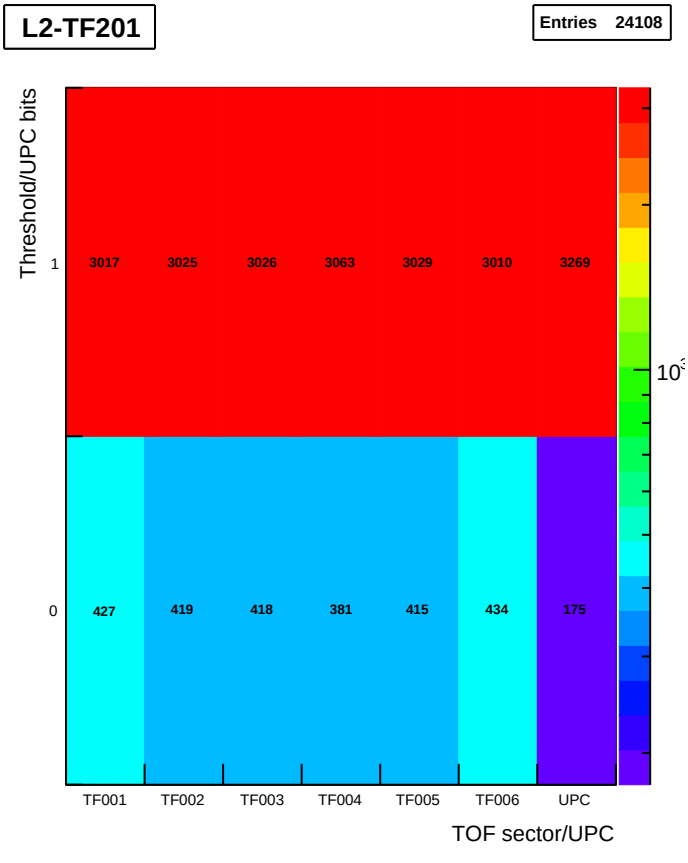

MIX-TF101
Entries 1174
TOF UPC-TH-EastLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

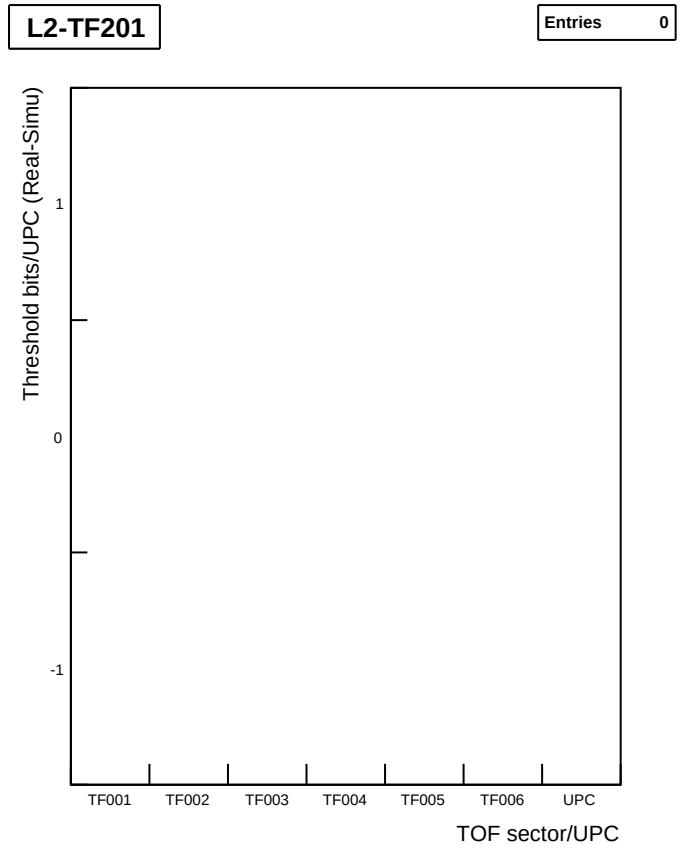

MIX-TF101
Entries 20664
TOF UPC-TH-EastHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

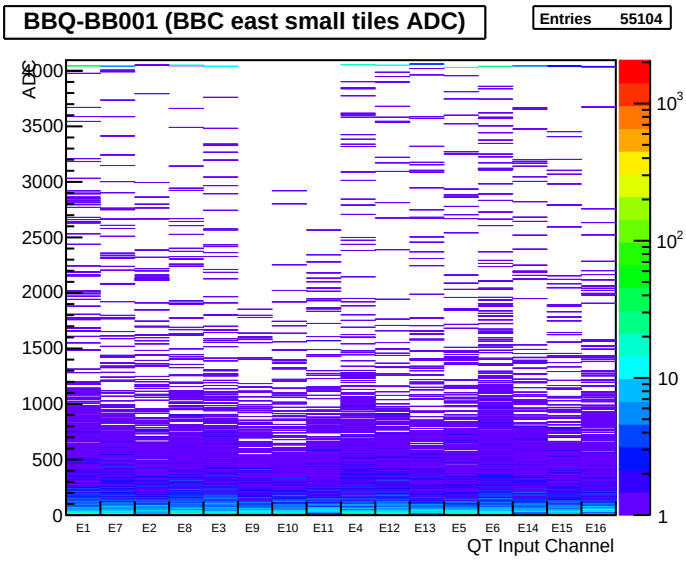

MIX-TF101
Entries 2523
TOF UPC-TH-EastHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

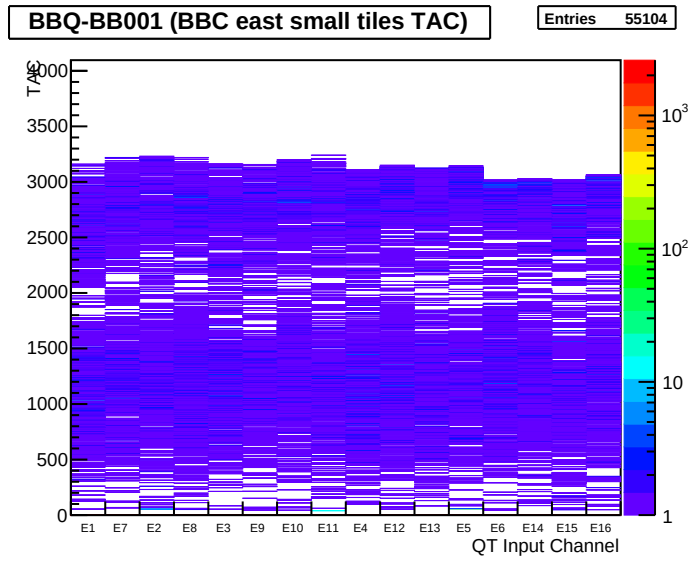

L2-TF201
Entries 3444
10^2
10
1
0
200
400
600
800
1000
1200
1400
TOF total mult

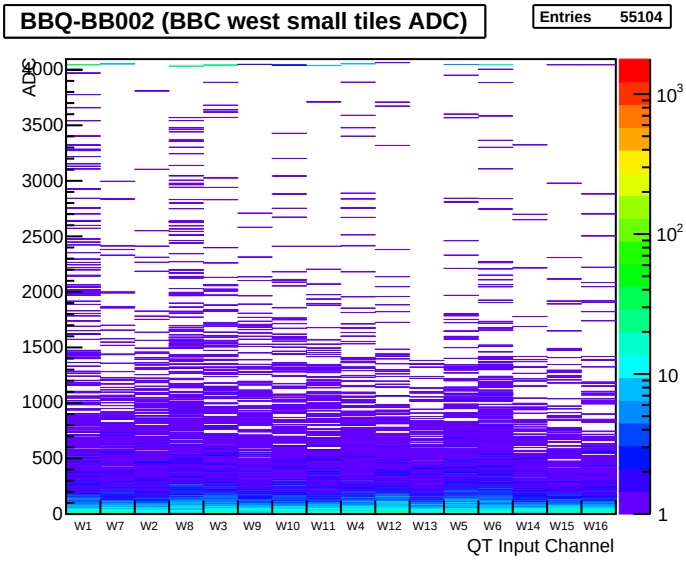

L2-TF201
Entries 0
10
1
10^{-1}
10^{-2}
−1500
−1000
−500
0
500
1000
1500
TOF total mult(Real-Simu)

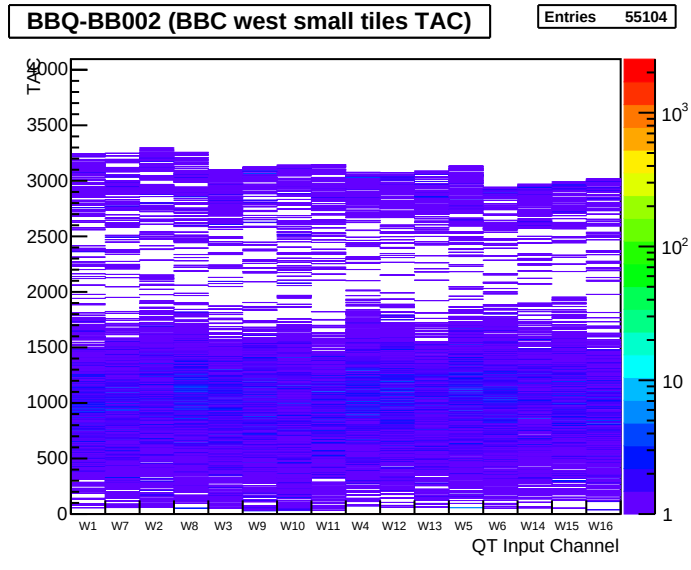

L2-TF201
Entries 24108
Threshold/UPC bits
1
0
3017
3025
3026
3063
3029
3010
3269
427
419
418
381
415
434
175
TF001
TF002
TF003
TF004
TF005
TF006
UPC
TOF sector/UPC

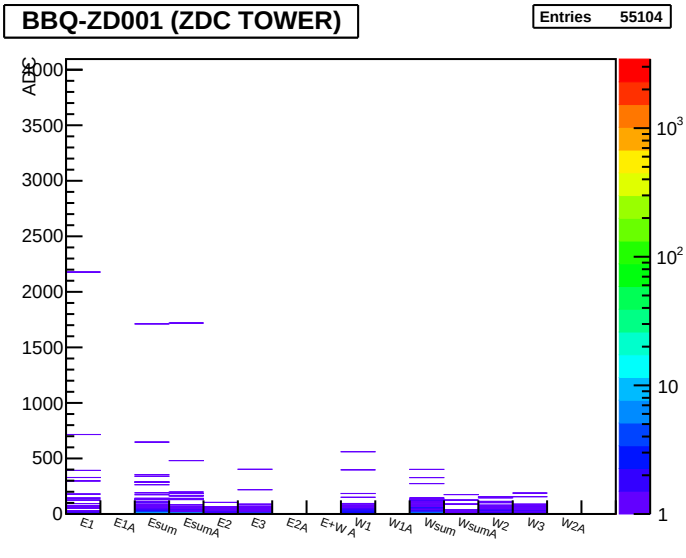

L2-TF201
Entries 0
Threshold bits/UPC (Real-Simu)
1
0
-1
TF001
TF002
TF003
TF004
TF005
TF006
UPC
TOF sector/UPC

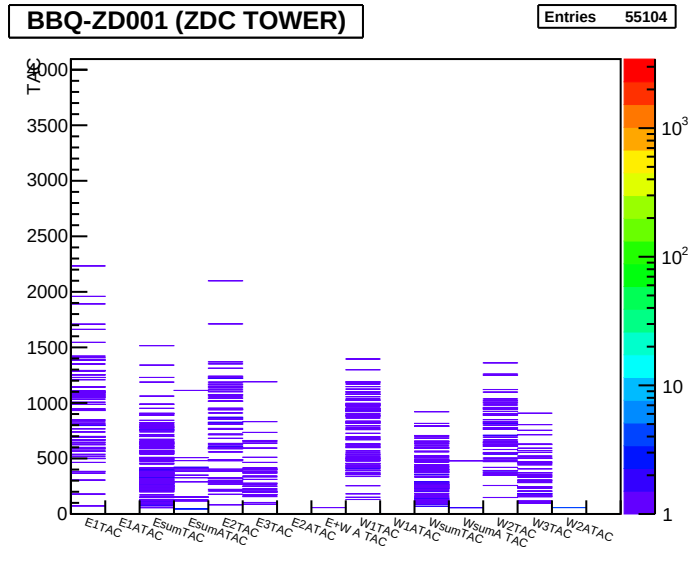

BBQ-BB001 (BBC east small tiles ADC)
Entries 55104
ADC
QT Input Channel
E1 E7 E2 E8 E3 E9 E10 E11 E4 E12 E13 E5 E6 E14 E15 E16

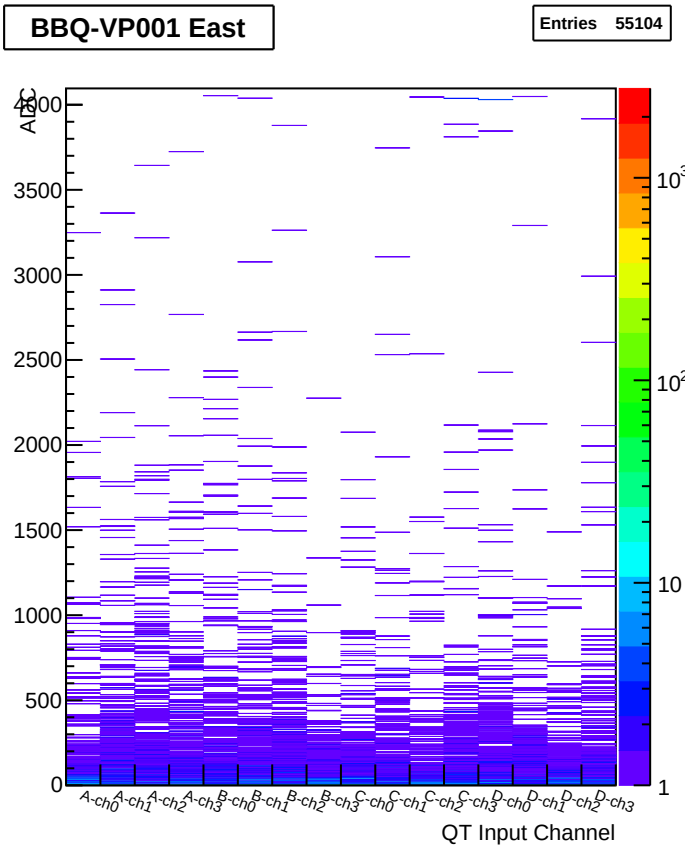

BBQ-BB001 (BBC east small tiles TAC)
Entries 55104
TAC
QT Input Channel
E1 E7 E2 E8 E3 E9 E10 E11 E4 E12 E13 E5 E6 E14 E15 E16

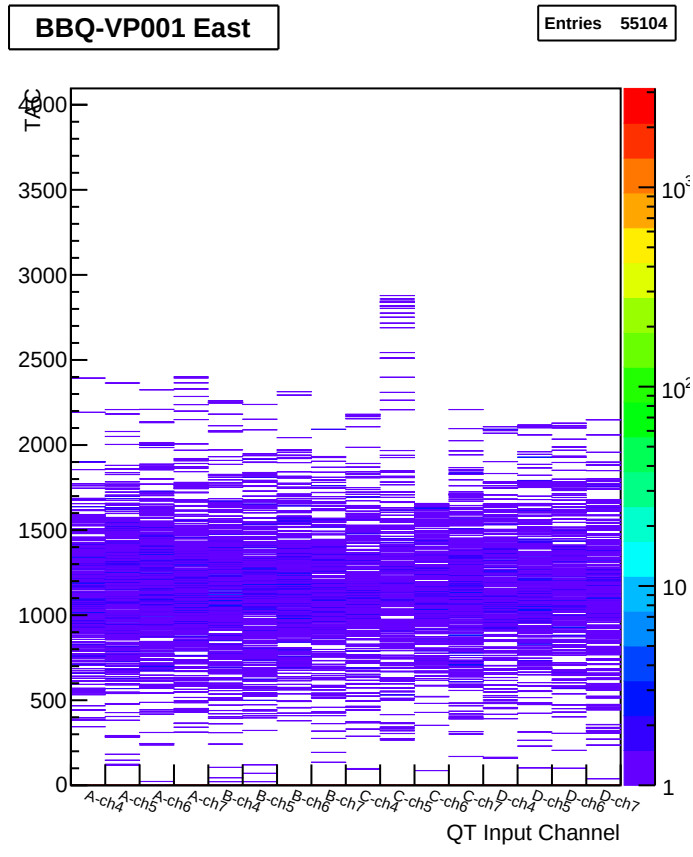

BBQ-BB002 (BBC west small tiles ADC)
Entries 55104
ADC
QT Input Channel
W1 W7 W2 W8 W3 W9 W10 W11 W4 W12 W13 W5 W6 W14 W15 W16

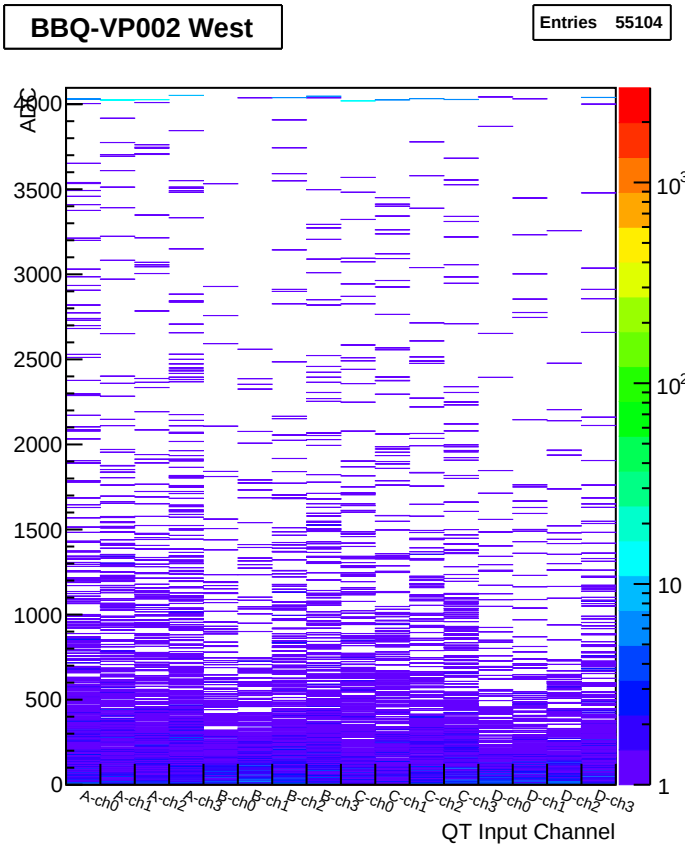

BBQ-BB002 (BBC west small tiles TAC)
Entries 55104
TAC
QT Input Channel
W1 W7 W2 W8 W3 W9 W10 W11 W4 W12 W13 W5 W6 W14 W15 W16

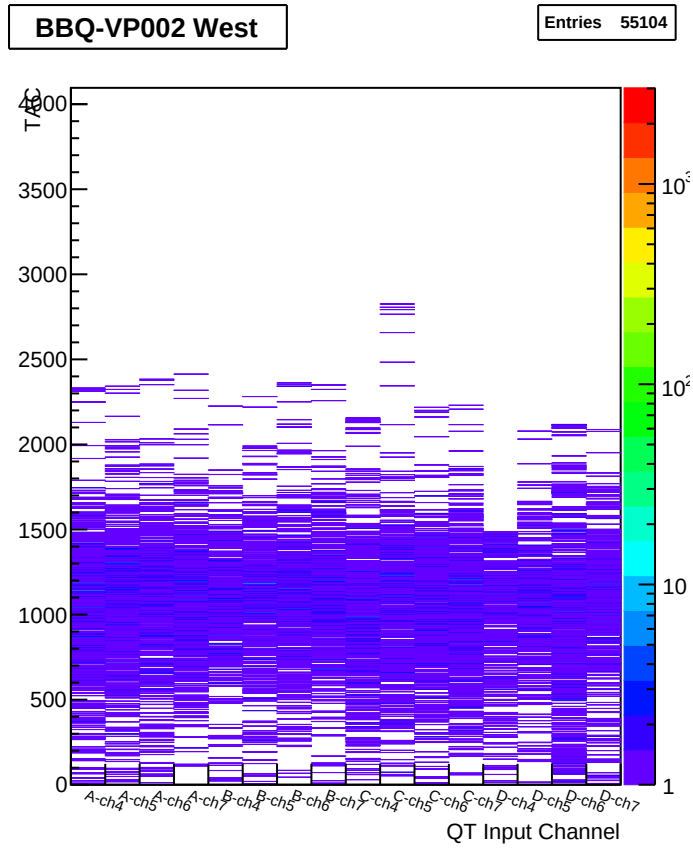

BBQ-ZD001 (ZDC TOWER)
Entries 55104
ADC
E1 E1A Esum EsumA E2 E3 E2A E+W A W1 W1A Wsum WsumA W2 W3 W2A

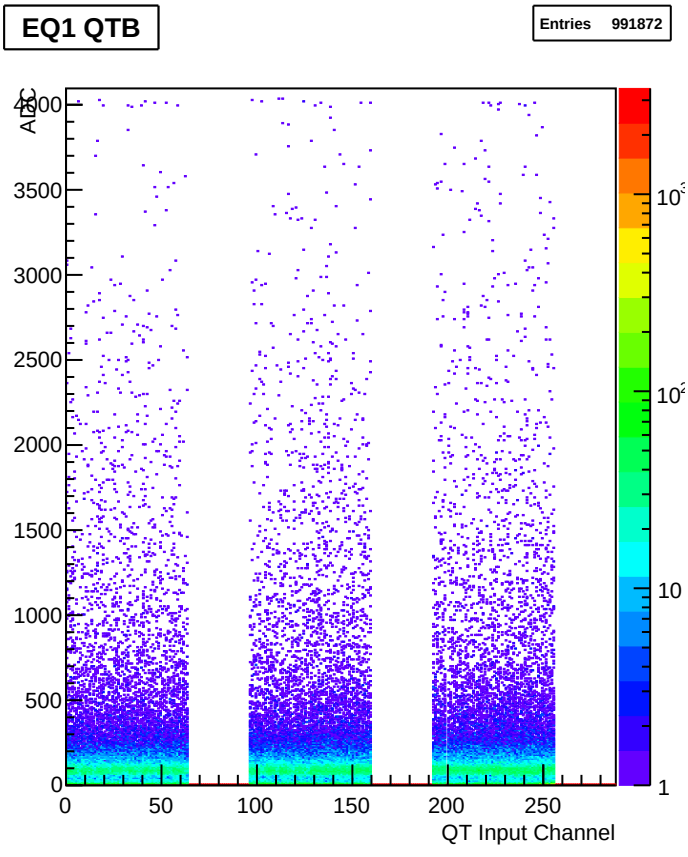

BBQ-ZD001 (ZDC TOWER)
Entries 55104
TAC
E1TAC E1ATAC EsumTAC EsumATAC E2TAC E3TAC E2ATAC E+W A TAC W1TAC W1ATAC WsumTAC WsumA TAC W2TAC W3TAC W2ATAC

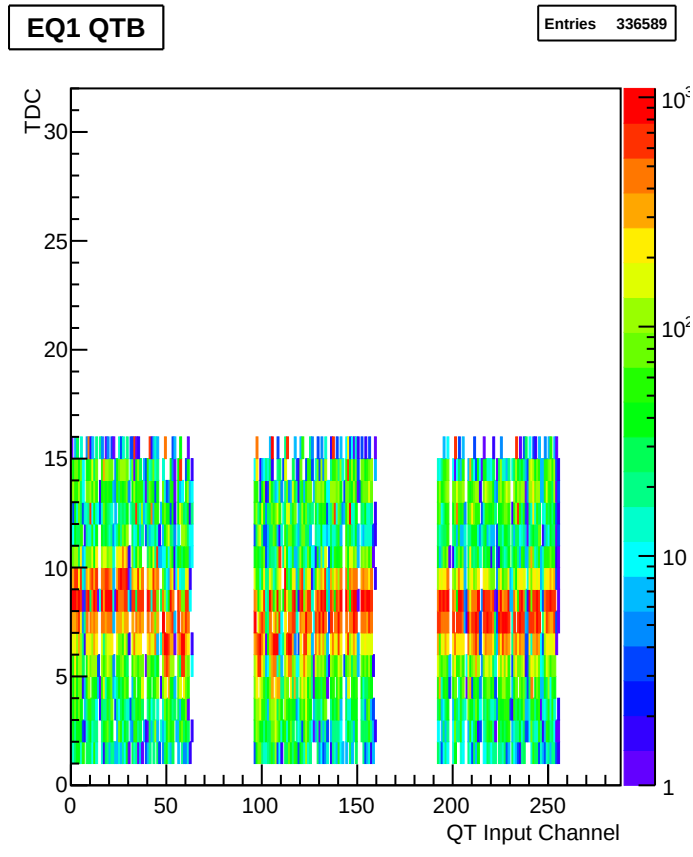

BBQ-VP001 East
Entries 55104
ADC
QT Input Channel

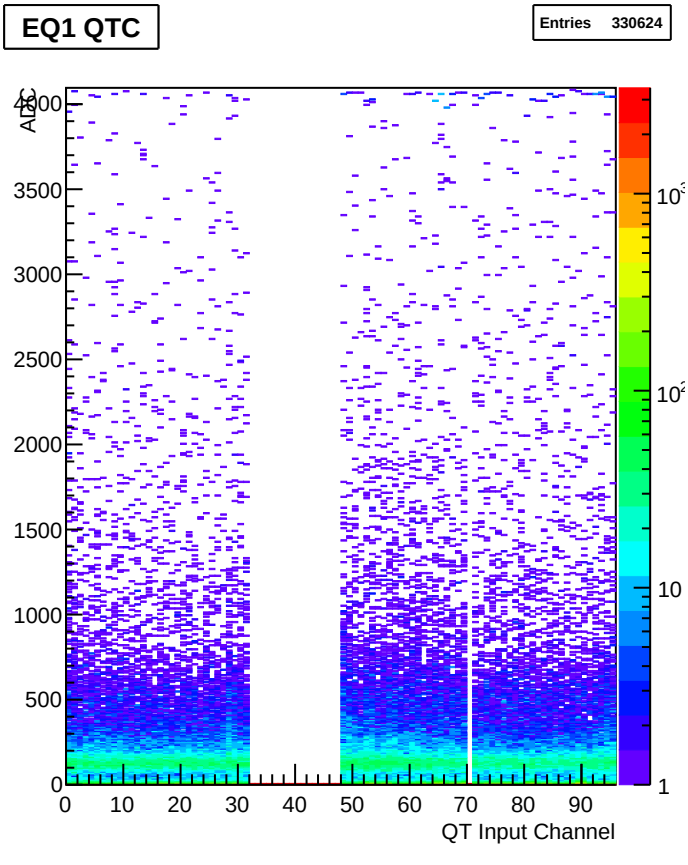

BBQ-VP001 East
Entries 55104
TADC
QT Input Channel

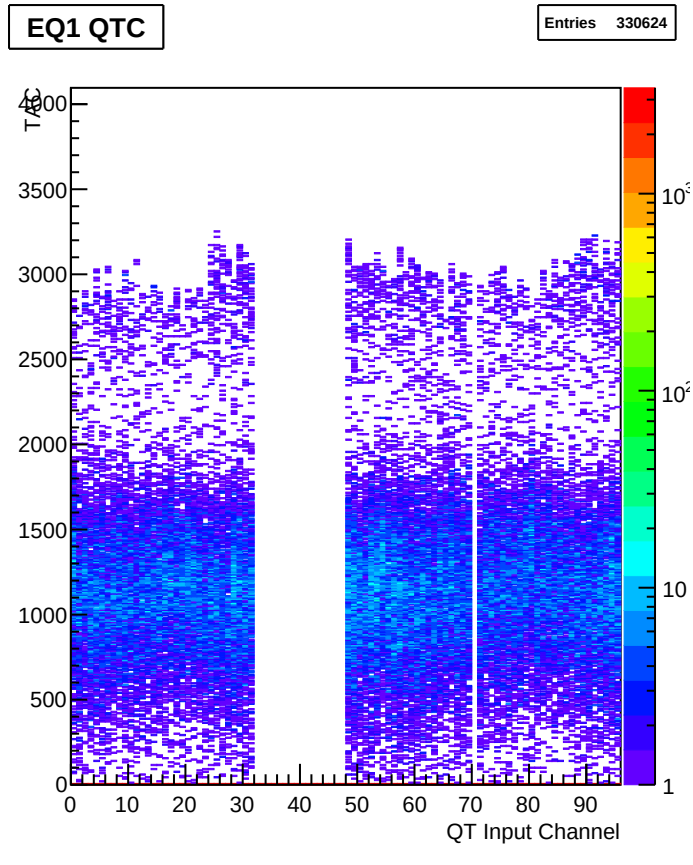

BBQ-VP002 West
Entries 55104
ADC
QT Input Channel

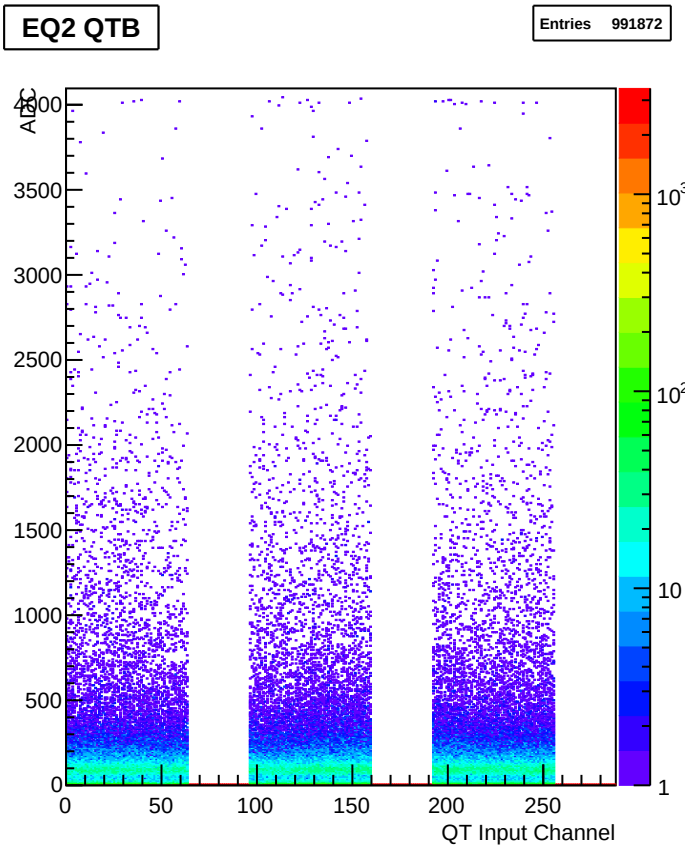

BBQ-VP002 West
Entries 55104
TADC
QT Input Channel

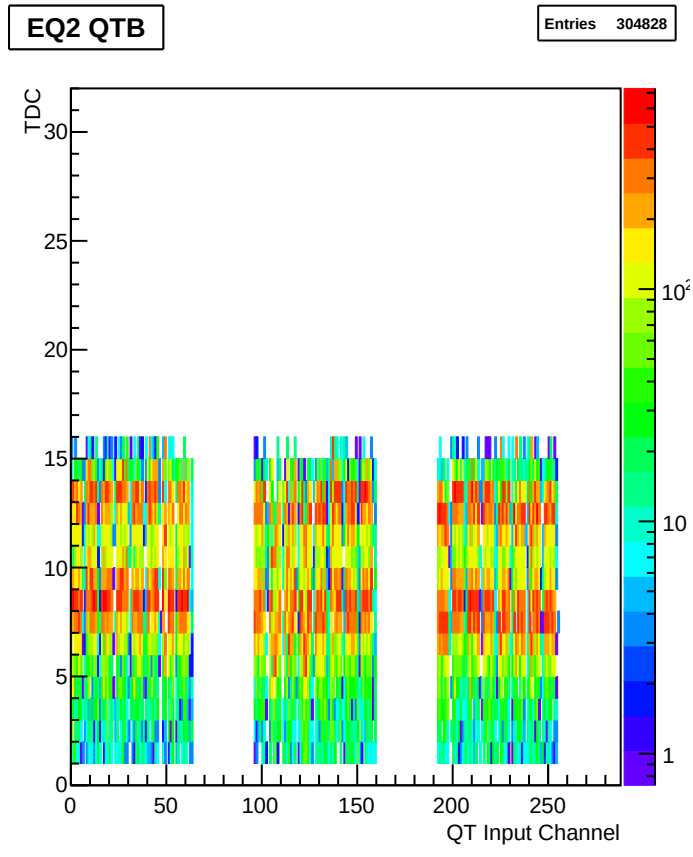

EQ1 QTB
Entries 991872
ADC
QT Input Channel

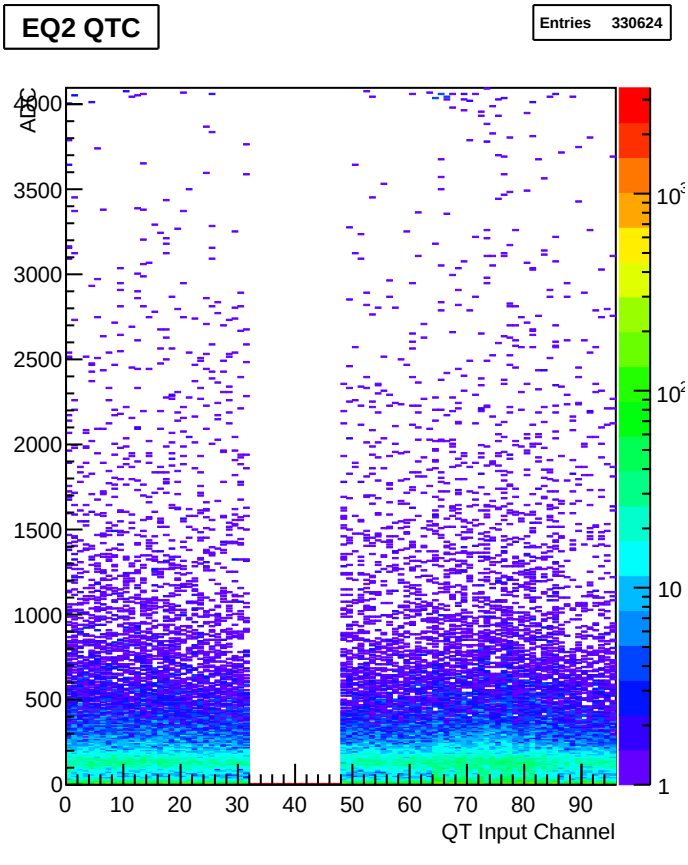

EQ1 QTB
Entries 336589
TDC
QT Input Channel

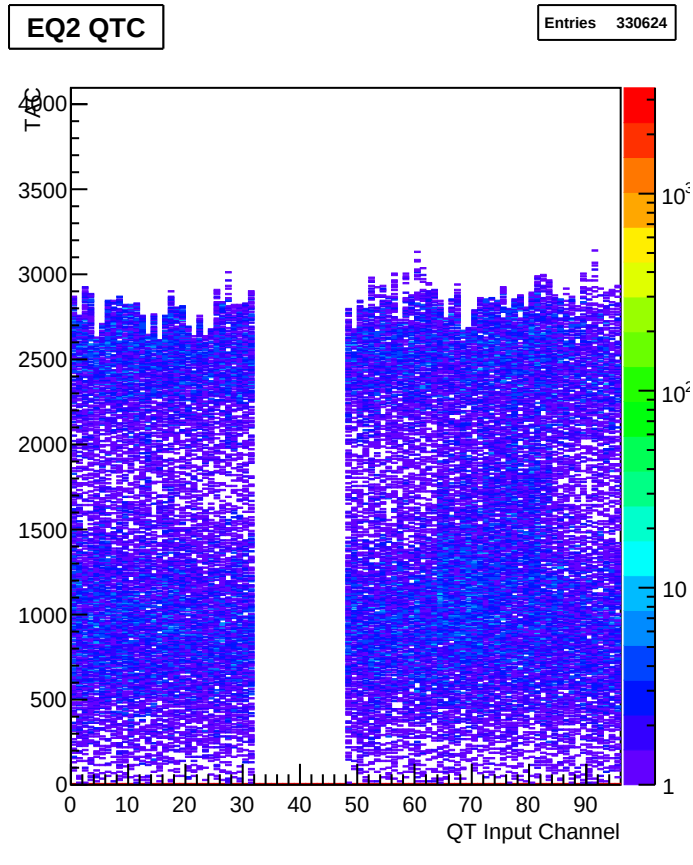

EQ1 QTC
Entries 330624
ADC
QT Input Channel

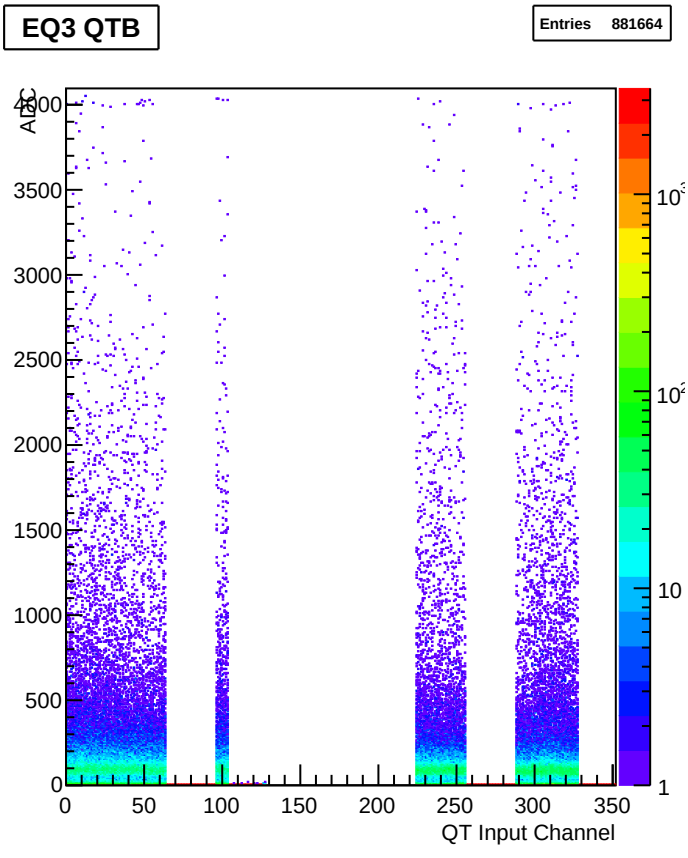

EQ1 QTC
Entries 330624
TADC
QT Input Channel

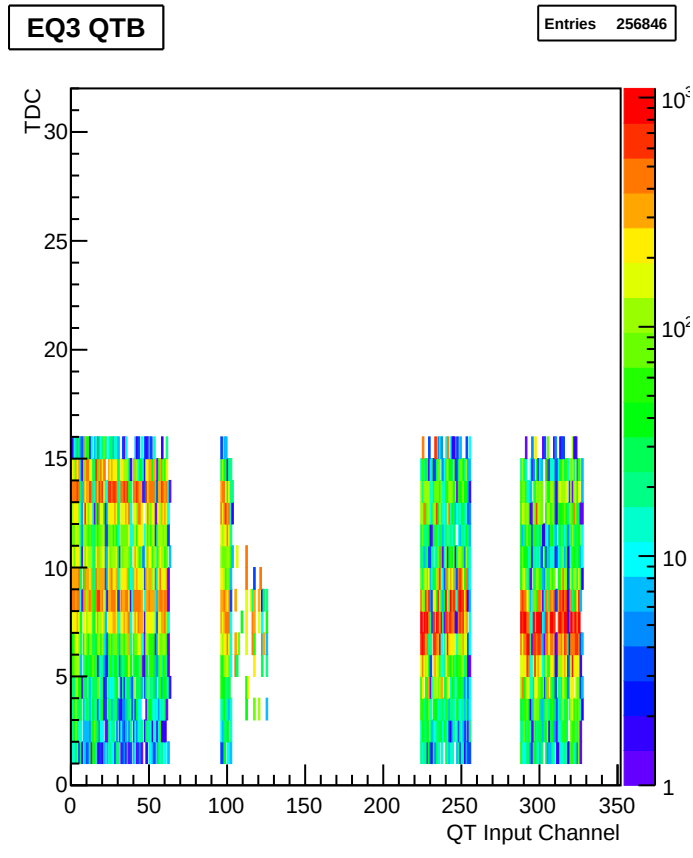

EQ2 QTB
Entries 991872
ADC
QT Input Channel

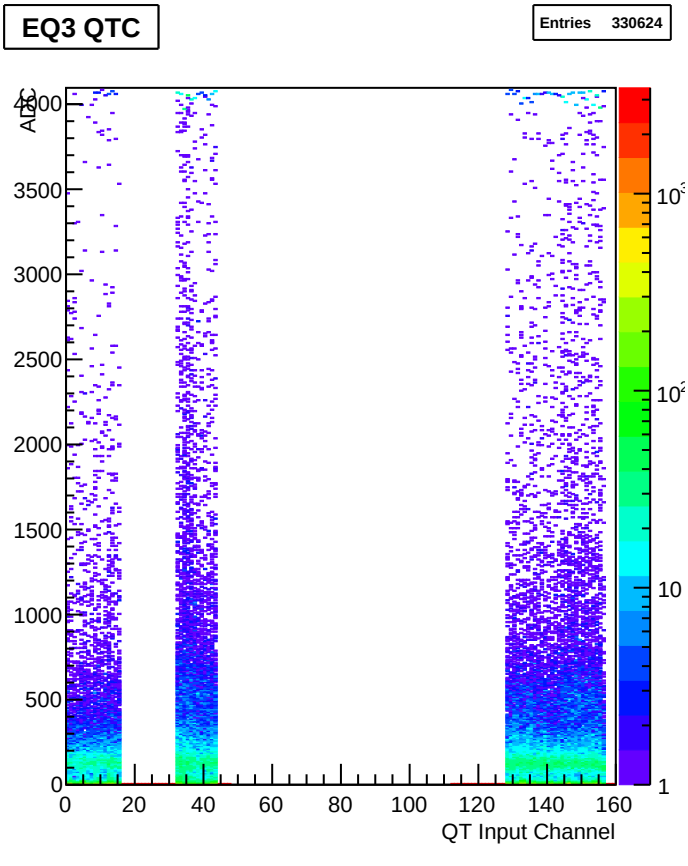

EQ2 QTB
Entries 304828
TDC
QT Input Channel

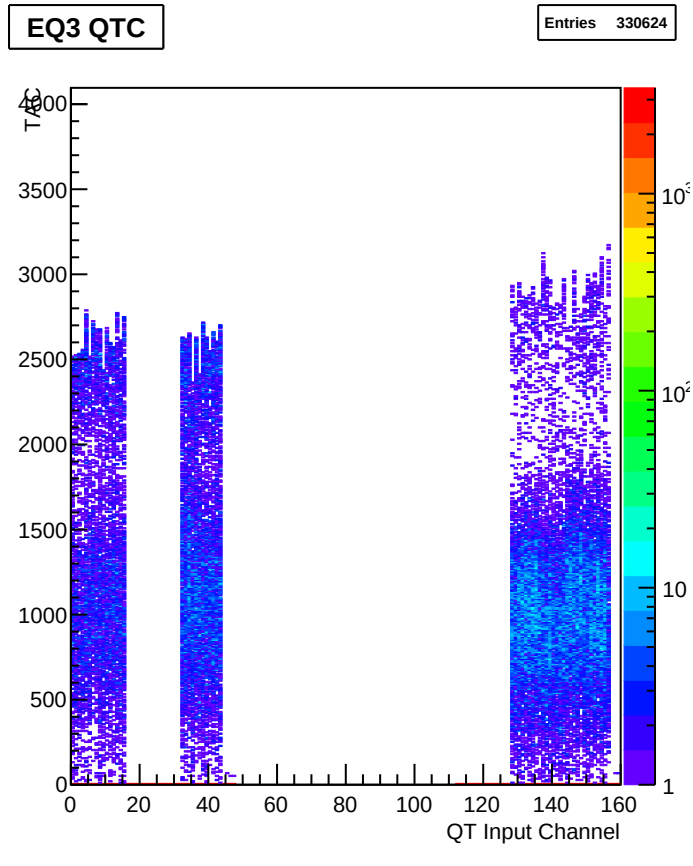

EQ2 QTC
Entries 330624
ADC
QT Input Channel

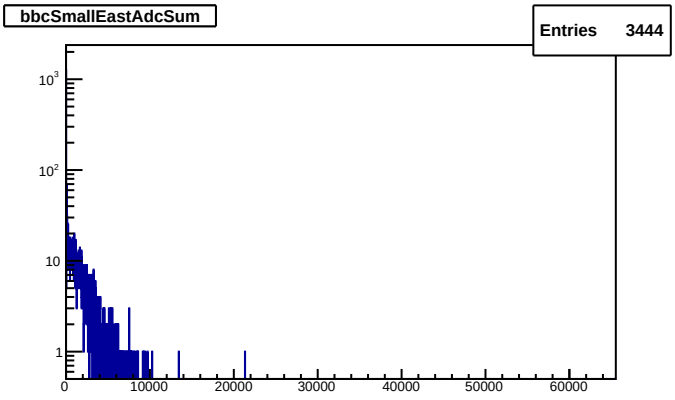

EQ2 QTC
Entries 330624
TADC
QT Input Channel

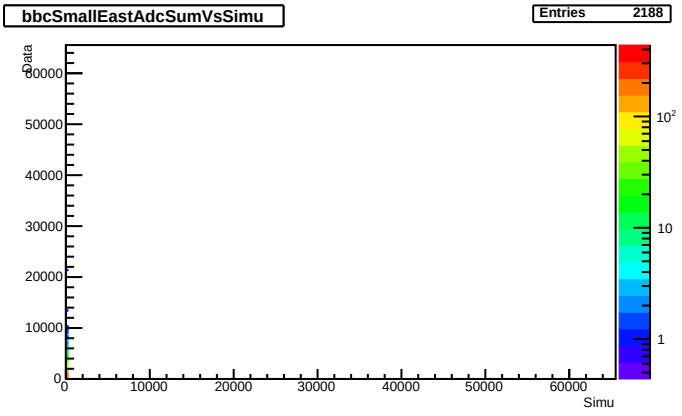

EQ3 QTB
Entries 881664
ADC
QT Input Channel

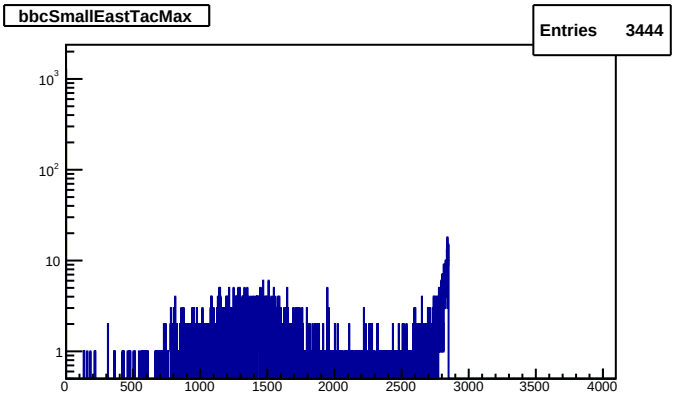

EQ3 QTB
Entries 256846
TDC
QT Input Channel

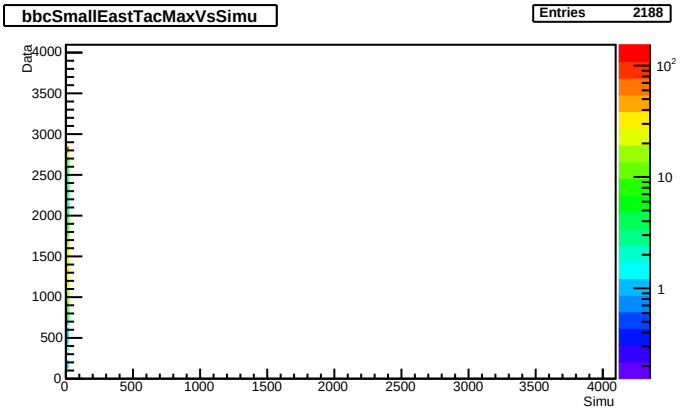

EQ3 QTC
Entries 330624
ADC
QT Input Channel

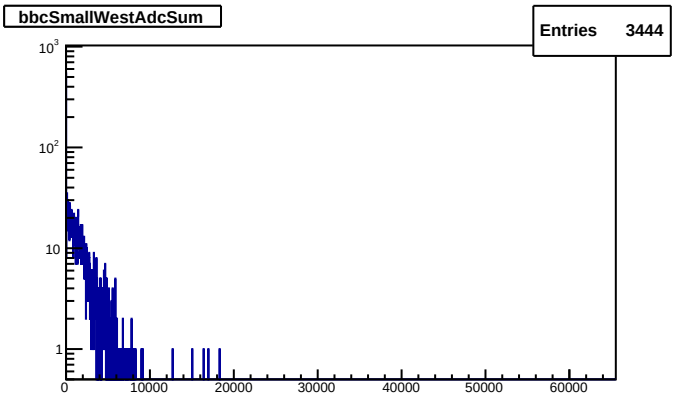

EQ3 QTC
Entries 330624
TADC
QT Input Channel

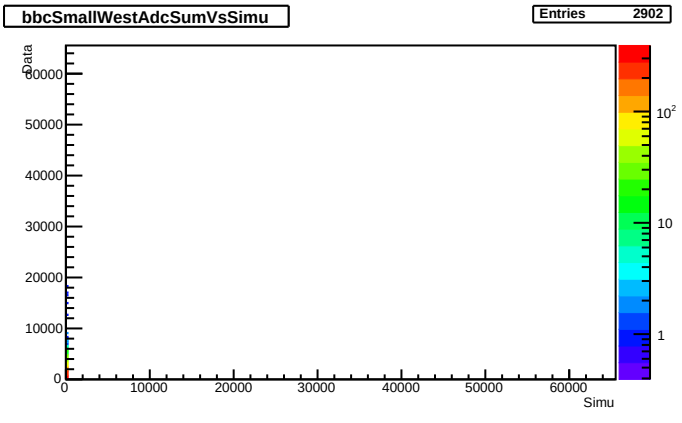

bbcSmallEastAdcSum
Entries 3444

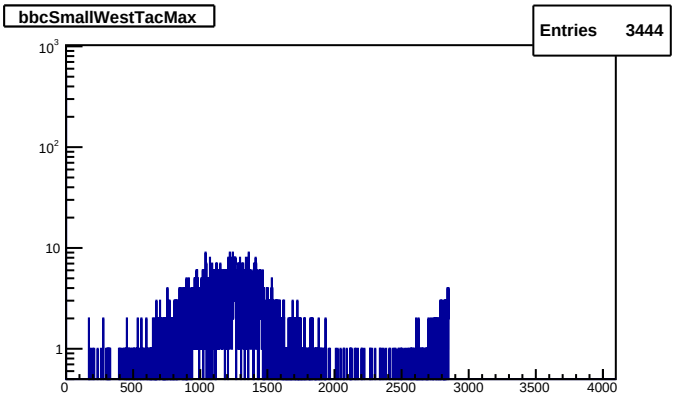

bbcSmallEastAdcSumVsSimu
Entries 2188
Data
Simu

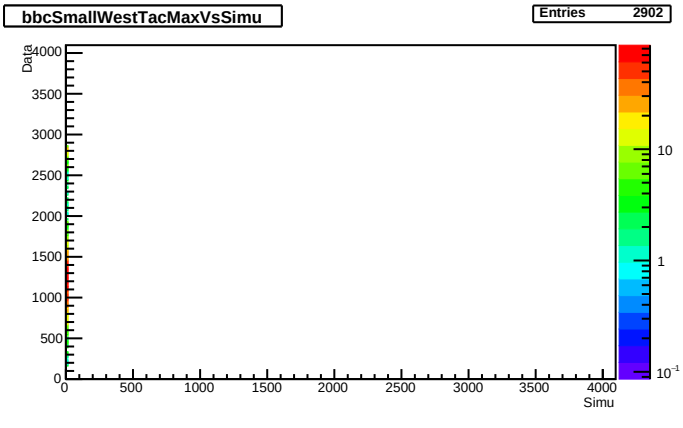

bbcSmallEastTacMax
Entries 3444

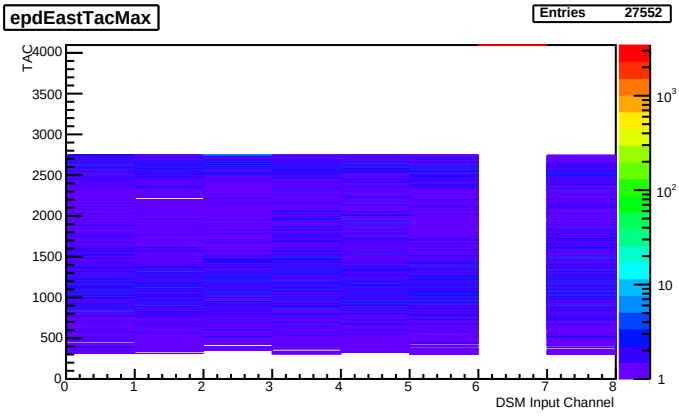

bbcSmallEastTacMaxVsSimu
Entries 2188
Data
Simu

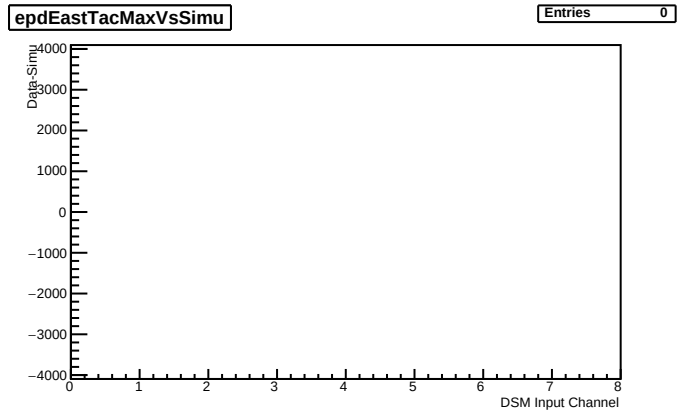

bbcSmallWestAdcSum
Entries 3444

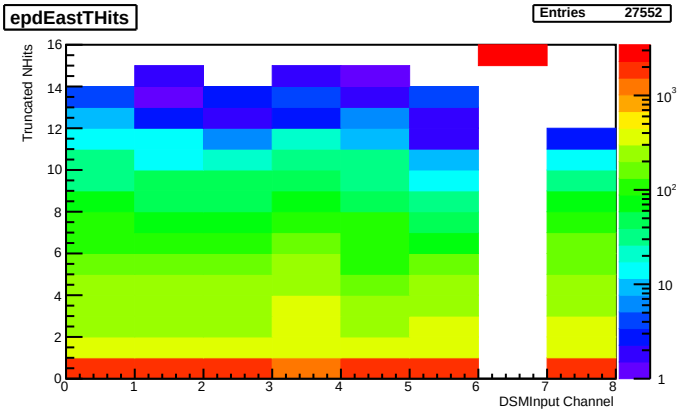

bbcSmallWestAdcSumVsSimu
Entries 2902
Data
Simu

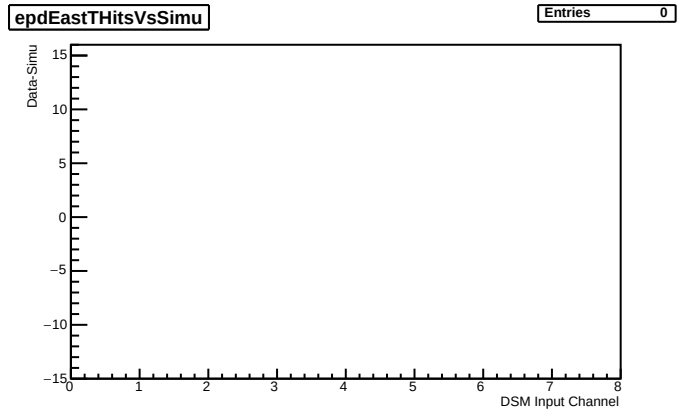

bbcSmallWestTacMax
Entries 3444


bbcSmallWestTacMaxVsSimu
Entries 2902
Data
Simu


epdEastTacMax
Entries 27552
TAC
DSM Input Channel


epdEastTacMaxVsSimu
Entries 0
Data-Simu
DSM Input Channel


epdEastTHits
Entries 27552
Truncated NHits
DSMInput Channel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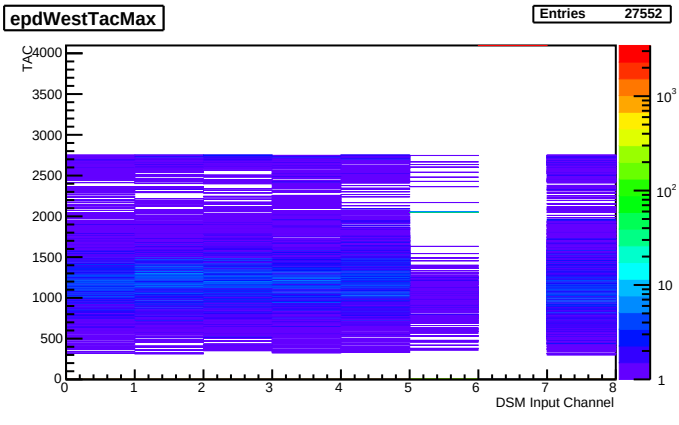
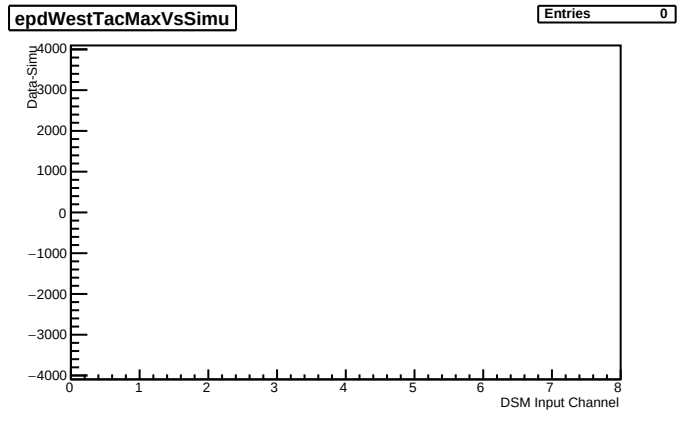
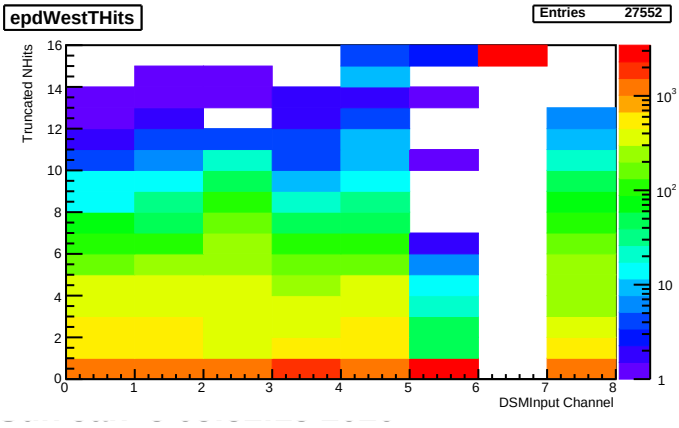
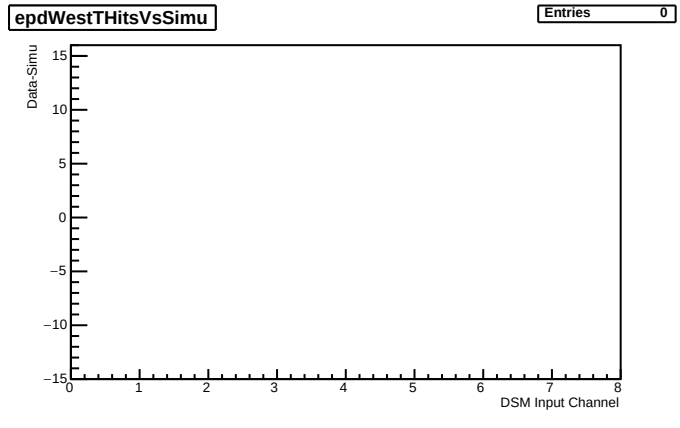
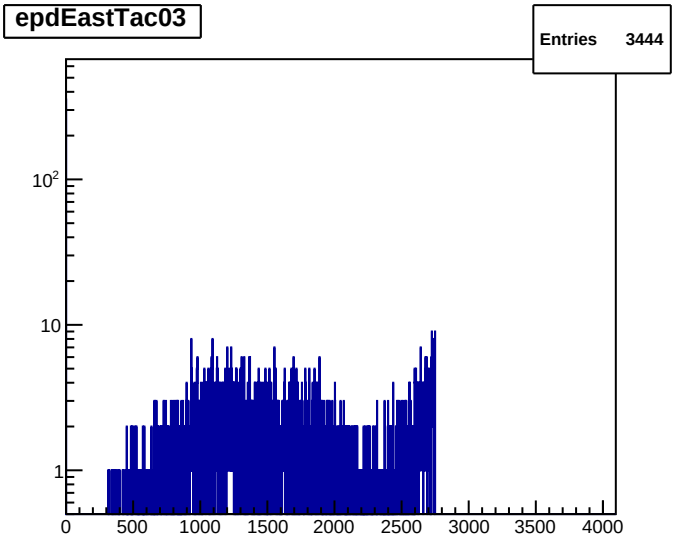
Data-Simu
DSM Input Channel

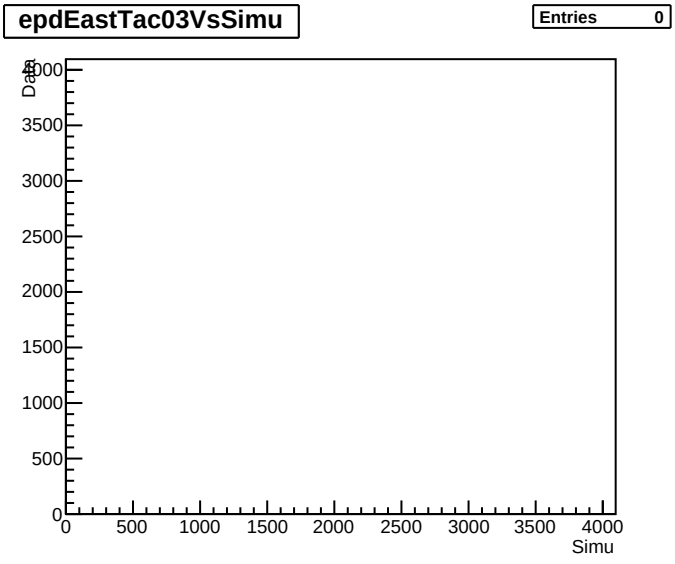

epdWestTacMax
Entries 27552
TAC
DSM Input Channel

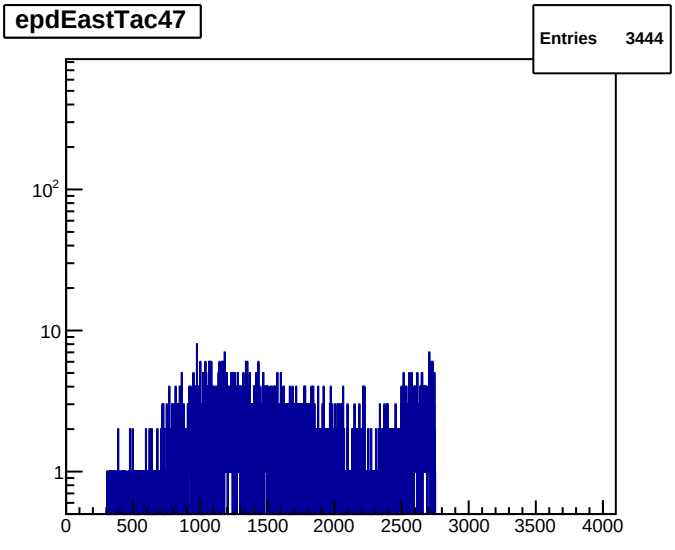

epdWestTacMaxVsSimu
Entries 0
Data-Simu
DSM Input Channel

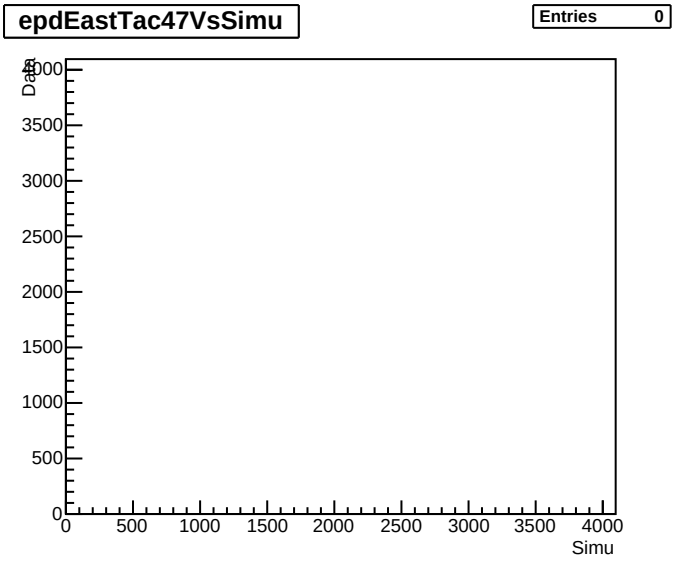

epdWestTHits
Entries 27552
Truncated NHits
DSMInput Channel

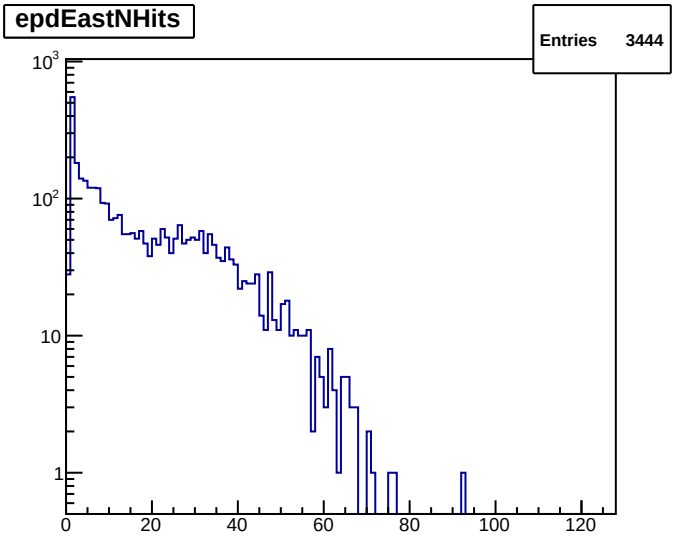

epdWestTHitsVsSimu
Entries 0
Data-Simu
DSM Input Channel

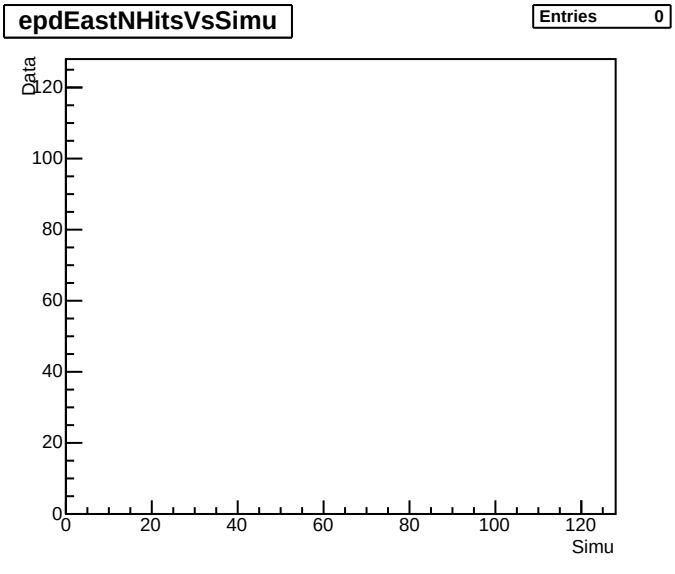

epdEastTac03
Entries 3444

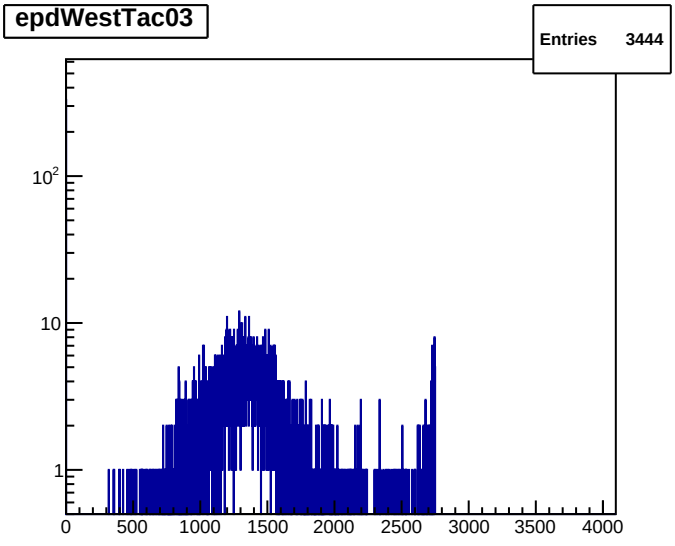

epdEastTac03VsSimu
Entries 0
Data
Simu

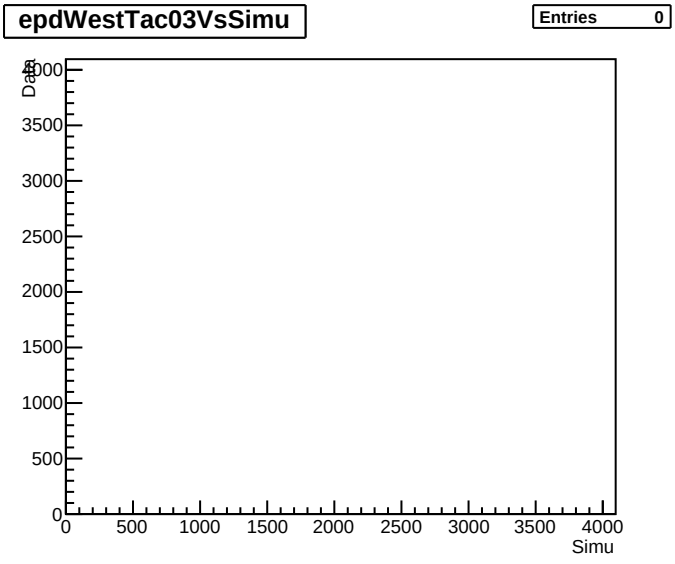

epdEastTac47
Entries 3444


epdEastTac47VsSimu
Entries 0
Data
Simu


epdEastNHits
Entries 3444


epdEastNHitsVsSimu
Entries 0
Data
Simu


epdWestTac03
Entries 3444
10^2
10
1
0 500 1000 1500 2000 2500 3000 3500 4000


epdWestTac03VsSimu
Entries 0
Data
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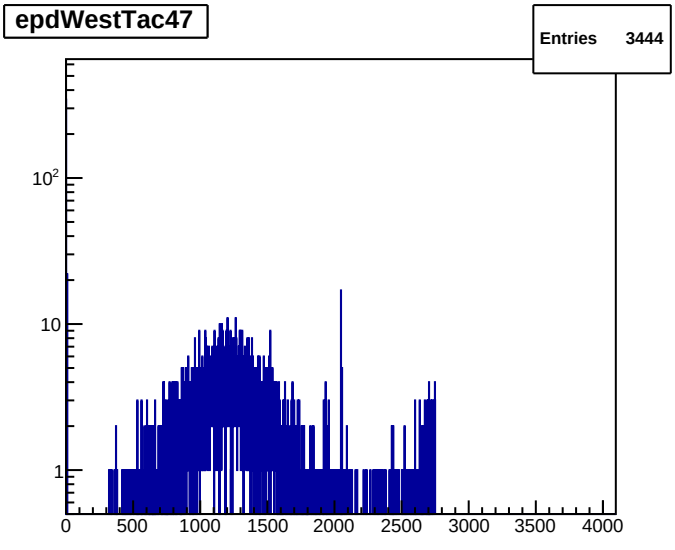
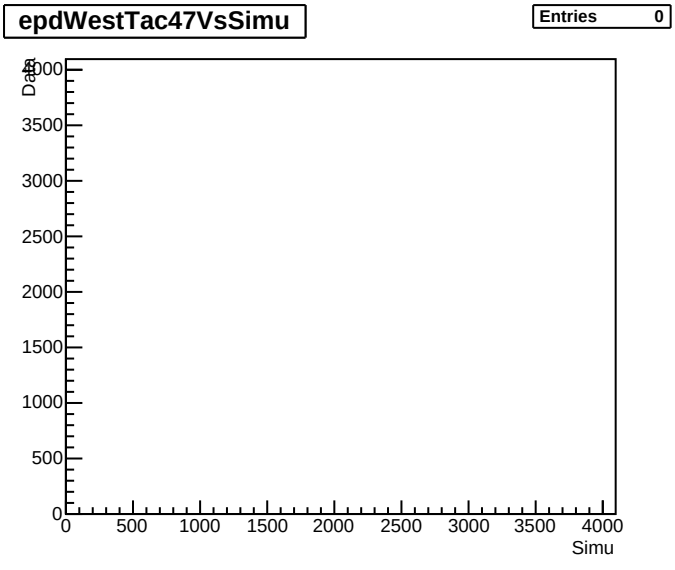
0 500 1000 1500 2000 2500 3000 3500 4000
Simu


epdWestTac47
Entries 3444
10^2
10
1
0 500 1000 1500 2000 2500 3000 3500 4000


epdWestTac47VsSimu
Entries 0
Data
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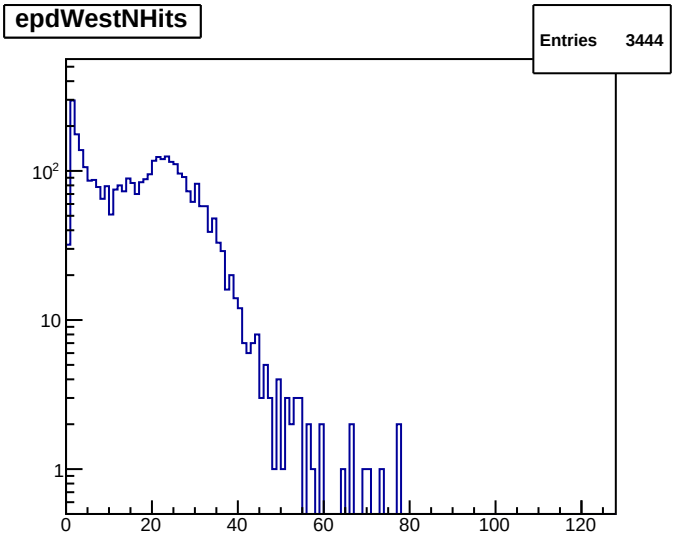
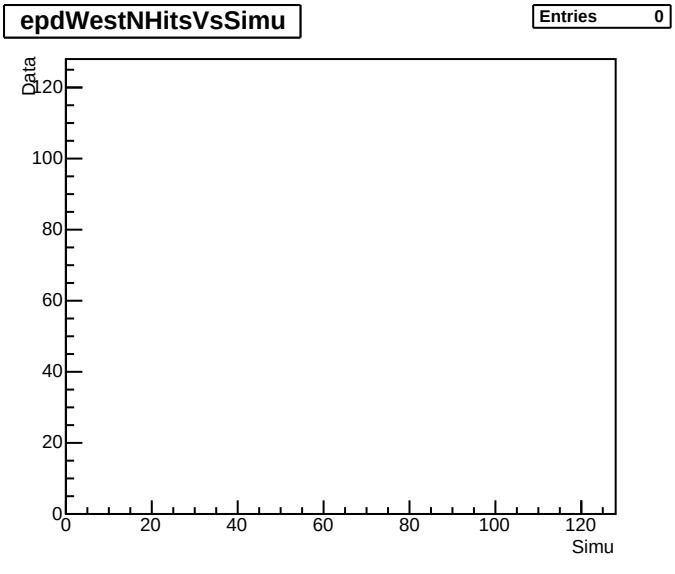
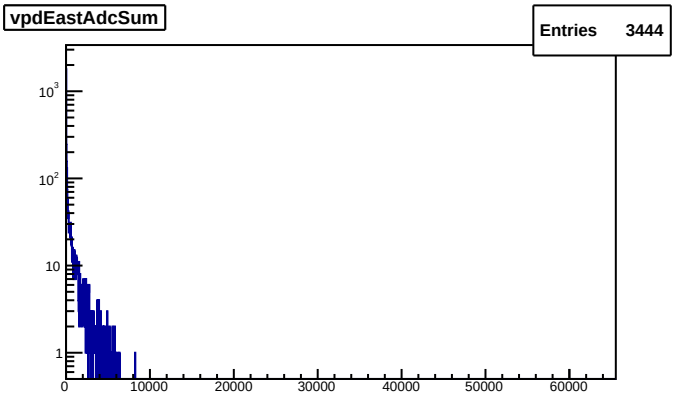
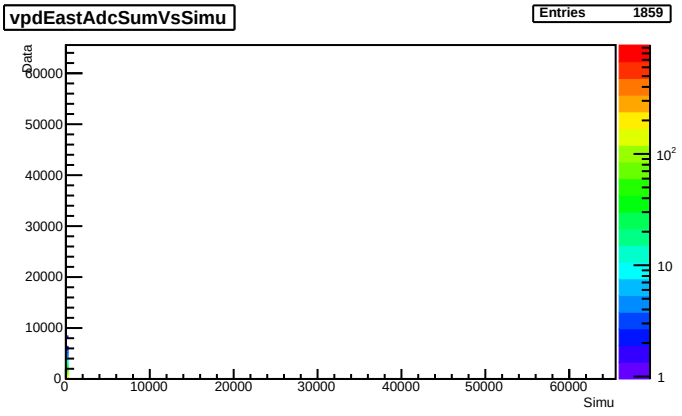
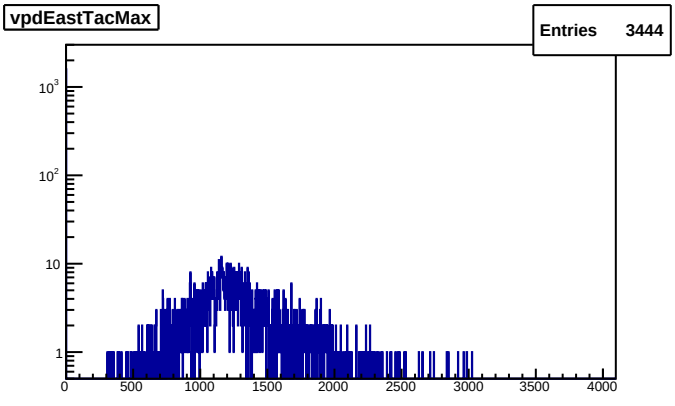
0 500 1000 1500 2000 2500 3000 3500 4000
Simu

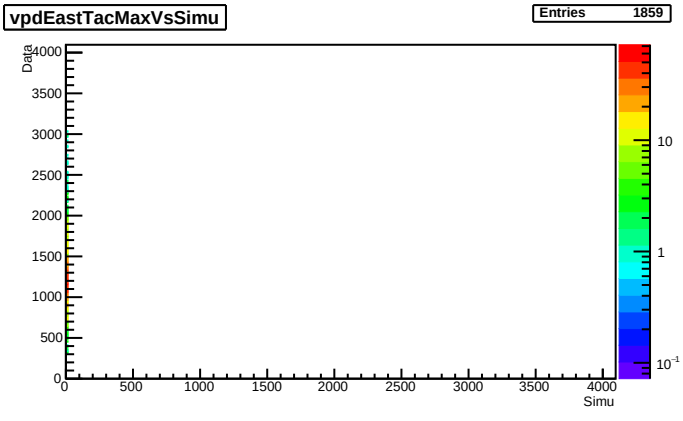

epdWestNHits
Entries 3444
10^2
10
1
0 20 40 60 80 100 120

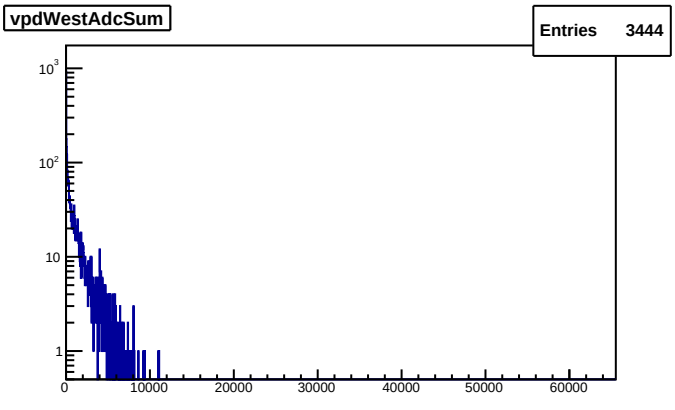

epdWestNHitsVsSimu
Entries 0
Data
120 100 80 60 40 20 0
0 20 40 60 80 100 120
Simu

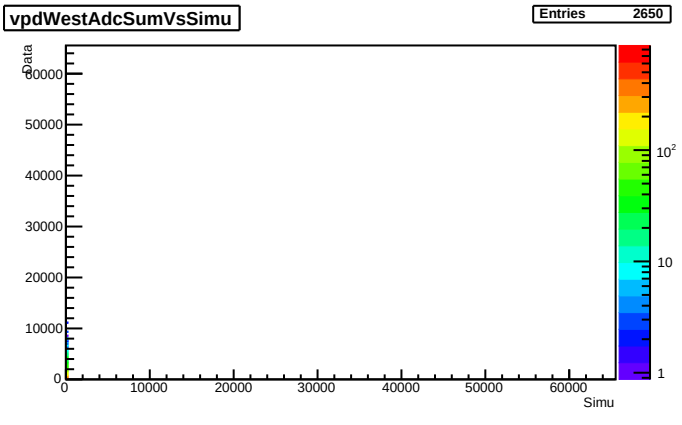

vpdEastAdcSum
Entries 3444

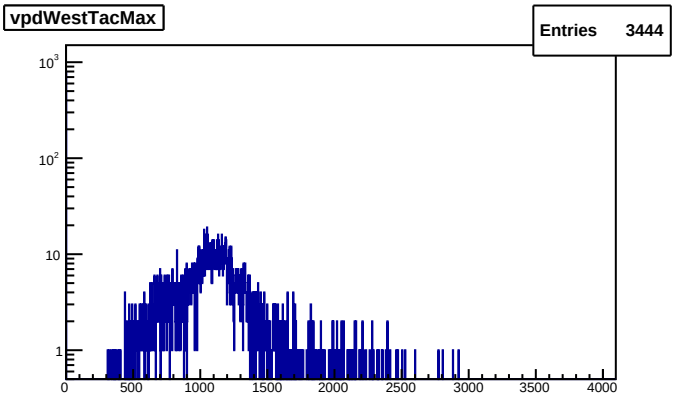

vpdEastAdcSumVsSimu
Entries 1859
Data
Simu

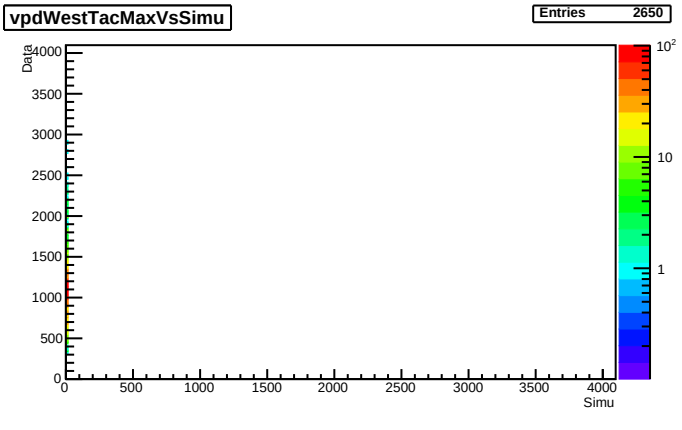

vpdEastTacMax
Entries 3444

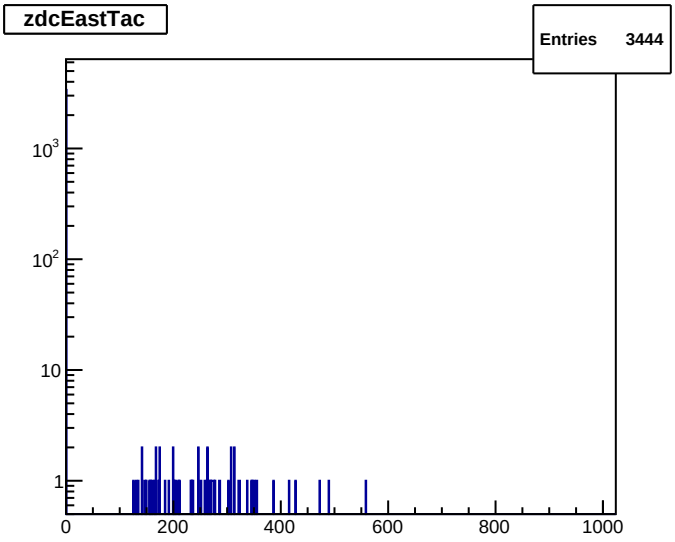

vpdEastTacMaxVsSimu
Entries 1859
Data
Simu

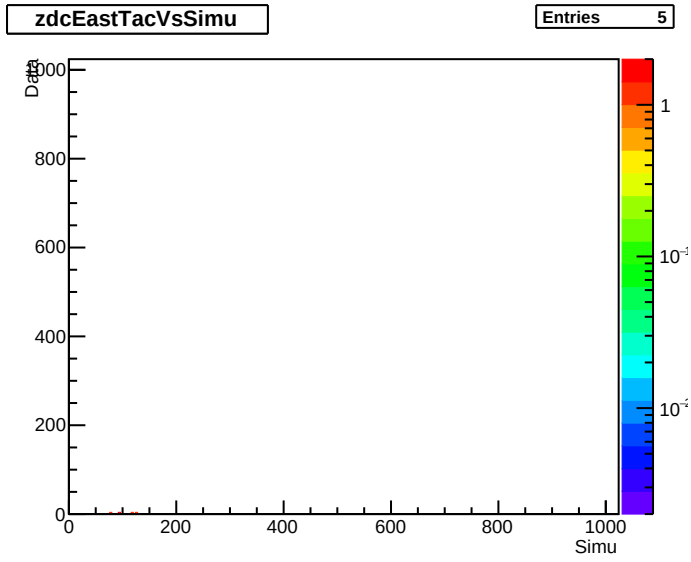

vpdWestAdcSum
Entries 3444

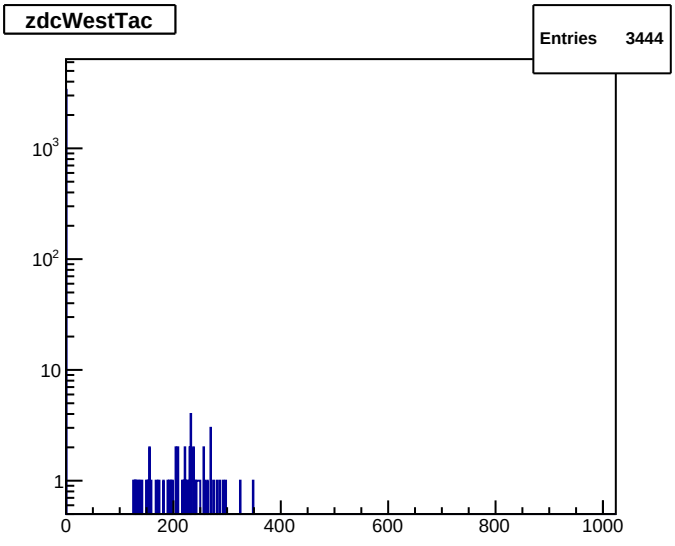

vpdWestAdcSumVsSimu
Entries 2650
Data
Simu

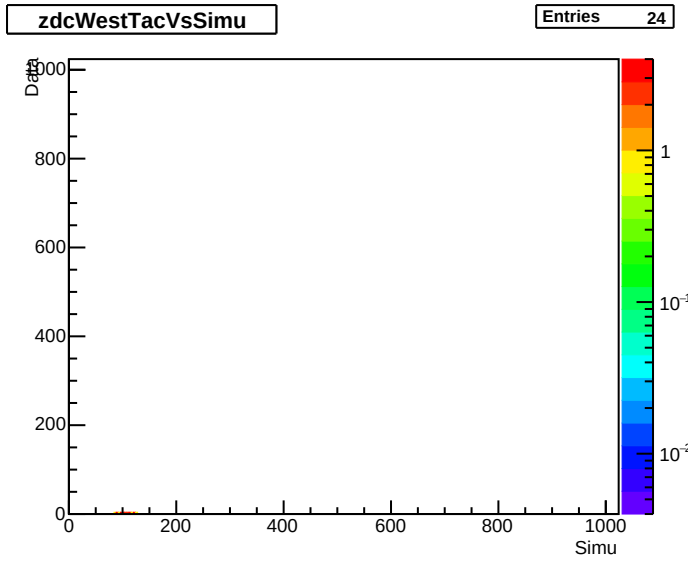

vpdWestTacMax
Entries 3444

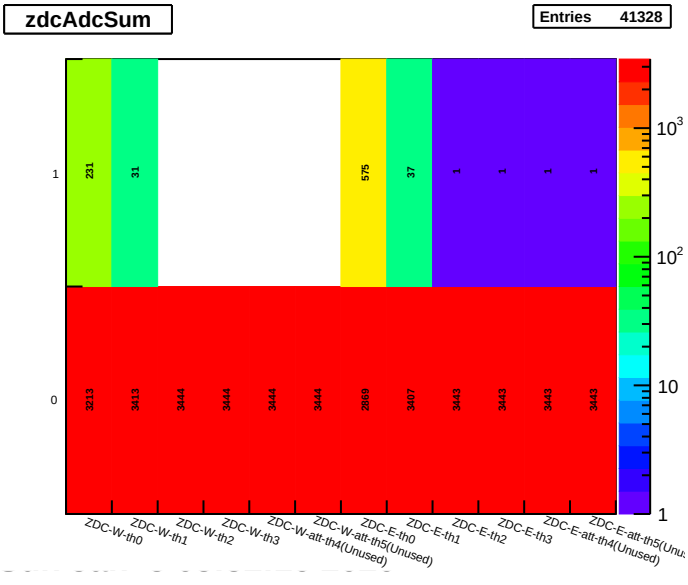

vpdWestTacMaxVsSimu
Entries 2650
Data
Simu

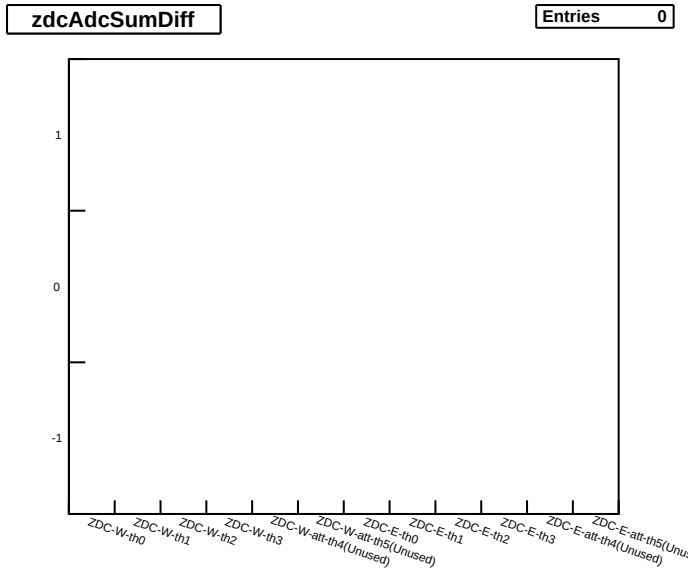

zdcEastTac
Entries 3444

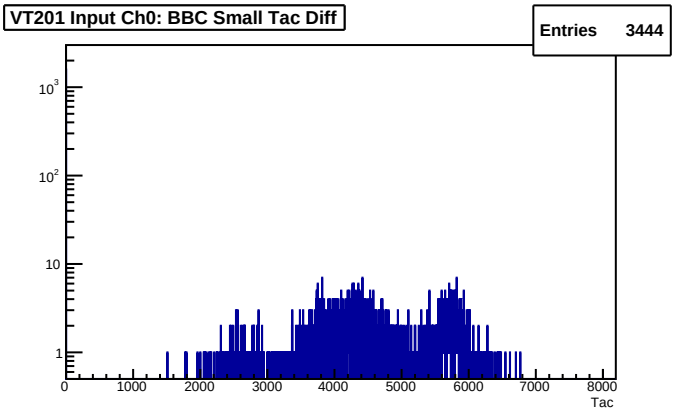

zdcEastTacVsSimu
Entries 5
Data
Simu

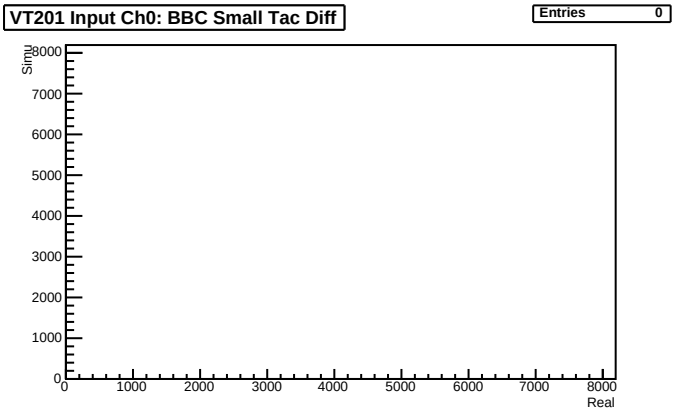

zdcWestTac
Entries 3444

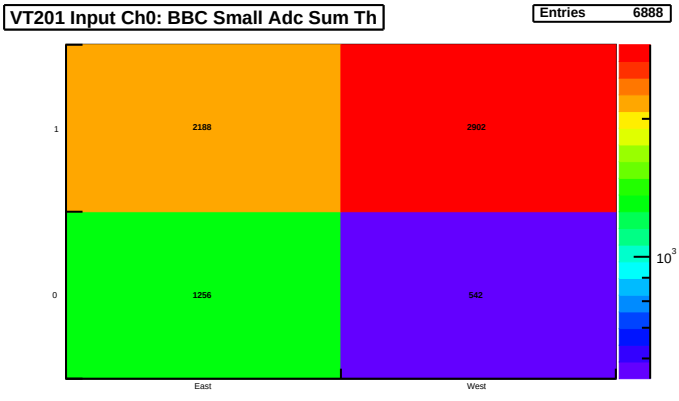

zdcWestTacVsSimu
Entries 24
Data
Simu

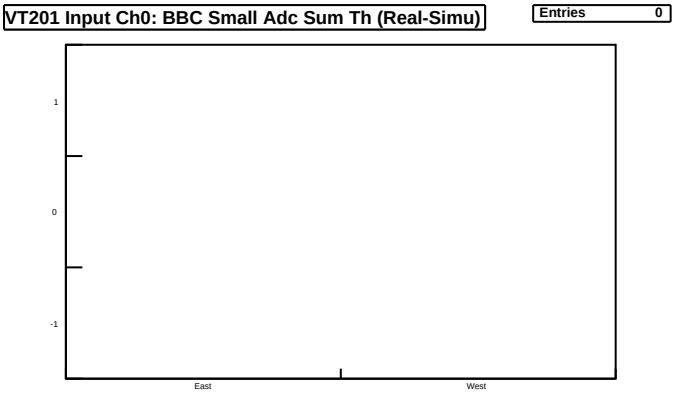

zdcAdcSum
Entries 41328
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)

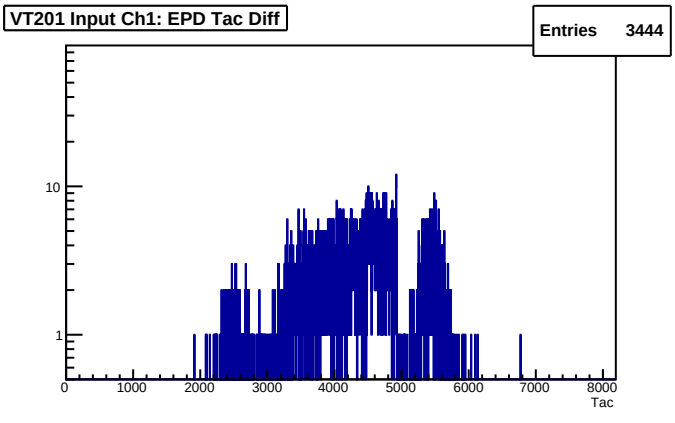

zdcAdcSumDiff
Entries 0
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)

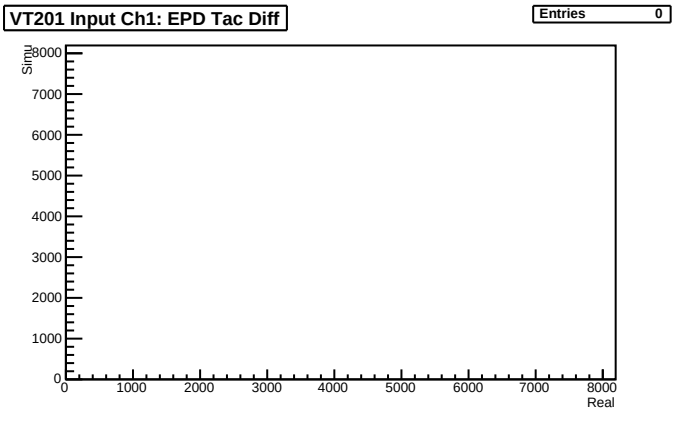

VT201 Input Ch0: BBC Small Tac Diff
Entries 3444
Tac

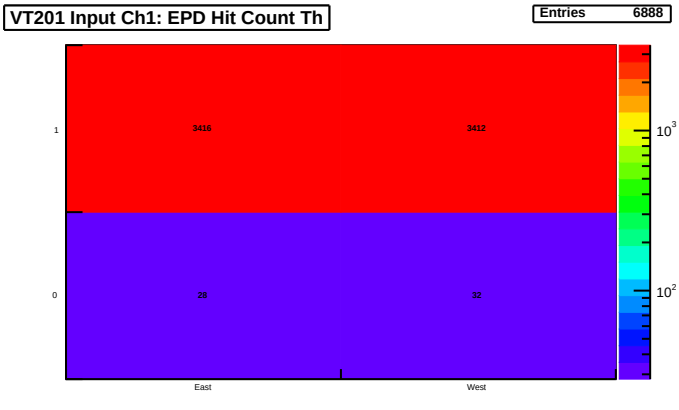

VT201 Input Ch0: BBC Small Tac Diff
Entries 0
Simu
Real

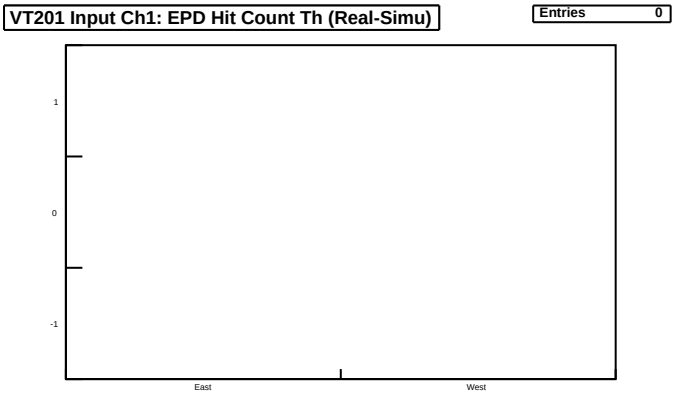

VT201 Input Ch0: BBC Small Adc Sum Th
Entries 6888
2188
2902
1256
542
East
West

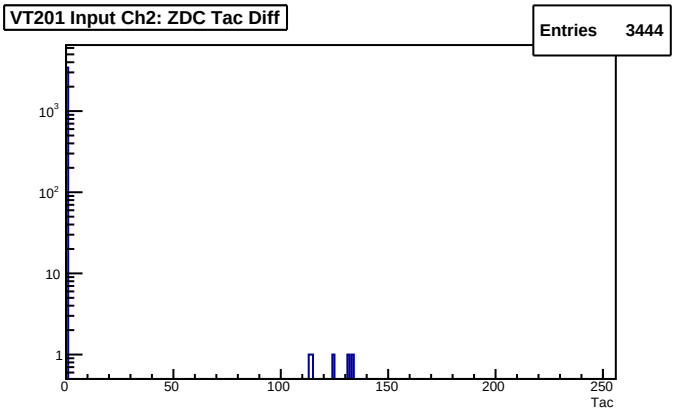

VT201 Input Ch0: BBC Small Adc Sum Th (Real-Simu)
Entries 0
East
West

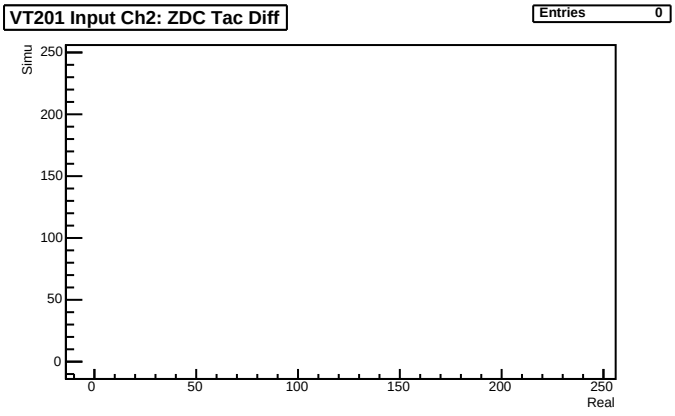

VT201 Input Ch1: EPD Tac Diff
Entries 3444
Tac

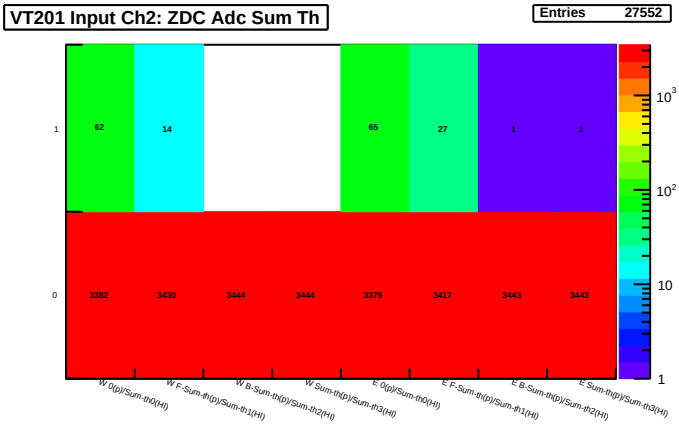

VT201 Input Ch1: EPD Tac Diff
Entries 0
Simu
Real

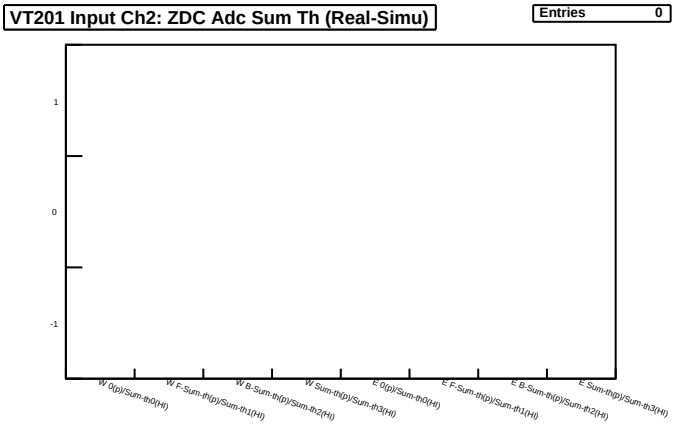

VT201 Input Ch1: EPD Hit Count Th
Entries 6888
3416
3412
28
32
East
West

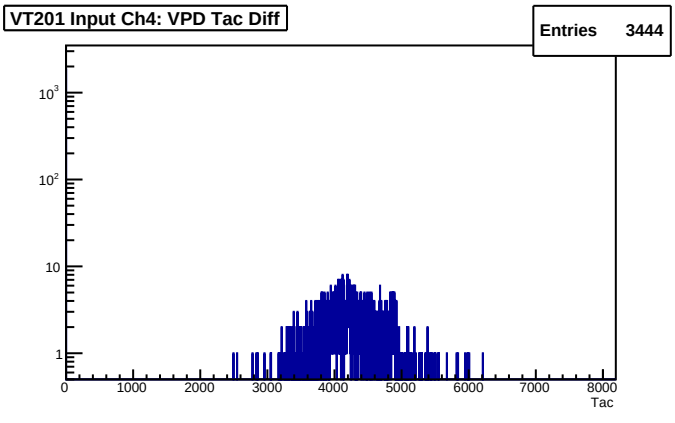

VT201 Input Ch1: EPD Hit Count Th (Real-Simu)
Entries 0
East
West

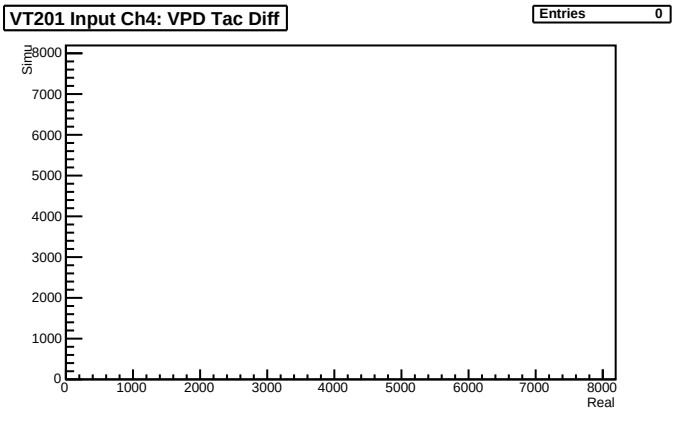

VT201 Input Ch2: ZDC Tac Diff
Entries 3444
Tac

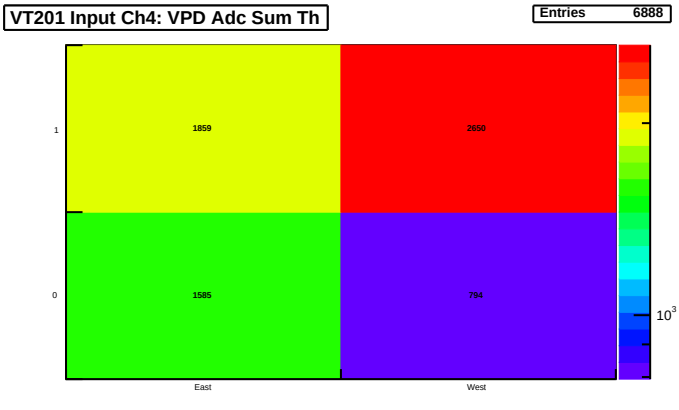

VT201 Input Ch2: ZDC Tac Diff
Entries 0
Simu
Real

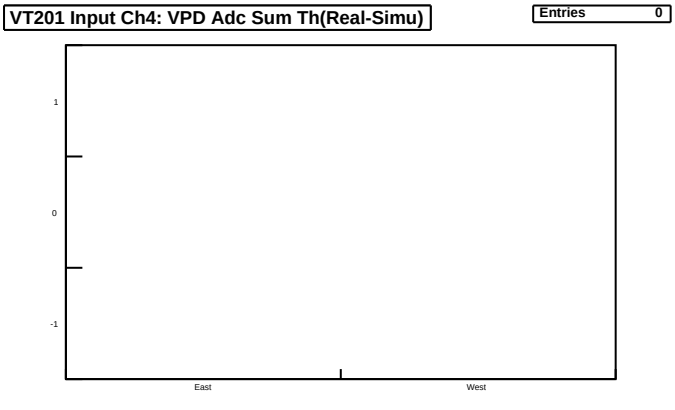

VT201 Input Ch2: ZDC Adc Sum Th
Entries 27552
62
14
65
27
1
1
3382
3430
3444
3444
3379
3417
3443
3443
W 0(p)/Sum-th0(H)
W F-Sum-th(p)/Sum-th1(H)
W B-Sum-th(p)/Sum-th2(H)
W Sum-th(p)/Sum-th3(H)
E 0(p)/Sum-th0(H)
E F-Sum-th(p)/Sum-th1(H)
E B-Sum-th(p)/Sum-th2(H)
E Sum-th(p)/Sum-th3(H)

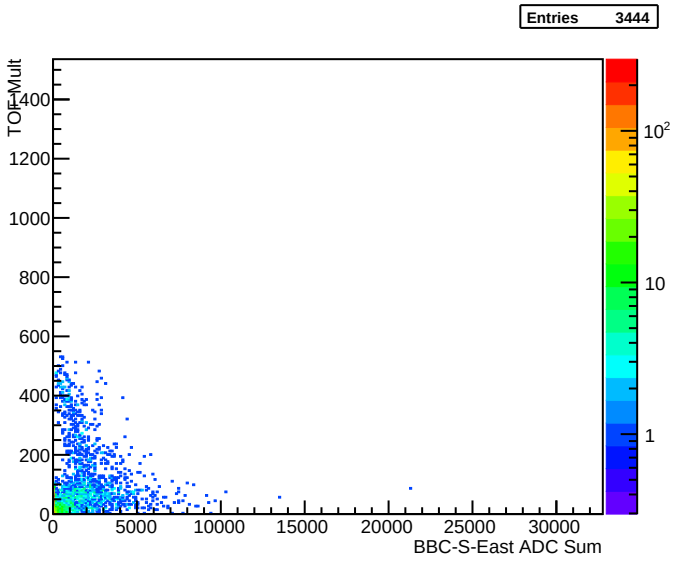

VT201 Input Ch2: ZDC Adc Sum Th (Real-Simu)
Entries 0
W 0(p)/Sum-th0(H)
W F-Sum-th(p)/Sum-th1(H)
W B-Sum-th(p)/Sum-th2(H)
W Sum-th(p)/Sum-th3(H)
E 0(p)/Sum-th0(H)
E F-Sum-th(p)/Sum-th1(H)
E B-Sum-th(p)/Sum-th2(H)
E Sum-th(p)/Sum-th3(H)

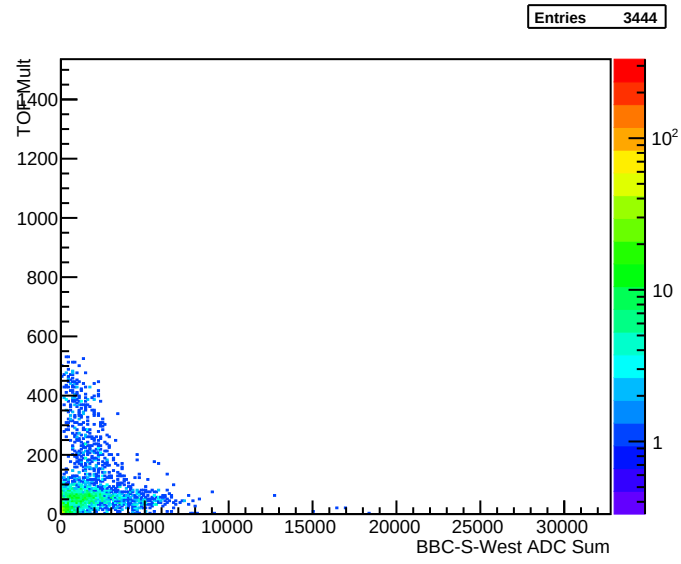

VT201 Input Ch4: VPD Tac Diff
Entries 3444
Tac

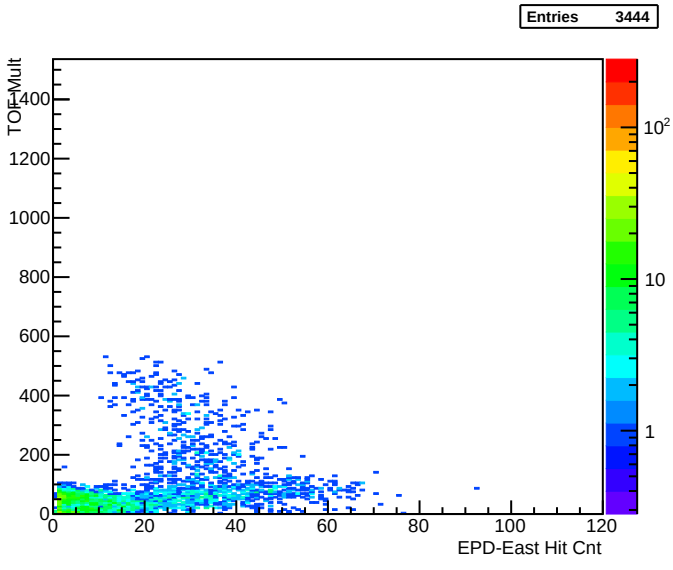

VT201 Input Ch4: VPD Tac Diff
Entries 0
Simu
Real

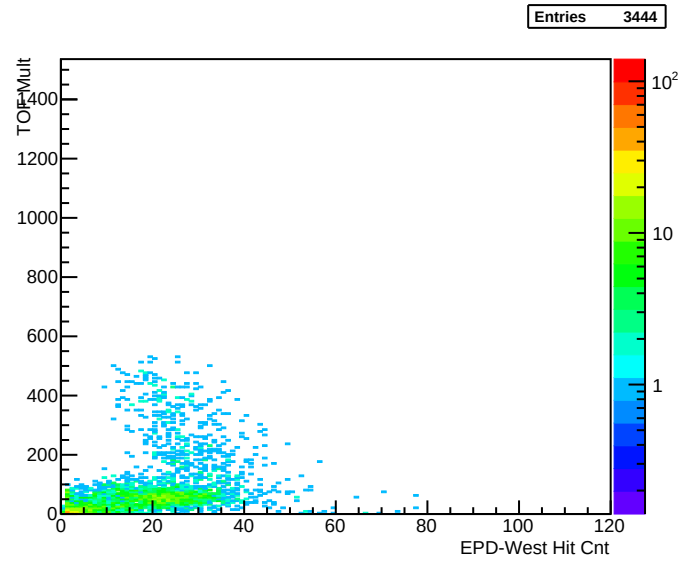

VT201 Input Ch4: VPD Adc Sum Th
Entries 6888
1859
2650
1585
794
East
West

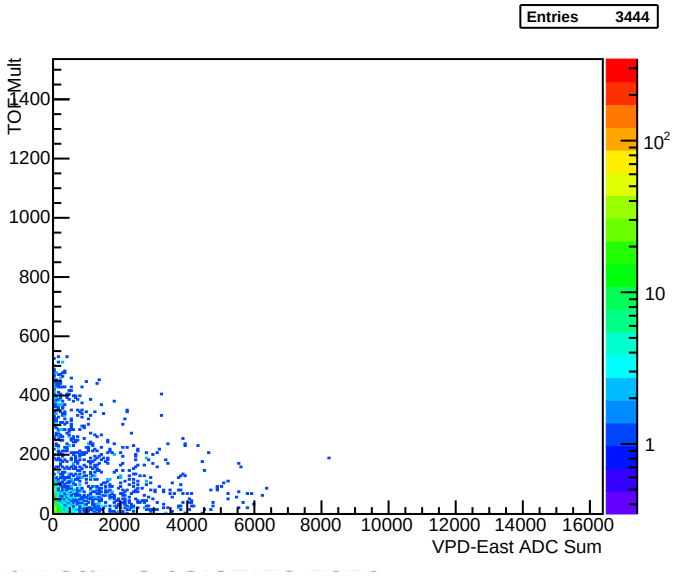

VT201 Input Ch4: VPD Adc Sum Th(Real-Simu)
Entries 0
East
West

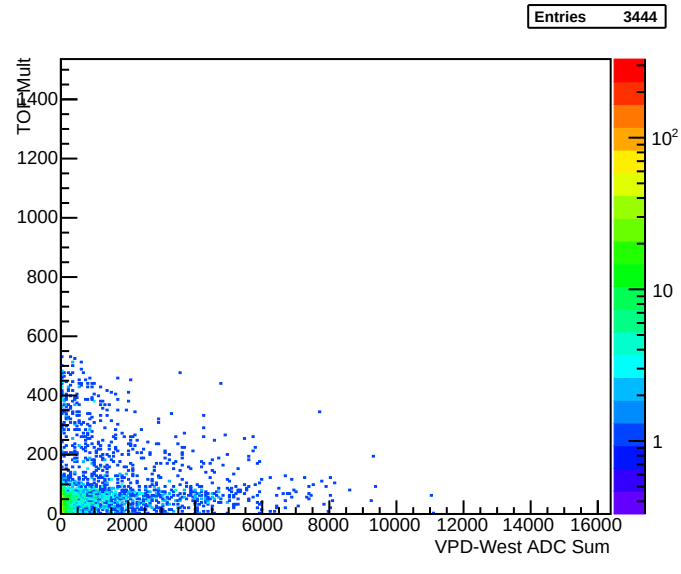

Entries 3444
TOF-Mult
BBC-S-East ADC Sum

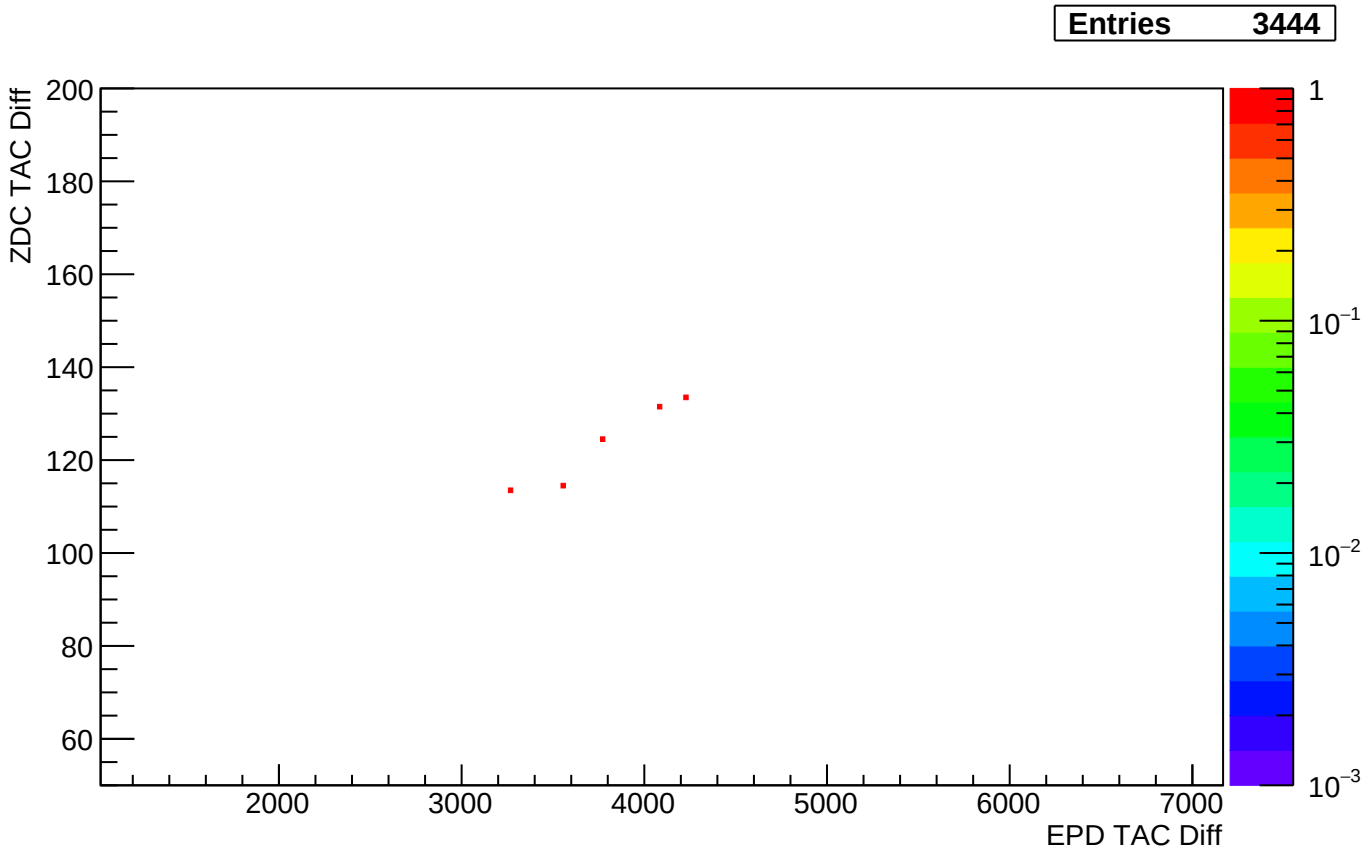

Entries 3444
TOF-Mult
BBC-S-West ADC Sum

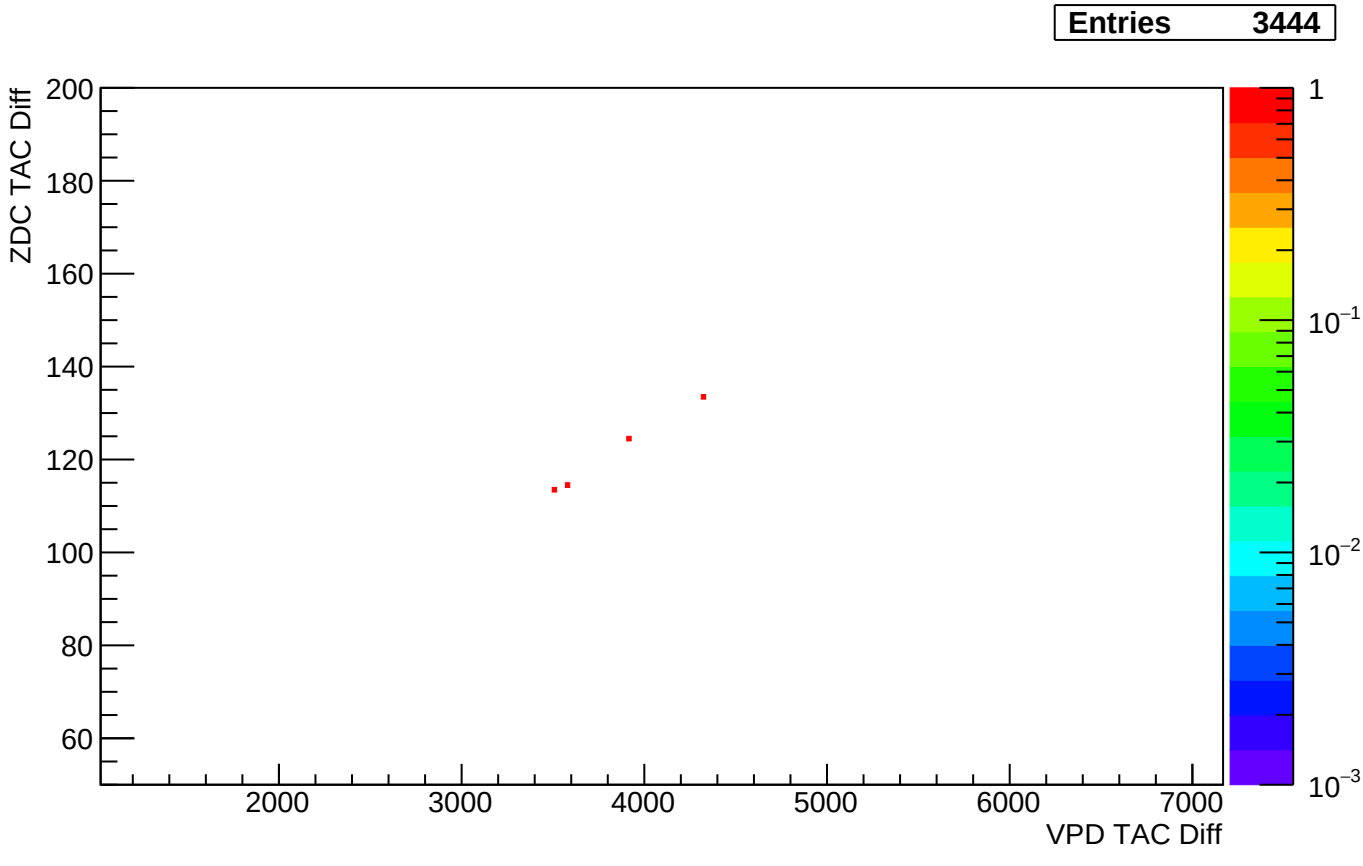

Entries 3444
TOF-Mult
EPD-East Hit Cnt

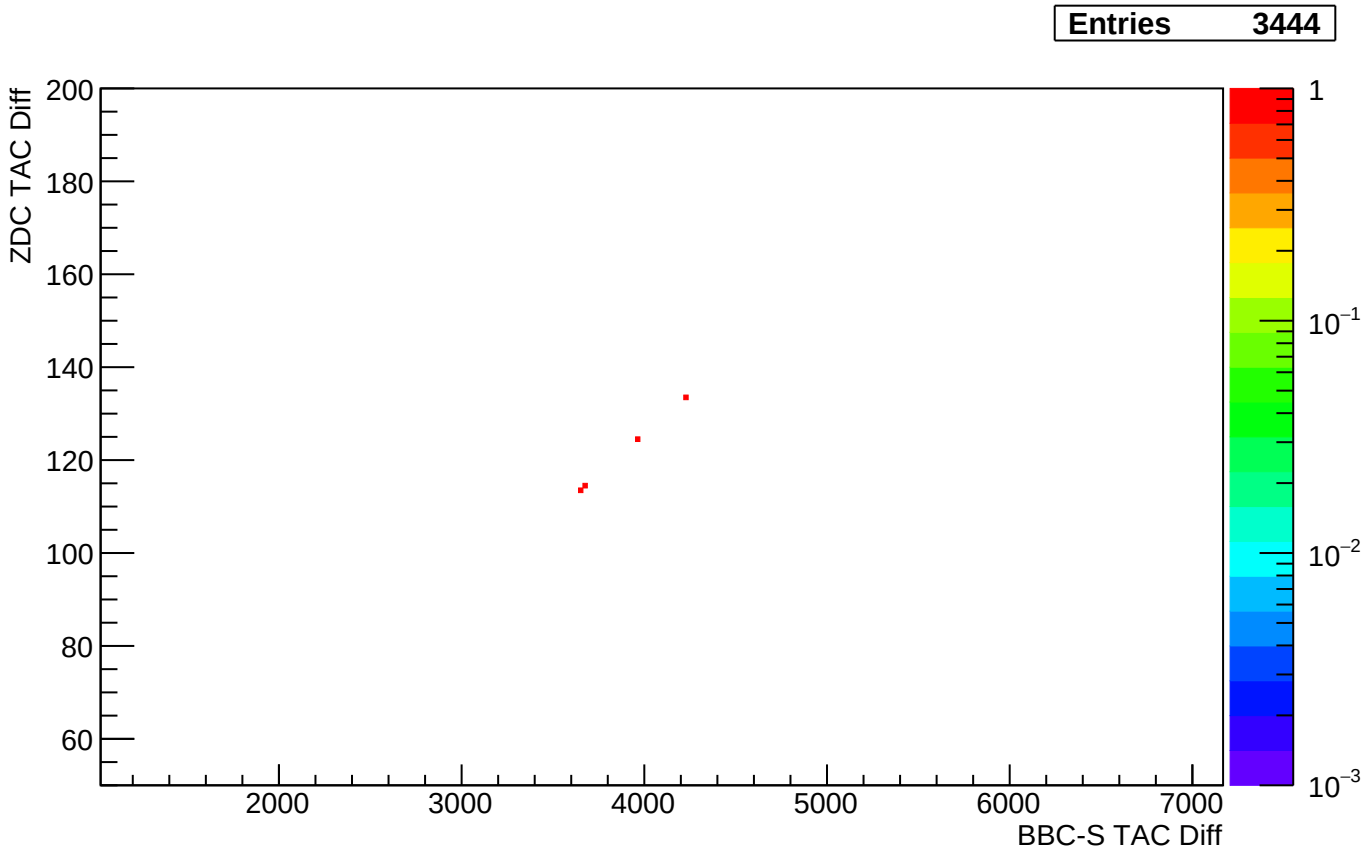

Entries 3444
TOF-Mult
EPD-West Hit Cnt

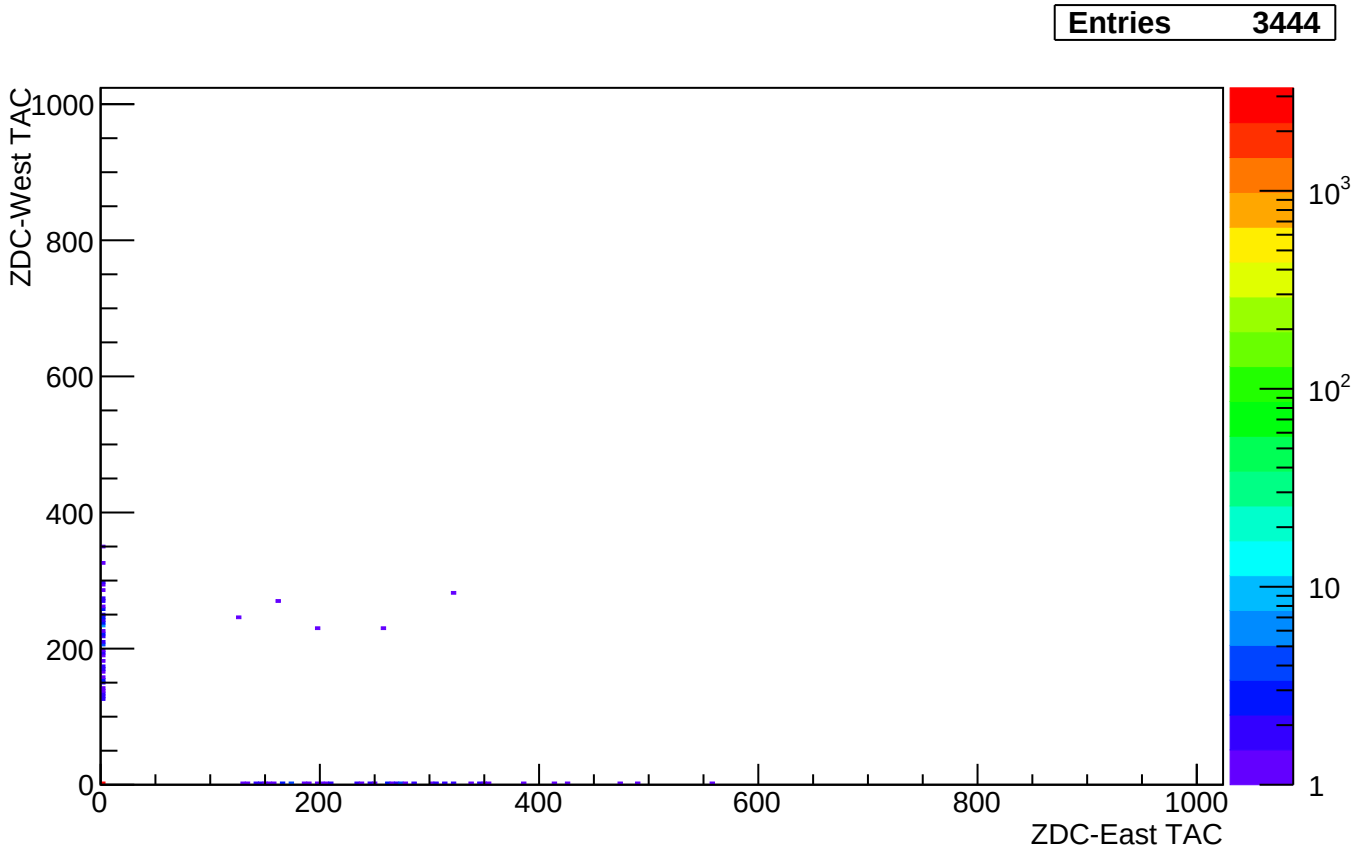

Entries 3444
TOF-Mult
VPD-East ADC Sum

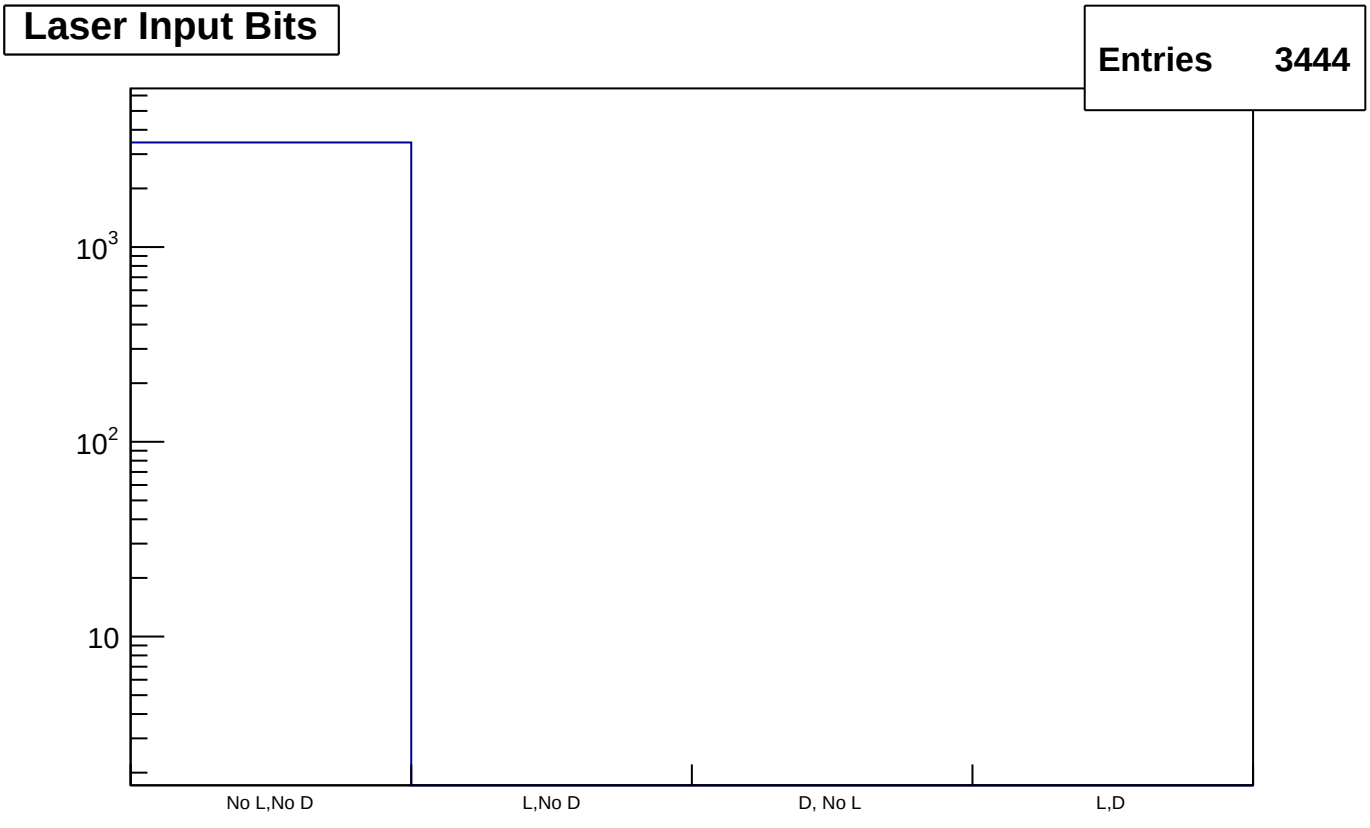

Entries 3444
TOF-Mult
VPD-West ADC Sum

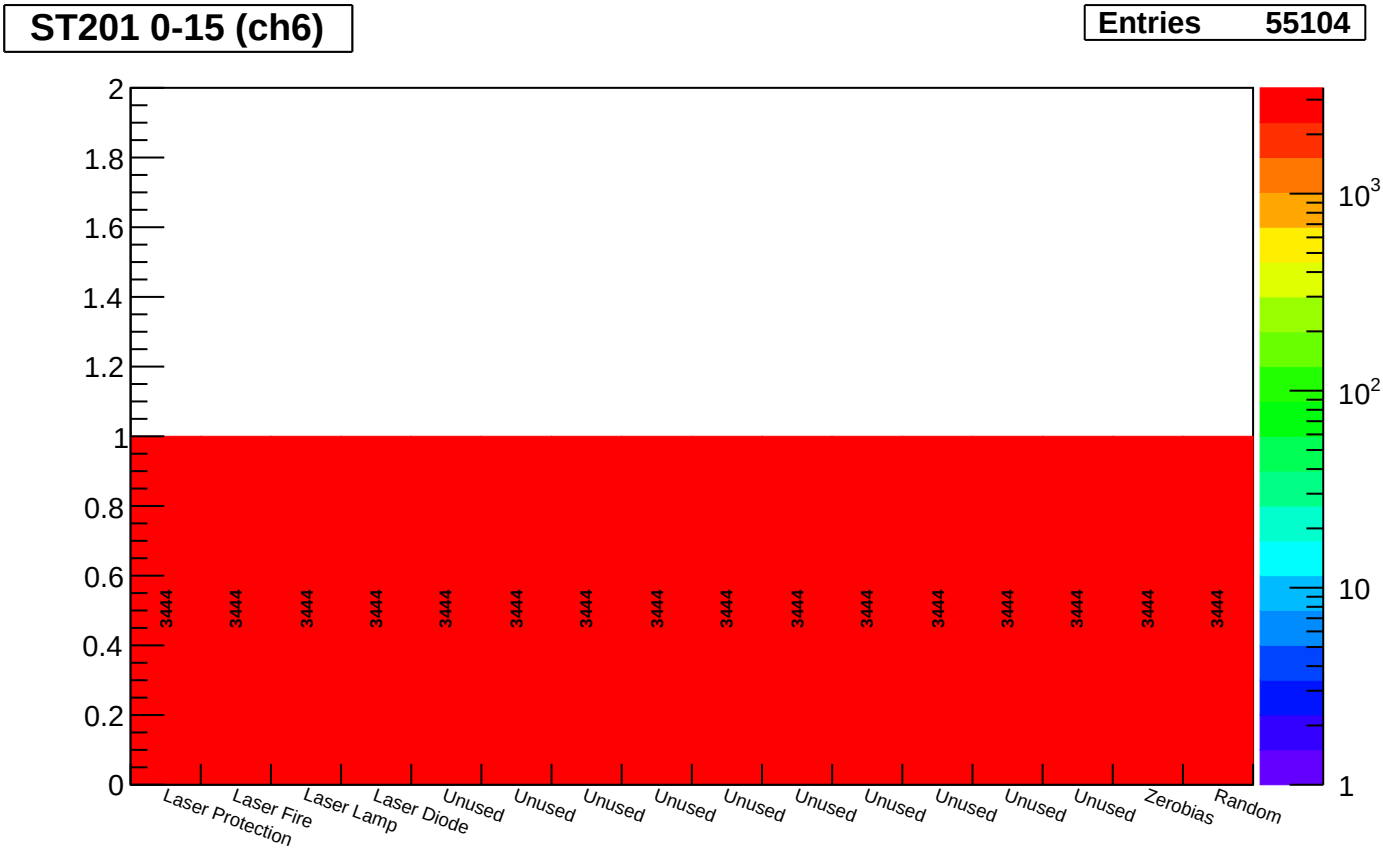

Entries 3444
ZDC TAC Diff
200
180
160
140
120
100
80
60
2000
3000
4000
5000
6000
7000
EPD TAC Diff
1
10^{-1}
10^{-2}
10^{-3}

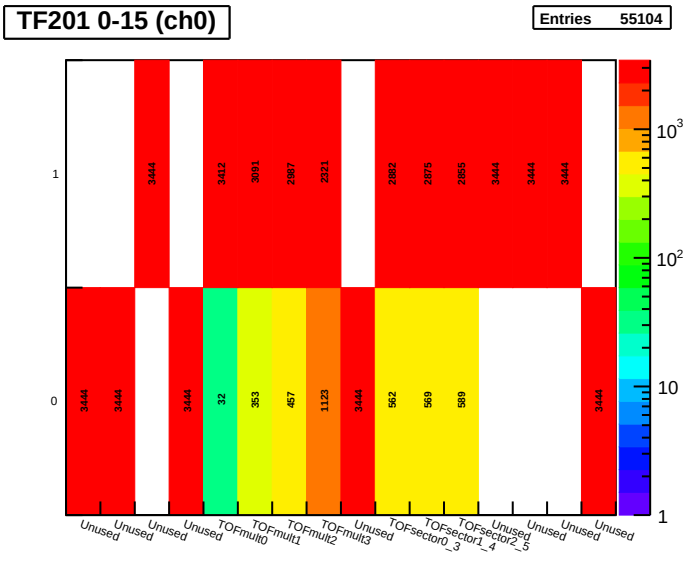

Entries 3444
ZDC TAC Diff
200
180
160
140
120
100
80
60
2000
3000
4000
5000
6000
7000
VPD TAC Diff
1
10^{-1}
10^{-2}
10^{-3}

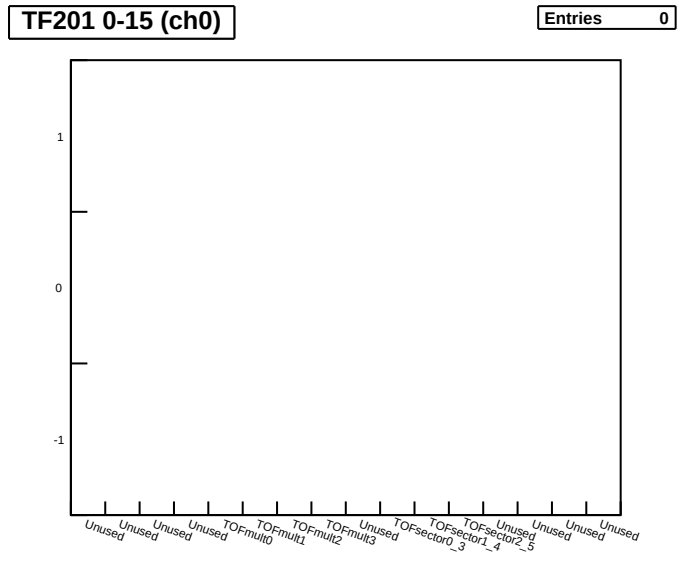

Entries 3444
ZDC TAC Diff
BBC-S TAC Diff
200
180
160
140
120
100
80
60
2000
3000
4000
5000
6000
7000
1
10^{-1}
10^{-2}
10^{-3}

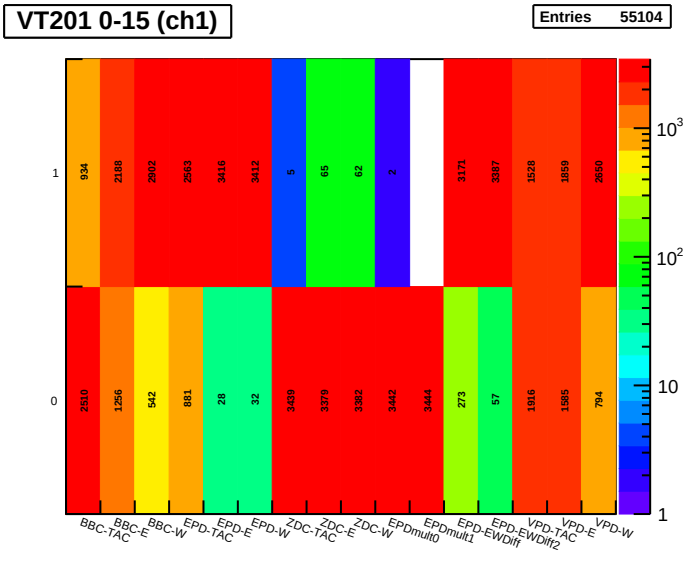

Entries 3444
ZDC-West TAC
ZDC-East TAC
1000
800
600
400
200
0
0
200
400
600
800
1000
10^3
10^2
10
1

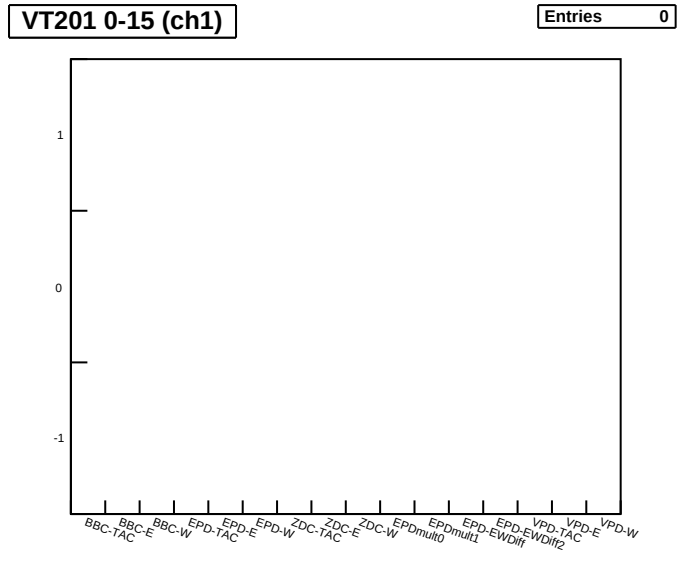

Laser Input Bits
Entries 3444
10^3
10^2
10
No L,No D
L,No D
D, No L
L,D

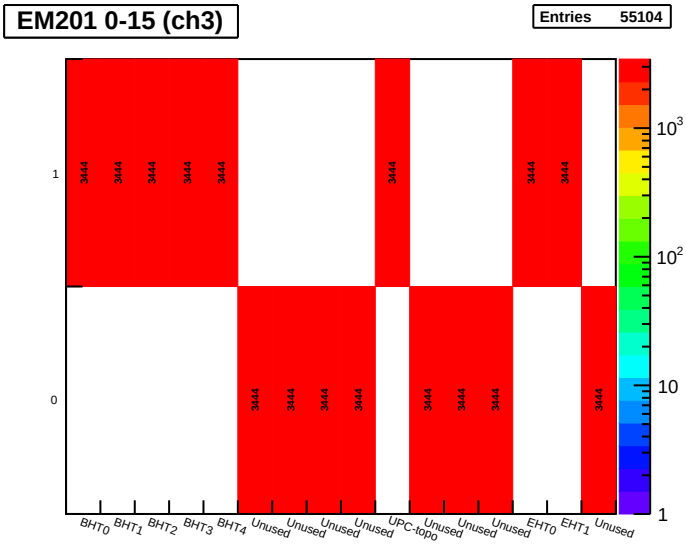

ST201 0-15 (ch6)
Entries 55104
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
3444
Laser Protection
Laser Fire
Laser Lamp
Laser Diode
Unused
Zerobias
Random
10^3
10^2
10
1

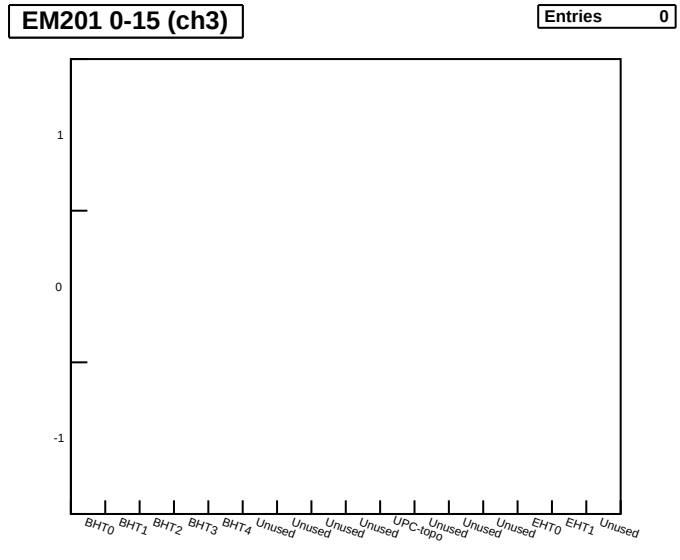

TF201 0-15 (ch0)
Entries 55104
1
0
3444 3444 3444 3444 3412 3091 2887 2221 2882 2875 2855 3444 3444 3444
3444 3444 3444 32 353 457 1123 3444 562 569 589 3444
Unused Unused Unused Unused TOFmult0 TOFmult1 TOFmult2 TOFmult3 Unused TOFsector0_3 TOFsector1_4 TOFsector2_5 Unused Unused Unused Unused
10^3
10^2
10
1


TF201 0-15 (ch0)
Entries 0
1
0
-1
Unused Unused Unused Unused TOFmult0 TOFmult1 TOFmult2 TOFmult3 Unused TOFsector0_3 TOFsector1_4 TOFsector2_5 Unused Unused Unused


VT201 0-15 (ch1)
Entries 55104
1
0
934 2188 2902 2563 3416 3412 5 65 62 2 3171 3387 1528 1859 2650
2510 1256 542 881 28 32 3439 3379 3382 3442 3444 273 57 1916 1585 794
BBC-TAC BBC-E BBC-W EPD-TAC EPD-E EPD-W ZDC-TAC ZDC-E ZDC-W EPDmult0 EPDmult1 EPD-EWDiff EPD-EWDiff2 VPD-TAC VPD-E VPD-W
10^3
10^2
10
1


VT201 0-15 (ch1)
Entries 0
1
0
-1
BBC-TAC BBC-E BBC-W EPD-TAC EPD-E EPD-W ZDC-TAC ZDC-E ZDC-W EPDmult0 EPDmult1 EPD-EWDiff EPD-EWDiff2 VPD-TAC VPD-E VPD-W


EM201 0-15 (ch3)
Entries 55104
1
0
3444 3444 3444 3444 3444 3444 3444 3444
3444 3444 3444 3444 3444 3444 3444 3444
BHT0 BHT1 BHT2 BHT3 BHT4 Unused Unused Unused Unused UPC-topo Unused Unused Unused EHT0 EHT1 Unused
10^3
10^2
10
1


EM201 0-15 (ch3)
Entries 0
1
0
-1
BHT0 BHT1 BHT2 BHT3 BHT4 Unused Unused Unused Unused UPC-topo Unused Unused Unused EHT0 EHT1 Unused