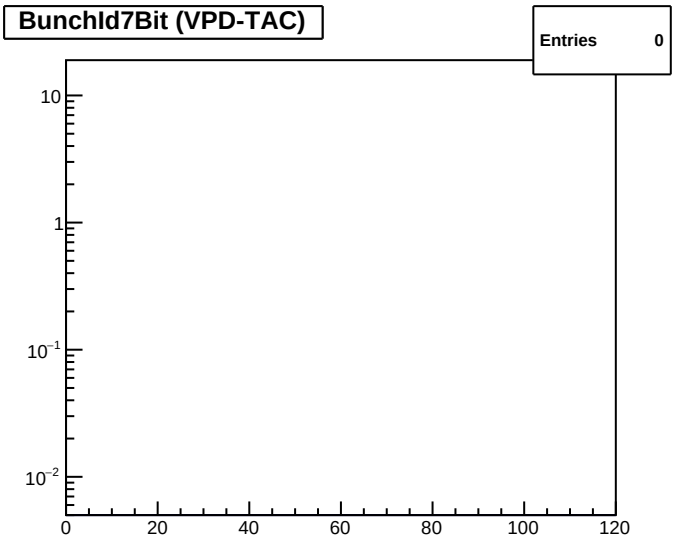
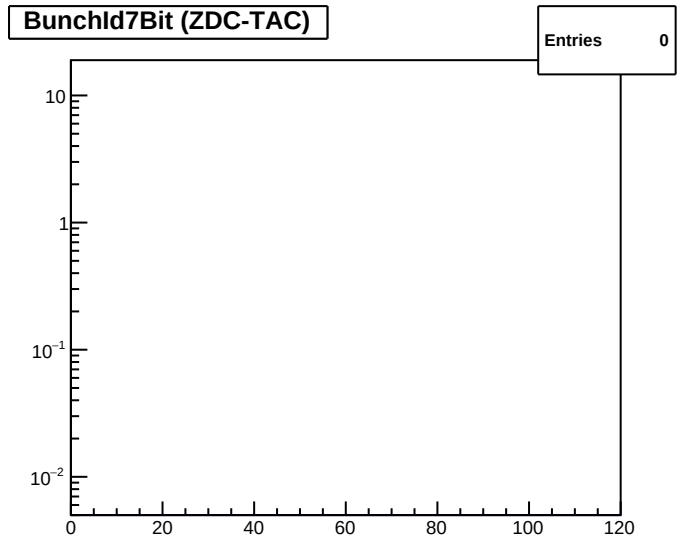
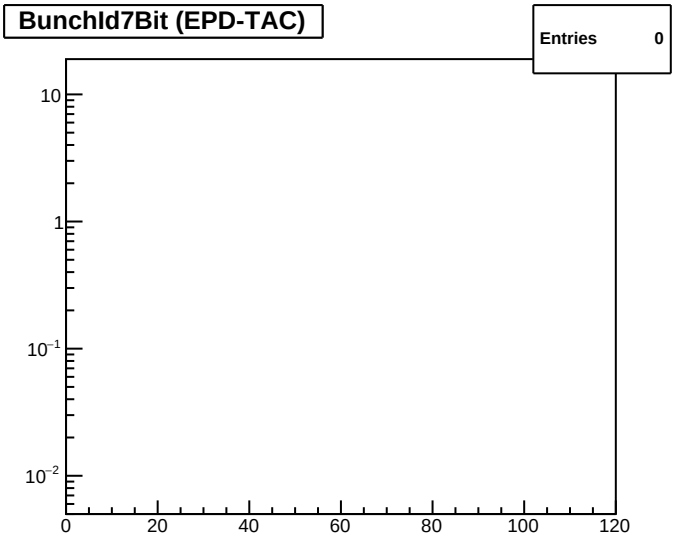
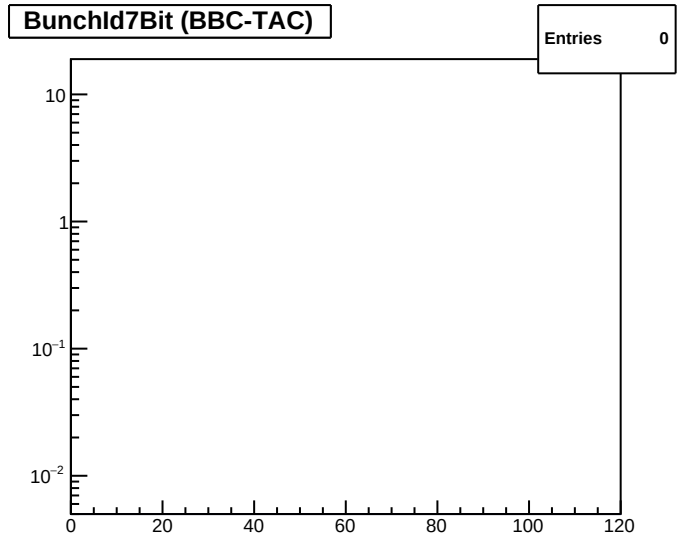
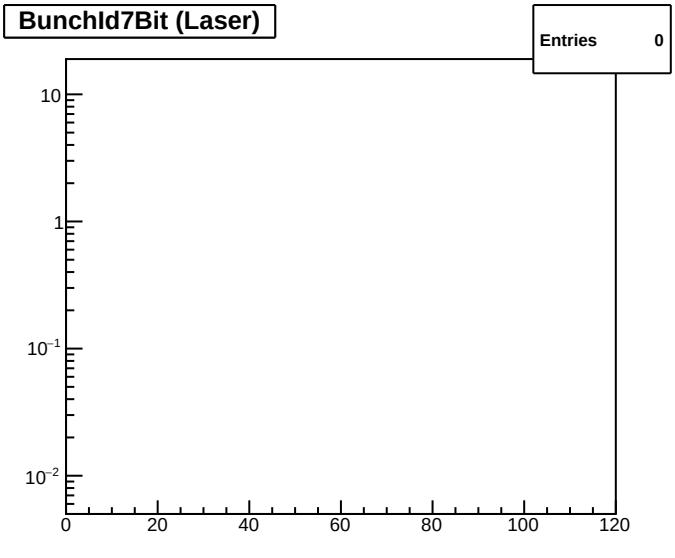
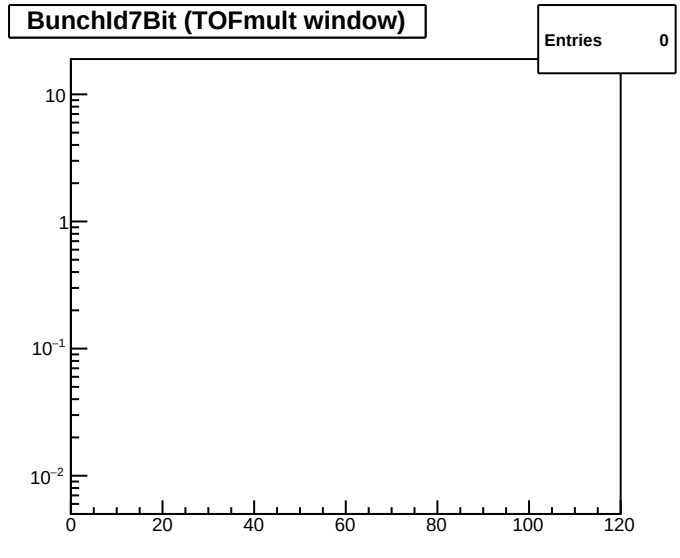

BunchId7Bit (VPD-TAC)
Entries 0
10
1
10^{-1}
10^{-2}
0 20 40 60 80 100 120

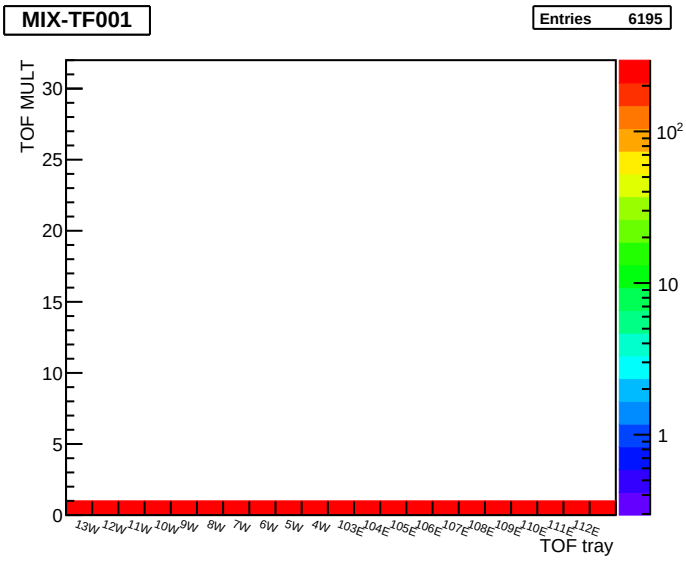

BunchId7Bit (ZDC-TAC)
Entries 0
10
1
10^{-1}
10^{-2}
0 20 40 60 80 100 120

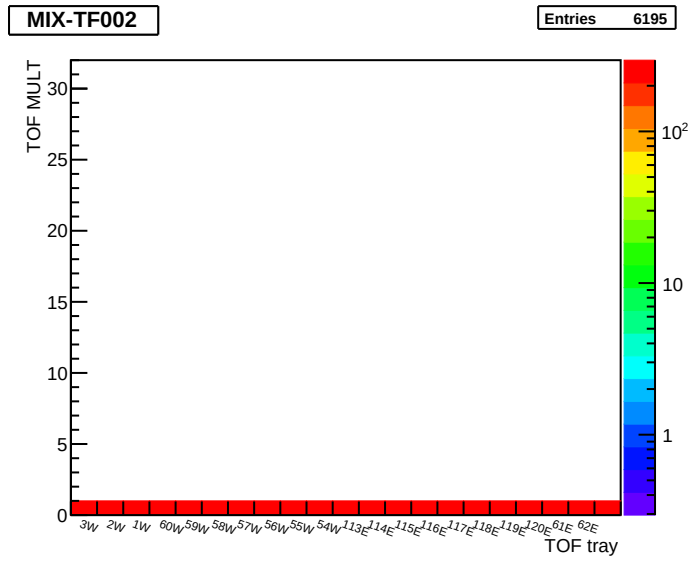

BunchId7Bit (EPD-TAC)
Entries 0
10
1
10^{-1}
10^{-2}
0 20 40 60 80 100 120

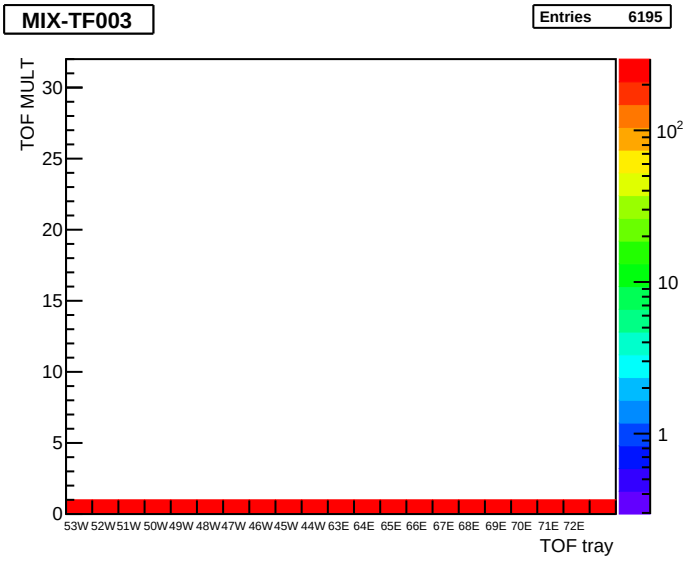

BunchId7Bit (BBC-TAC)
Entries 0
10
1
10^{-1}
10^{-2}
0 20 40 60 80 100 120

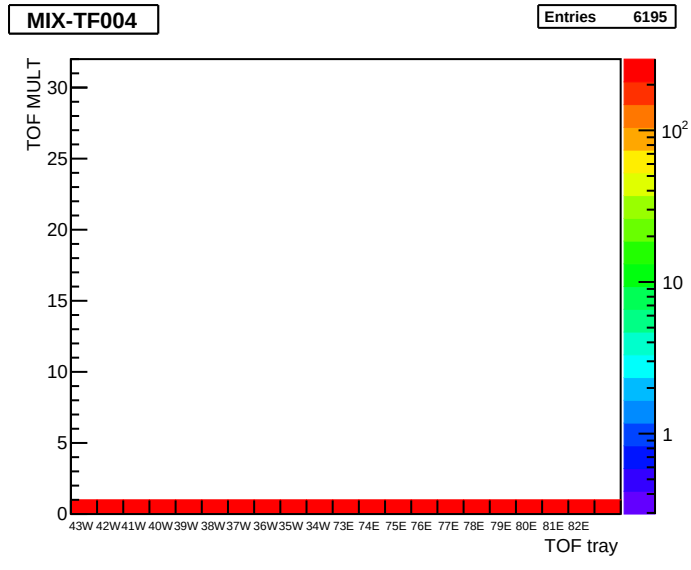

BunchId7Bit (Laser)
Entries 0
10
1
10^{-1}
10^{-2}
0 20 40 60 80 100 120

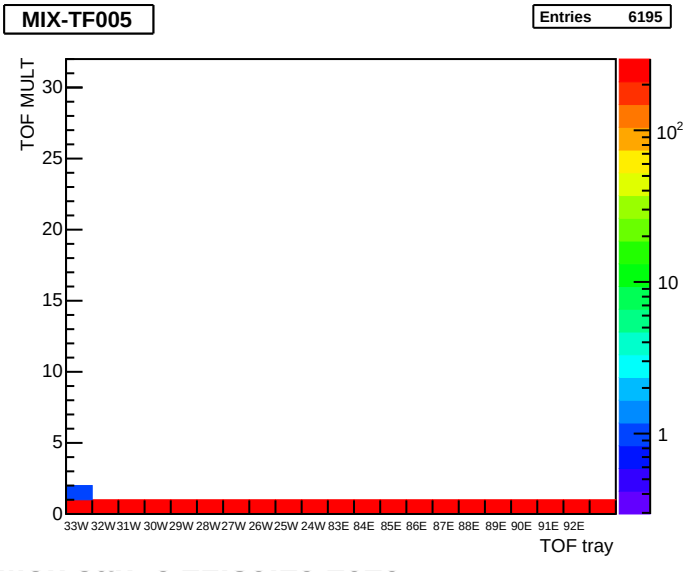

BunchId7Bit (TOFmult window)
Entries 0
10
1
10^{-1}
10^{-2}
0 20 40 60 80 100 120

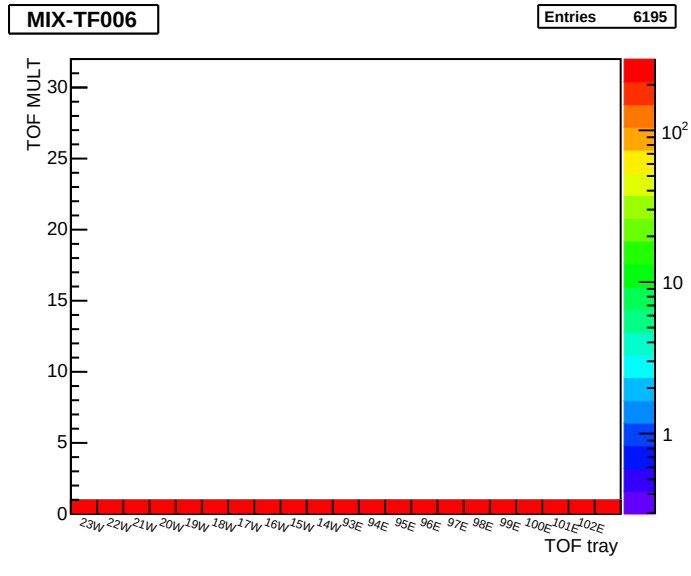

MIX-TF001
Entries 6195
TOF MULT
TOF tray

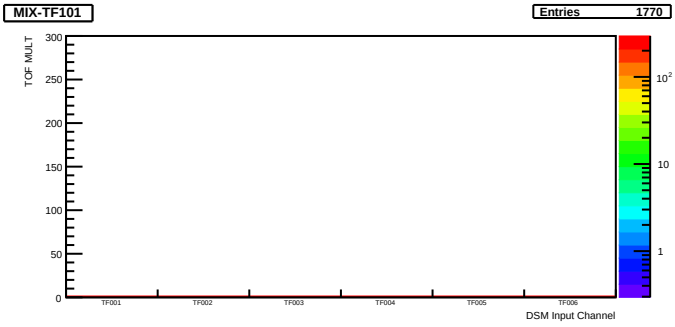

MIX-TF002
Entries 6195
TOF MULT
TOF tray

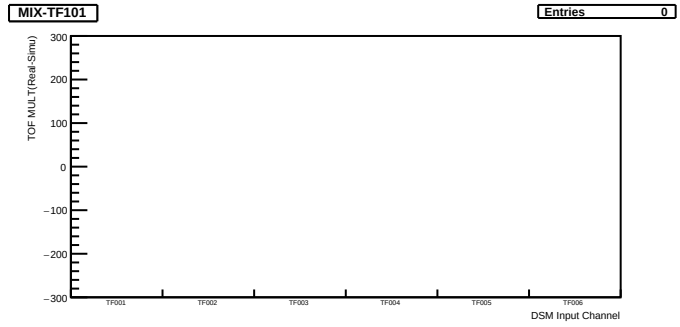

MIX-TF003
Entries 6195
TOF MULT
TOF tray

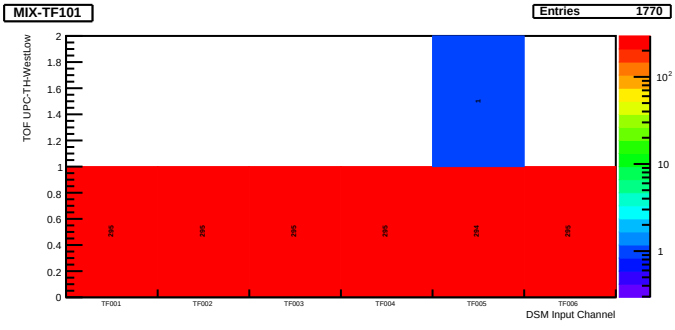

MIX-TF004
Entries 6195
TOF MULT
TOF tray

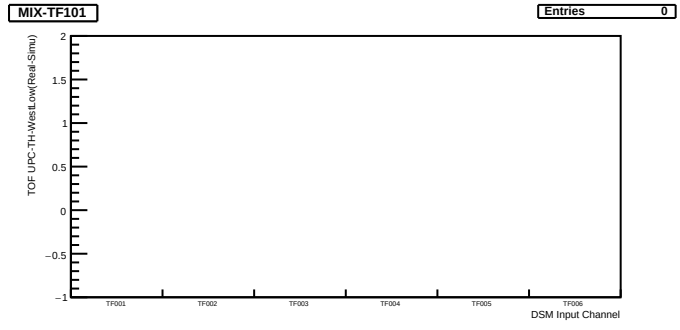

MIX-TF005
Entries 6195
TOF MULT
TOF tray

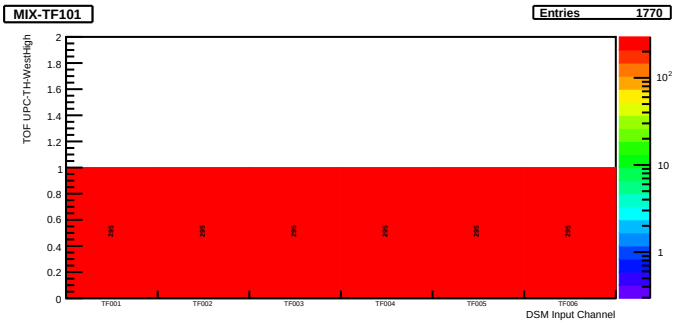

MIX-TF006
Entries 6195
TOF MULT
TOF tray

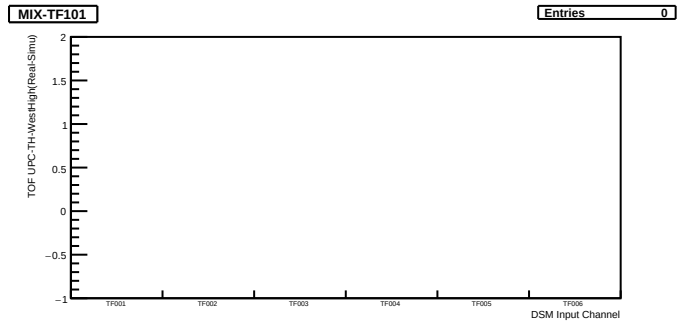

MIX-TF101
Entries 1770
TOF MULT
DSM Input Channel

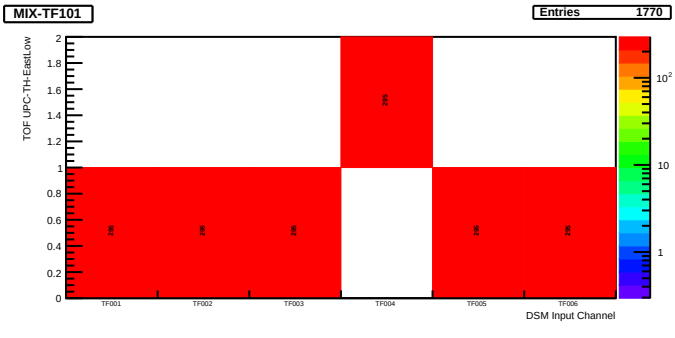

MIX-TF101
Entries 0
TOF MULT(Real-Simu)
DSM Input Channel

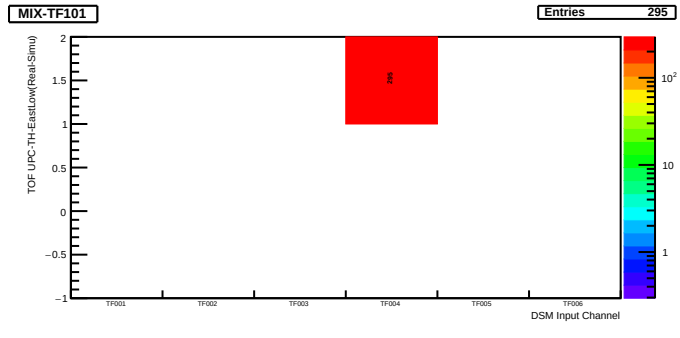

MIX-TF101
Entries 1770
TOF UPC-TH-WestLow
DSM Input Channel

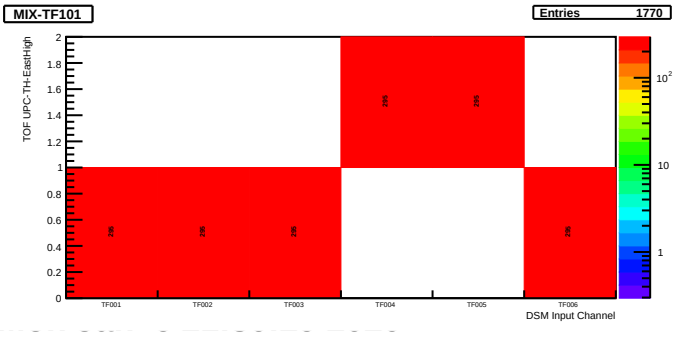

MIX-TF101
Entries 0
TOF UPC-TH-WestLow(Real-Simu)
DSM Input Channel

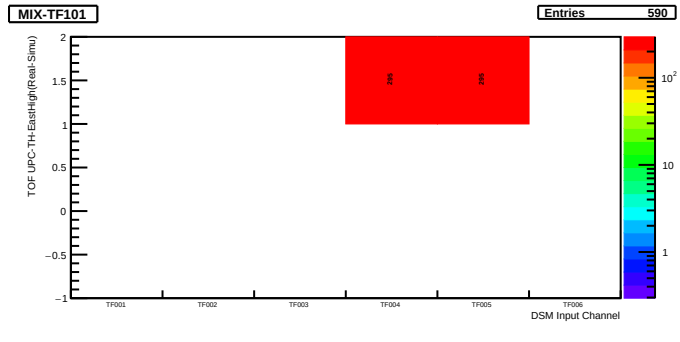

MIX-TF101
Entries 1770
TOF UPC-TH-WestHigh
DSM Input Channel

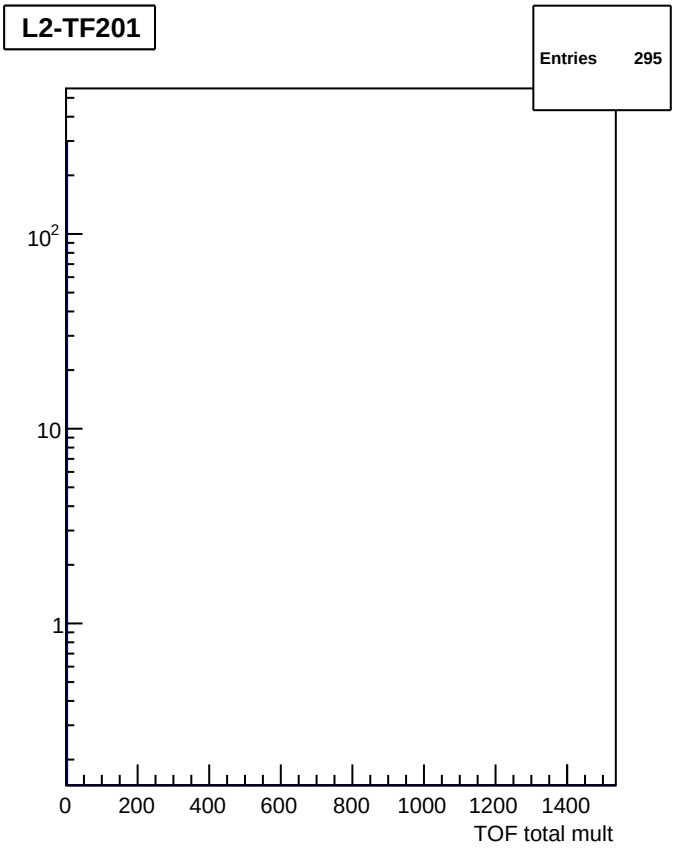

MIX-TF101
Entries 0
TOF UPC-TH-WestHigh(Real-Simu)
DSM Input Channel

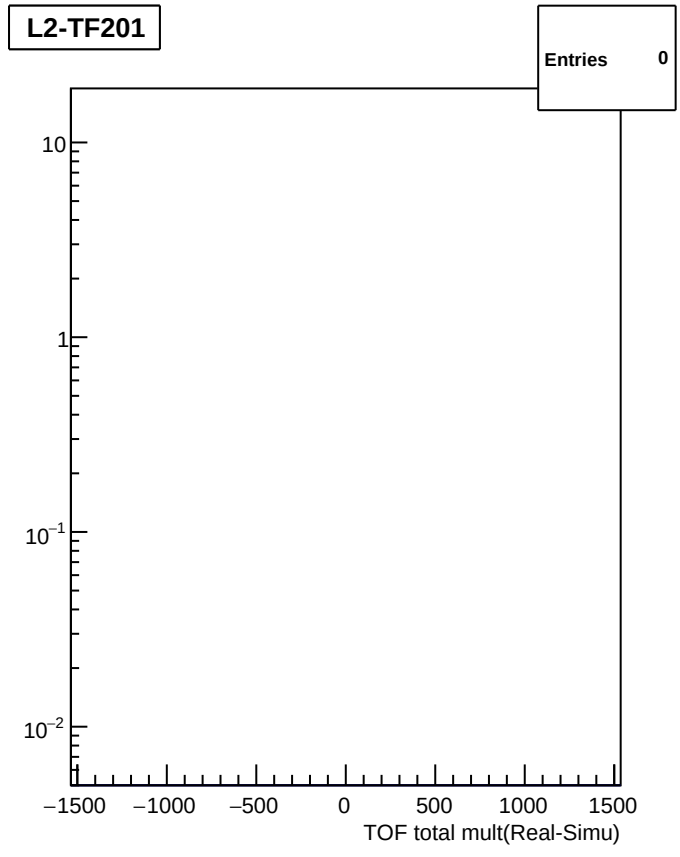

MIX-TF101
Entries 1770
TOF UPC-TH-EastLow
DSM Input Channel

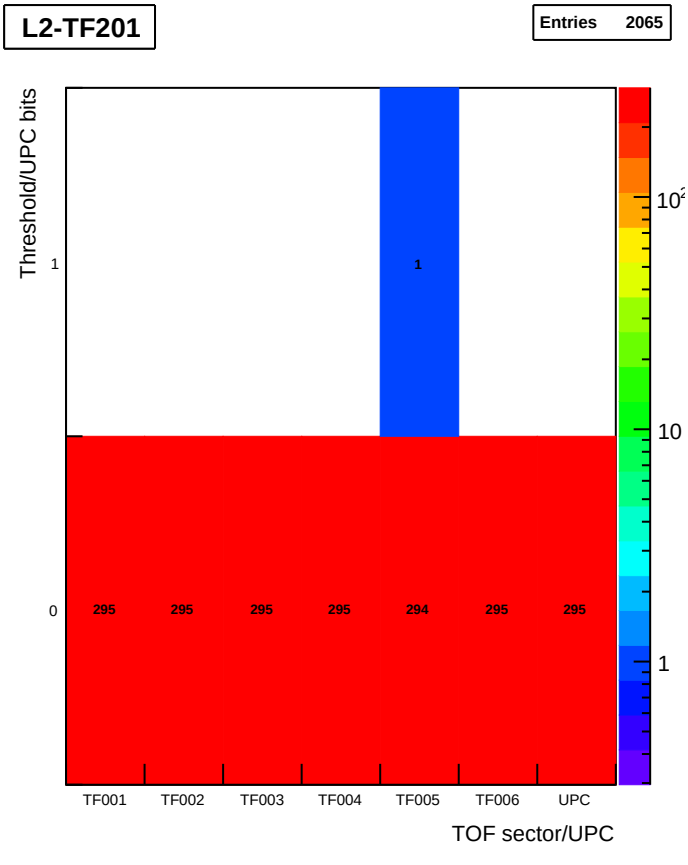

MIX-TF101
Entries 295
TOF UPC-TH-EastLow(Real-Simu)
DSM Input Channel

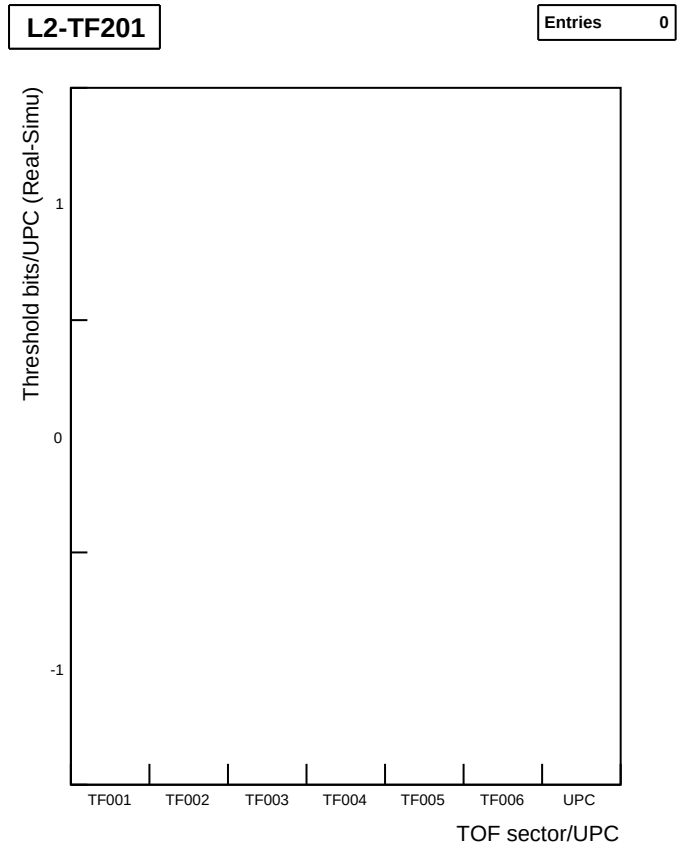

MIX-TF101
Entries 1770
TOF UPC-TH-EastHigh
DSM Input Channel

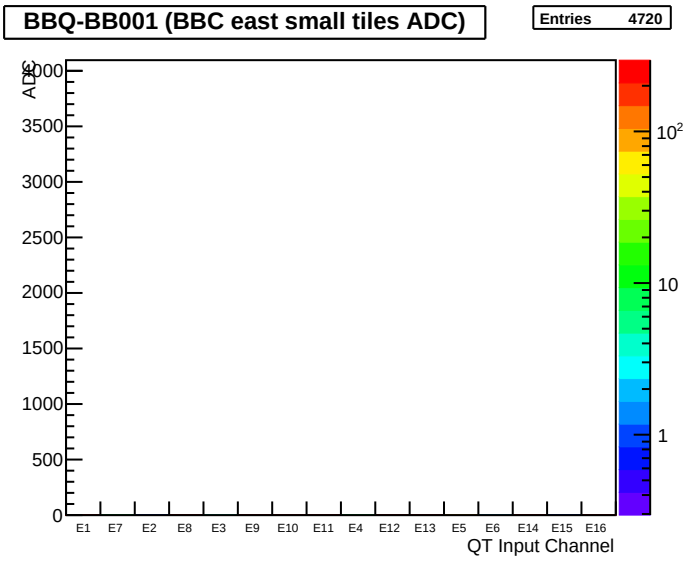

MIX-TF101
Entries 590
TOF UPC-TH-EastHigh(Real-Simu)
DSM Input Channel

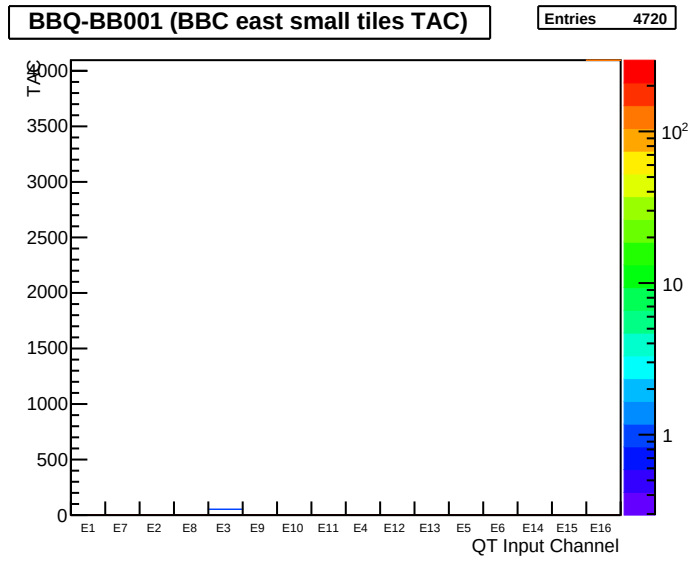

L2-TF201
Entries 295
10^2
10
1
0
200
400
600
800
1000
1200
1400
TOF total mult

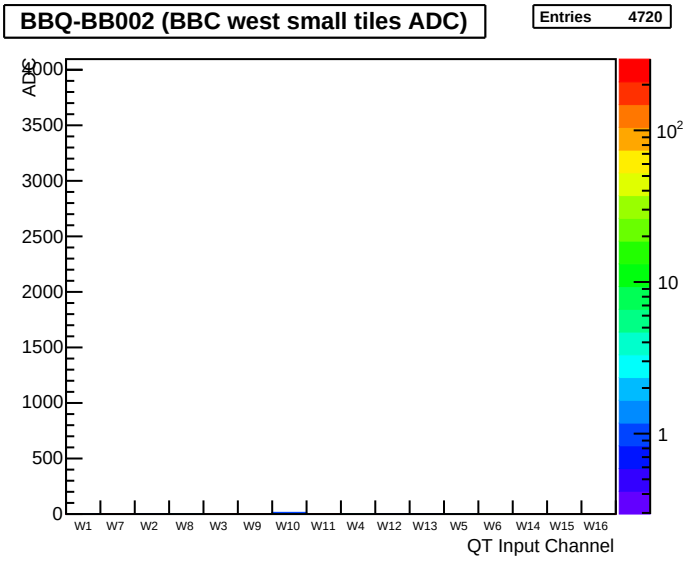

L2-TF201
Entries 0
10
1
10^{-1}
10^{-2}
−1500
−1000
−500
0
500
1000
1500
TOF total mult(Real-Simu)

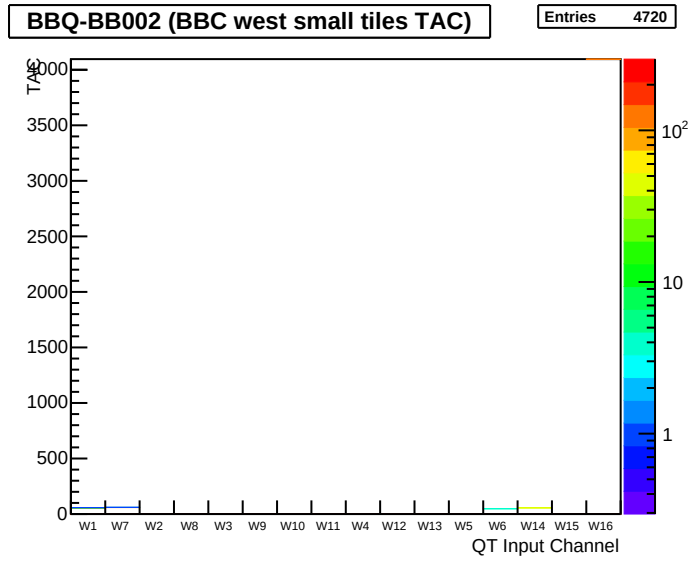

L2-TF201
Entries 2065
Threshold/UPC bits
1
0
1
295
295
295
295
294
295
295
10^2
10
1
TF001
TF002
TF003
TF004
TF005
TF006
UPC
TOF sector/UPC

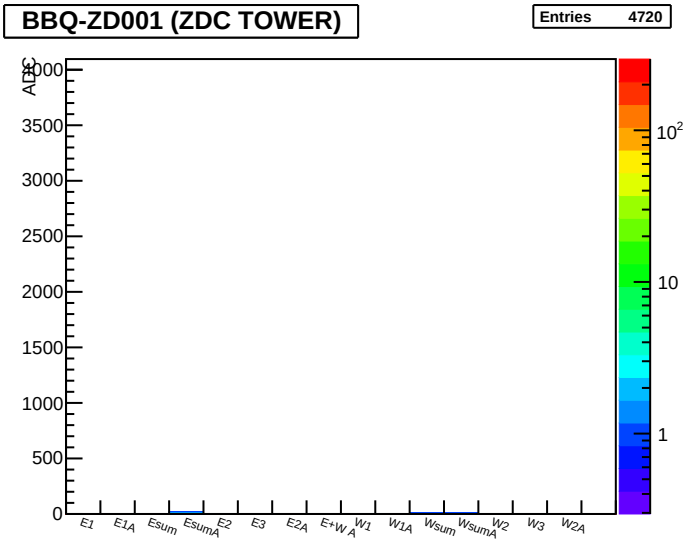

L2-TF201
Entries 0
Threshold bits/UPC (Real-Simu)
1
0
-1
TF001
TF002
TF003
TF004
TF005
TF006
UPC
TOF sector/UPC

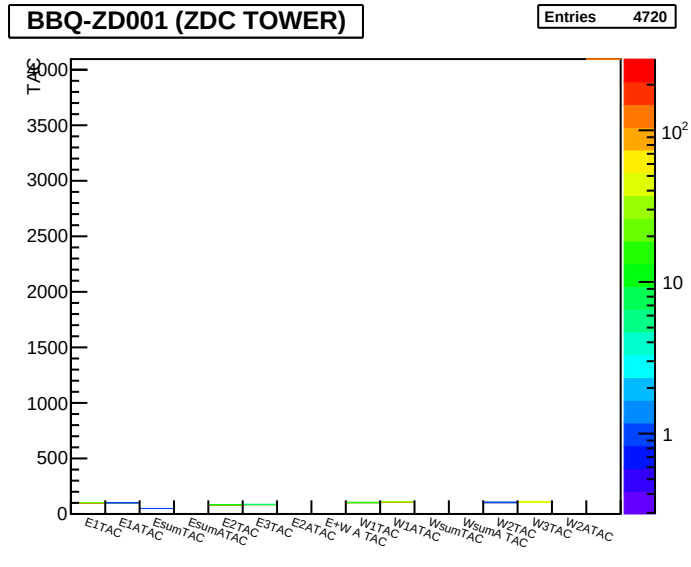

BBQ-BB001 (BBC east small tiles ADC)
Entries 4720
ADC
QT Input Channel
E1 E7 E2 E8 E3 E9 E10 E11 E4 E12 E13 E5 E6 E14 E15 E16

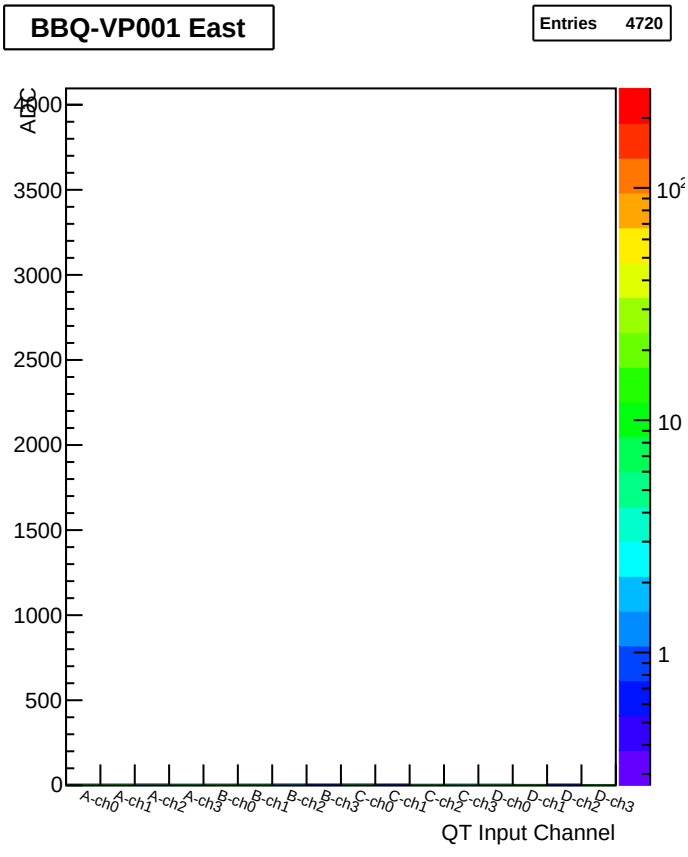

BBQ-BB001 (BBC east small tiles TAC)
Entries 4720
TAC
QT Input Channel
E1 E7 E2 E8 E3 E9 E10 E11 E4 E12 E13 E5 E6 E14 E15 E16

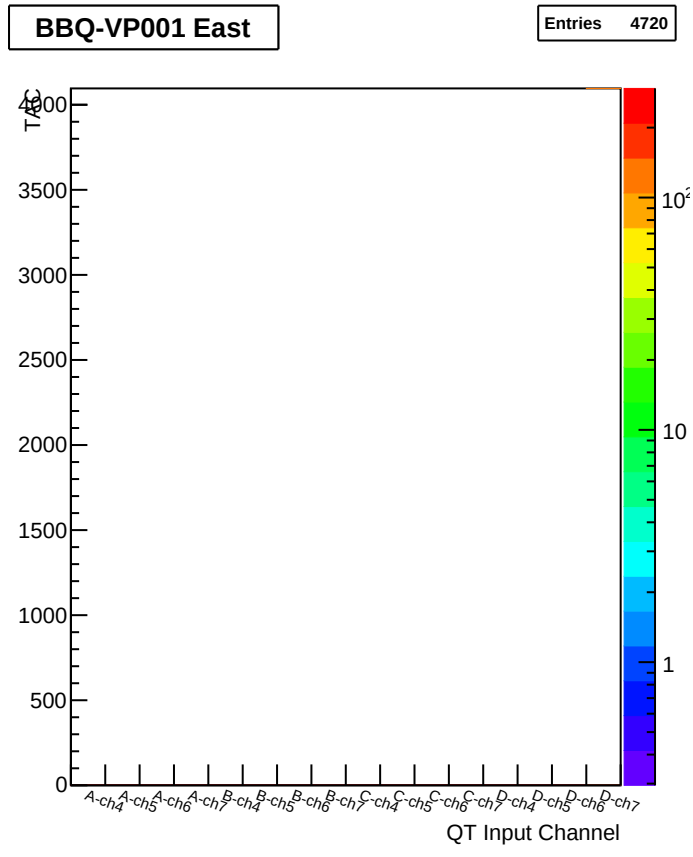

BBQ-BB002 (BBC west small tiles ADC)
Entries 4720
ADC
QT Input Channel
W1 W7 W2 W8 W3 W9 W10 W11 W4 W12 W13 W5 W6 W14 W15 W16

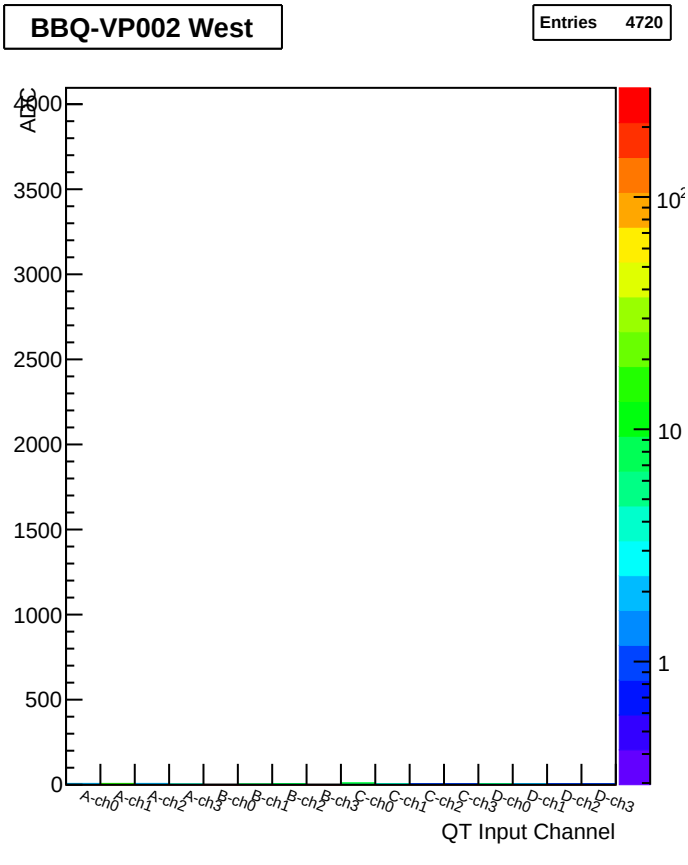

BBQ-BB002 (BBC west small tiles TAC)
Entries 4720
TAC
QT Input Channel
W1 W7 W2 W8 W3 W9 W10 W11 W4 W12 W13 W5 W6 W14 W15 W16

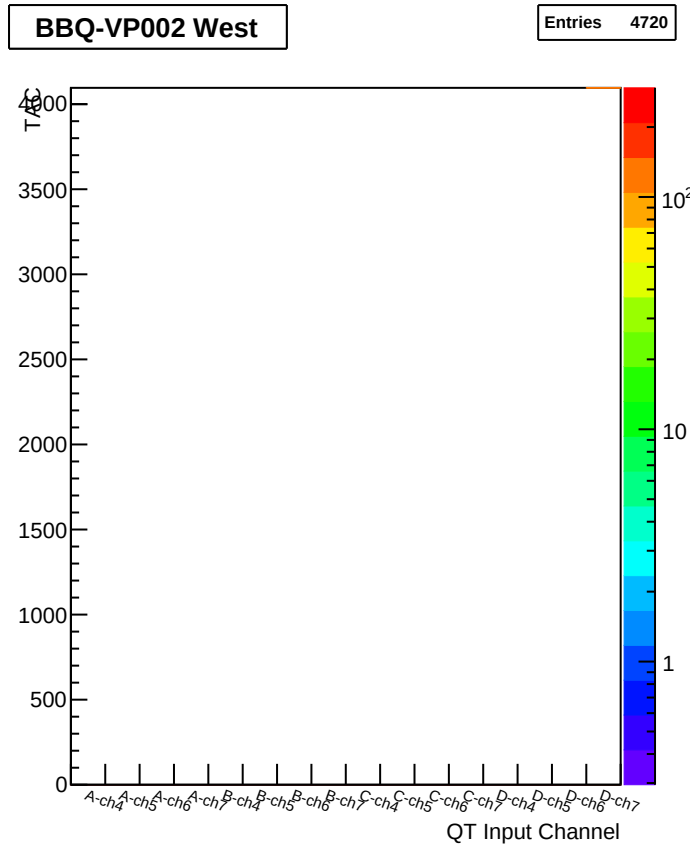

BBQ-ZD001 (ZDC TOWER)
Entries 4720
ADC
E1 E1A Esum EsumA E2 E3 E2A E+W A W1 W1A Wsum WsumA W2 W3 W2A

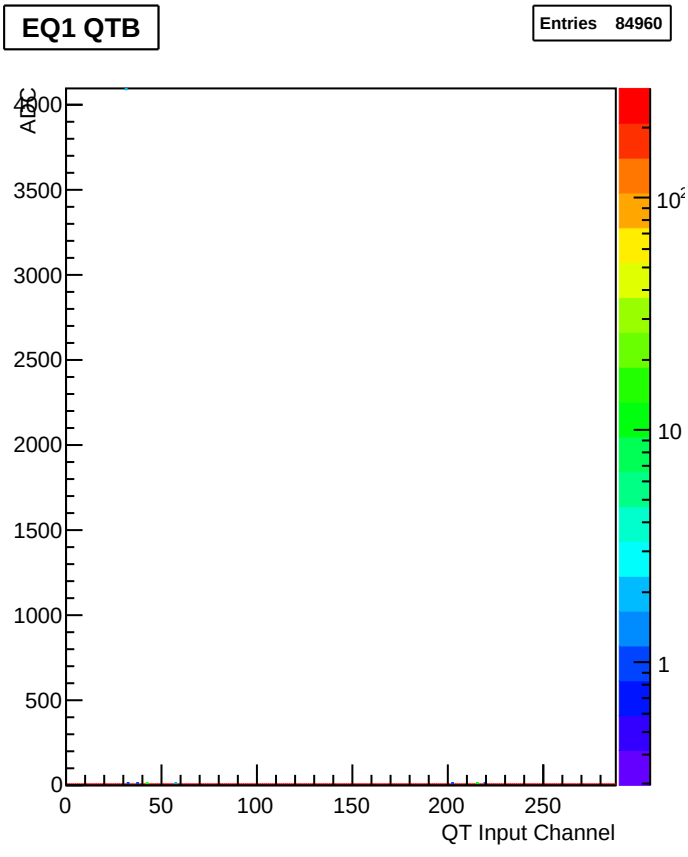

BBQ-ZD001 (ZDC TOWER)
Entries 4720
TAC
E1TAC E1ATAC EsumTAC EsumATAC E2TAC E3TAC E2ATAC E+W A TAC W1TAC W1ATAC WsumTAC WsumA TAC W2TAC W3TAC W2ATAC

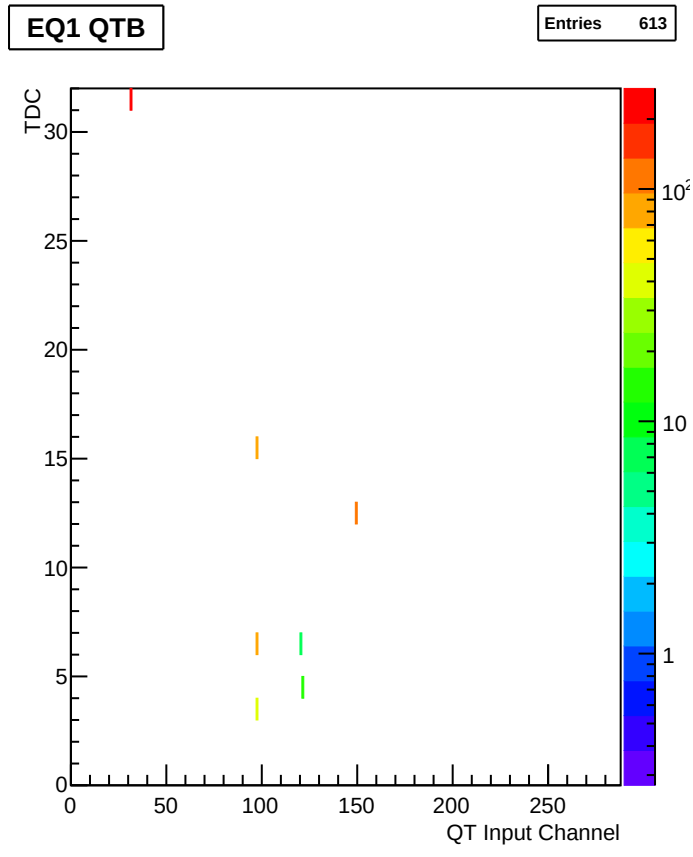

BBQ-VP001 East
Entries 4720
ADC
QT Input Channel

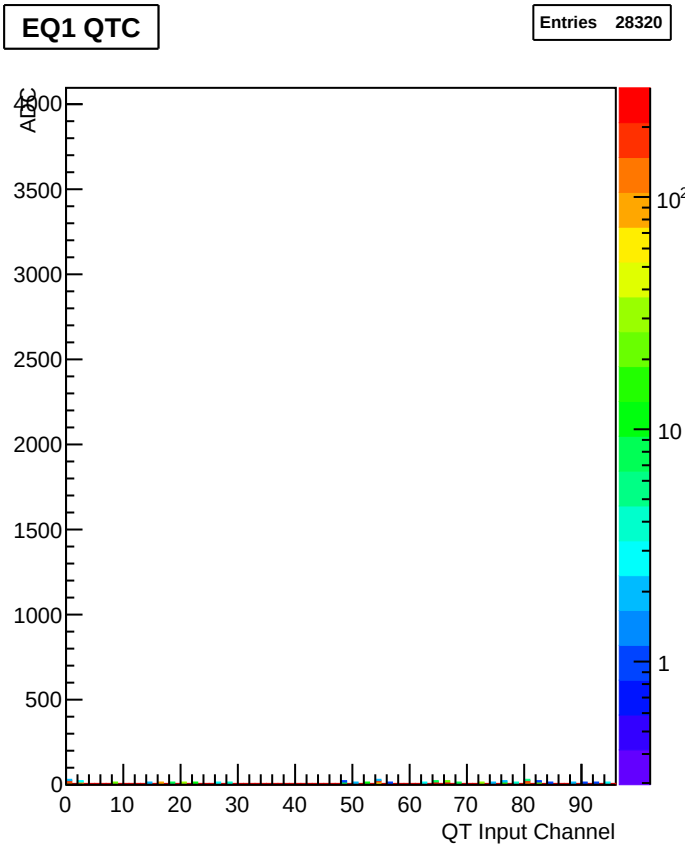

BBQ-VP001 East
Entries 4720
TADC
QT Input Channel

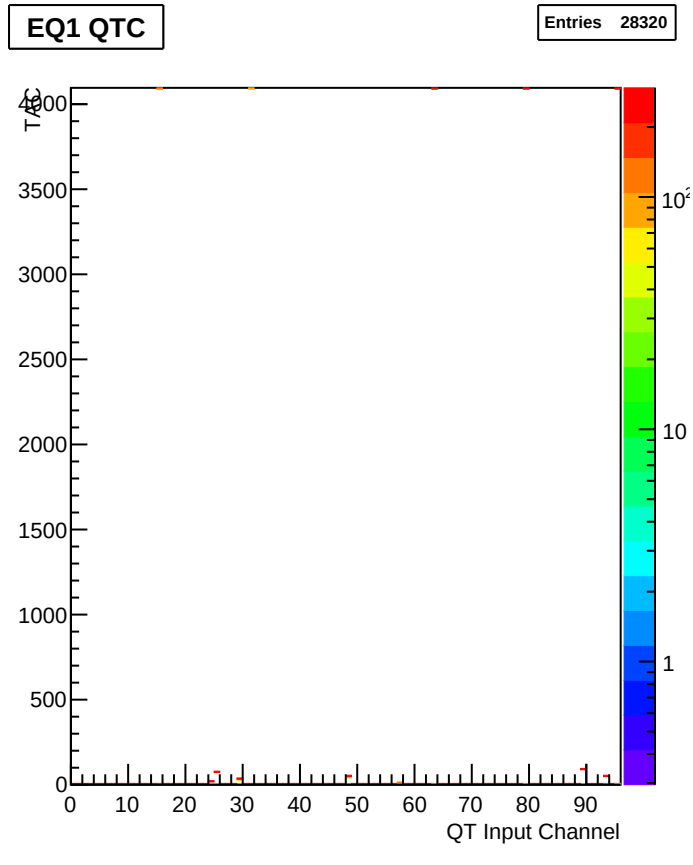

BBQ-VP002 West
Entries 4720
ADC
QT Input Channel

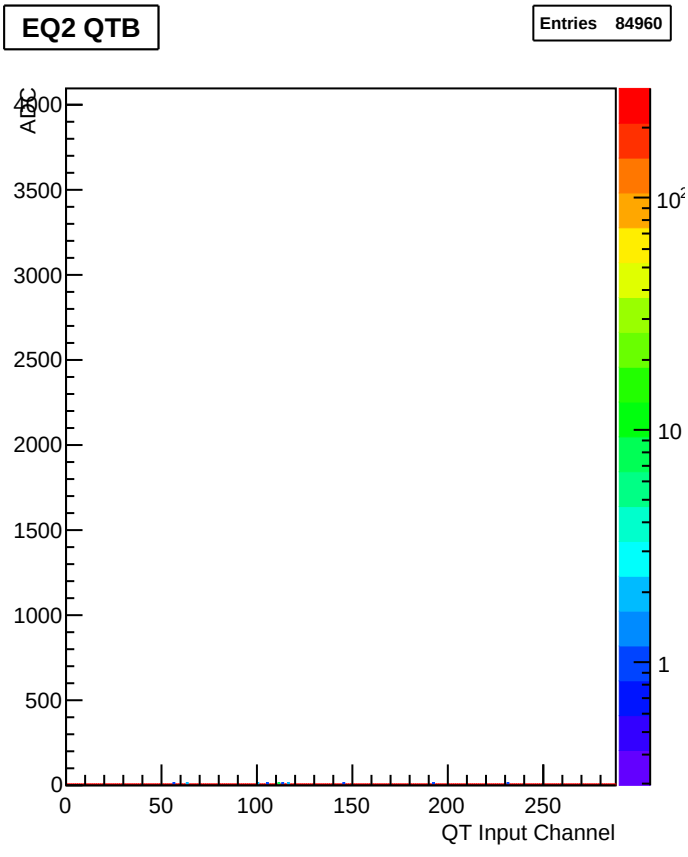

BBQ-VP002 West
Entries 4720
TADC
QT Input Channel

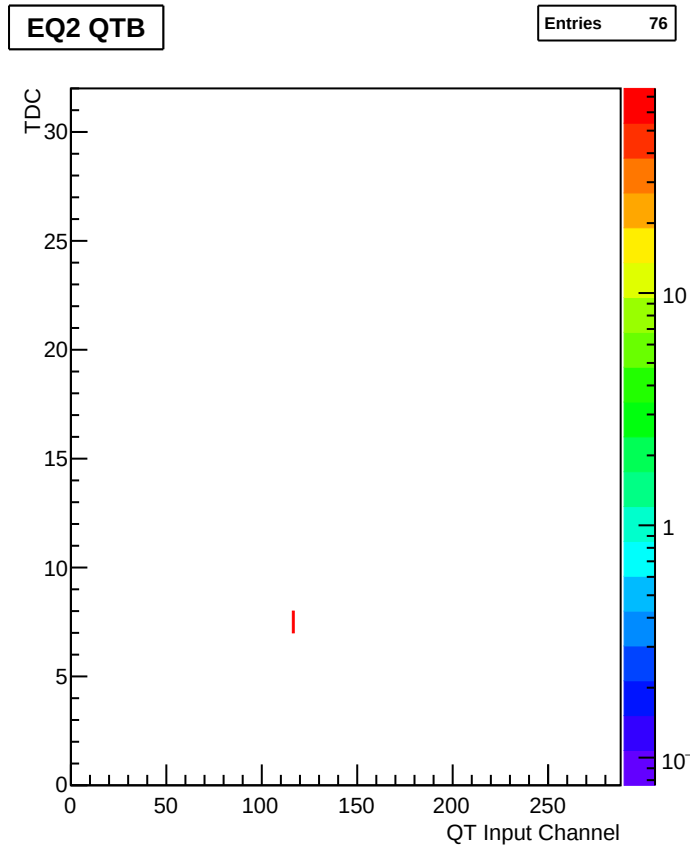

EQ1 QTB
Entries 84960
ADC
QT Input Channel

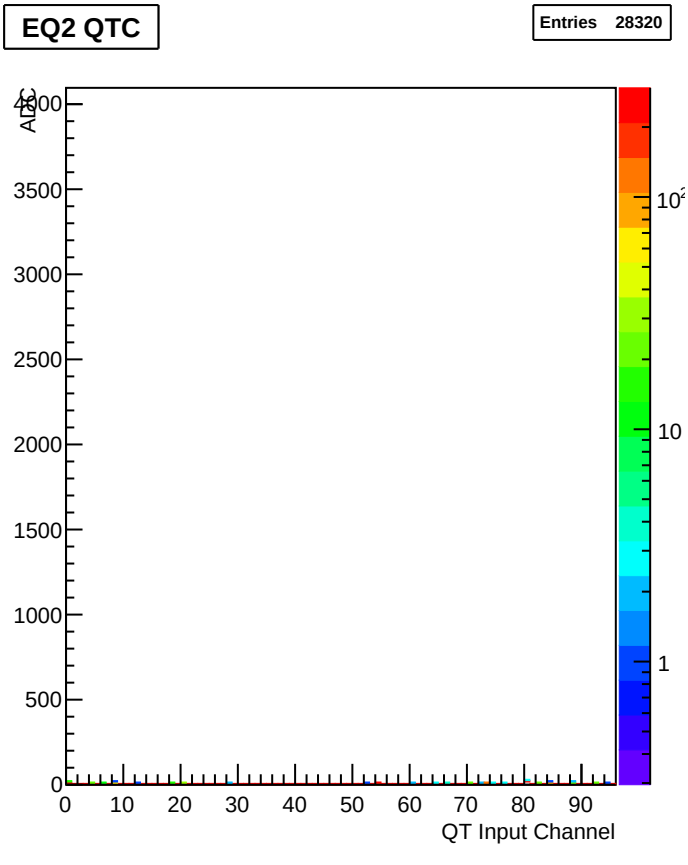

EQ1 QTB
Entries 613
TDC
QT Input Channel

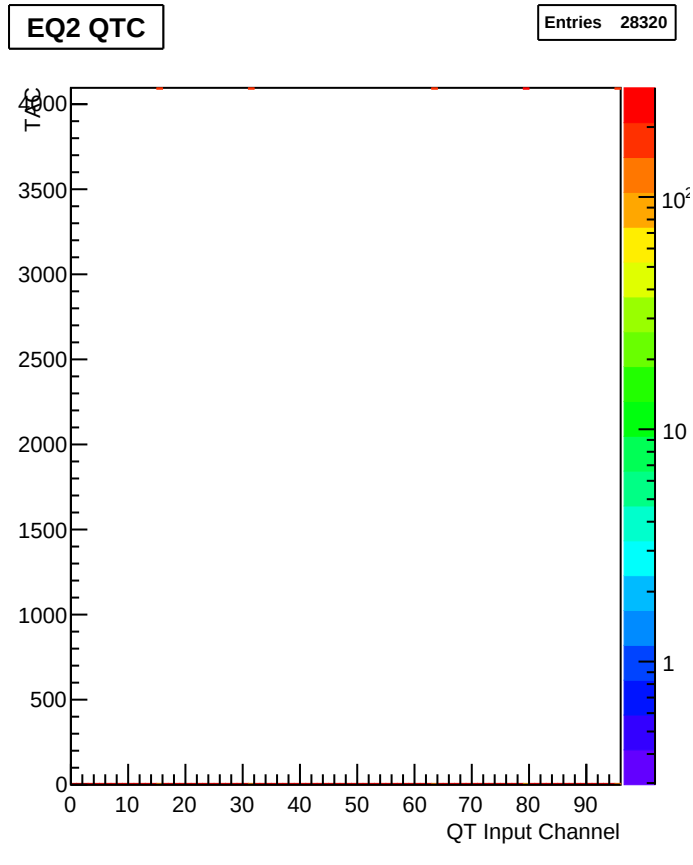

EQ1 QTC
Entries 28320
ADC
QT Input Channel

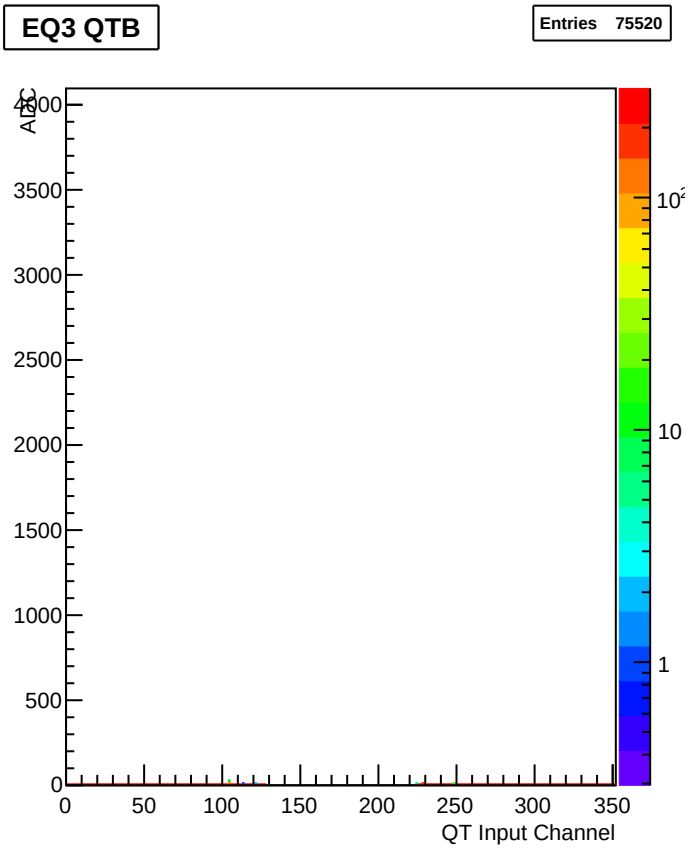

EQ1 QTC
Entries 28320
TADC
QT Input Channel

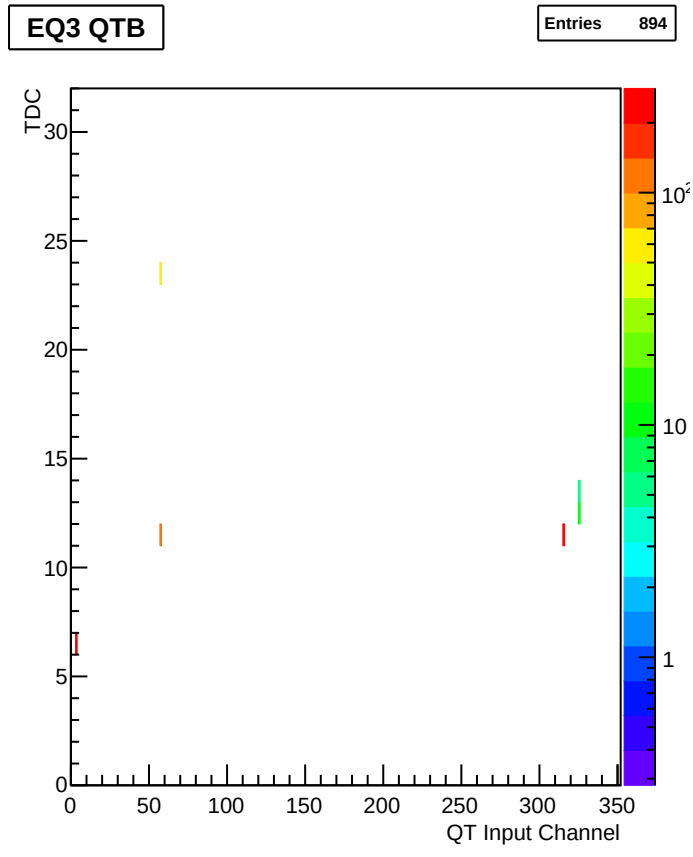

EQ2 QTB
Entries 84960
ADC
QT Input Channel

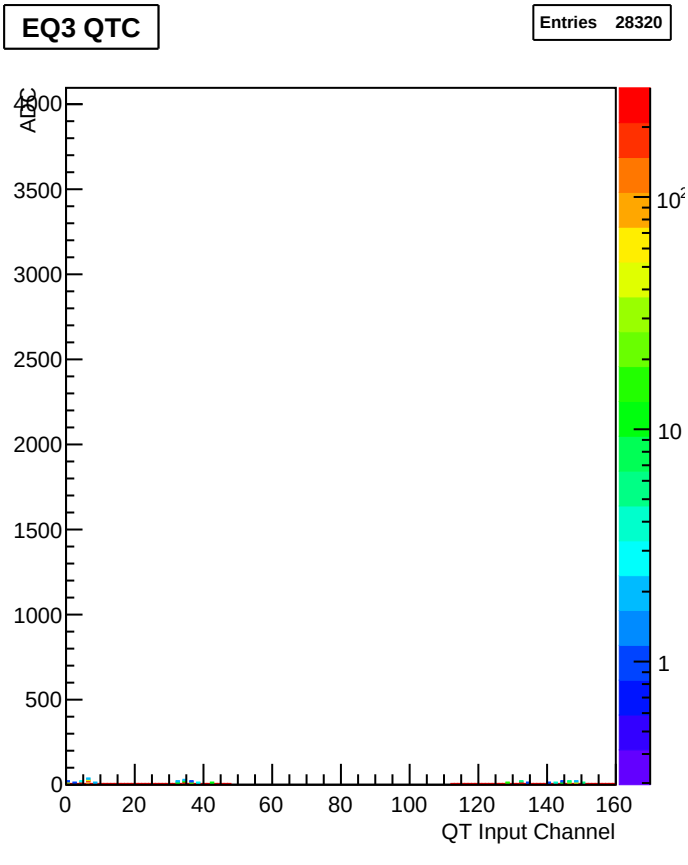

EQ2 QTB
Entries 76
TDC
QT Input Channel

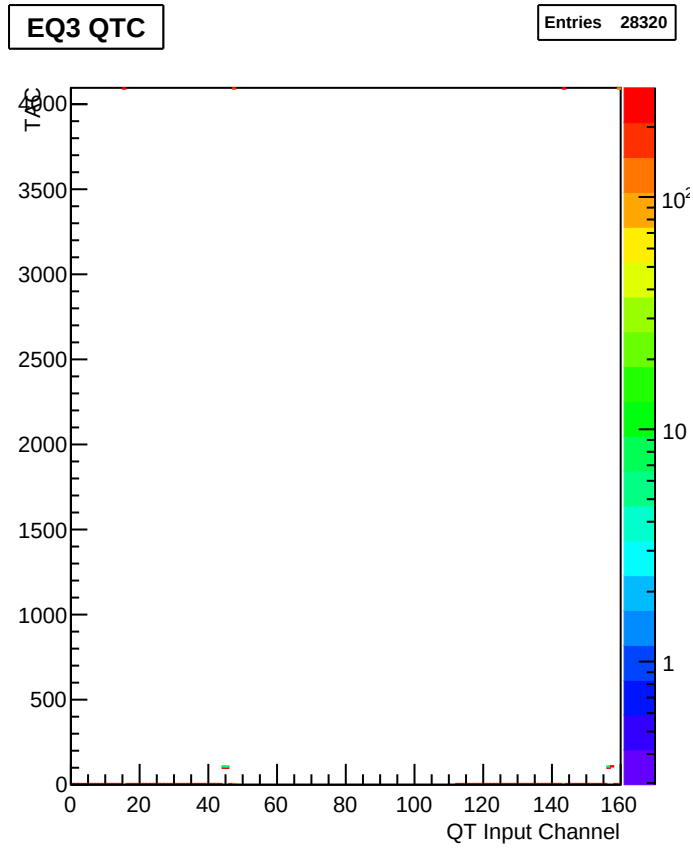

EQ2 QTC
Entries 28320
ADC
QT Input Channel

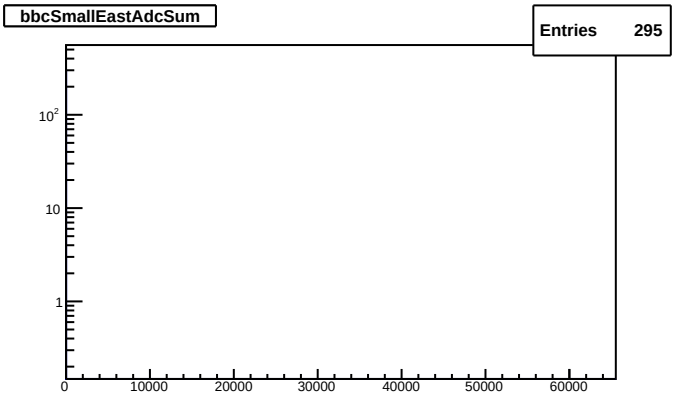

EQ2 QTC
Entries 28320
TADC
QT Input Channel

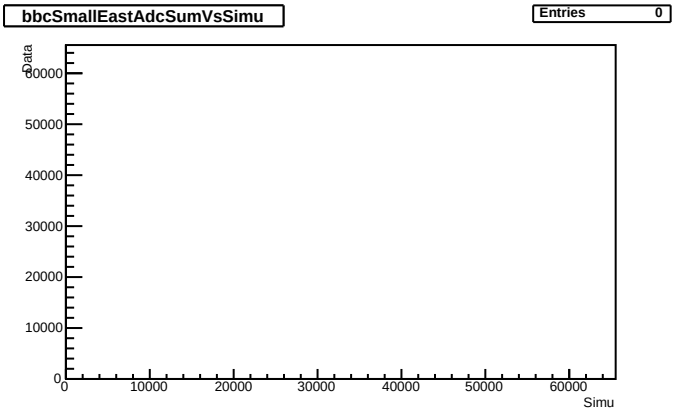

EQ3 QTB
Entries 75520
ADC
QT Input Channel

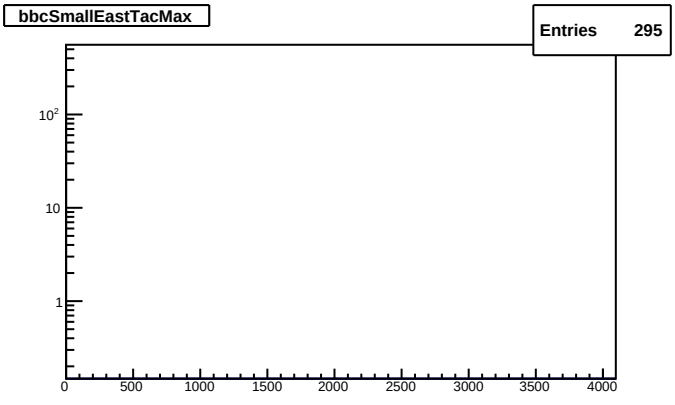

EQ3 QTB
Entries 894
TDC
QT Input Channel

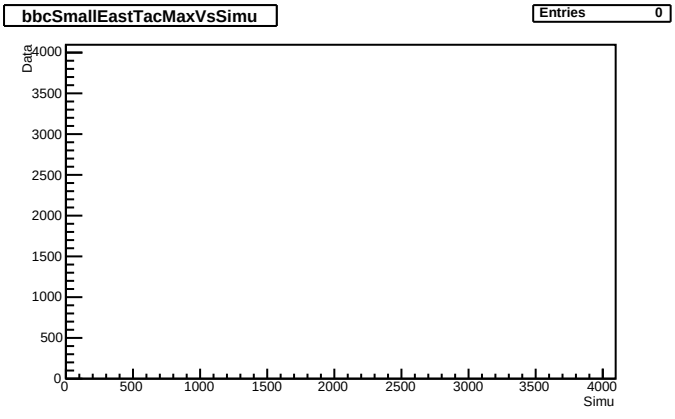

EQ3 QTC
Entries 28320
ADC
QT Input Channel

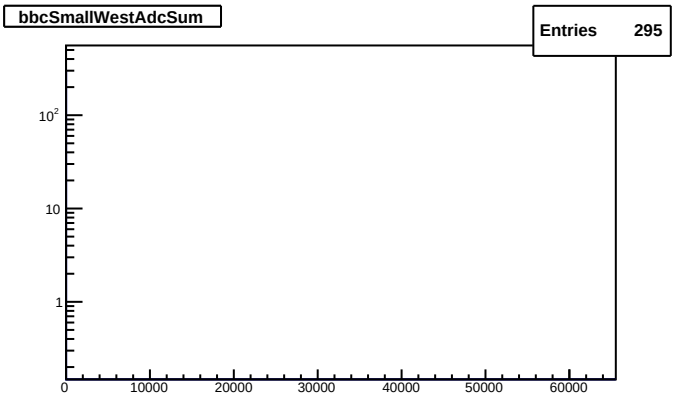

EQ3 QTC
Entries 28320
TADC
QT Input Channel

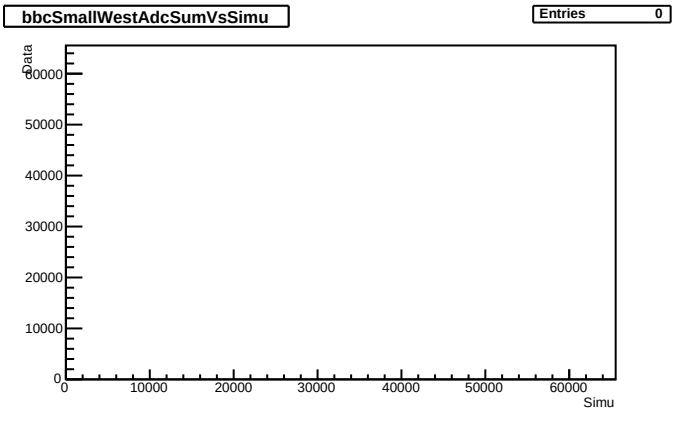

bbcSmallEastAdcSum
Entries 295
10^2
10
1
0
10000
20000
30000
40000
50000
60000

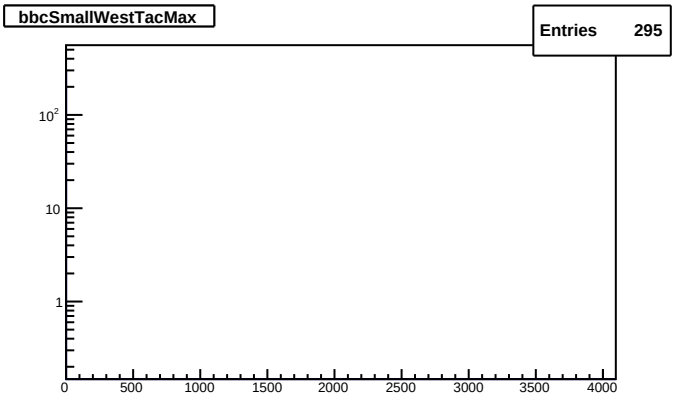

bbcSmallEastAdcSumVsSimu
Entries 0
Data
60000
50000
40000
30000
20000
10000
0
10000
20000
30000
40000
50000
60000
Simu

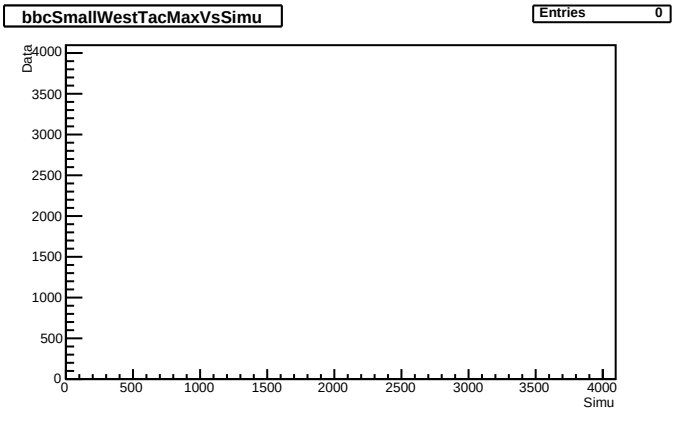

bbcSmallEastTacMax
Entries 295
10^2
10
1
0
500
1000
1500
2000
2500
3000
3500
4000

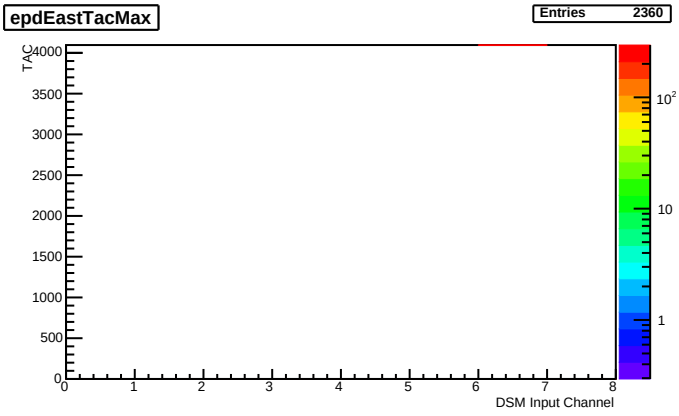

bbcSmallEastTacMaxVsSimu
Entries 0
Data
4000
3500
3000
2500
2000
1500
1000
500
0
500
1000
1500
2000
2500
3000
3500
4000
Simu

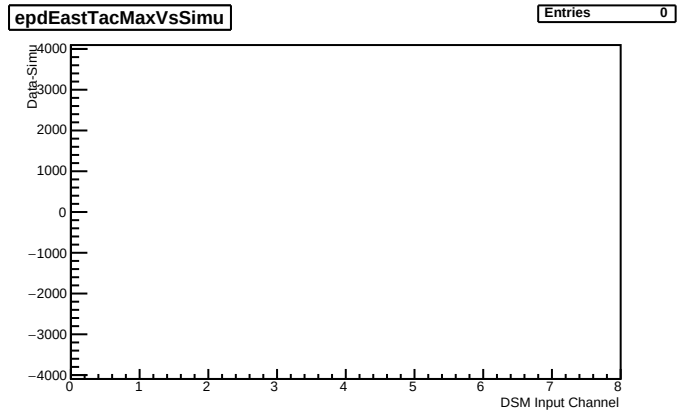

bbcSmallWestAdcSum
Entries 295
10^2
10
1
0
10000
20000
30000
40000
50000
60000

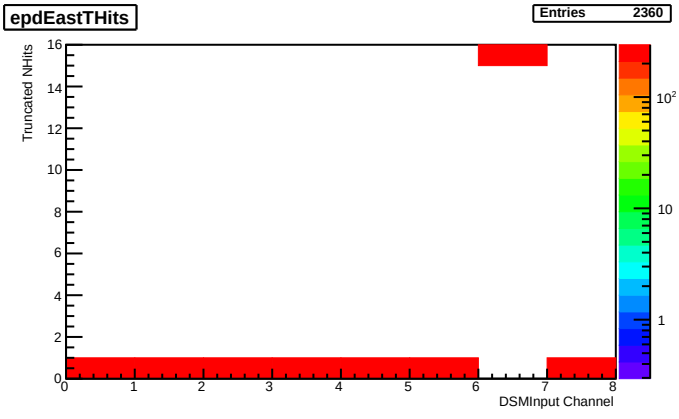

bbcSmallWestAdcSumVsSimu
Entries 0
Data
60000
50000
40000
30000
20000
10000
0
10000
20000
30000
40000
50000
60000
Simu

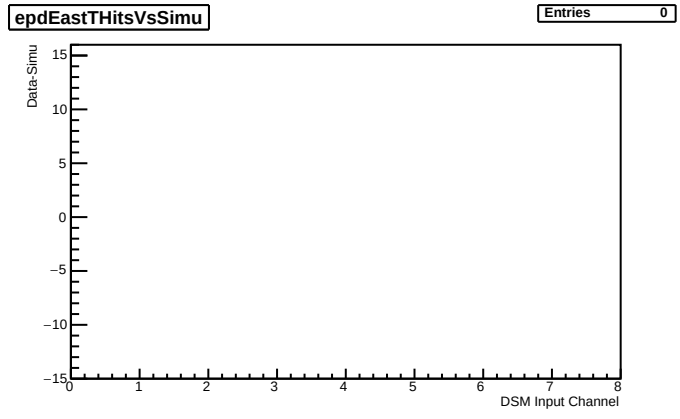

bbcSmallWestTacMax
Entries 295
10^2
10
1
0
500
1000
1500
2000
2500
3000
3500
4000

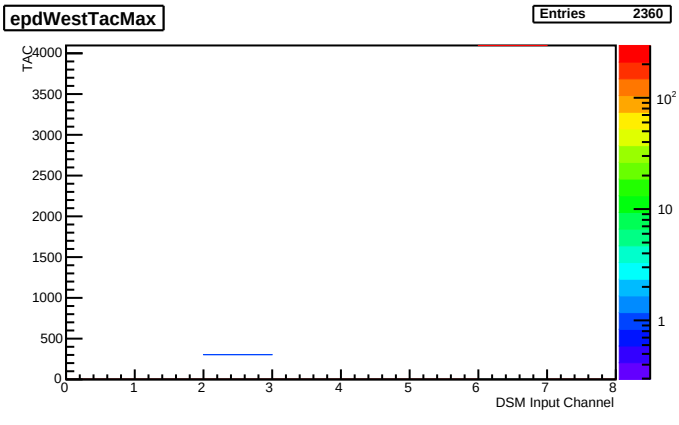

bbcSmallWestTacMaxVsSimu
Entries 0
Data
4000
3500
3000
2500
2000
1500
1000
500
0
500
1000
1500
2000
2500
3000
3500
4000
Simu

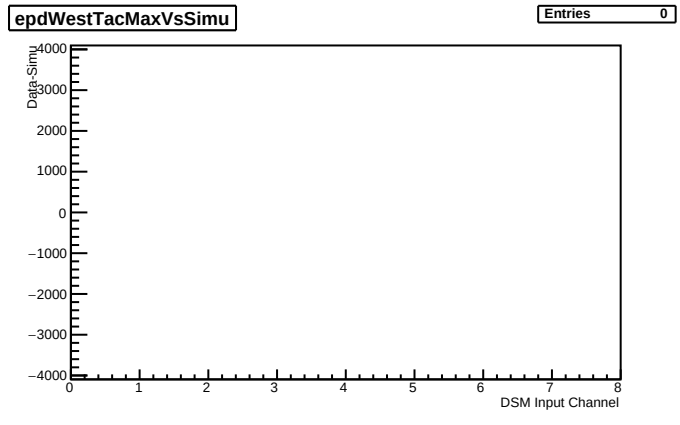

epdEastTacMax
Entries 2360
TAC
DSM Input Channel

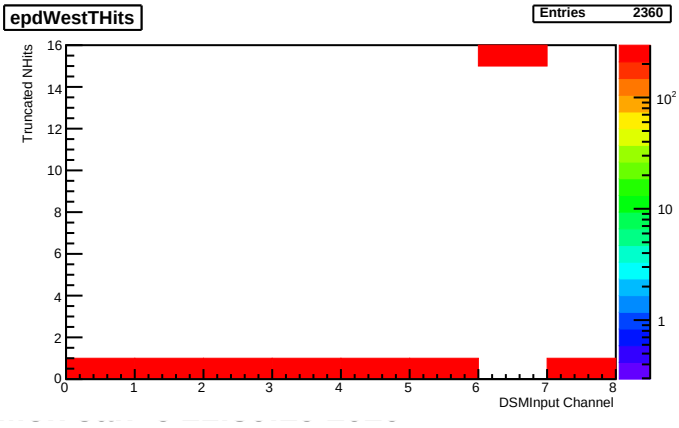

epdEastTacMaxVsSimu
Entries 0
Data-Simu
DSM Input Channel

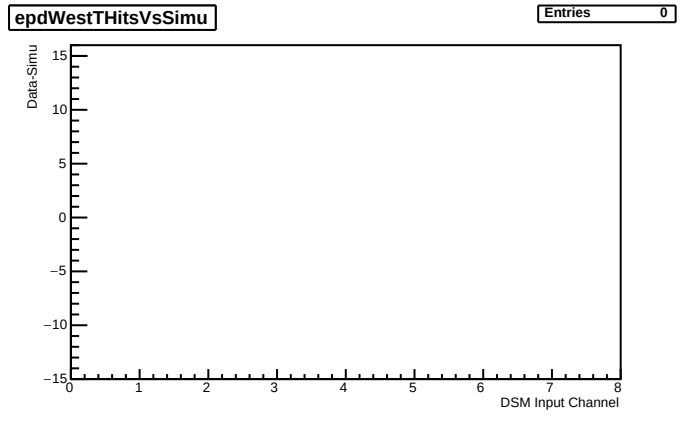

epdEastTHits
Entries 2360
Truncated NHits
DSMInput Channel

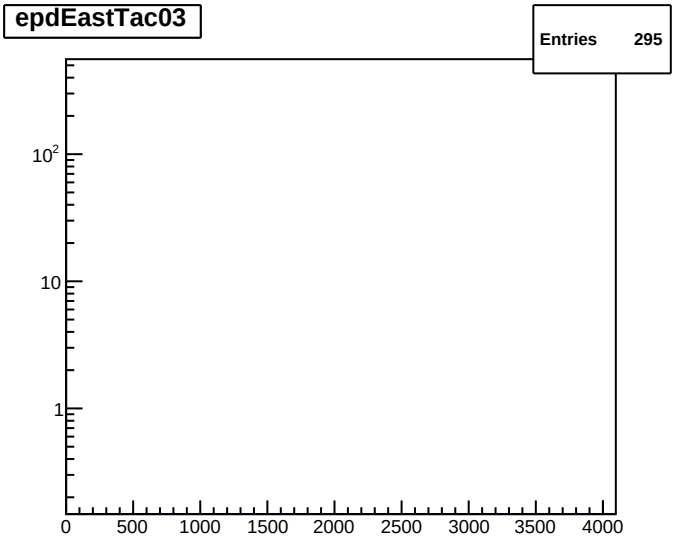

epdEastTHitsVsSimu
Entries 0
Data-Simu
DSM Input Channel

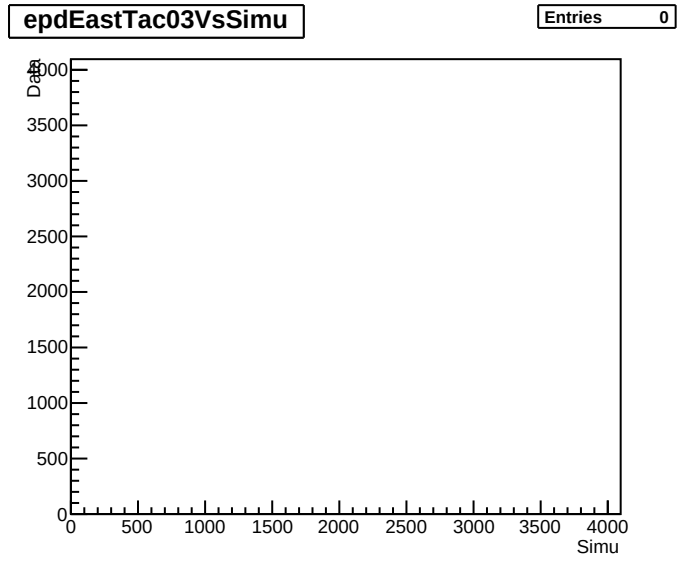

epdWestTacMax
Entries 2360
TAC
DSM Input Channel

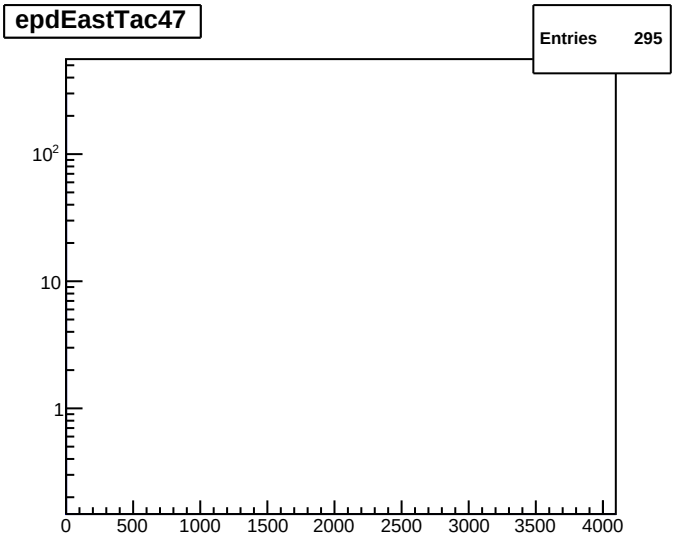

epdWestTacMaxVsSimu
Entries 0
Data-Simu
DSM Input Channel

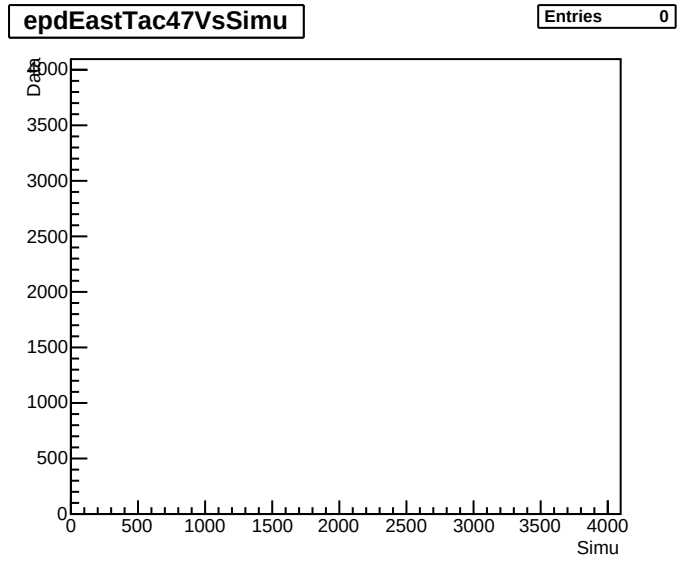

epdWestTHits
Entries 2360
Truncated NHits
DSMInput Channel

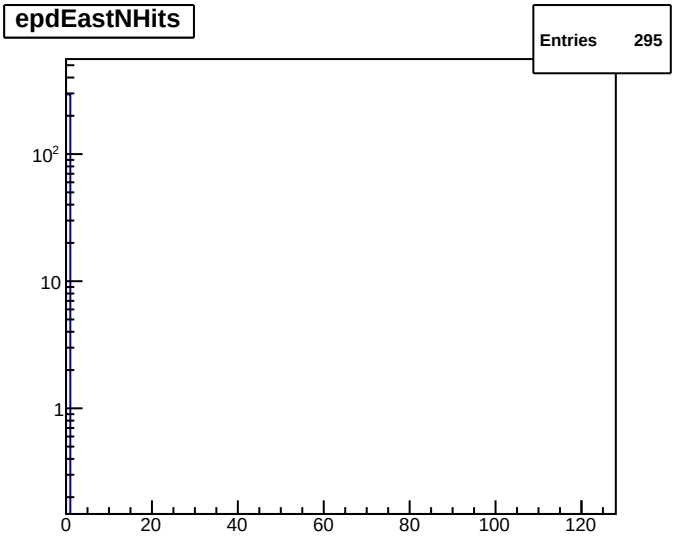

epdWestTHitsVsSimu
Entries 0
Data-Simu
DSM Input Channel

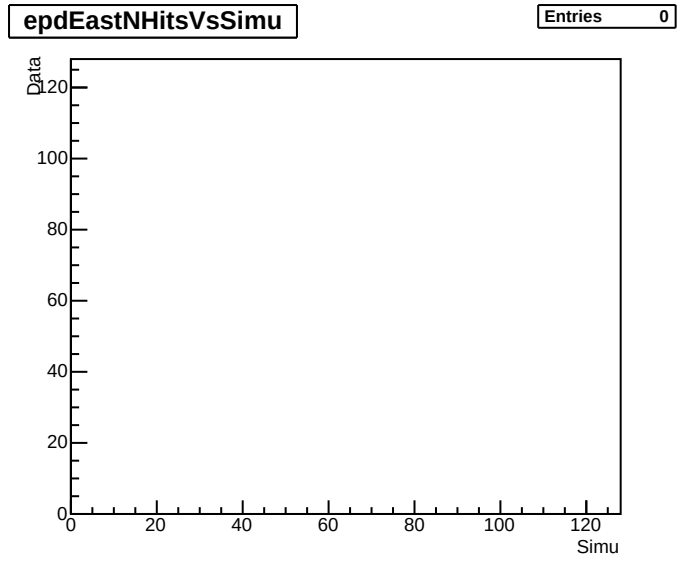

epdEastTac03
Entries 295
10^2
10
1
0 500 1000 1500 2000 2500 3000 3500 4000

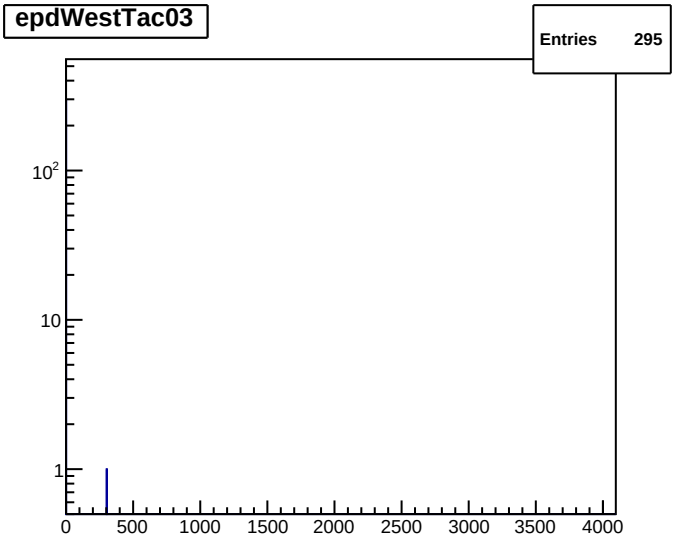

epdEastTac03VsSimu
Entries 0
Data
4000
3500
3000
2500
2000
1500
1000
500
0
0 500 1000 1500 2000 2500 3000 3500 4000
Simu


epdEastTac47
Entries 295
10^2
10
1
0 500 1000 1500 2000 2500 3000 3500 4000


epdEastTac47VsSimu
Entries 0
Data
4000
3500
3000
2500
2000
1500
1000
500
0
0 500 1000 1500 2000 2500 3000 3500 4000
Simu


epdEastNHits
Entries 295
10^2
10
1
0 20 40 60 80 100 120


epdEastNHitsVsSimu
Entries 0
Data
120
100
80
60
40
20
0
0 20 40 60 80 100 120
Simu


epdWestTac03
Entries 295
10^2
10
1
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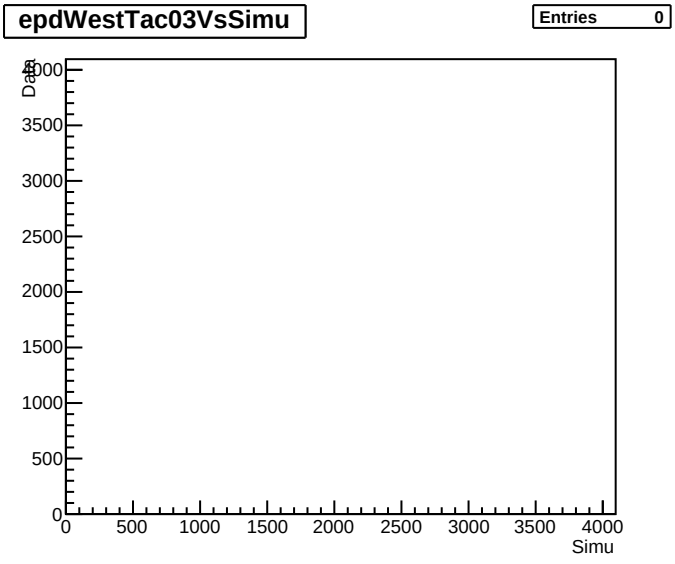
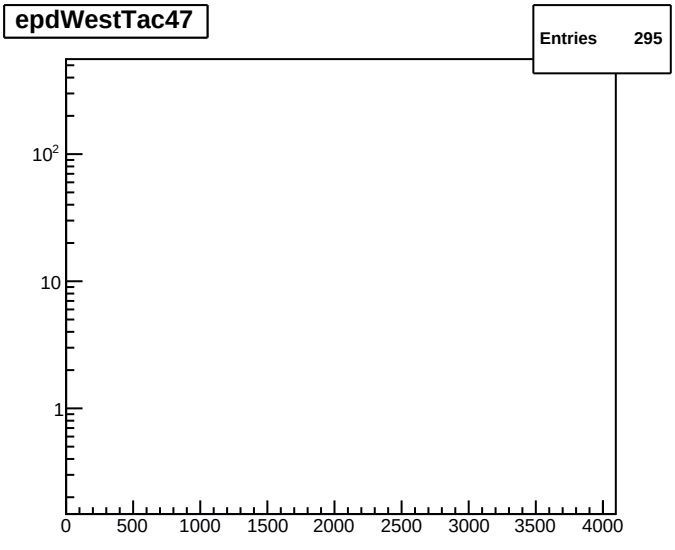
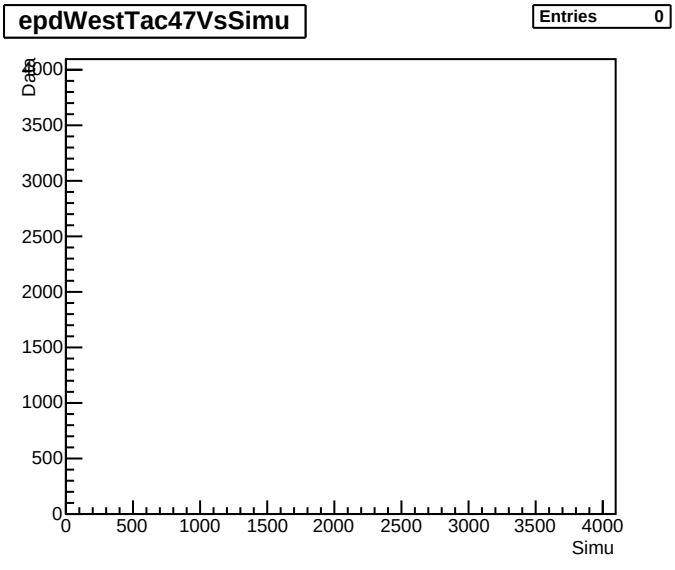
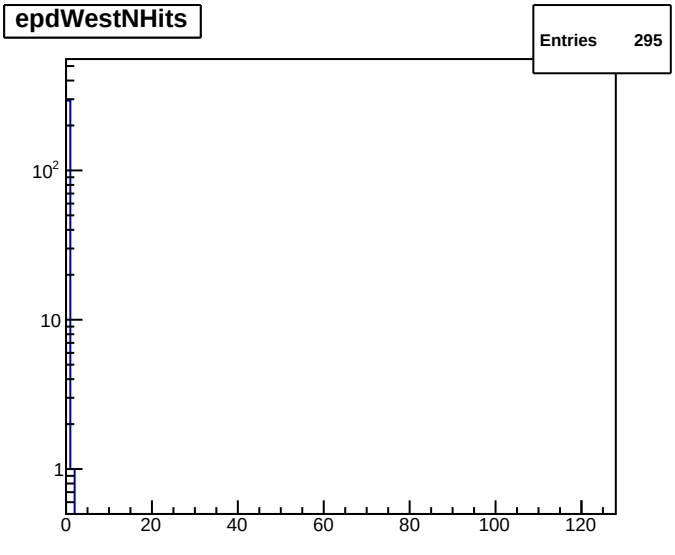
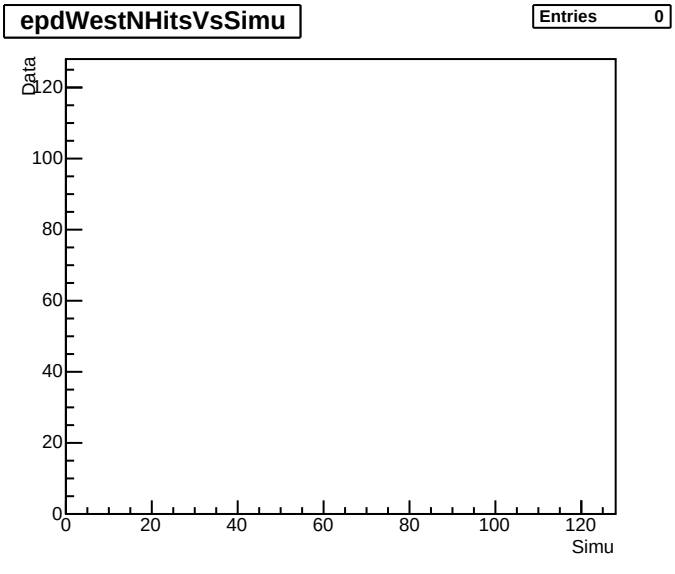
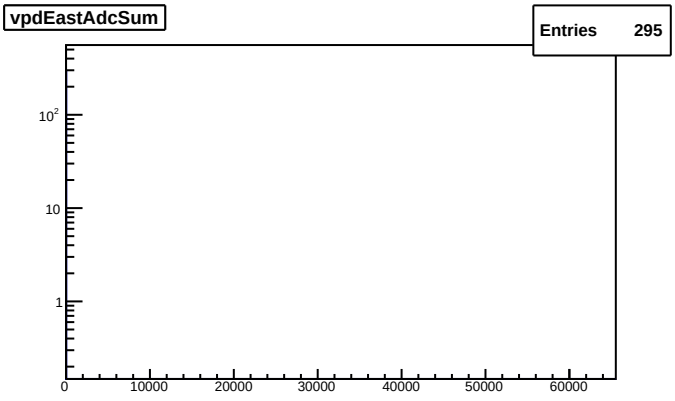

epdWestTac03VsSimu
Entries 0
Data
4000
3500
3000
2500
2000
1500
1000
500
0
0 500 1000 1500 2000 2500 3000 3500 4000
Simu

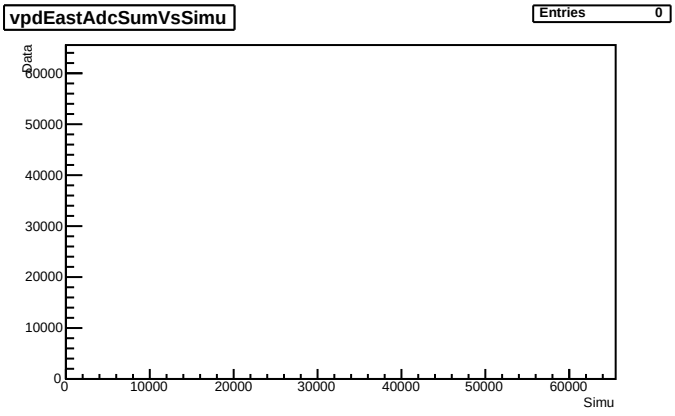

epdWestTac47
Entries 295
10^2
10
1
0 500 1000 1500 2000 2500 3000 3500 4000

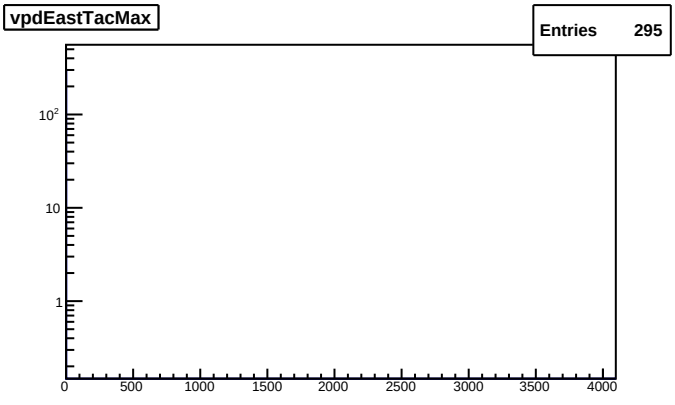

epdWestTac47VsSimu
Entries 0
Data
4000
3500
3000
2500
2000
1500
1000
500
0
0 500 1000 1500 2000 2500 3000 3500 4000
Simu

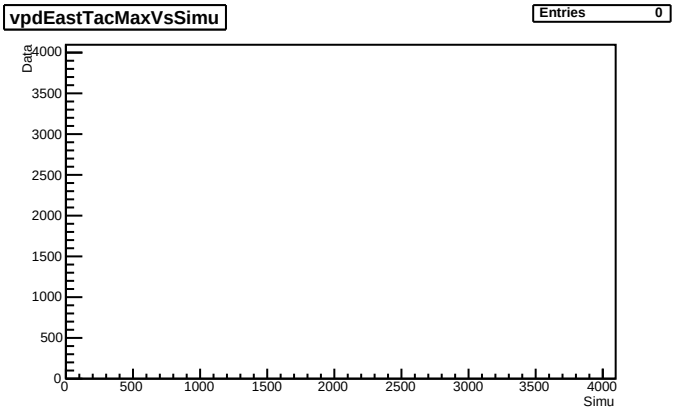

epdWestNHits
Entries 295
10^2
10
1
0 20 40 60 80 100 120

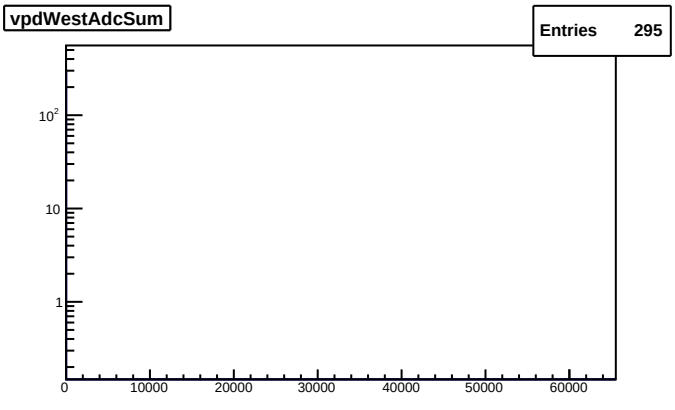

epdWestNHitsVsSimu
Entries 0
Data
120
100
80
60
40
20
0
0 20 40 60 80 100 120
Simu

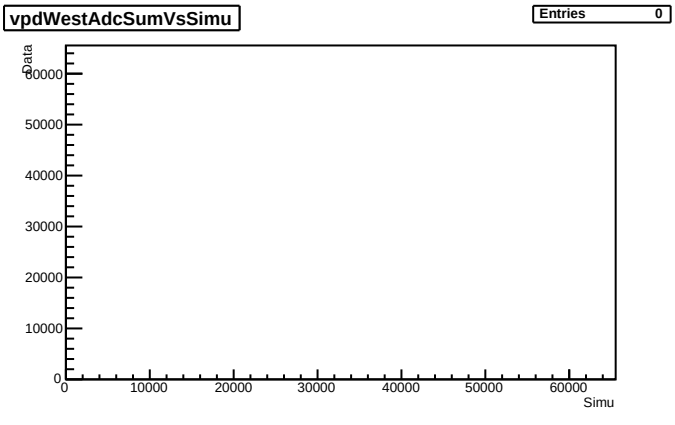

vpdEastAdcSum
Entries 295
10^2
10
1
0
10000
20000
30000
40000
50000
60000

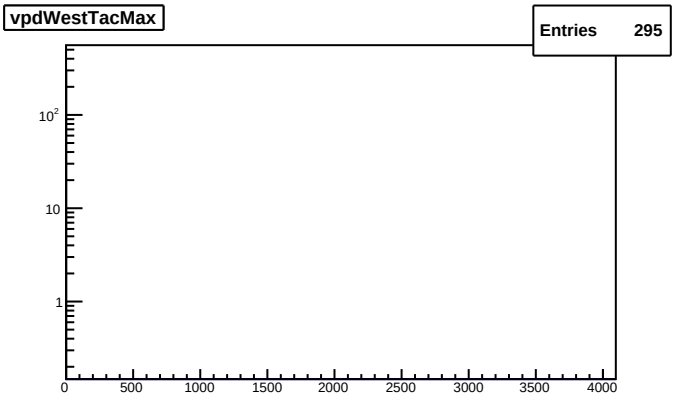

vpdEastAdcSumVsSimu
Entries 0
Data
60000
50000
40000
30000
20000
10000
0
0
10000
20000
30000
40000
50000
60000
Simu

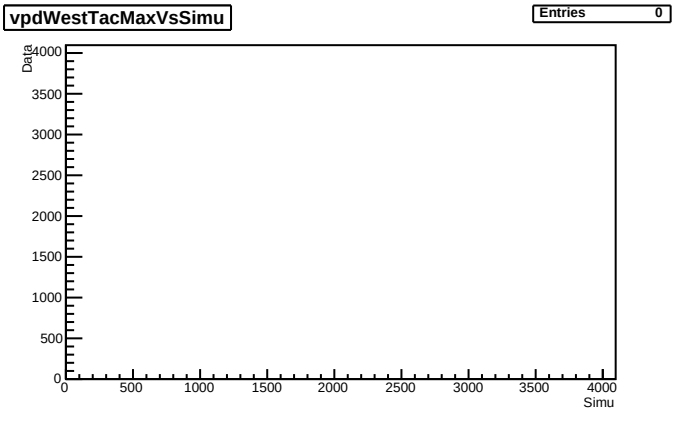

vpdEastTacMax
Entries 295
10^2
10
1
0
500
1000
1500
2000
2500
3000
3500
4000

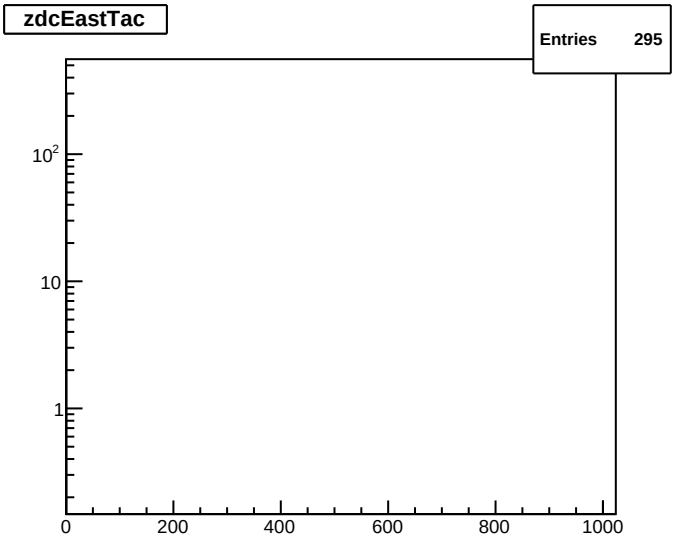

vpdEastTacMaxVsSimu
Entries 0
Data
4000
3500
3000
2500
2000
1500
1000
500
0
0
500
1000
1500
2000
2500
3000
3500
4000
Simu

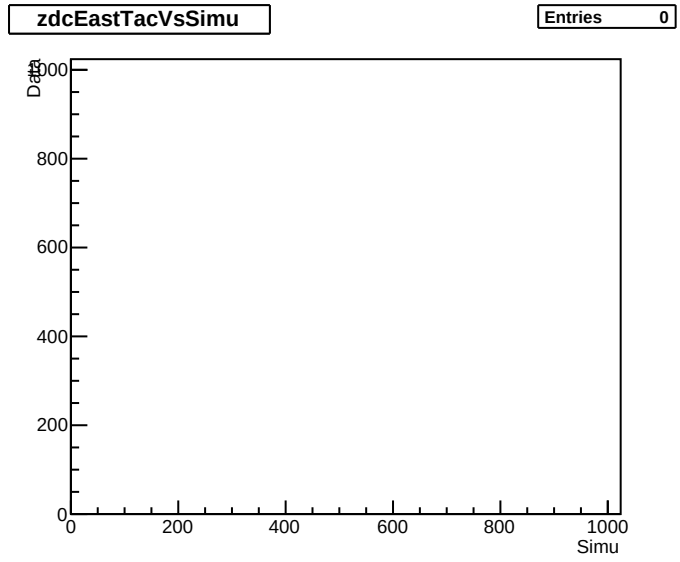

vpdWestAdcSum
Entries 295
10^2
10
1
0
10000
20000
30000
40000
50000
60000

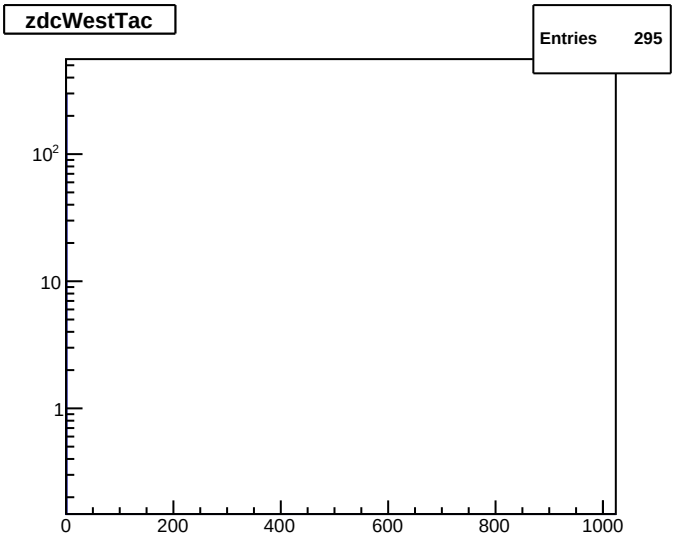

vpdWestAdcSumVsSimu
Entries 0
Data
60000
50000
40000
30000
20000
10000
0
0
10000
20000
30000
40000
50000
60000
Simu

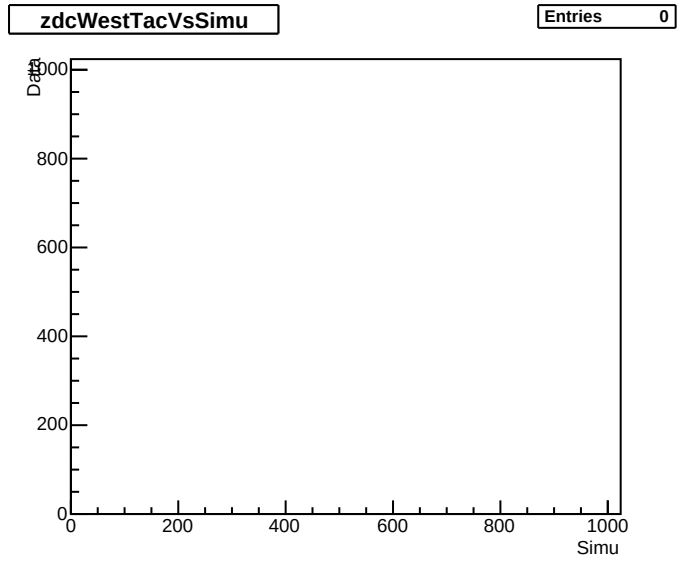

vpdWestTacMax
Entries 295
10^2
10
1
0
500
1000
1500
2000
2500
3000
3500
4000

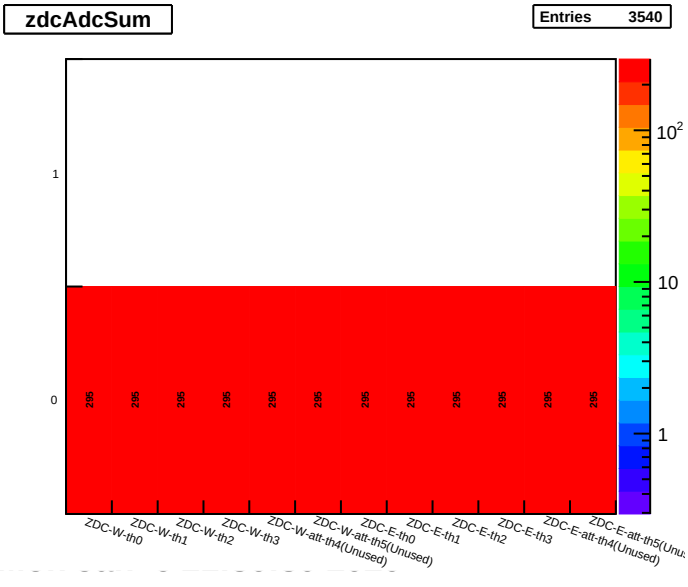

vpdWestTacMaxVsSimu
Entries 0
Data
4000
3500
3000
2500
2000
1500
1000
500
0
0
500
1000
1500
2000
2500
3000
3500
4000
Simu

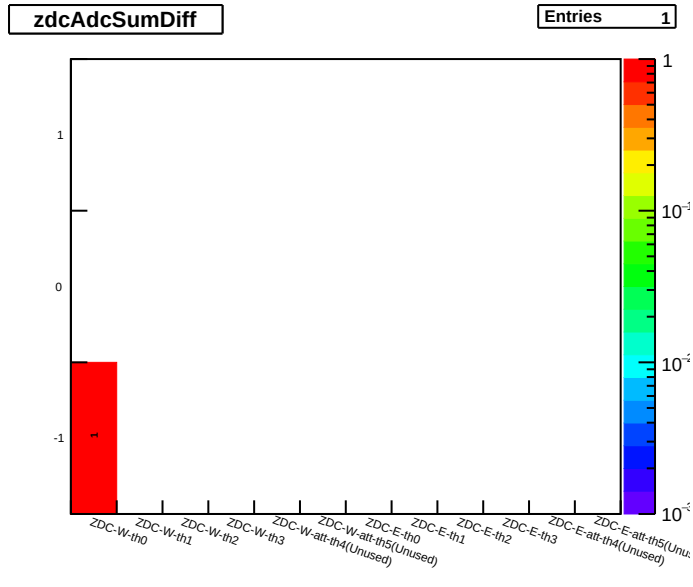

zdcEastTac
Entries 295
10^2
10
1
0
200
400
600
800
1000

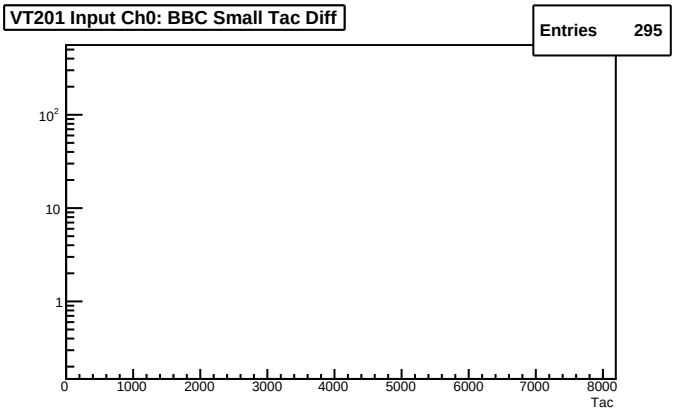

zdcEastTacVsSimu
Entries 0
Data
1000
800
600
400
200
0
0
200
400
600
800
1000
Simu

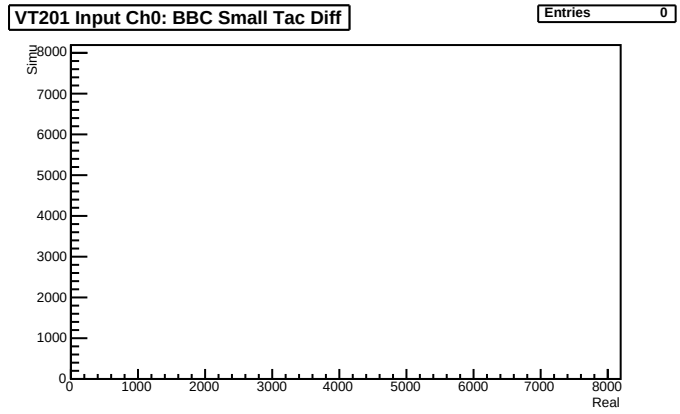

zdcWestTac
Entries 295
10^2
10
1
0
200
400
600
800
1000

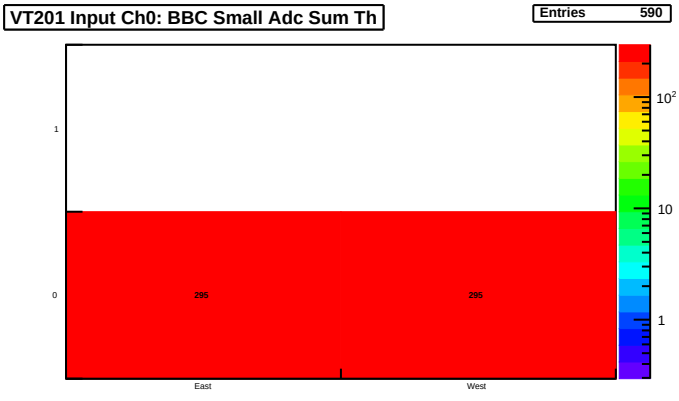

zdcWestTacVsSimu
Entries 0
Data
1000
800
600
400
200
0
0
200
400
600
800
1000
Simu

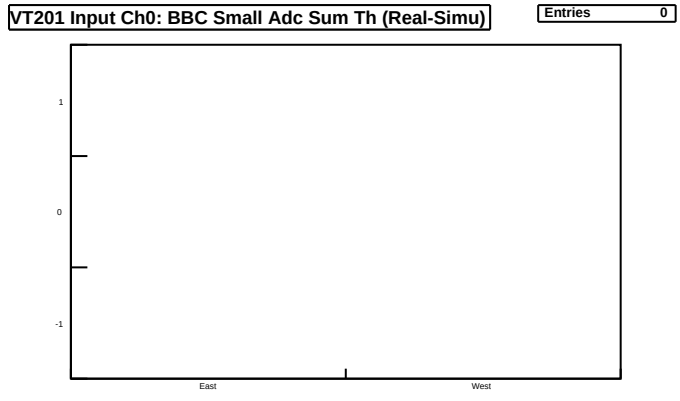

zdcAdcSum
Entries 3540
1
0
295
10^2
10
1
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)
ZDC-E-att-th5(Unu

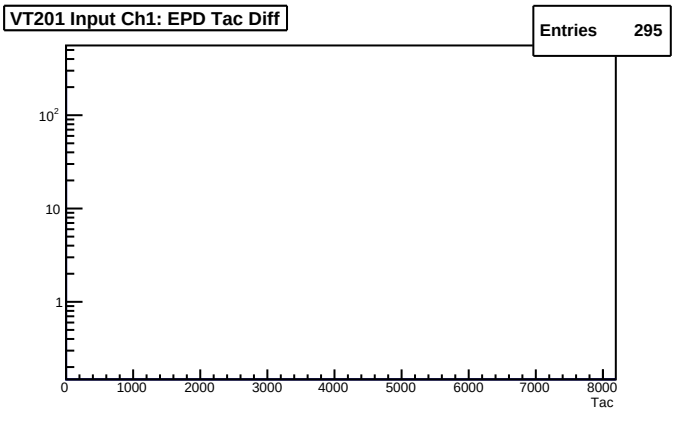

zdcAdcSumDiff
Entries 1
1
0
-1
1
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)
ZDC-E-att-th5(Unu

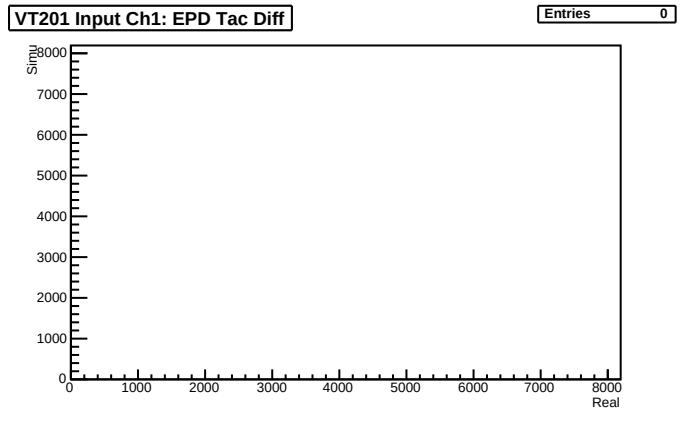

VT201 Input Ch0: BBC Small Tac Diff
Entries 295
Tac

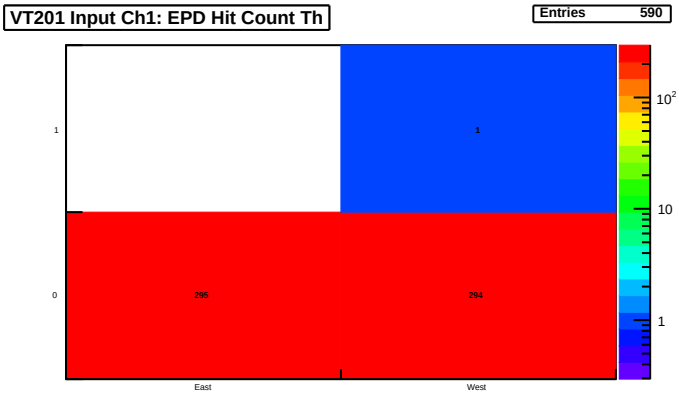

VT201 Input Ch0: BBC Small Tac Diff
Entries 0
Simu
Real

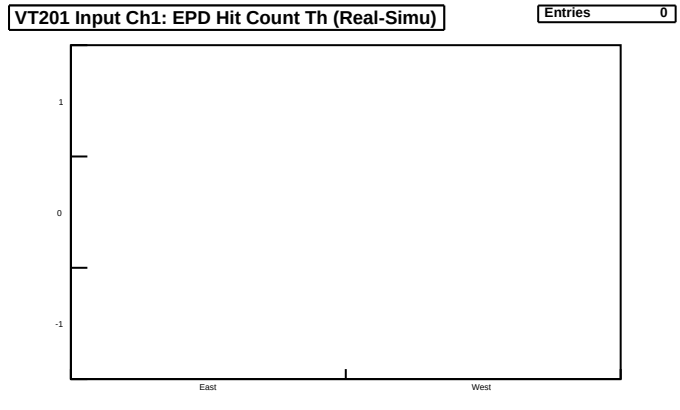

VT201 Input Ch0: BBC Small Adc Sum Th
Entries 590
295
295
East
West

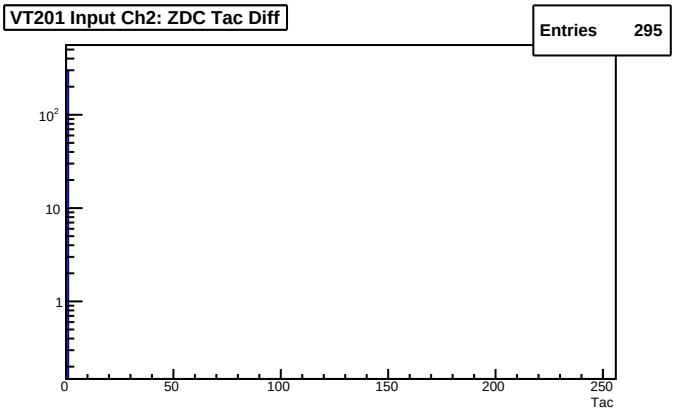

VT201 Input Ch0: BBC Small Adc Sum Th (Real-Simu)
Entries 0
East
West

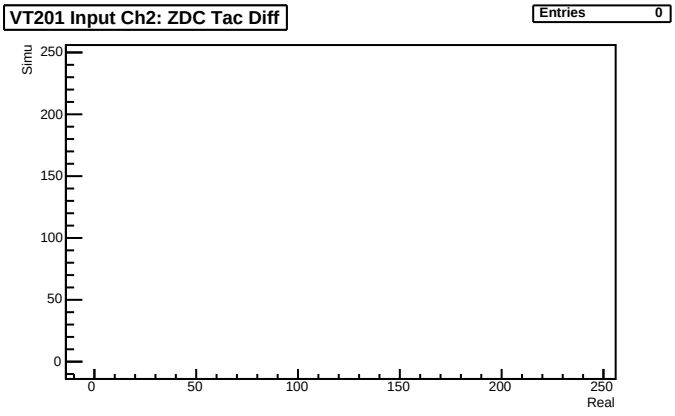

VT201 Input Ch1: EPD Tac Diff
Entries 295
Tac

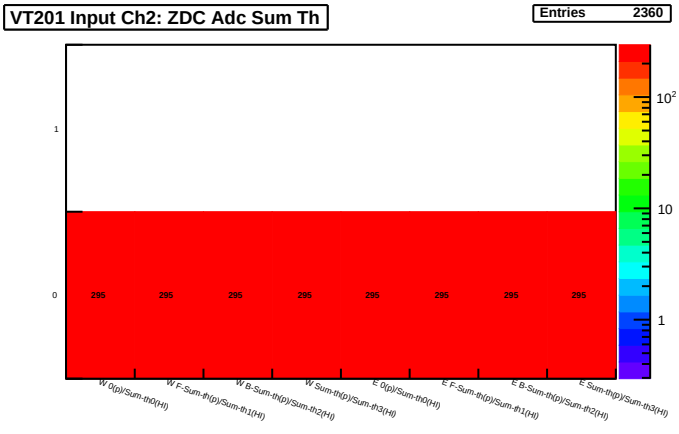

VT201 Input Ch1: EPD Tac Diff
Entries 0
Simu
Real

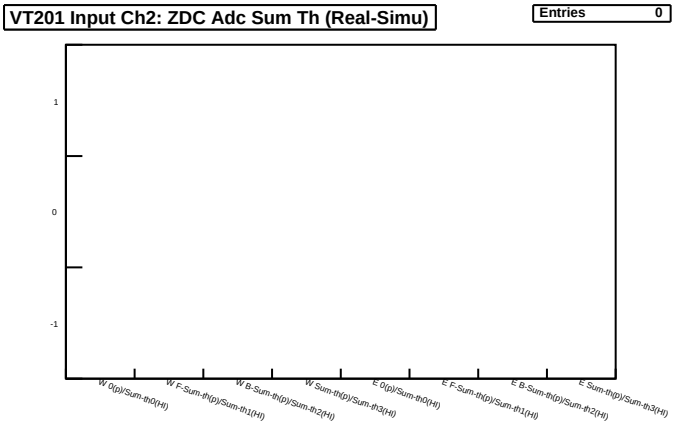

VT201 Input Ch1: EPD Hit Count Th
Entries 590
1
295
294
East
West

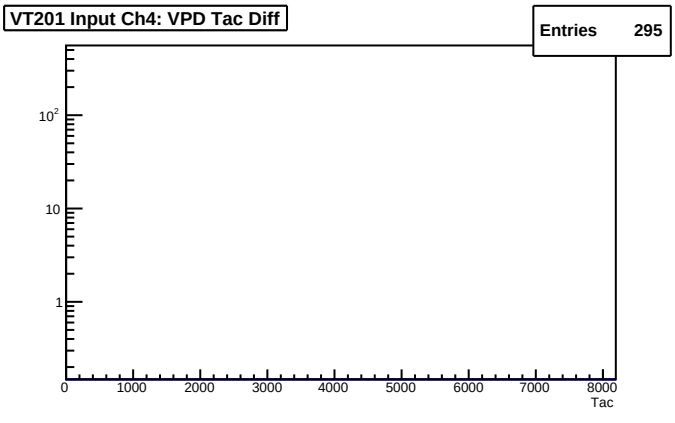

VT201 Input Ch1: EPD Hit Count Th (Real-Simu)
Entries 0
East
West

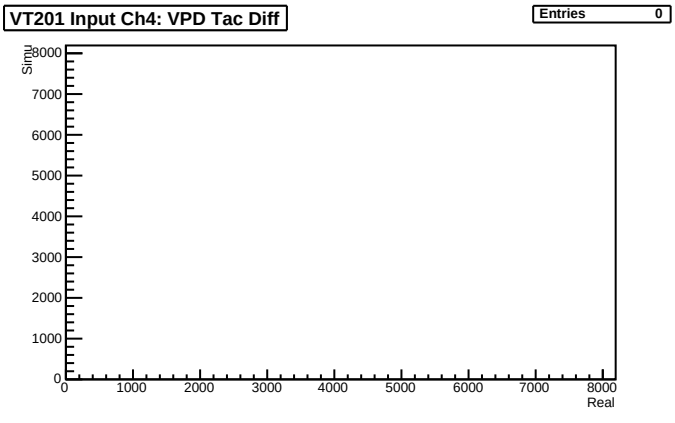

VT201 Input Ch2: ZDC Tac Diff
Entries 295
Tac

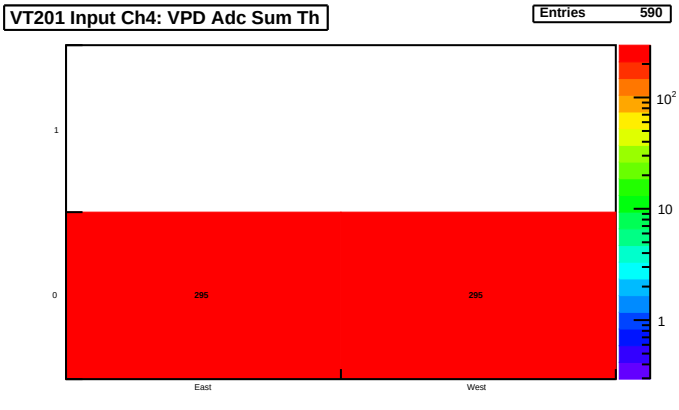

VT201 Input Ch2: ZDC Tac Diff
Entries 0
Simu
Real

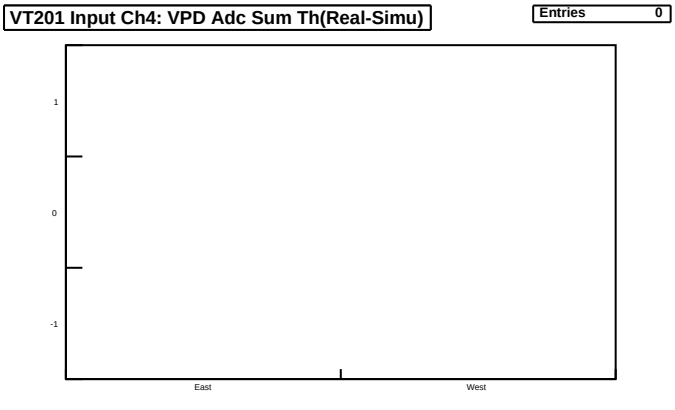

VT201 Input Ch2: ZDC Adc Sum Th
Entries 2360
295
W 0(p)/Sum-th0(Hl)
W F-Sum-th(p)/Sum-th1(Hl)
W B-Sum-th(p)/Sum-th2(Hl)
W Sum-th(p)/Sum-th3(Hl)
E 0(p)/Sum-th0(Hl)
E F-Sum-th(p)/Sum-th1(Hl)
E B-Sum-th(p)/Sum-th2(Hl)
E Sum-th(p)/Sum-th3(Hl)

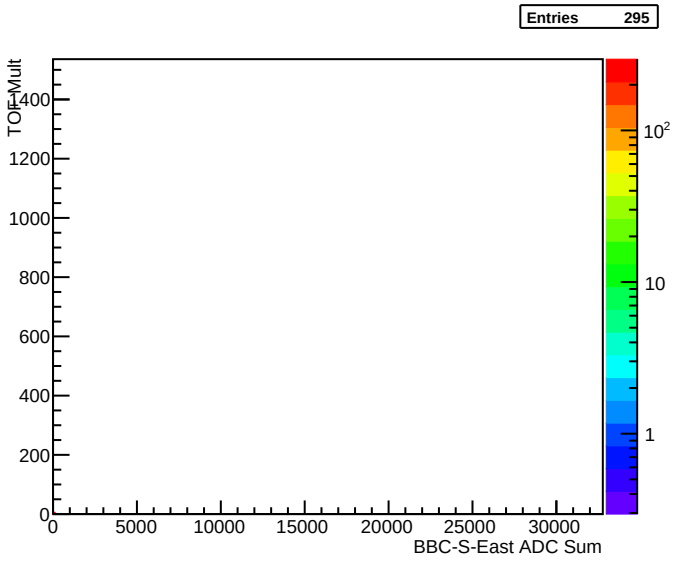

VT201 Input Ch2: ZDC Adc Sum Th (Real-Simu)
Entries 0
W 0(p)/Sum-th0(Hl)
W F-Sum-th(p)/Sum-th1(Hl)
W B-Sum-th(p)/Sum-th2(Hl)
W Sum-th(p)/Sum-th3(Hl)
E 0(p)/Sum-th0(Hl)
E F-Sum-th(p)/Sum-th1(Hl)
E B-Sum-th(p)/Sum-th2(Hl)
E Sum-th(p)/Sum-th3(Hl)

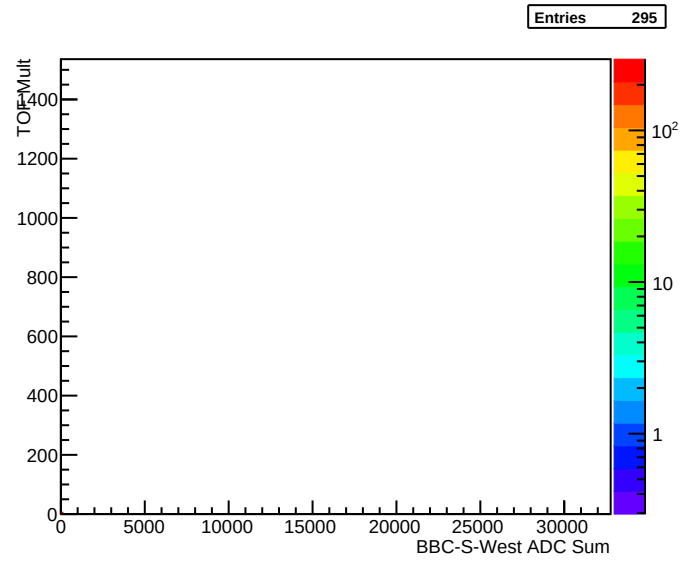

VT201 Input Ch4: VPD Tac Diff
Entries 295
Tac

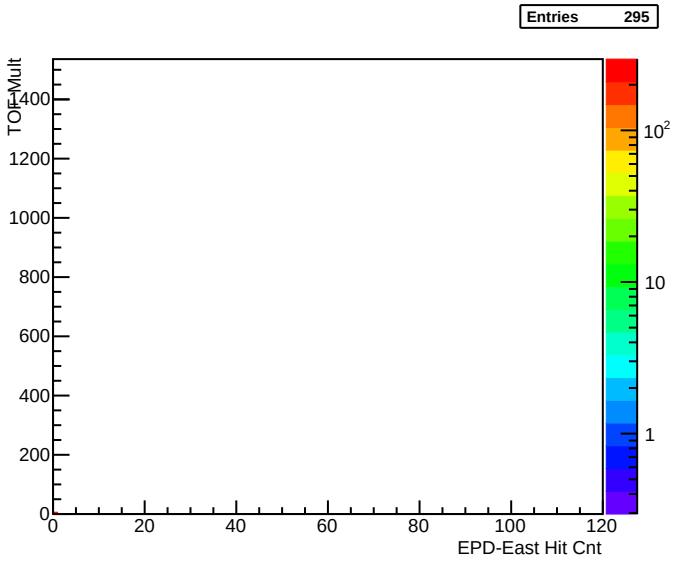

VT201 Input Ch4: VPD Tac Diff
Entries 0
Simu
Real

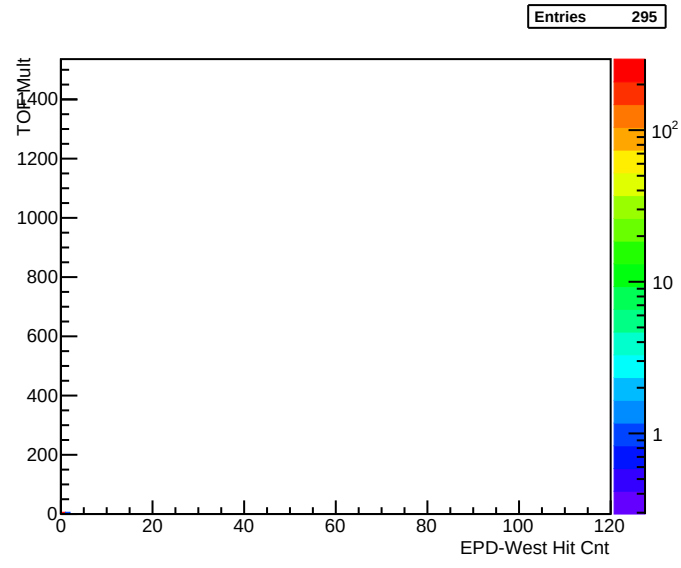

VT201 Input Ch4: VPD Adc Sum Th
Entries 590
295
295
East
West

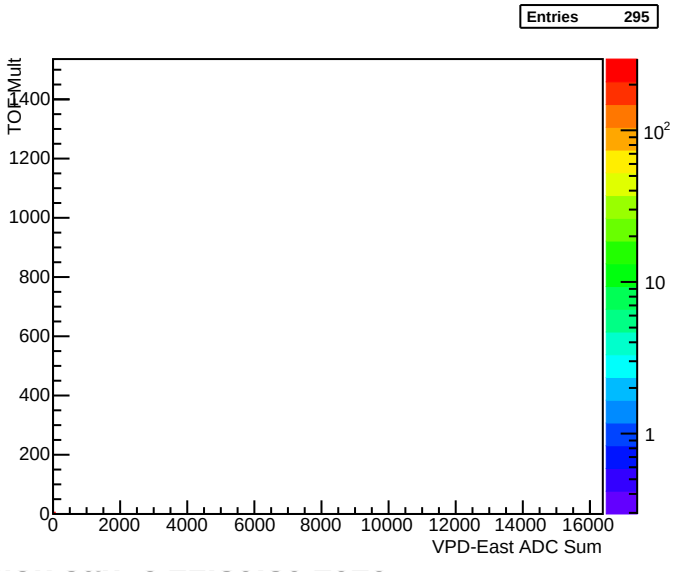

VT201 Input Ch4: VPD Adc Sum Th(Real-Simu)
Entries 0
East
West

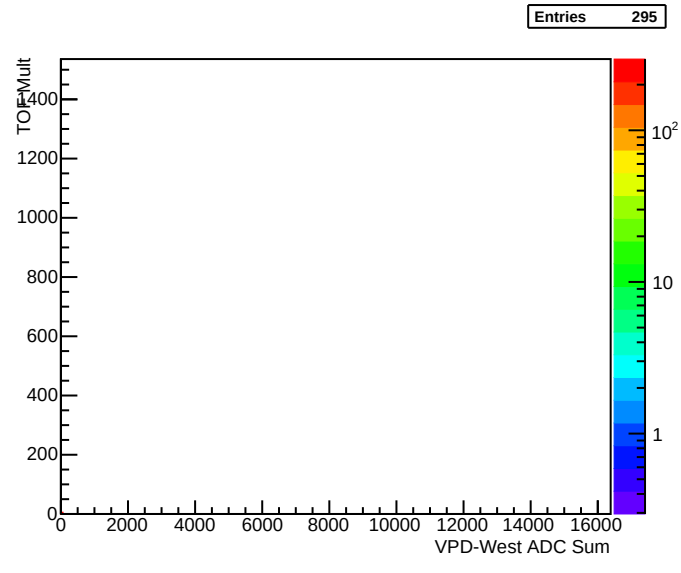

Entries 295
TOF-Mult
BBC-S-East ADC Sum

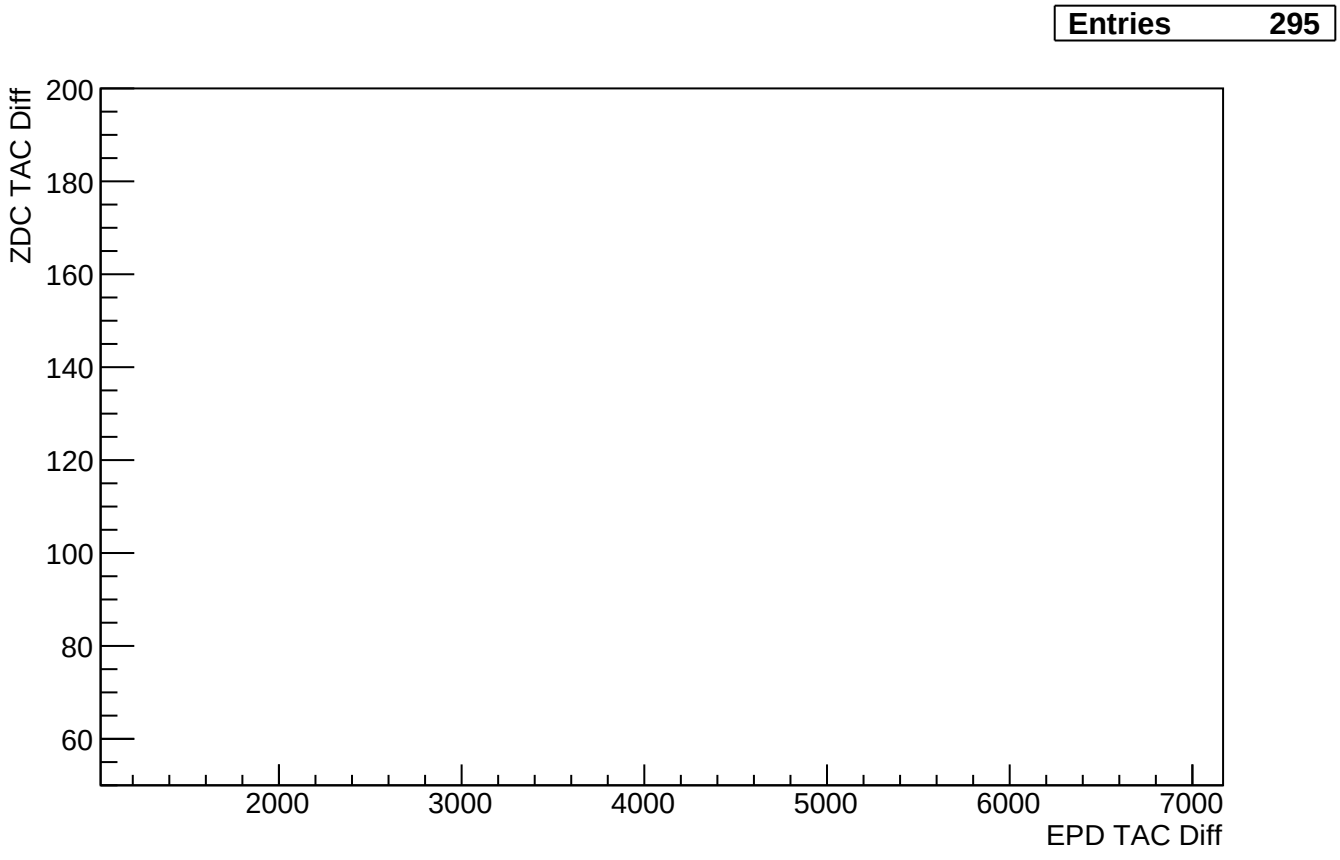

Entries 295
TOF-Mult
BBC-S-West ADC Sum

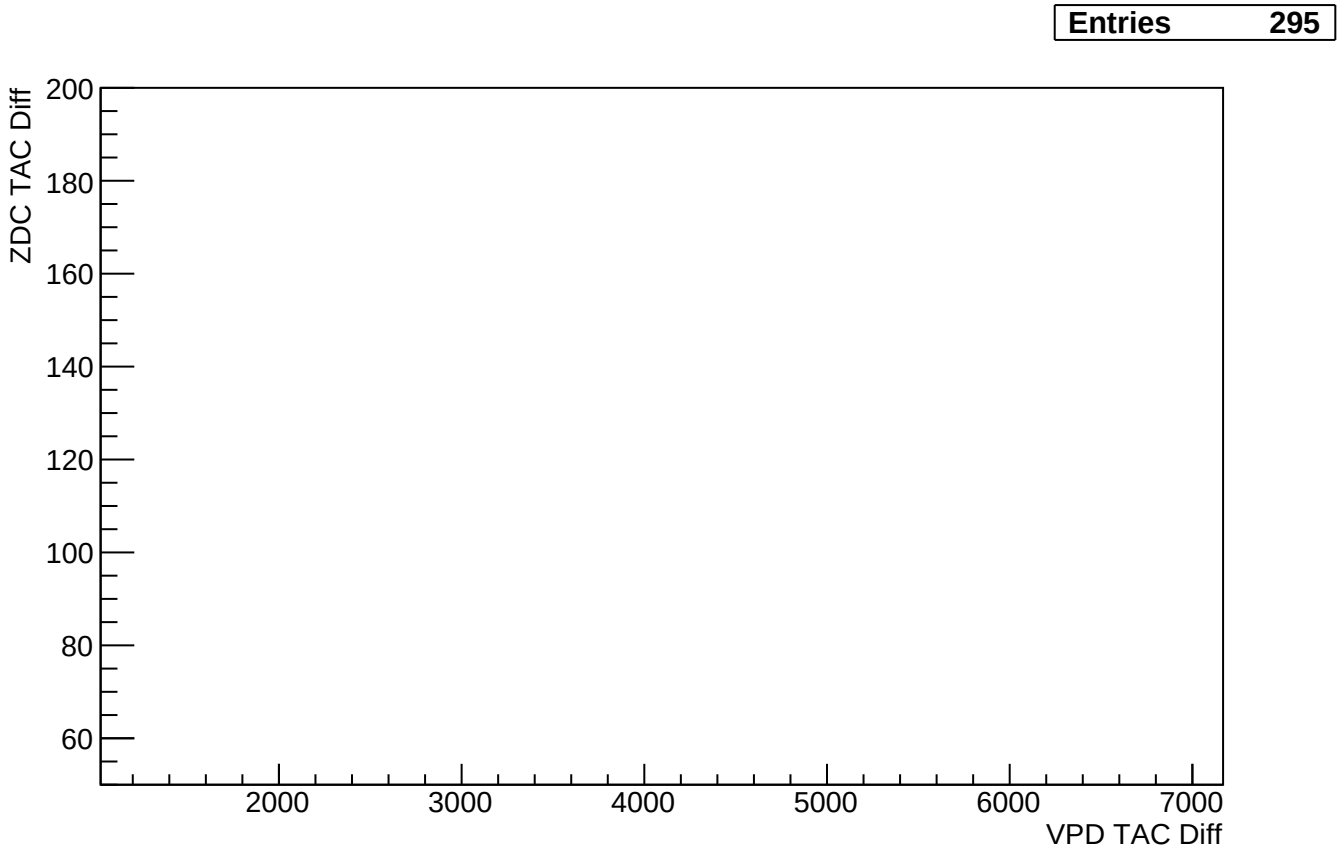

Entries 295
TOF-Mult
EPD-East Hit Cnt

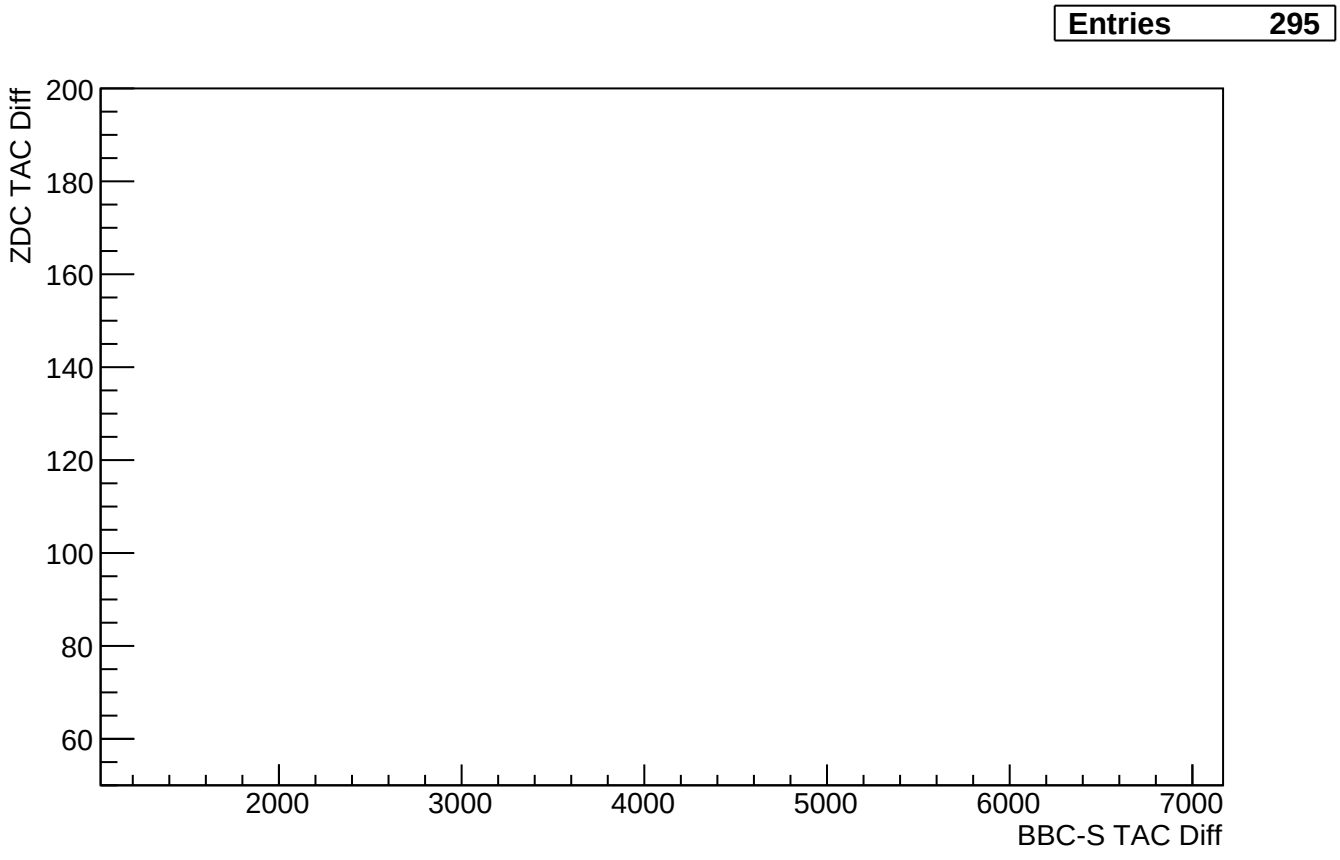

Entries 295
TOF-Mult
EPD-West Hit Cnt

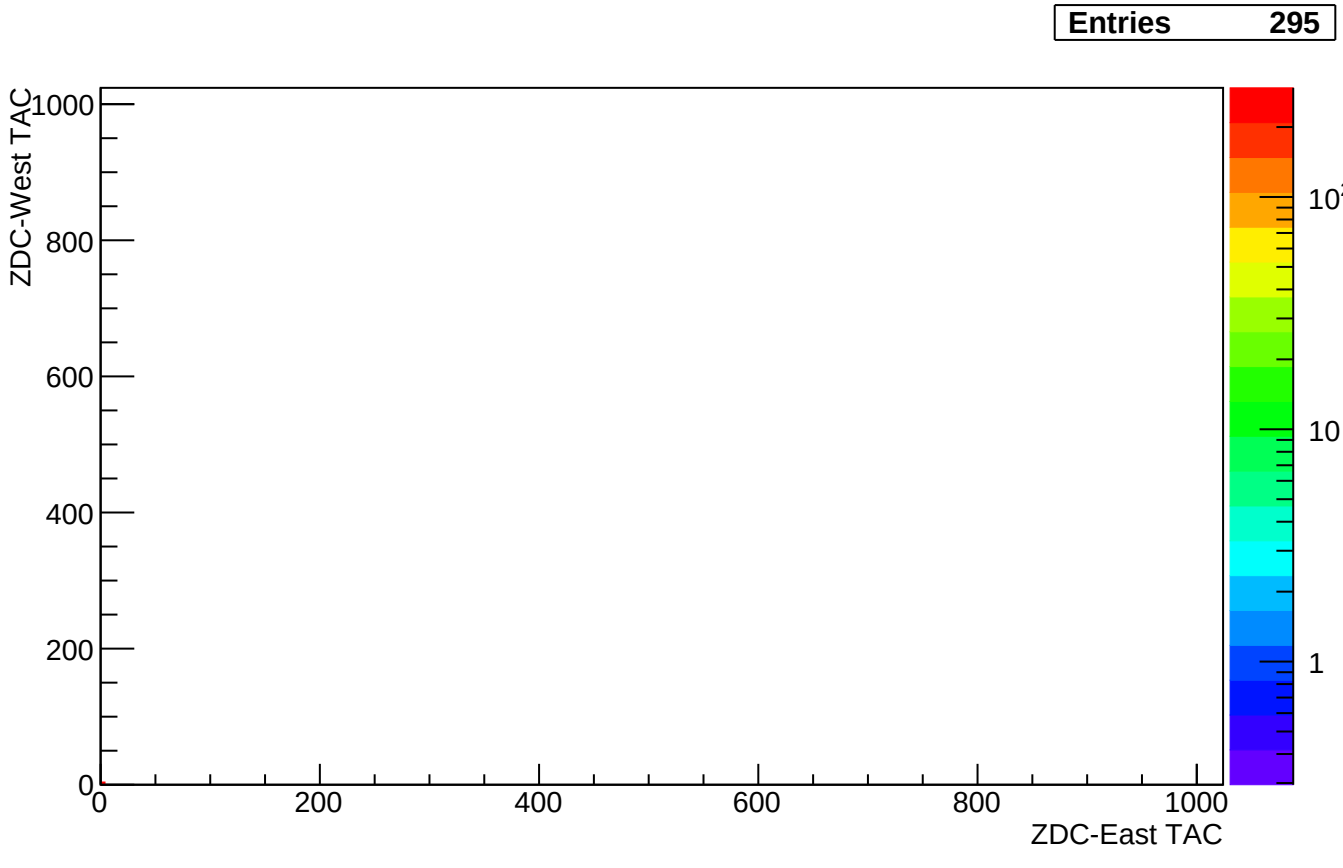

Entries 295
TOF-Mult
VPD-East ADC Sum

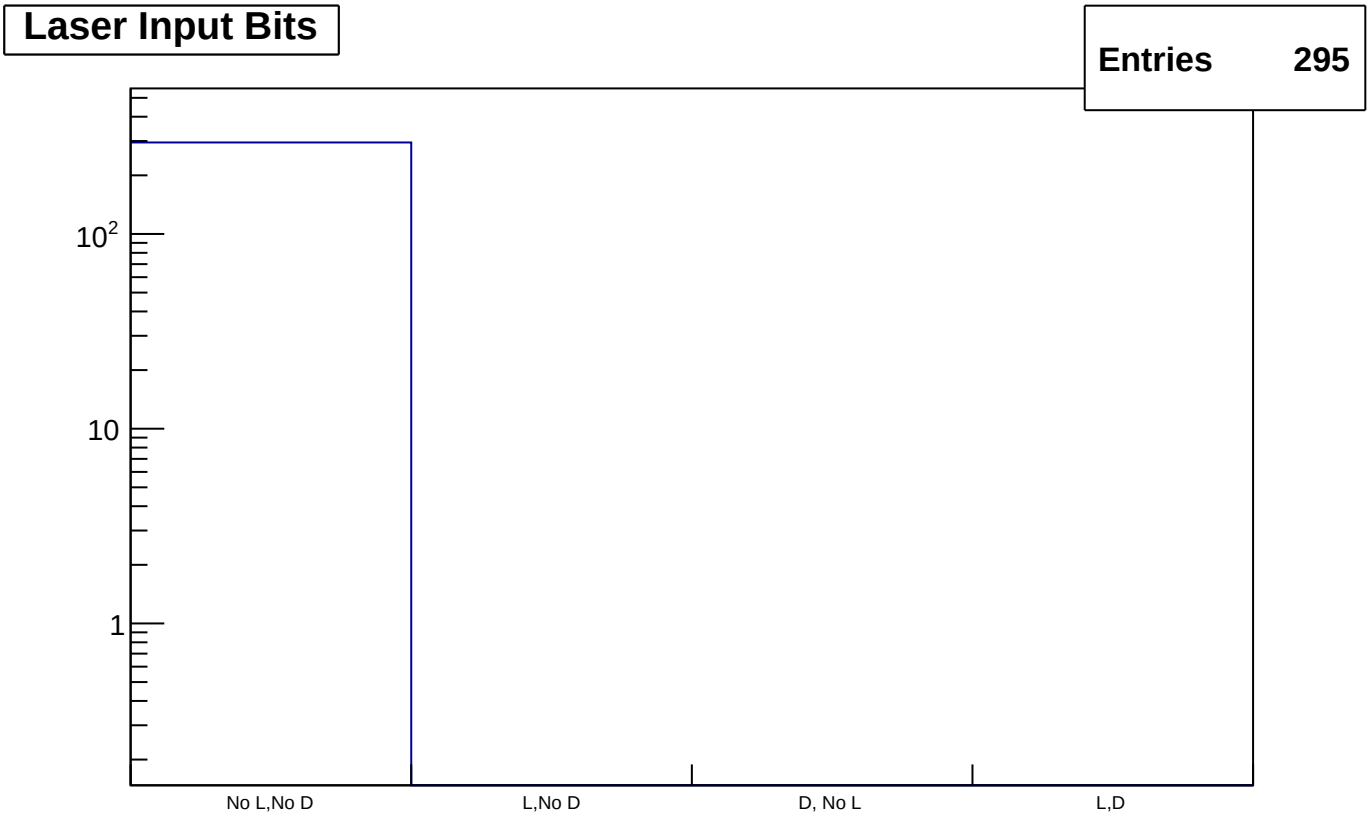

Entries 295
TOF-Mult
VPD-West ADC Sum

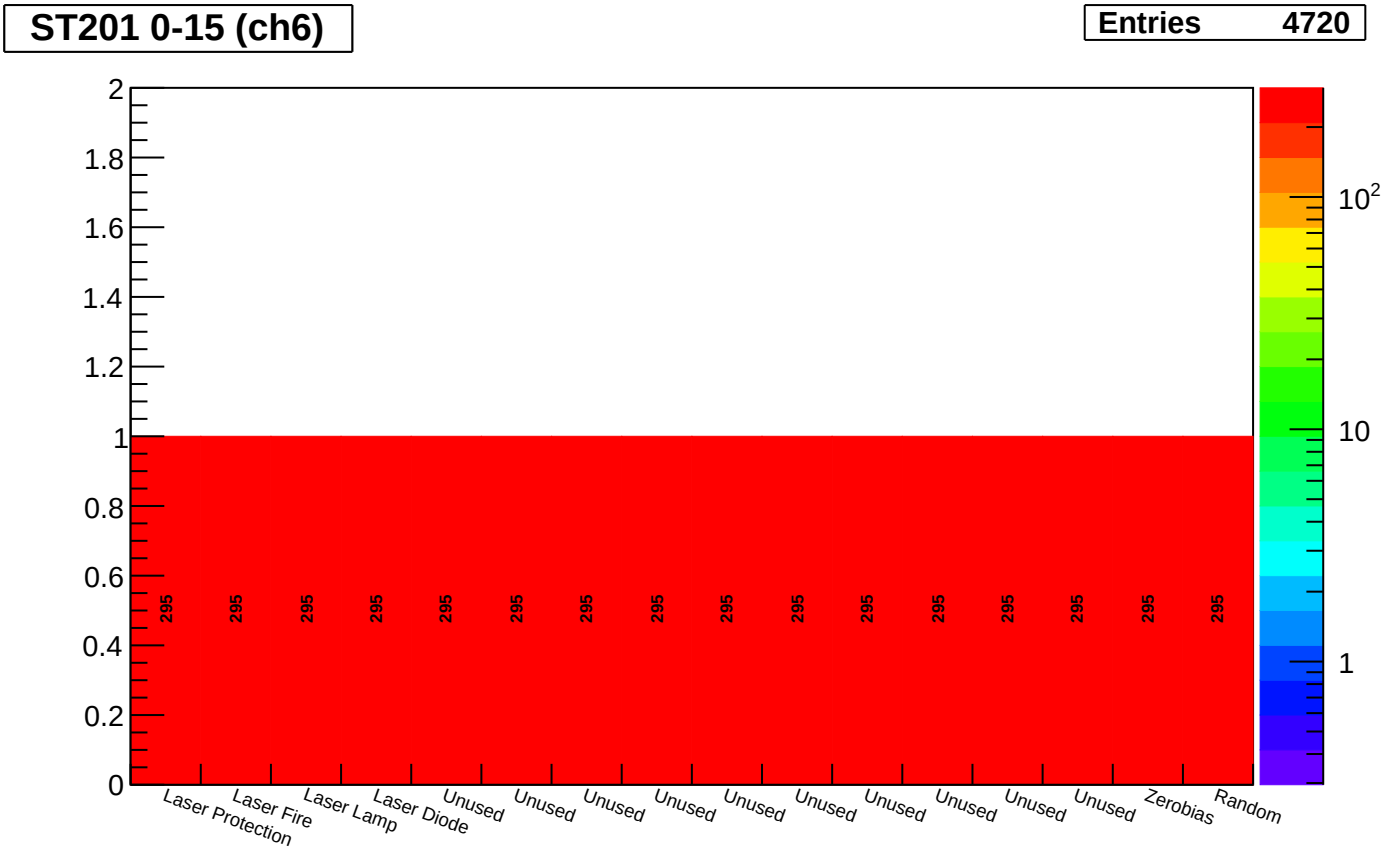

Entries 295
ZDC TAC Diff
200
180
160
140
120
100
80
60
2000
3000
4000
5000
6000
7000
EPD TAC Diff

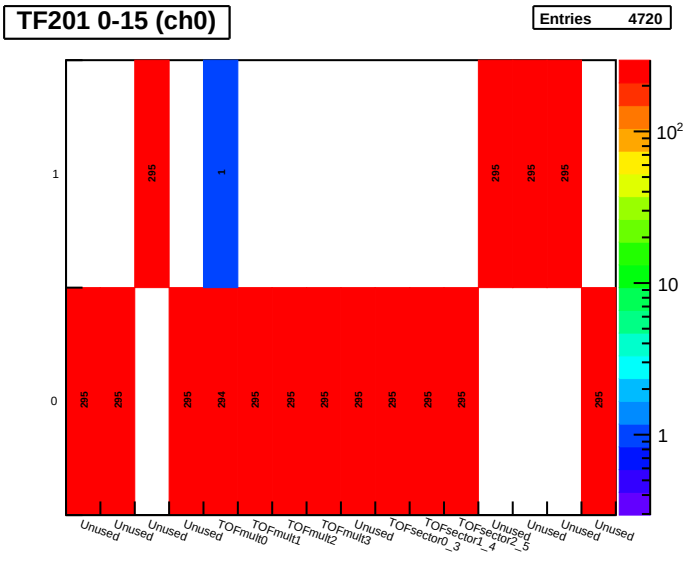

Entries 295
ZDC TAC Diff
200
180
160
140
120
100
80
60
2000
3000
4000
5000
6000
7000
VPD TAC Diff

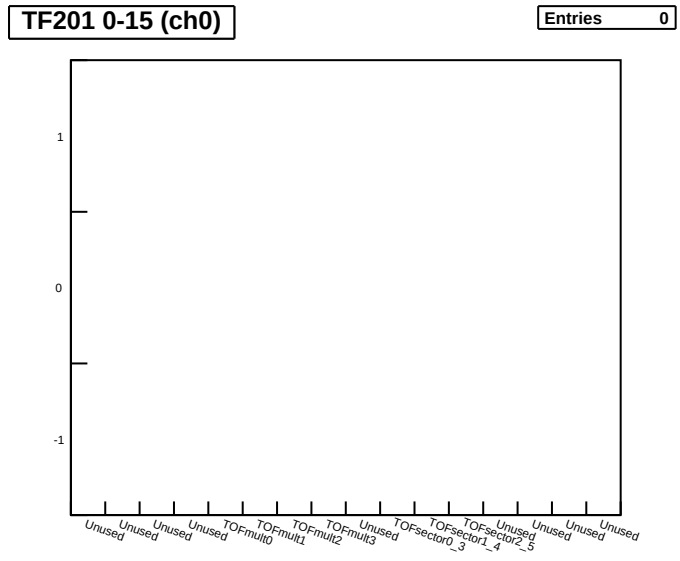

Entries 295
ZDC TAC Diff
200
180
160
140
120
100
80
60
2000
3000
4000
5000
6000
7000
BBC-S TAC Diff

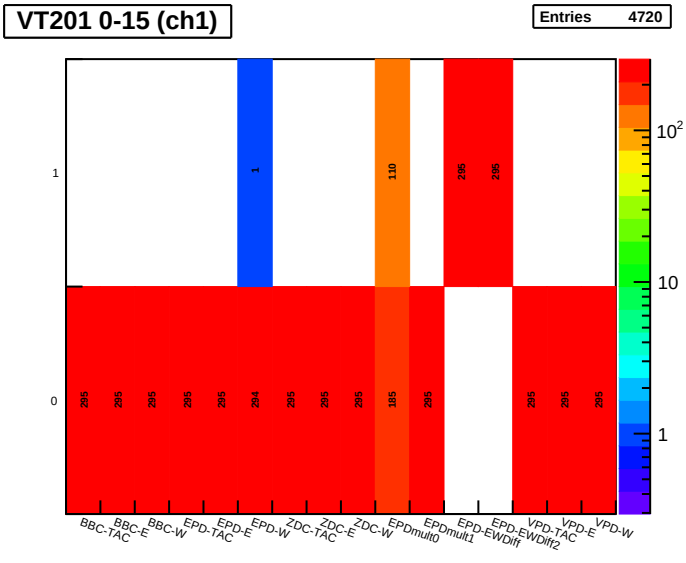

Entries 295
ZDC-West TAC
1000
800
600
400
200
0
0
200
400
600
800
1000
ZDC-East TAC
10^2
10
1

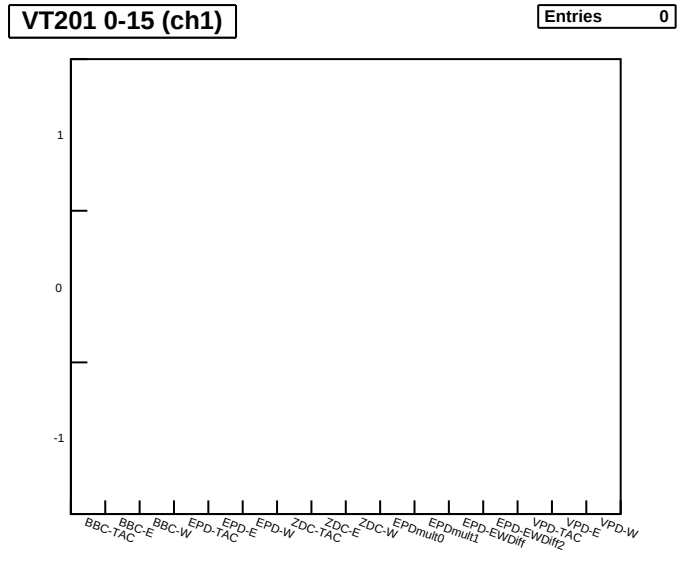

Laser Input Bits
Entries 295
10^2
10
1
No L,No D
L,No D
D, No L
L,D

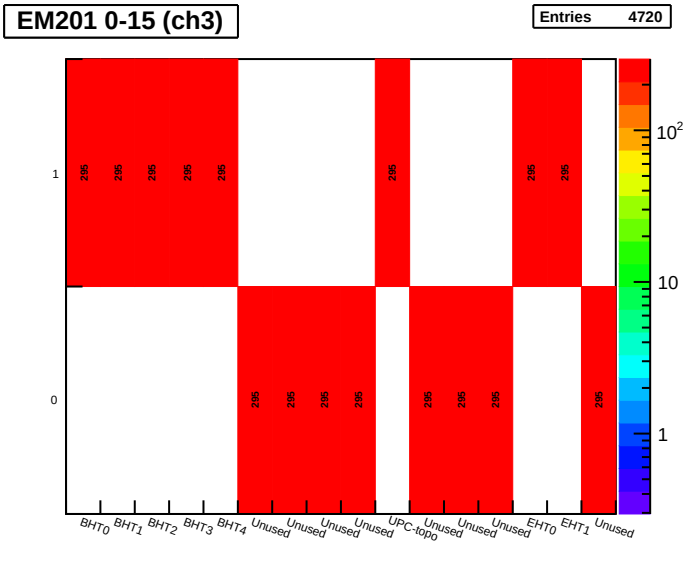

ST201 0-15 (ch6)
Entries 4720
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
295
Laser Protection
Laser Fire
Laser Lamp
Laser Diode
Unused
Zerobias
Random
10^2
10
1

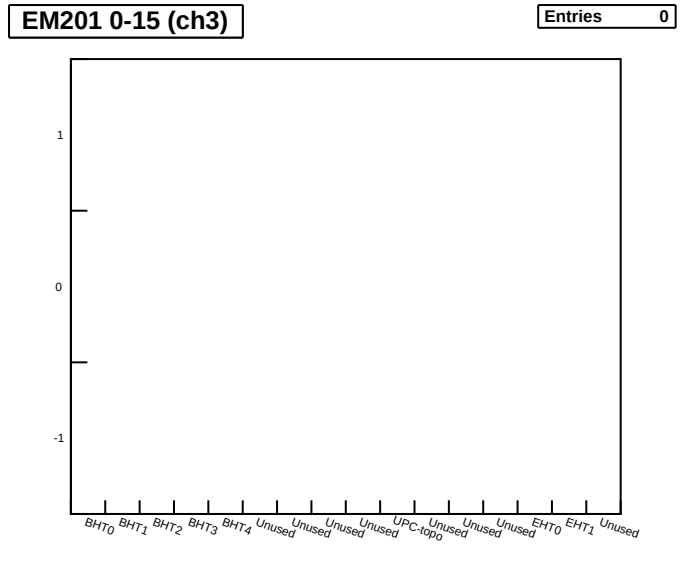

TF201 0-15 (ch0)
Entries 4720
Unused
TOFmult0
TOFmult1
TOFmult2
TOFmult3
TOFsector0_3
TOFsector1_4
TOFsector2_5


TF201 0-15 (ch0)
Entries 0
Unused
TOFmult0
TOFmult1
TOFmult2
TOFmult3
TOFsector0_3
TOFsector1_4
TOFsector2_5


VT201 0-15 (ch1)
Entries 4720
BBC-TAC
BBC-E
BBC-W
EPD-TAC
EPD-E
EPD-W
ZDC-TAC
ZDC-E
ZDC-W
EPDmult0
EPDmult1
EPD-EWDiff
EPD-EWDiff2
VPD-TAC
VPD-E
VPD-W


VT201 0-15 (ch1)
Entries 0
BBC-TAC
BBC-E
BBC-W
EPD-TAC
EPD-E
EPD-W
ZDC-TAC
ZDC-E
ZDC-W
EPDmult0
EPDmult1
EPD-EWDiff
EPD-EWDiff2
VPD-TAC
VPD-E
VPD-W


EM201 0-15 (ch3)
Entries 4720
BHT0
BHT1
BHT2
BHT3
BHT4
Unused
UPC-topo
EHT0
EHT1


EM201 0-15 (ch3)
Entries 0
BHT0
BHT1
BHT2
BHT3
BHT4
Unused
UPC-topo
EHT0
EHT1