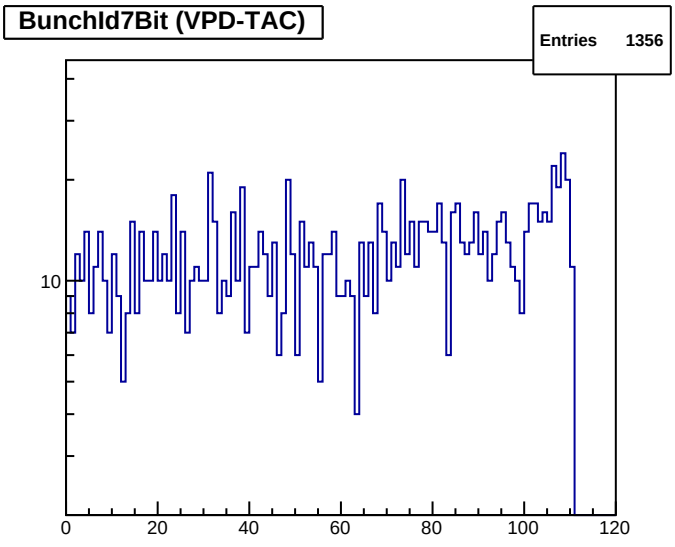
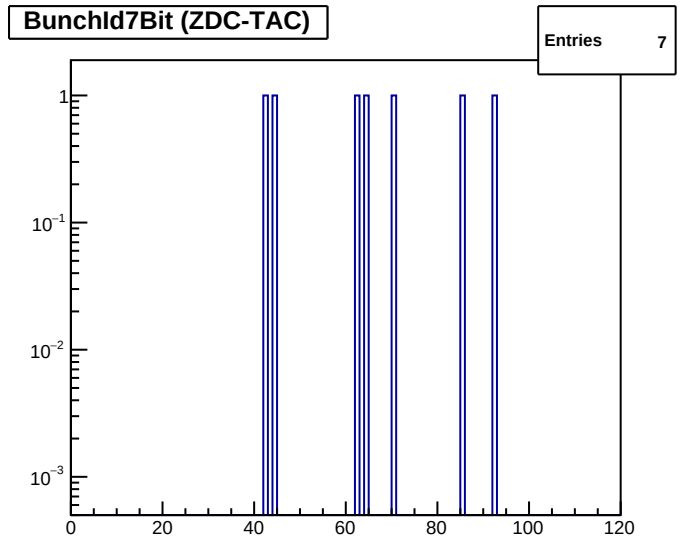
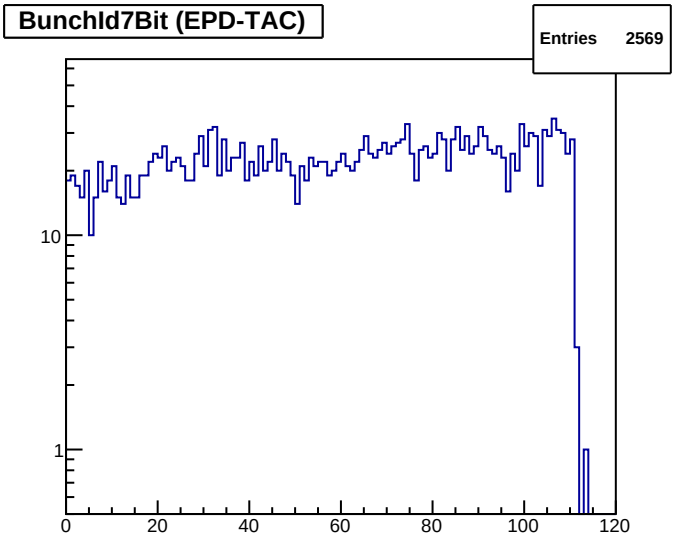
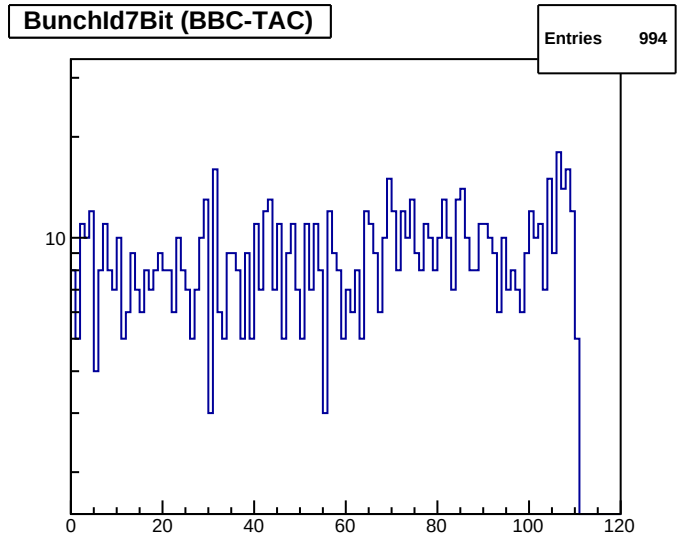
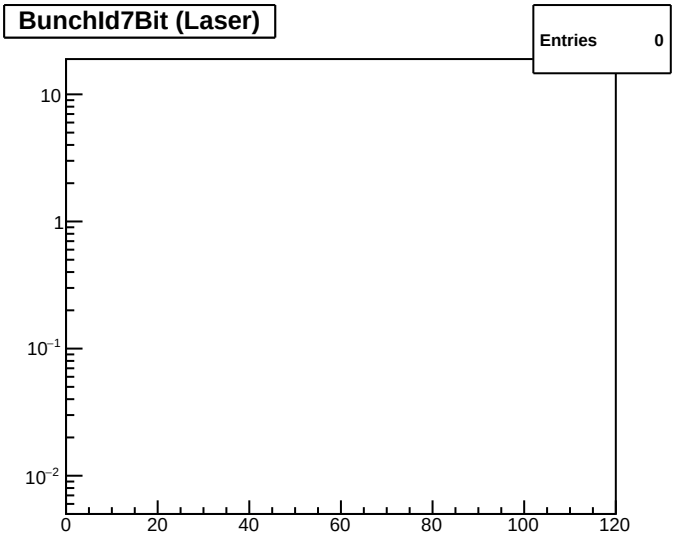
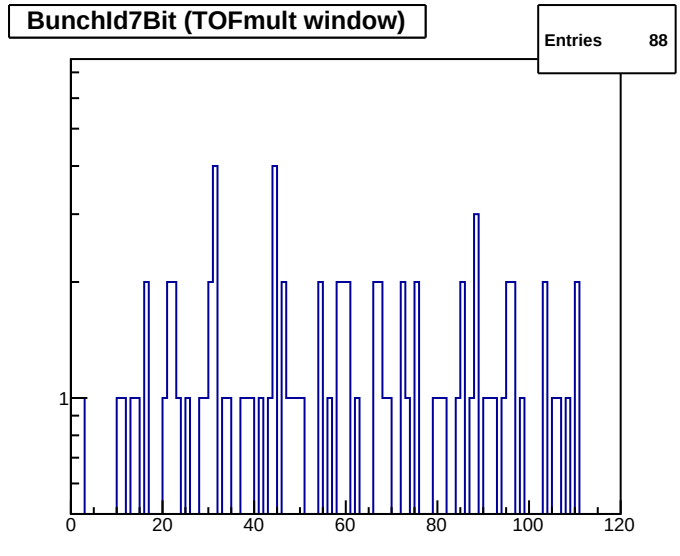

BunchId7Bit (VPD-TAC)
Entries 1356
10
0 20 40 60 80 100 120

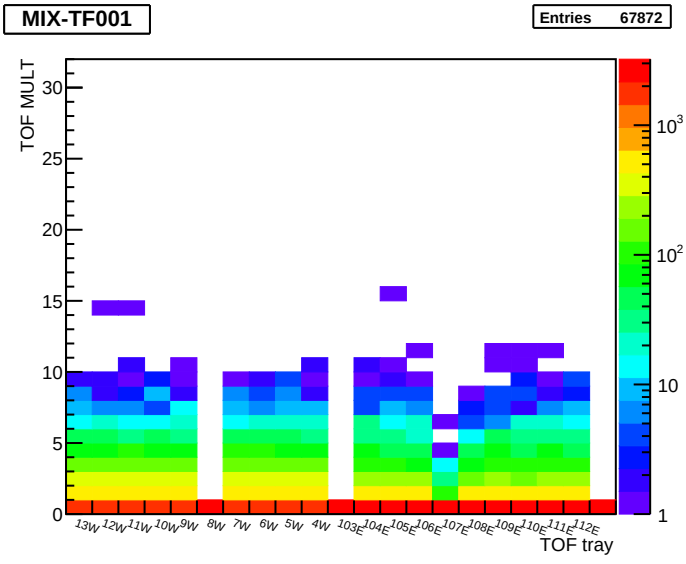

BunchId7Bit (ZDC-TAC)
Entries 7
1
10^{-1}
10^{-2}
10^{-3}
0 20 40 60 80 100 120

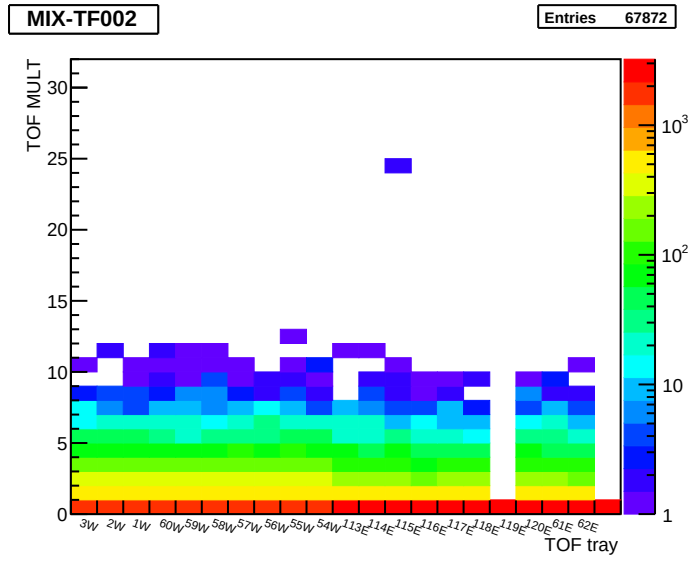

BunchId7Bit (EPD-TAC)
Entries 2569
10
1
0 20 40 60 80 100 120

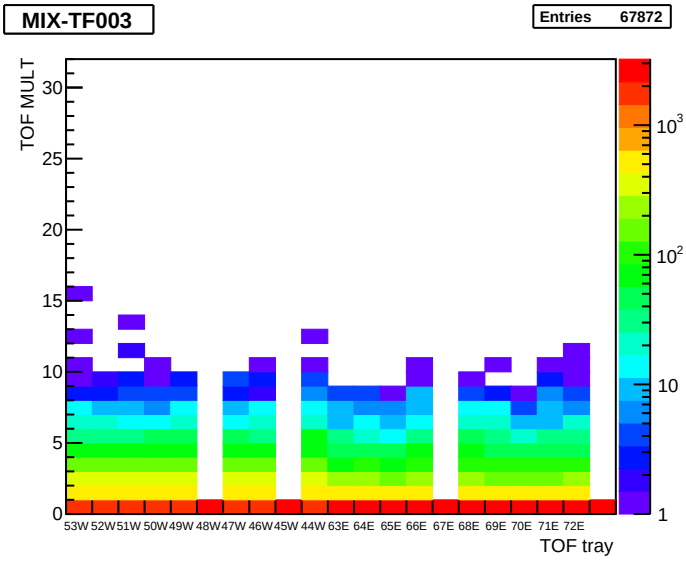

BunchId7Bit (BBC-TAC)
Entries 994
10
0 20 40 60 80 100 120

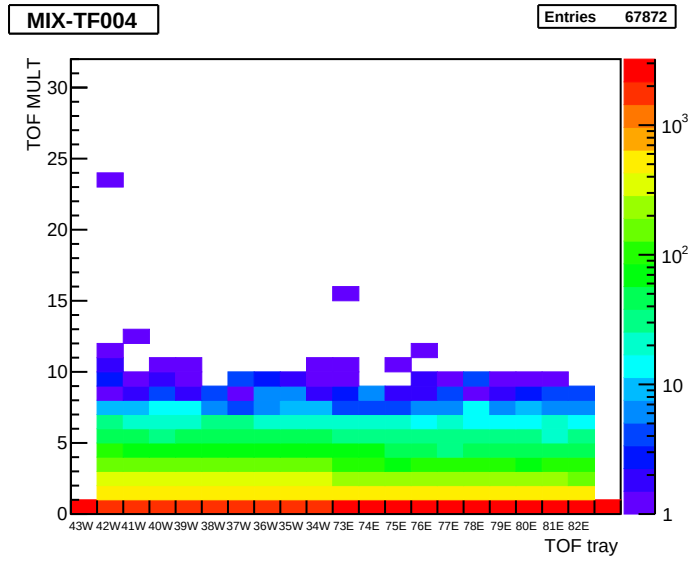

BunchId7Bit (Laser)
Entries 0
10
1
10^{-1}
10^{-2}
0 20 40 60 80 100 120

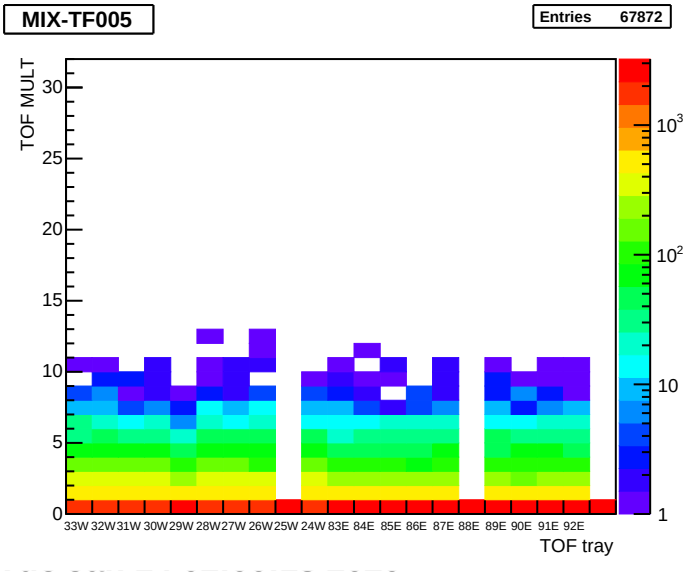

BunchId7Bit (TOFmult window)
Entries 88
1
0 20 40 60 80 100 120

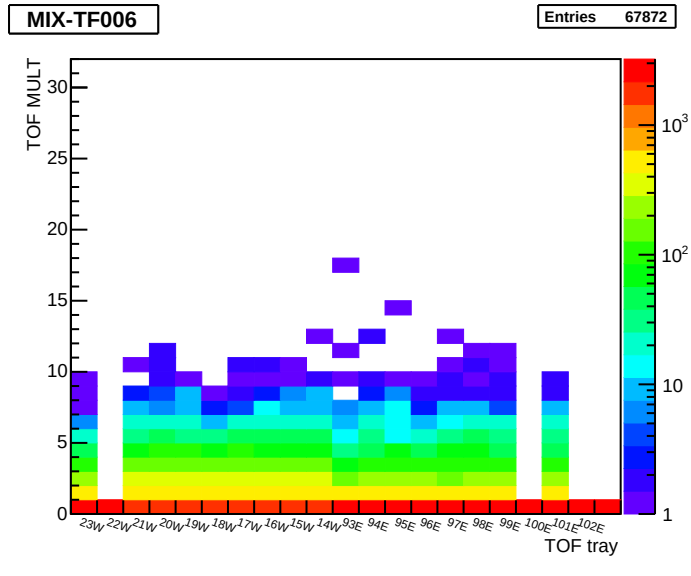

MIX-TF001
Entries 67872
TOF MULT
TOF tray

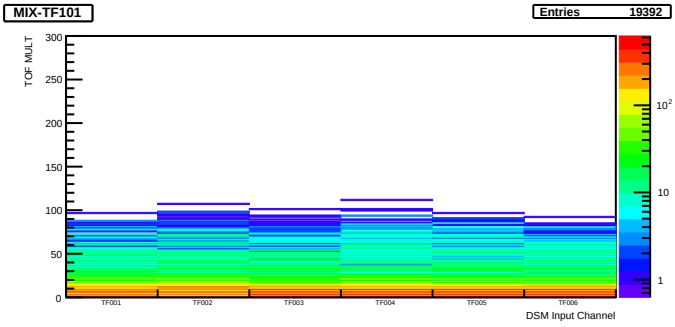

MIX-TF002
Entries 67872
TOF MULT
TOF tray

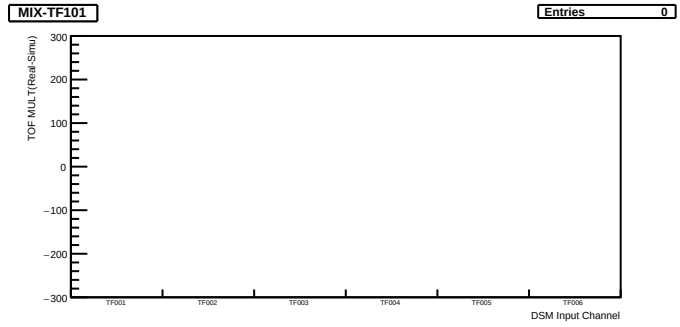

MIX-TF003
Entries 67872
TOF MULT
TOF tray

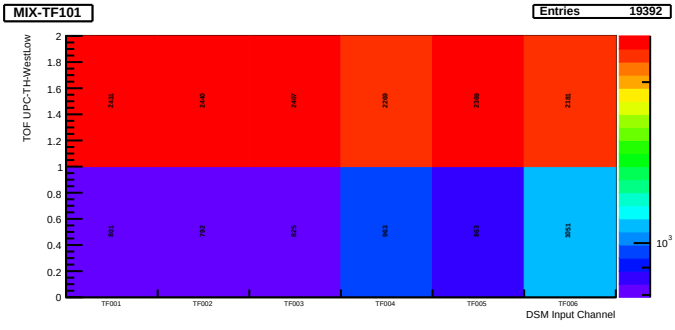

MIX-TF004
Entries 67872
TOF MULT
TOF tray

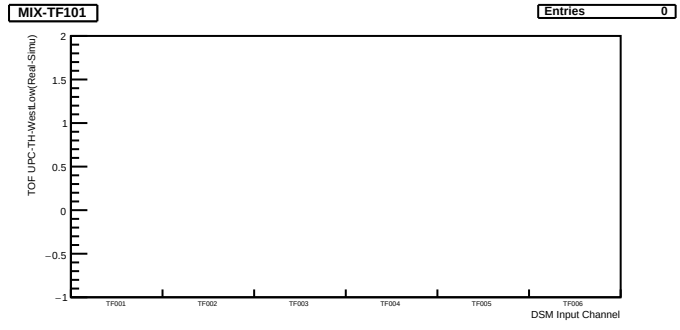

MIX-TF005
Entries 67872
TOF MULT
TOF tray

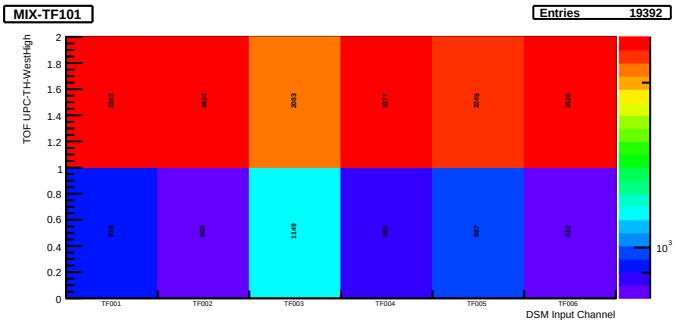

MIX-TF006
Entries 67872
TOF MULT
TOF tray

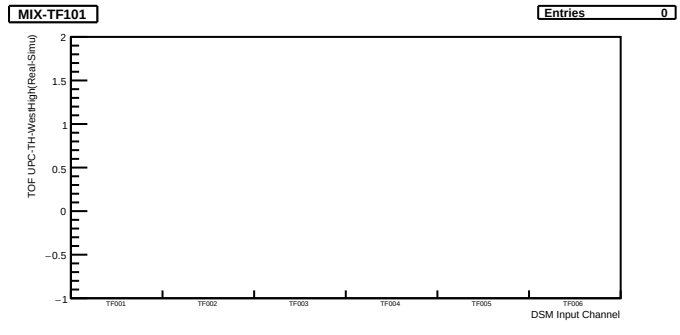

MIX-TF101
Entries 19392
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

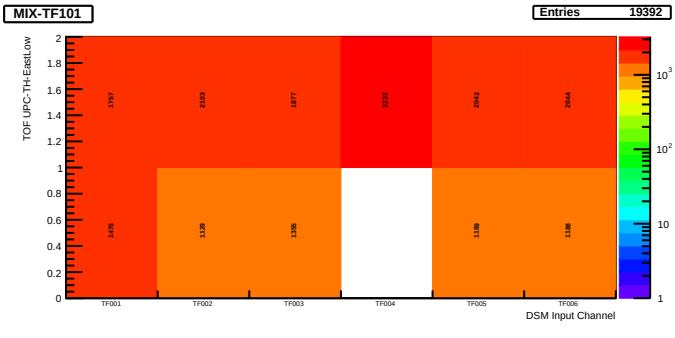

MIX-TF101
Entries 0
TOF MULT(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

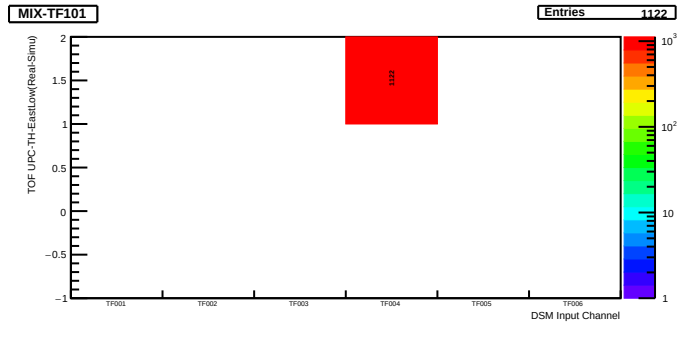

MIX-TF101
Entries 19392
TOF UPC-TH-WestLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

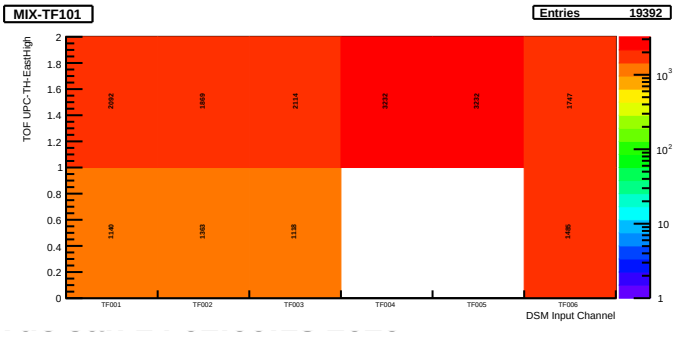

MIX-TF101
Entries 0
TOF UPC-TH-WestLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

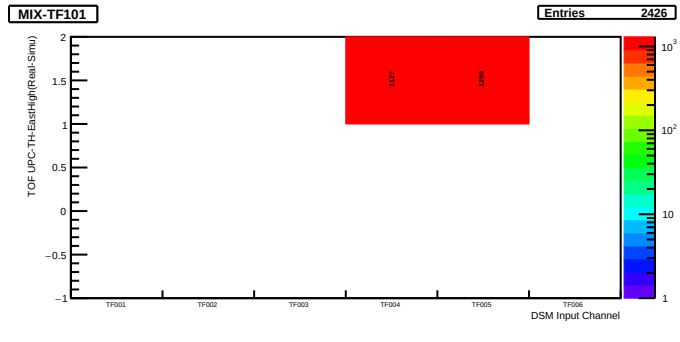

MIX-TF101
Entries 19392
TOF UPC-TH-WestHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

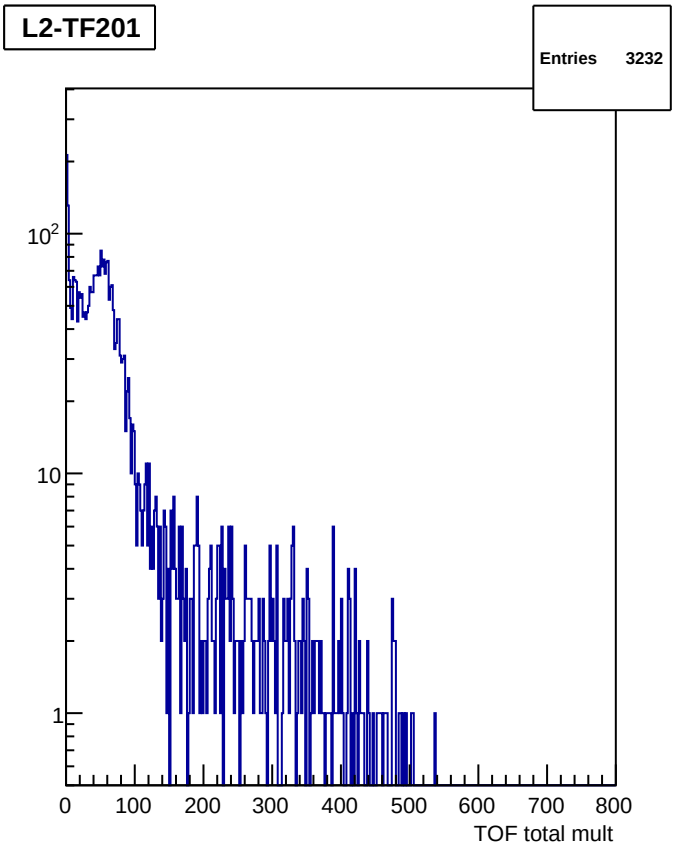

MIX-TF101
Entries 0
TOF UPC-TH-WestHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

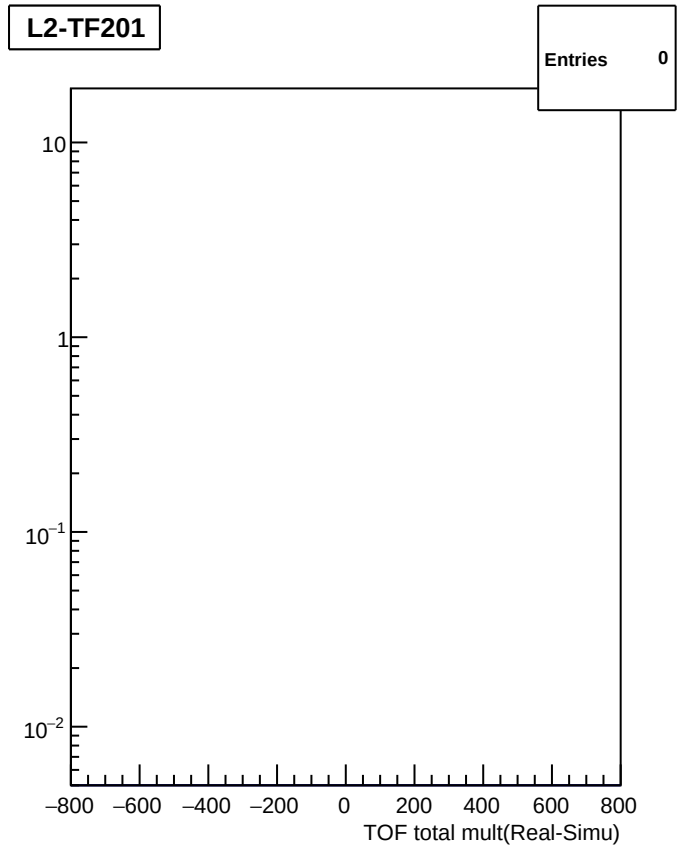

MIX-TF101
Entries 19392
TOF UPC-TH-EastLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

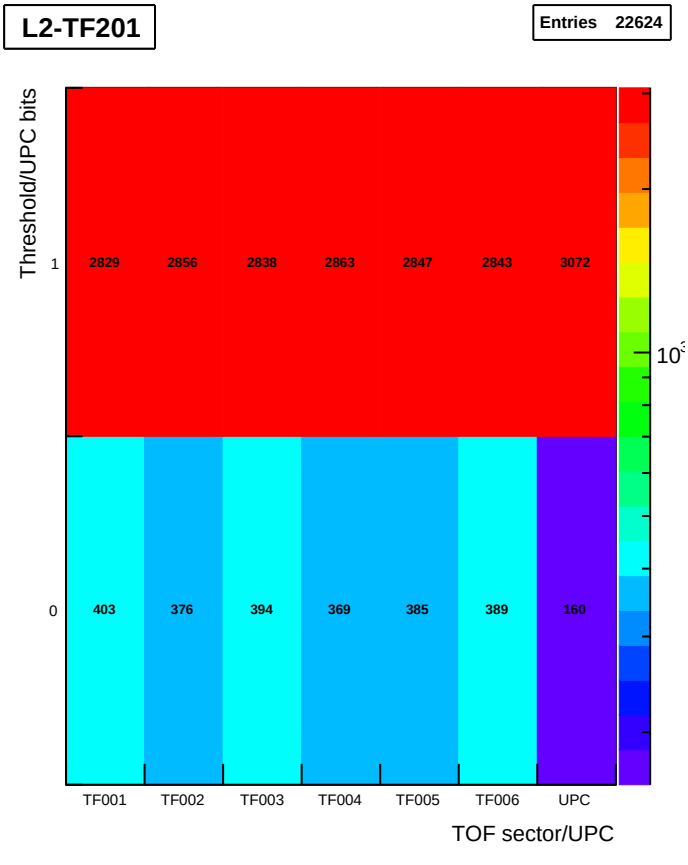

MIX-TF101
Entries 1122
TOF UPC-TH-EastLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

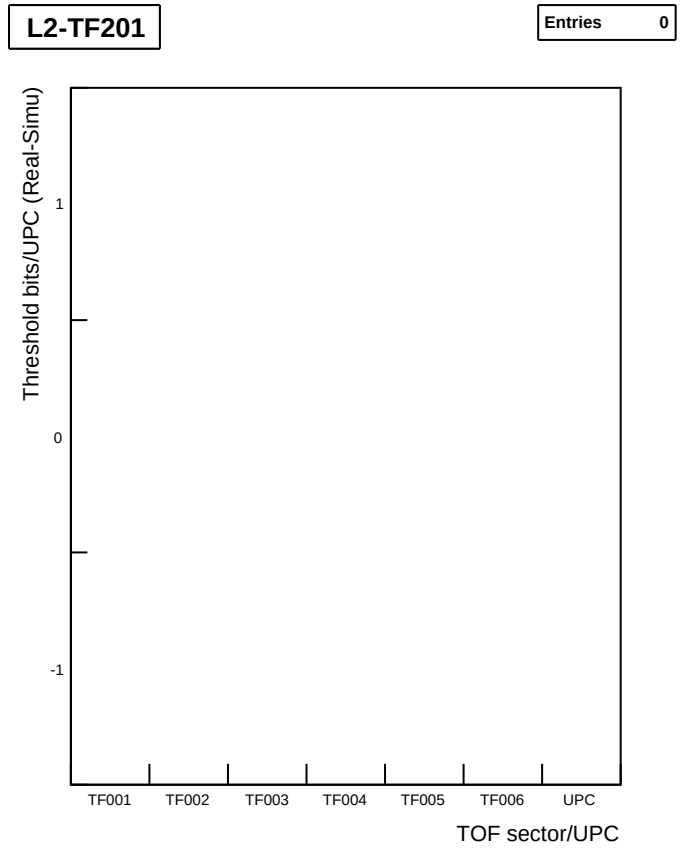

MIX-TF101
Entries 19392
TOF UPC-TH-EastHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

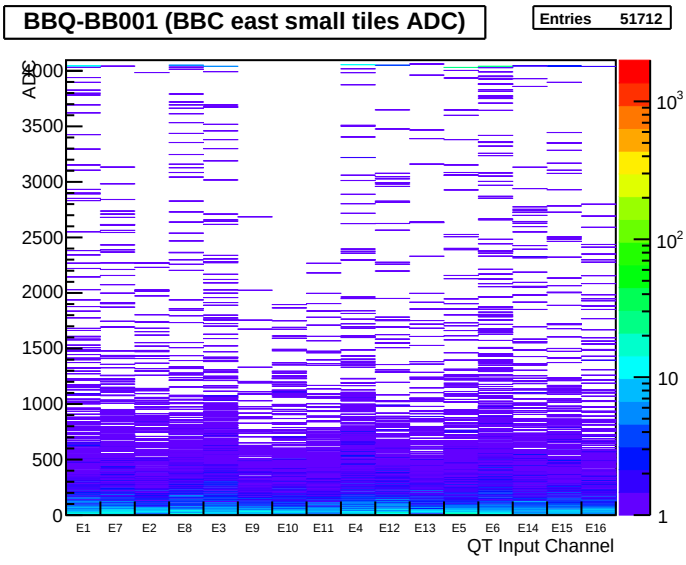

MIX-TF101
Entries 2426
TOF UPC-TH-EastHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

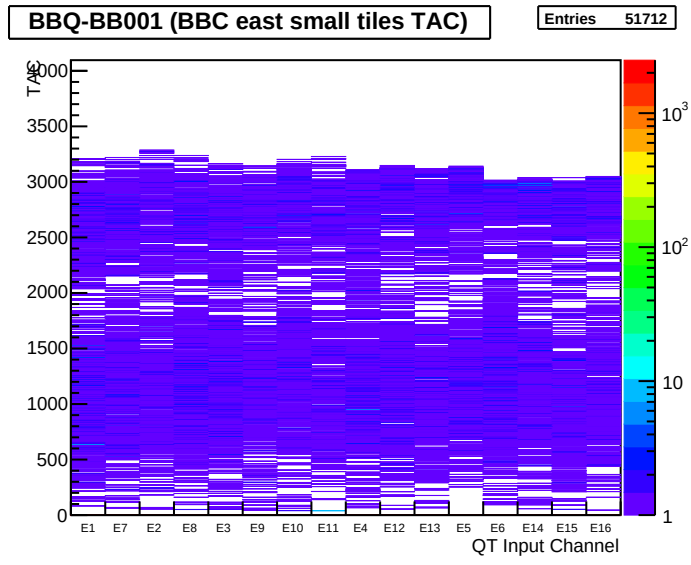

L2-TF201
Entries 3232
10^2
10
1
0 100 200 300 400 500 600 700 800
TOF total mult

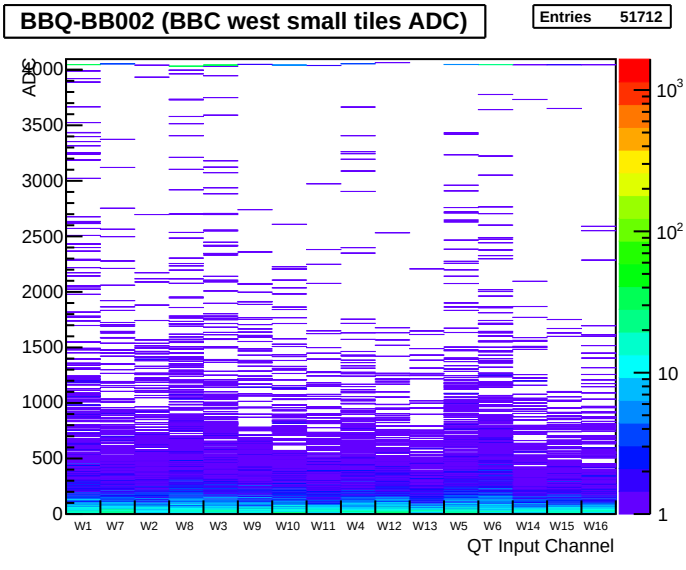

L2-TF201
Entries 0
10
1
10^{-1}
10^{-2}
−800 −600 −400 −200 0 200 400 600 800
TOF total mult(Real-Simu)

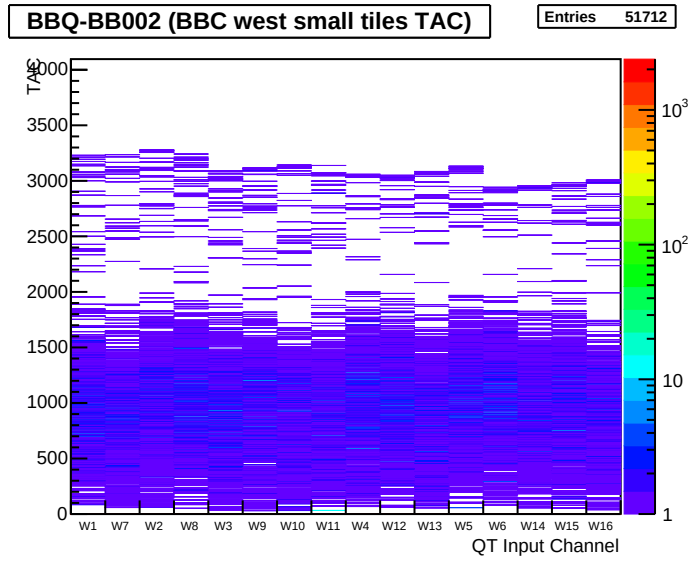

L2-TF201
Entries 22624
Threshold/UPC bits
1
0
2829 2856 2838 2863 2847 2843 3072
403 376 394 369 385 389 160
10^3
TF001 TF002 TF003 TF004 TF005 TF006 UPC
TOF sector/UPC

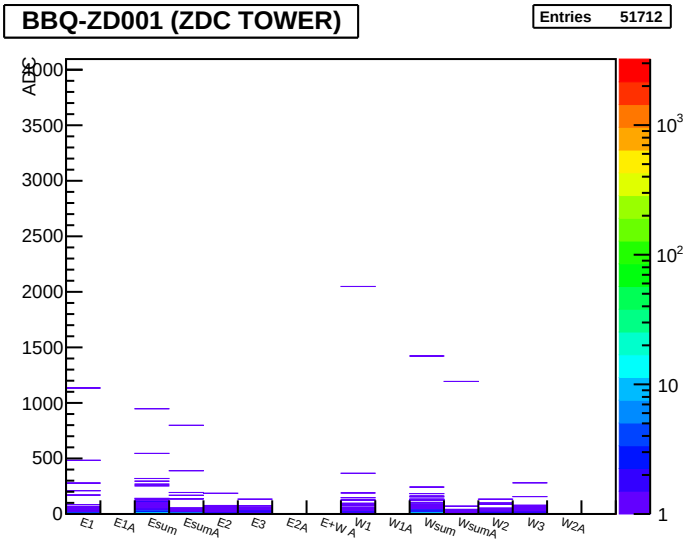

L2-TF201
Entries 0
Threshold bits/UPC (Real-Simu)
1
0
-1
TF001 TF002 TF003 TF004 TF005 TF006 UPC
TOF sector/UPC

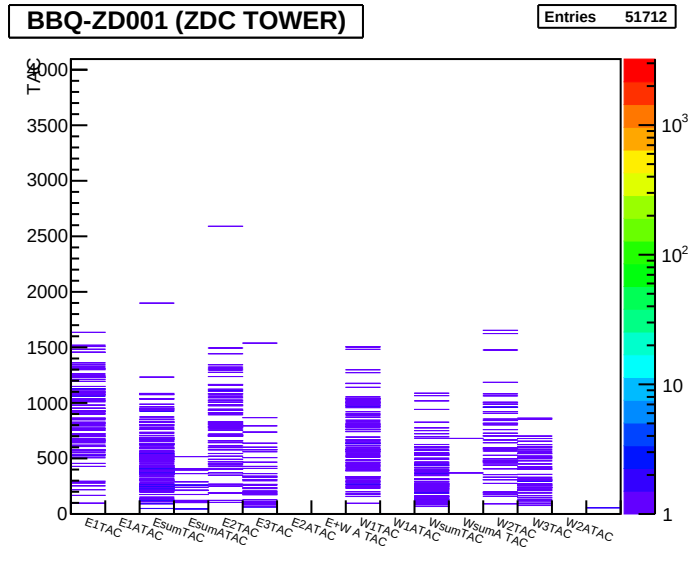

BBQ-BB001 (BBC east small tiles ADC)
Entries 51712
ADC
QT Input Channel

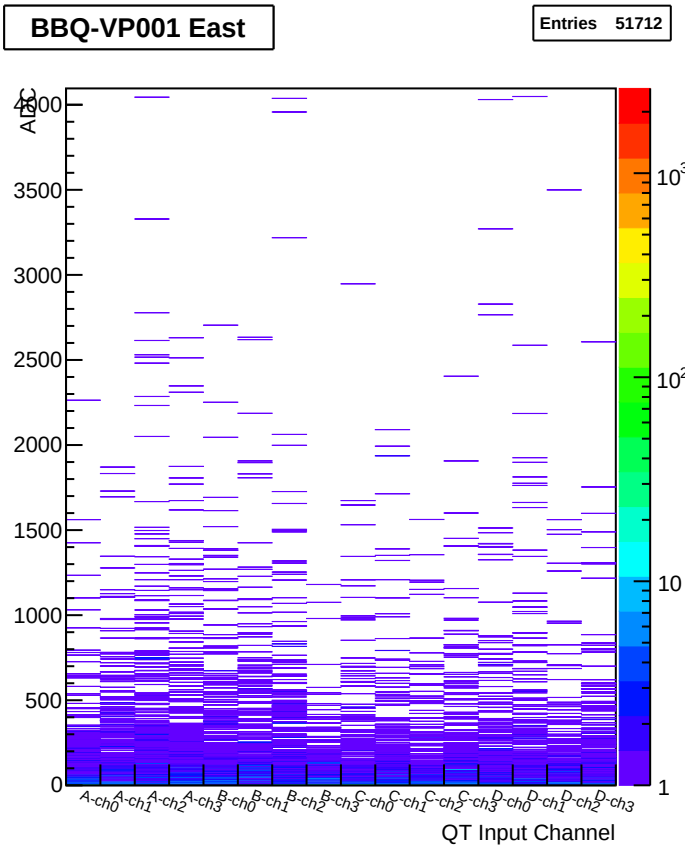

BBQ-BB001 (BBC east small tiles TAC)
Entries 51712
TAC
QT Input Channel

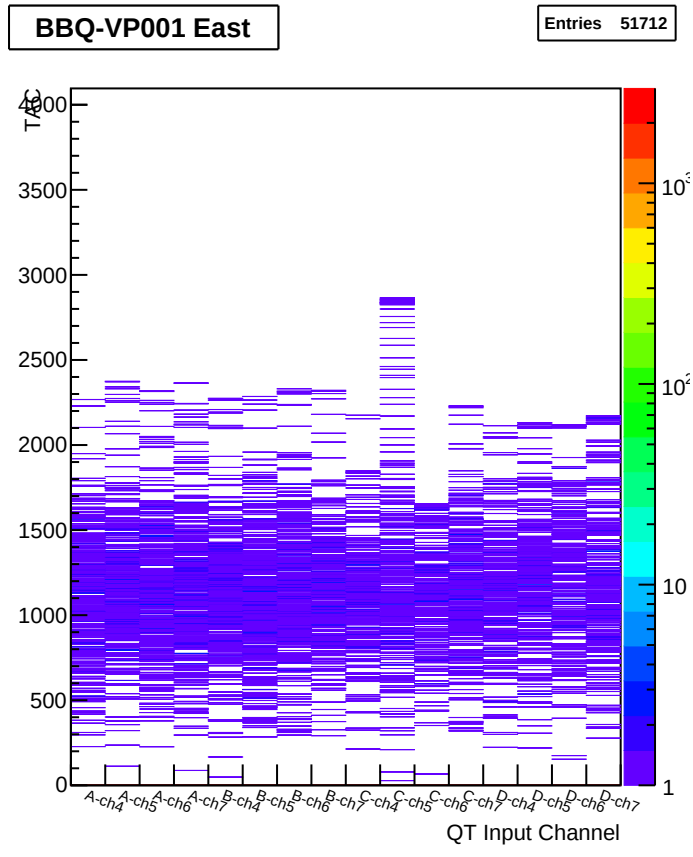

BBQ-BB002 (BBC west small tiles ADC)
Entries 51712
ADC
QT Input Channel

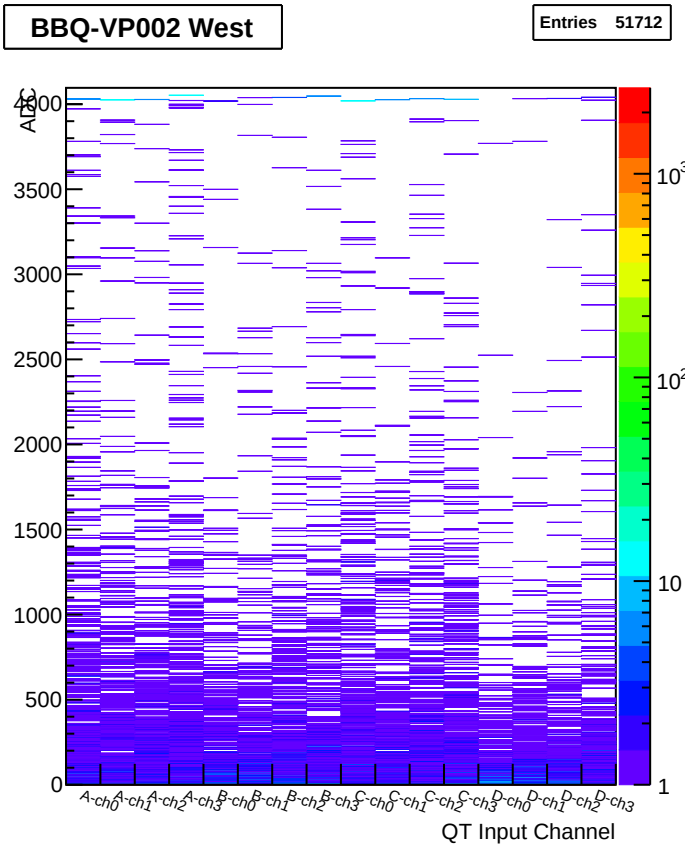

BBQ-BB002 (BBC west small tiles TAC)
Entries 51712
TAC
QT Input Channel

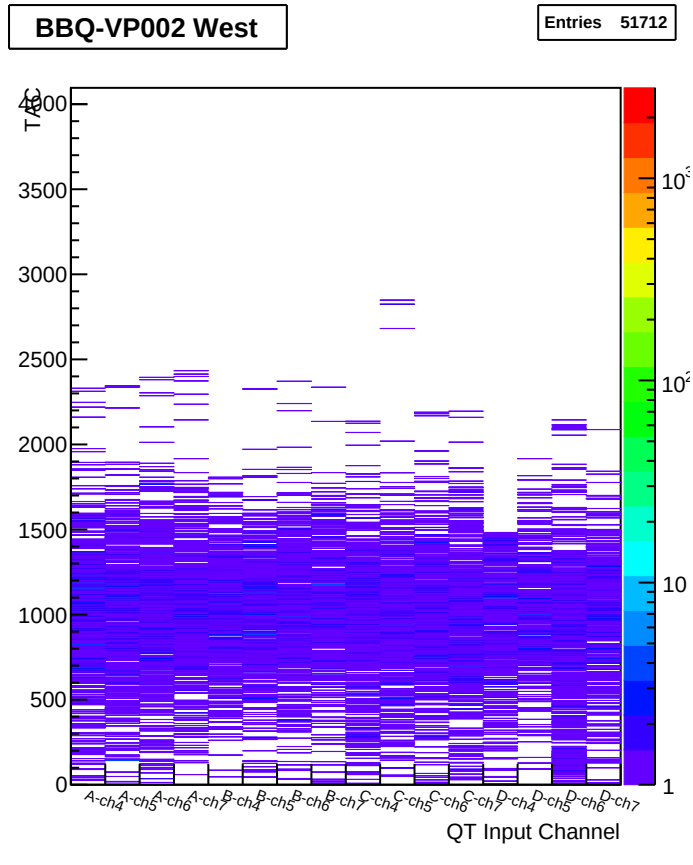

BBQ-ZD001 (ZDC TOWER)
Entries 51712
ADC

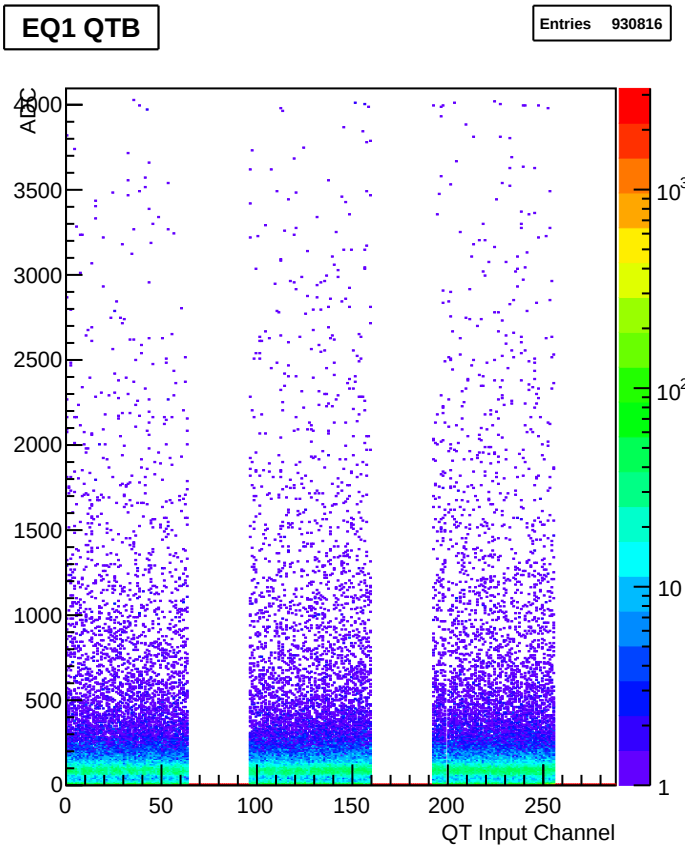

BBQ-ZD001 (ZDC TOWER)
Entries 51712
TAC

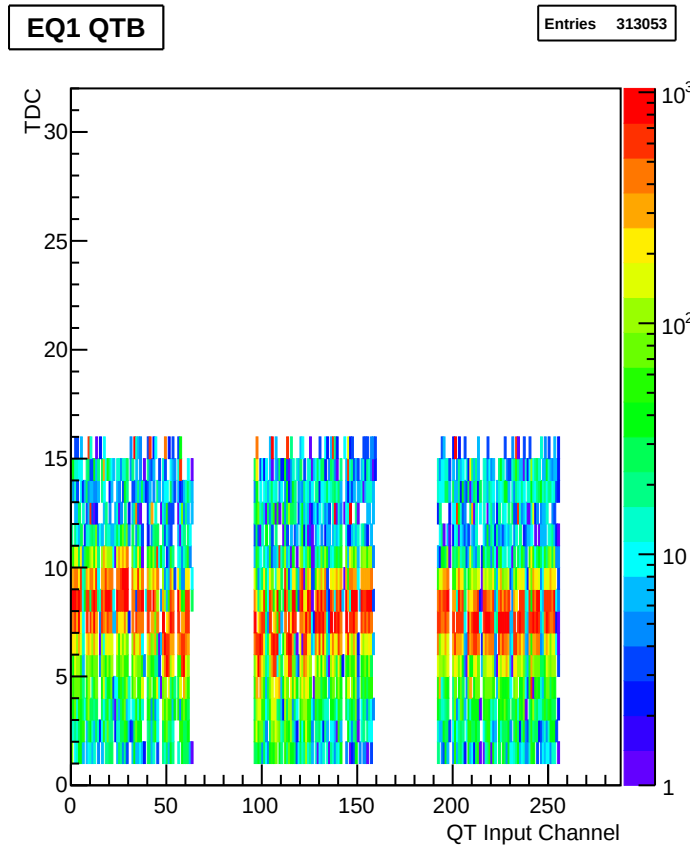

BBQ-VP001 East
Entries 51712
ADC
QT Input Channel

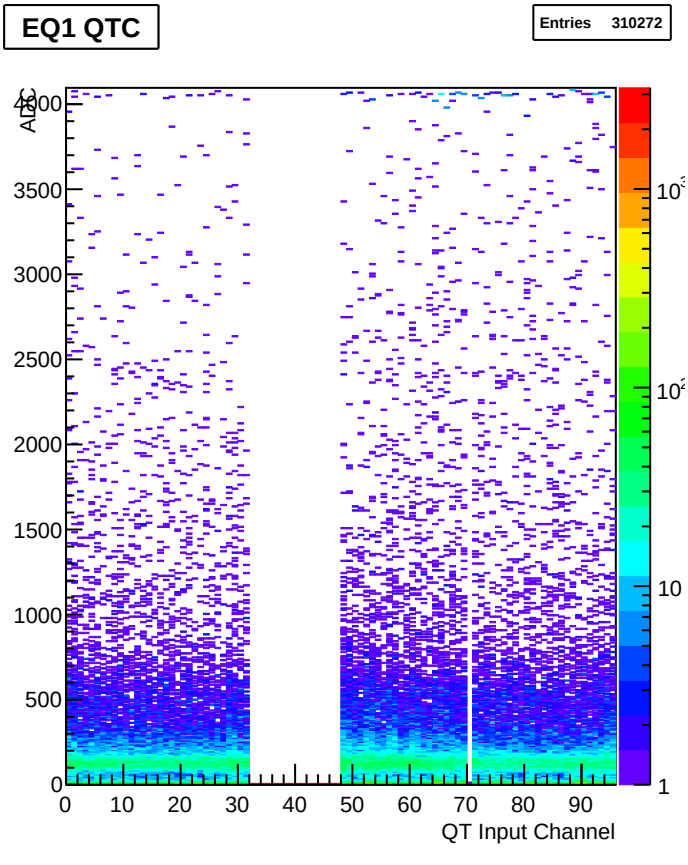

BBQ-VP001 East
Entries 51712
TADC
QT Input Channel

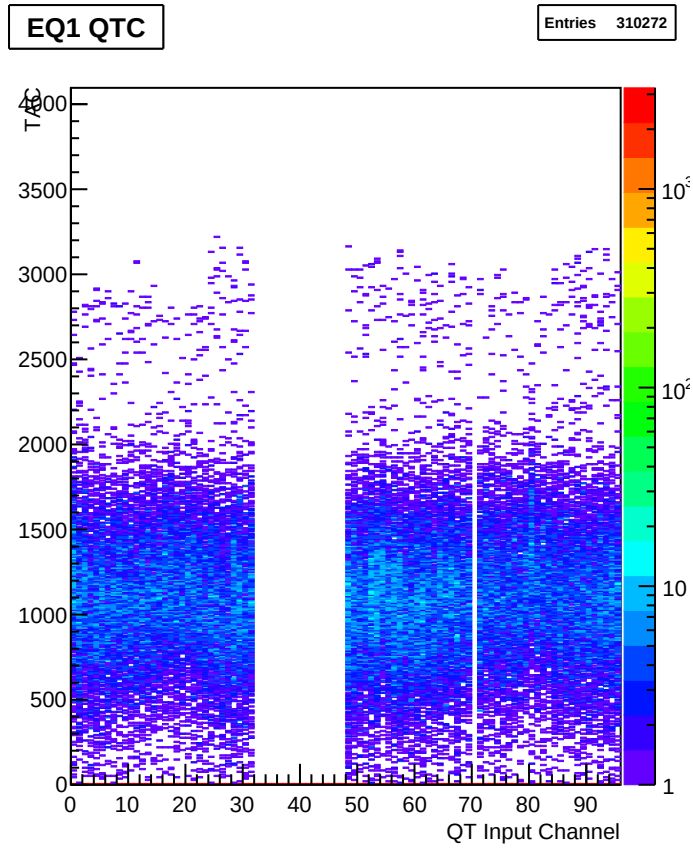

BBQ-VP002 West
Entries 51712
ADC
QT Input Channel

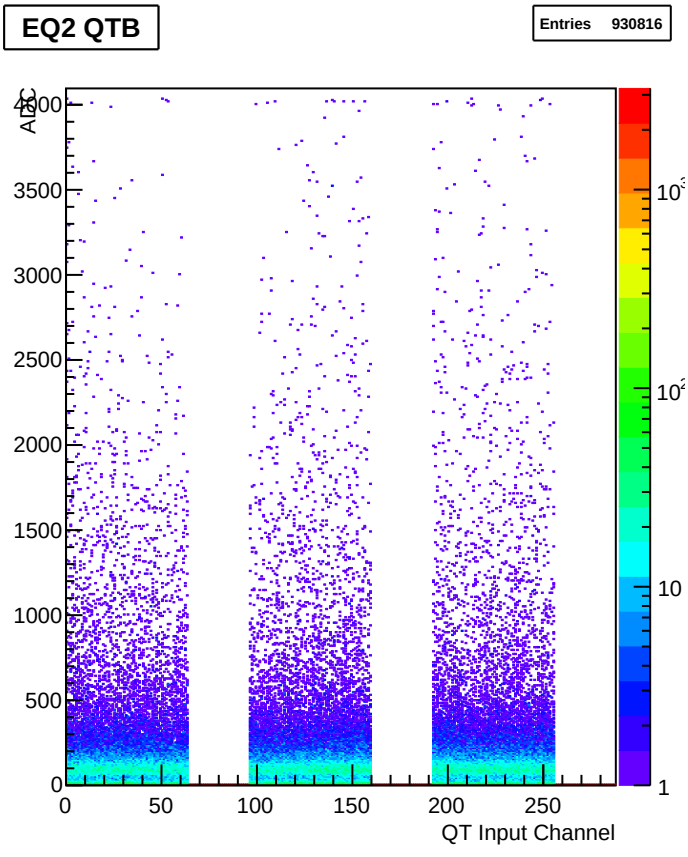

BBQ-VP002 West
Entries 51712
TADC
QT Input Channel

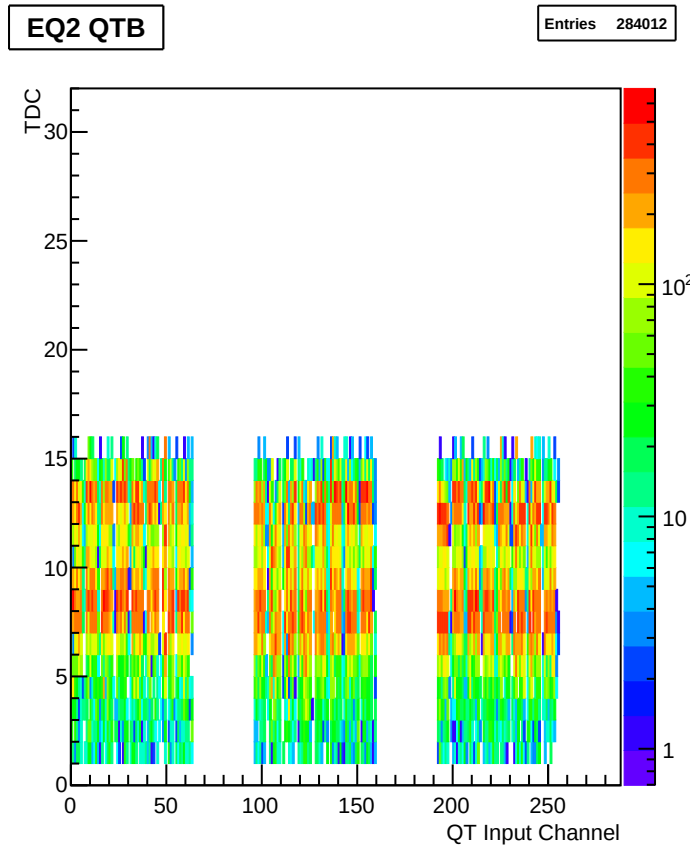

EQ1 QTB
Entries 930816
ADC
QT Input Channel

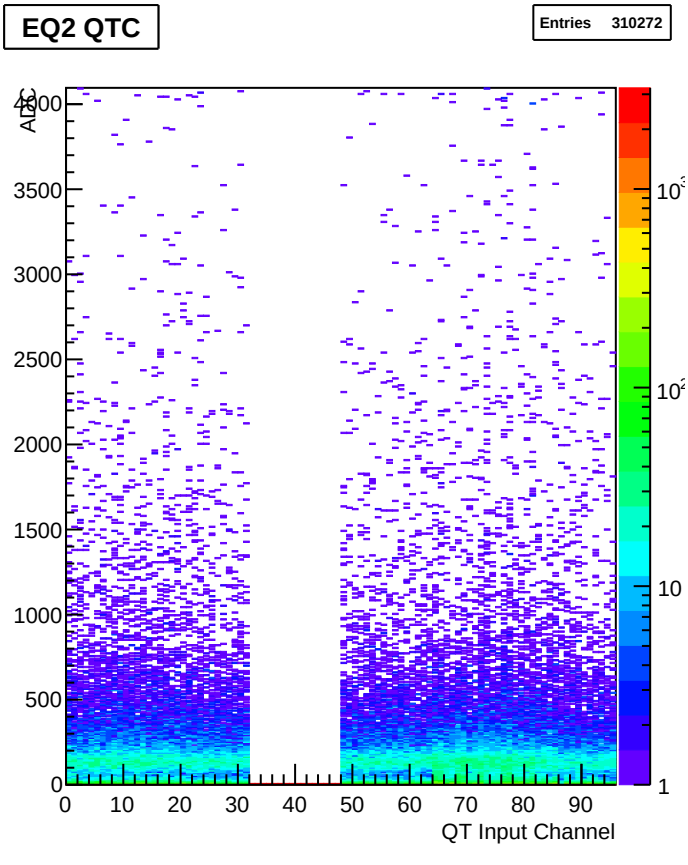

EQ1 QTB
Entries 313053
TDC
QT Input Channel

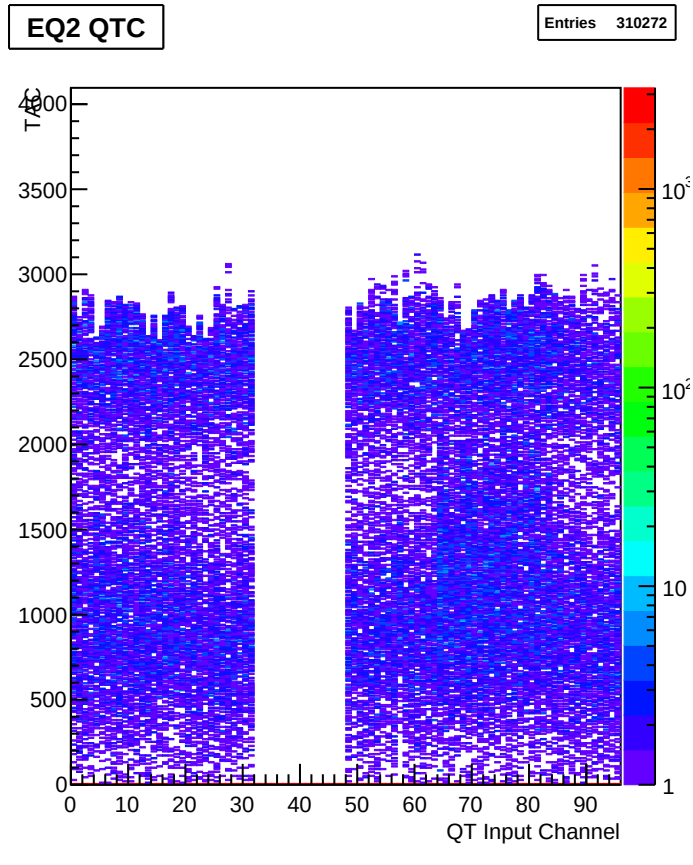

EQ1 QTC
Entries 310272
ADC
QT Input Channel

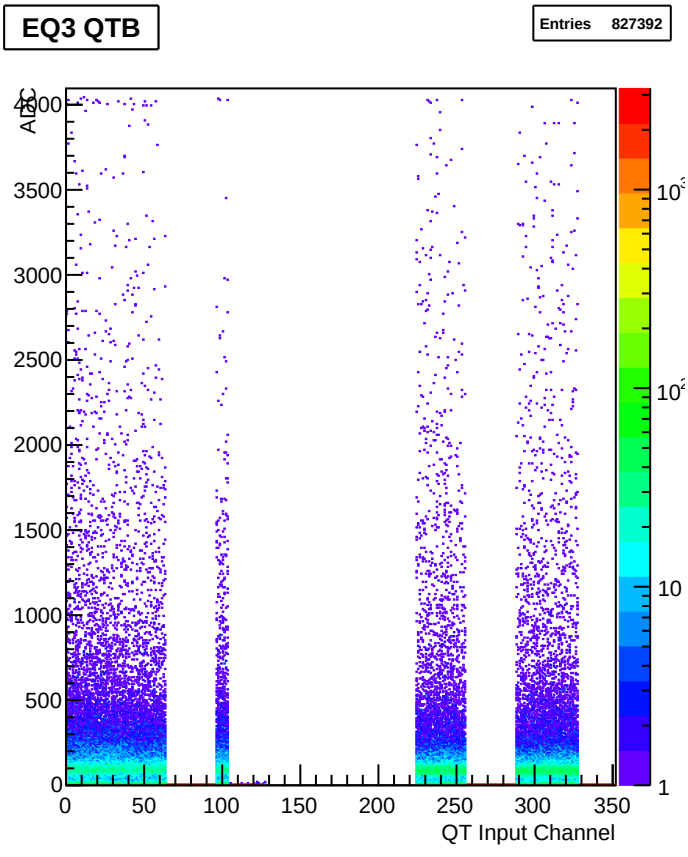

EQ1 QTC
Entries 310272
TADC
QT Input Channel

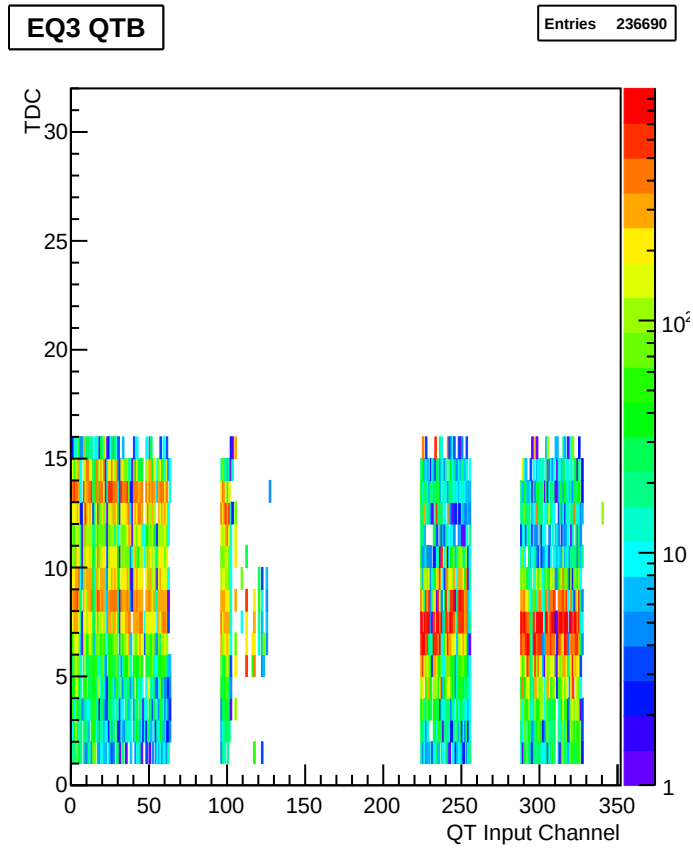

EQ2 QTB
Entries 930816
ADC
QT Input Channel

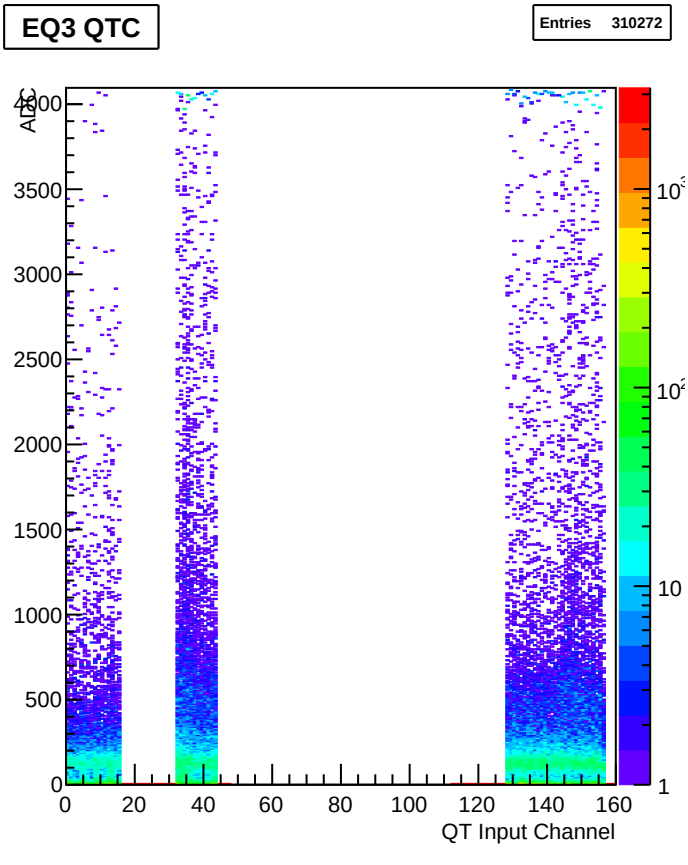

EQ2 QTB
Entries 284012
TDC
QT Input Channel

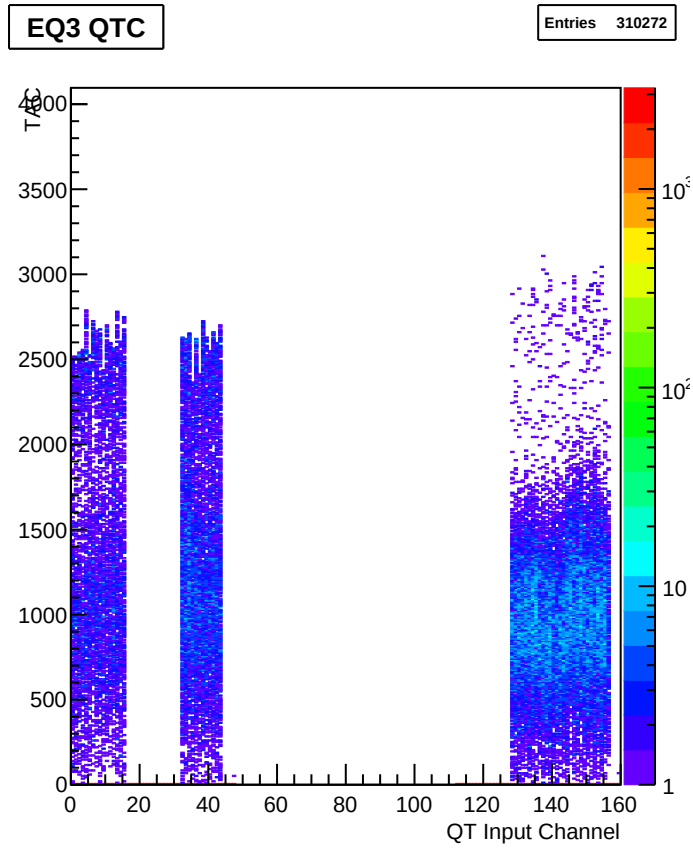

EQ2 QTC
Entries 310272
ADC
QT Input Channel

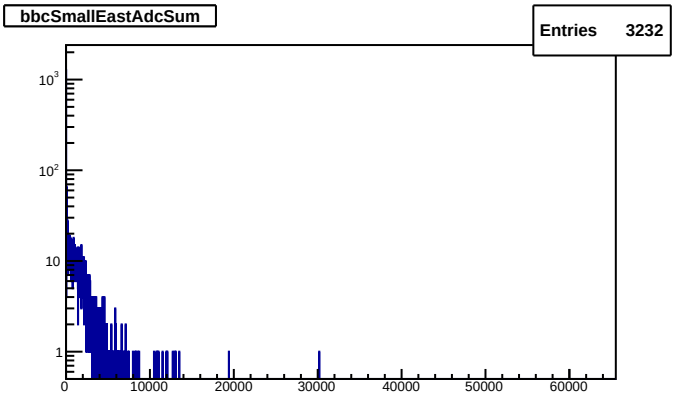

EQ2 QTC
Entries 310272
TADC
QT Input Channel

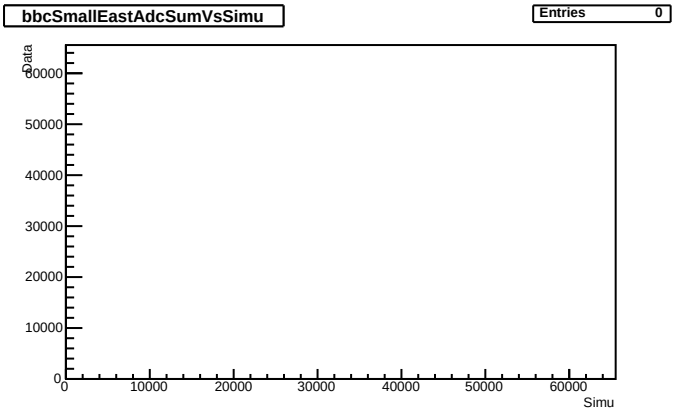

EQ3 QTB
Entries 827392
ADC
QT Input Channel

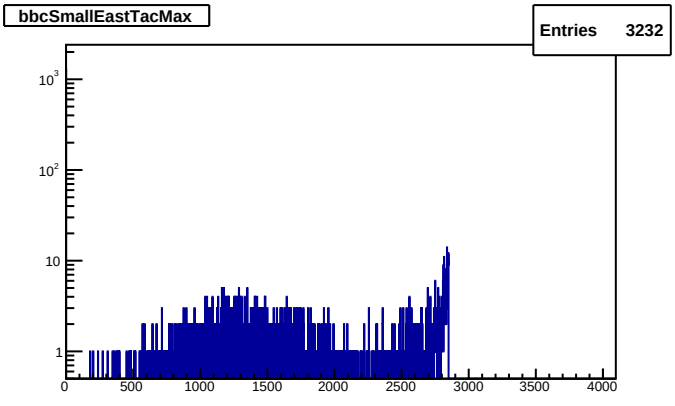

EQ3 QTB
Entries 236690
TDC
QT Input Channel

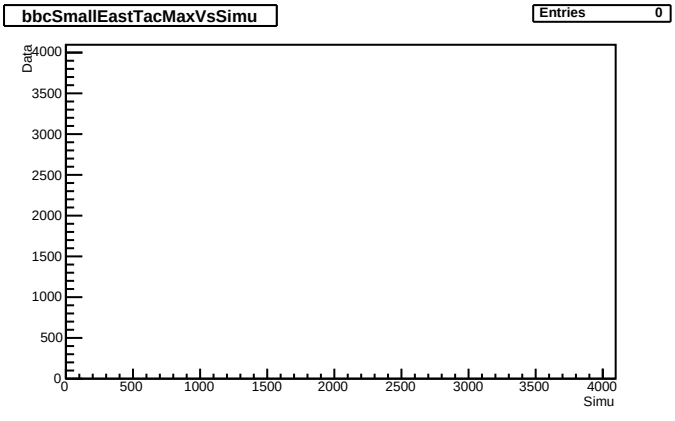

EQ3 QTC
Entries 310272
ADC
QT Input Channel

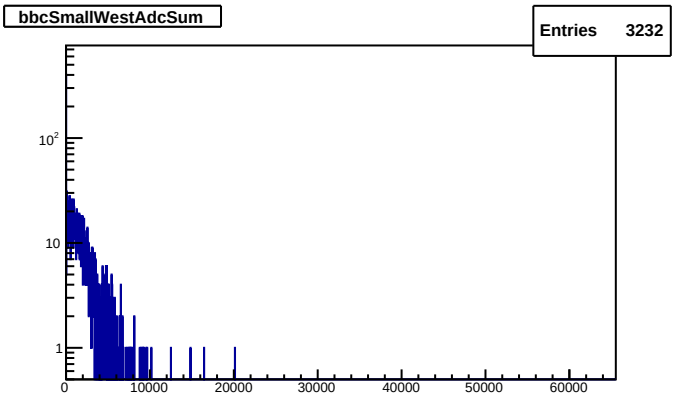

EQ3 QTC
Entries 310272
TADC
QT Input Channel

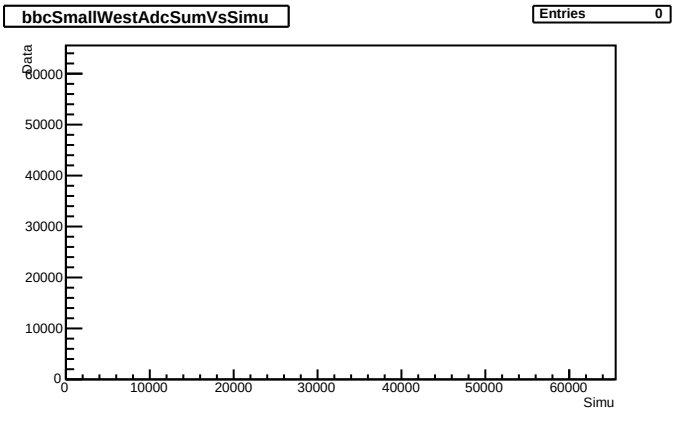

bbcSmallEastAdcSum
Entries 3232

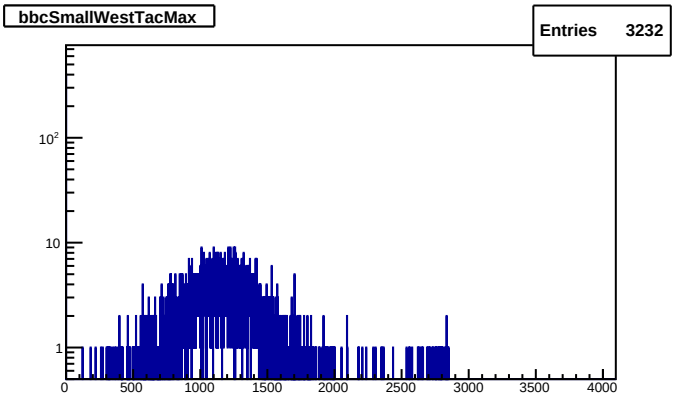

bbcSmallEastAdcSumVsSimu
Entries 0
Data
Simu

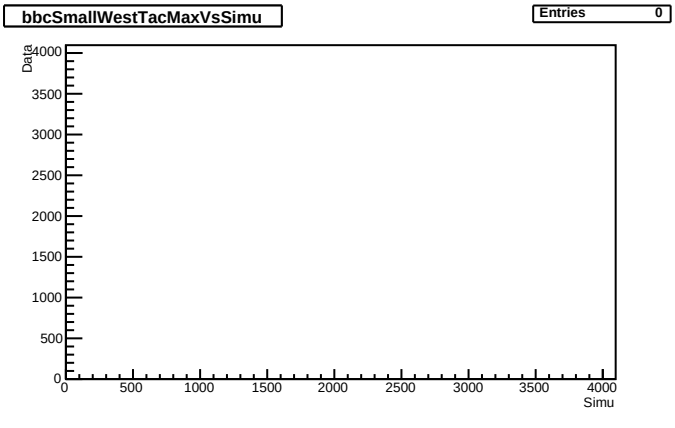

bbcSmallEastTacMax
Entries 3232

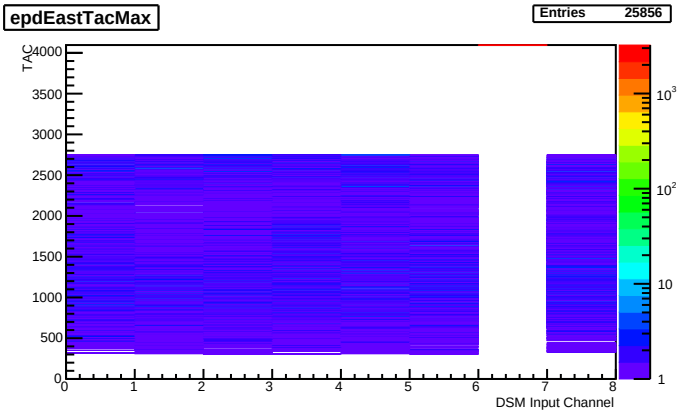

bbcSmallEastTacMaxVsSimu
Entries 0
Data
Simu

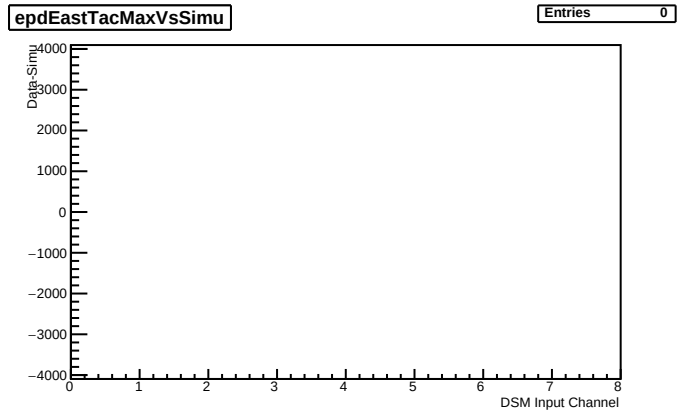

bbcSmallWestAdcSum
Entries 3232

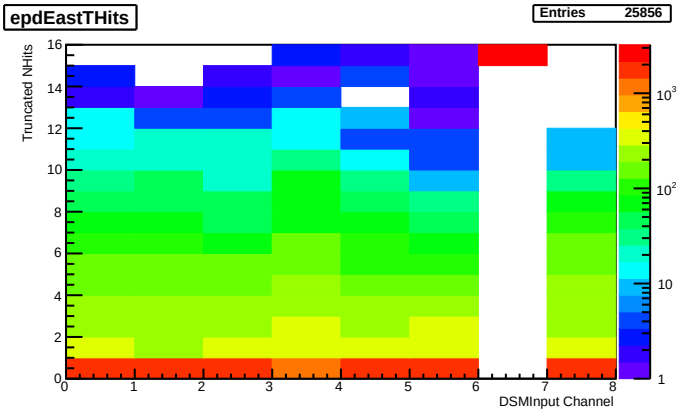

bbcSmallWestAdcSumVsSimu
Entries 0
Data
Simu

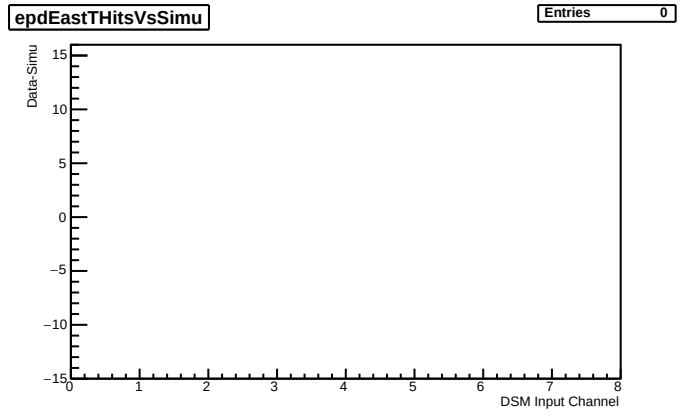

bbcSmallWestTacMax
Entries 3232

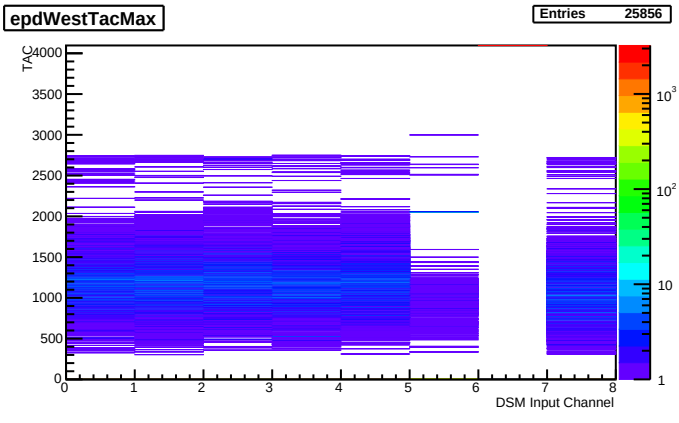

bbcSmallWestTacMaxVsSimu
Entries 0
Data
Simu

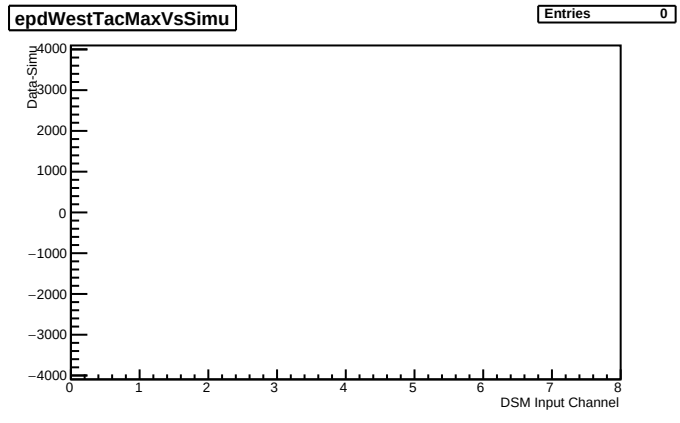

epdEastTacMax
Entries 25856
TAC
DSM Input Channel

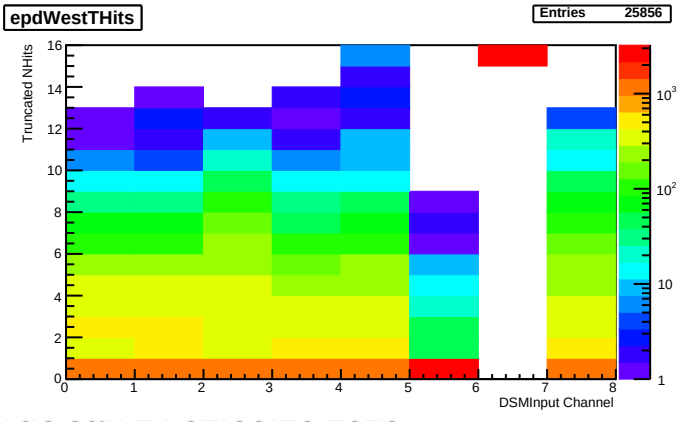

epdEastTacMaxVsSimu
Entries 0
Data-Simu
DSM Input Channel

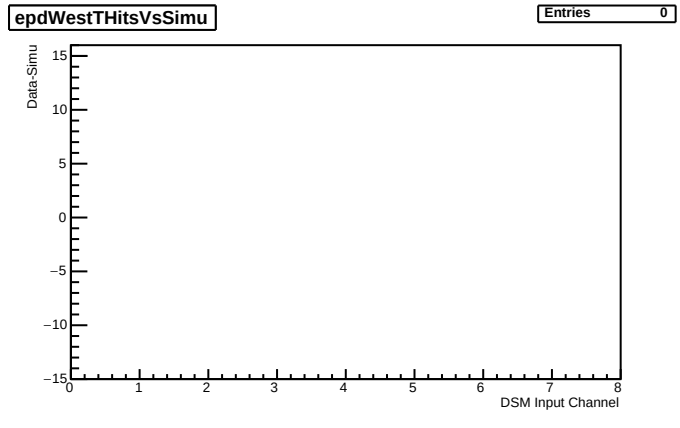

epdEastTHits
Entries 25856
Truncated NHits
DSMInput Channel

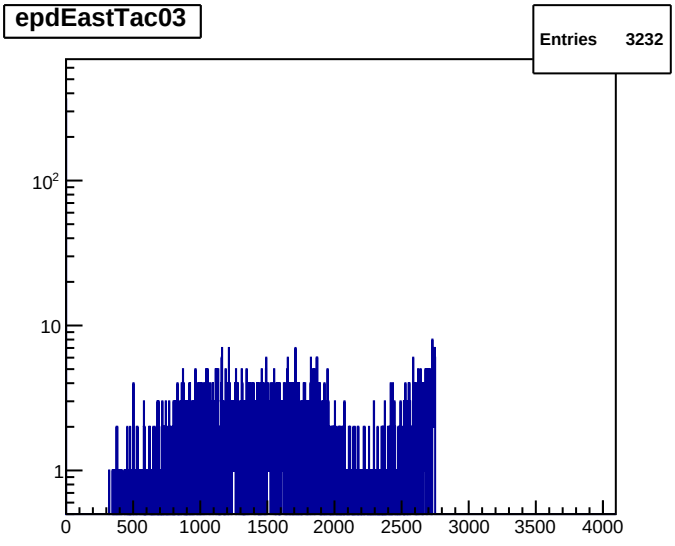

epdEastTHitsVsSimu
Entries 0
Data-Simu
DSM Input Channel

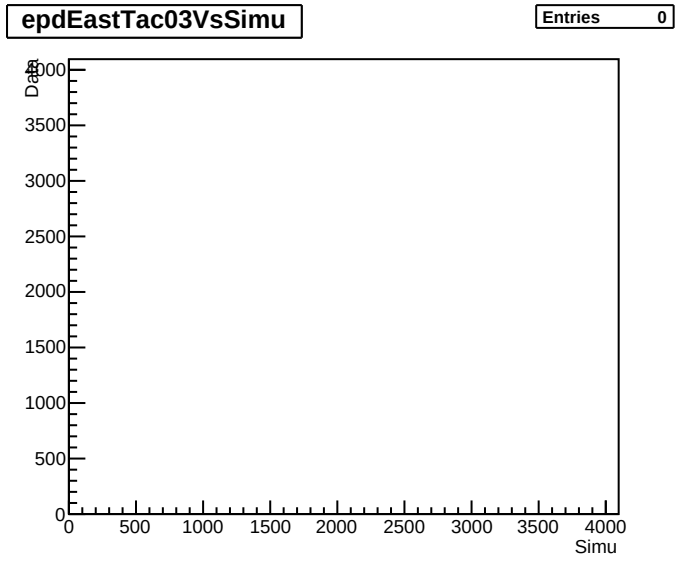

epdWestTacMax
Entries 25856
TAC
DSM Input Channel

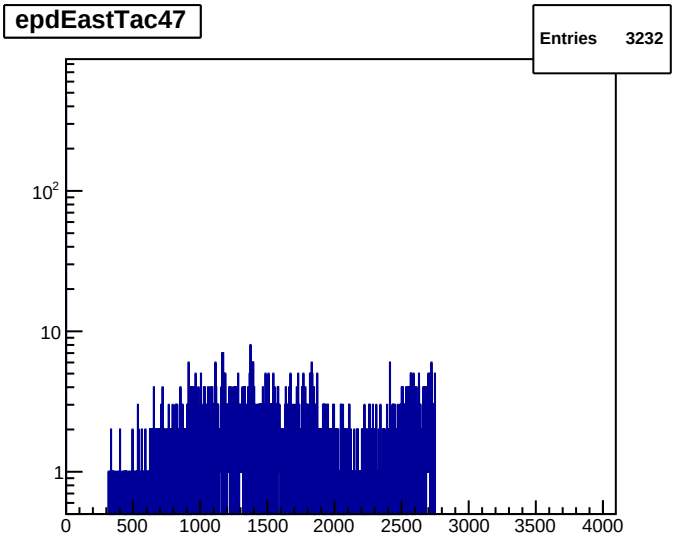

epdWestTacMaxVsSimu
Entries 0
Data-Simu
DSM Input Channel

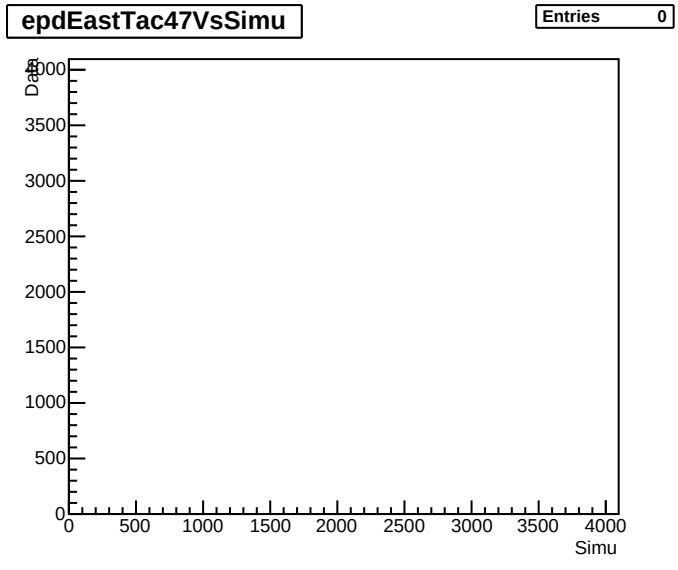

epdWestTHits
Entries 25856
Truncated NHits
DSMInput Channel

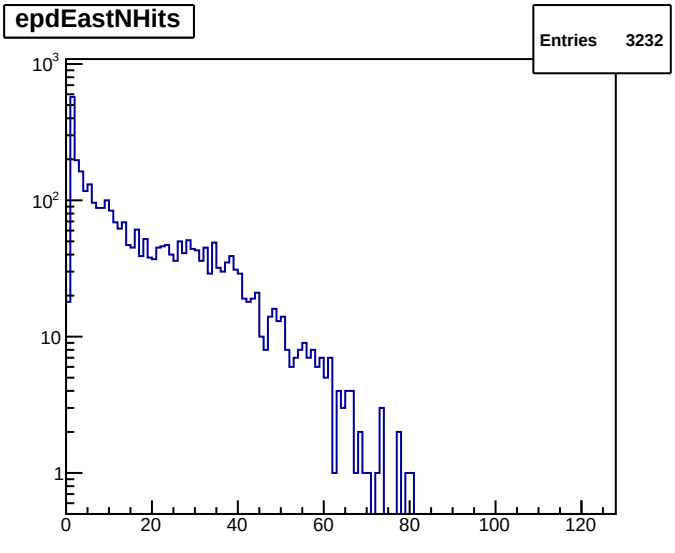

epdWestTHitsVsSimu
Entries 0
Data-Simu
DSM Input Channel

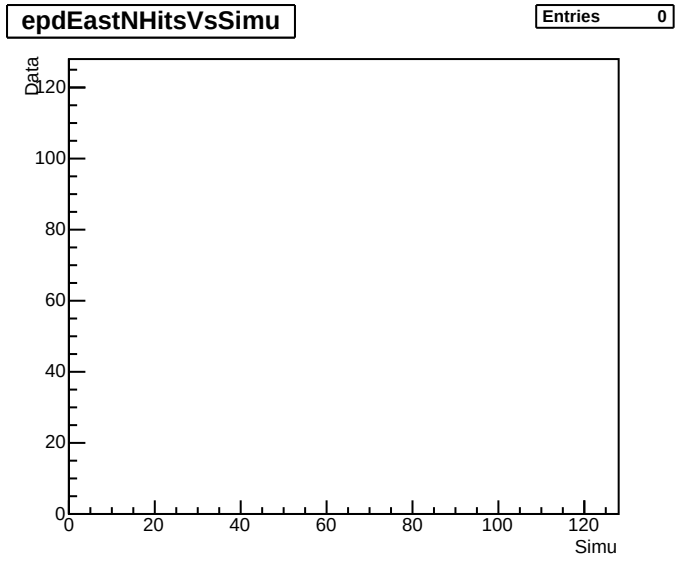

epdEastTac03
Entries 3232
10^2
10
1
0 500 1000 1500 2000 2500 3000 3500 4000

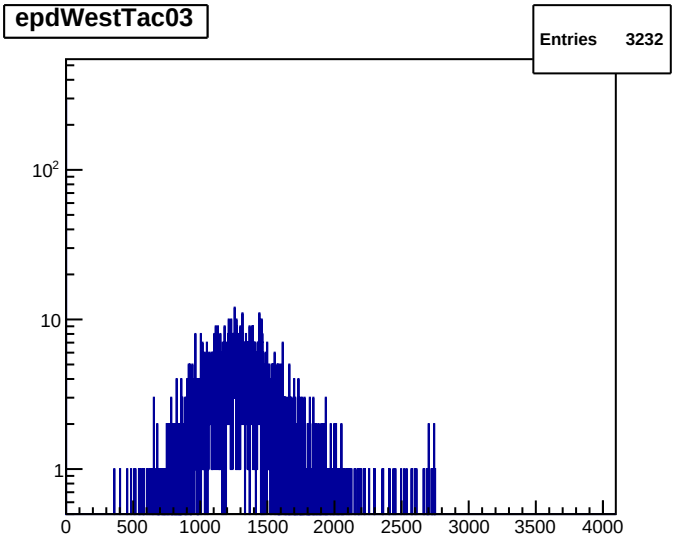

epdEastTac03VsSimu
Entries 0
Data
4000 3500 3000 2500 2000 1500 1000 500 0
0 500 1000 1500 2000 2500 3000 3500 4000
Simu

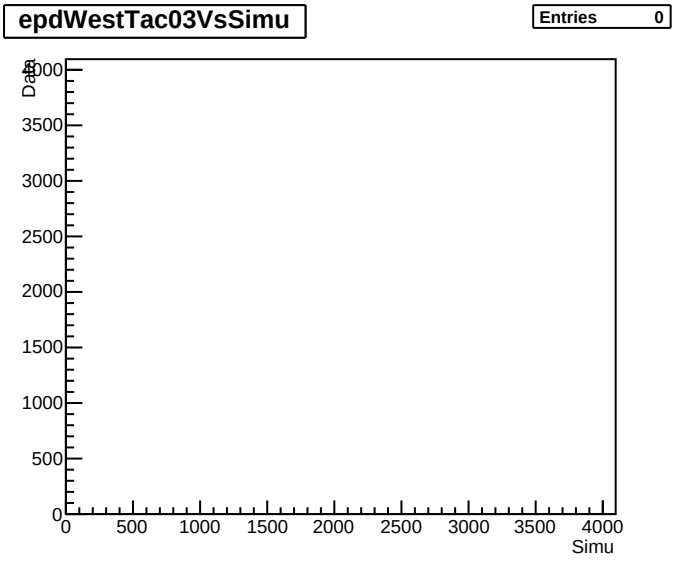

epdEastTac47
Entries 3232
10^2
10
1
0 500 1000 1500 2000 2500 3000 3500 4000

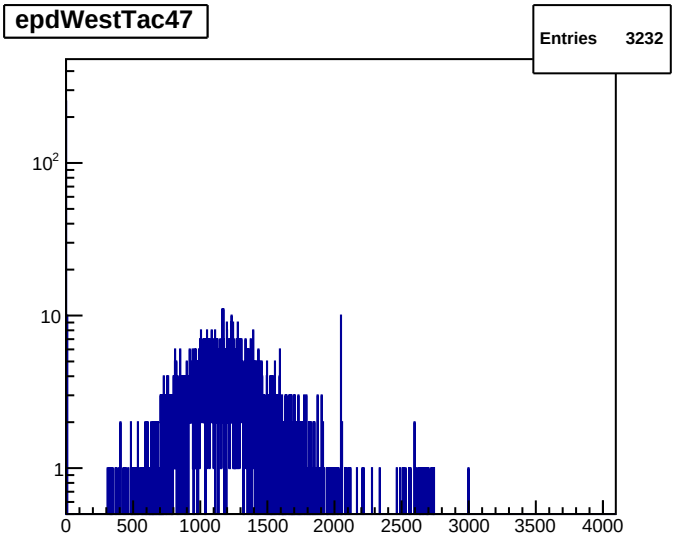

epdEastTac47VsSimu
Entries 0
Data
4000 3500 3000 2500 2000 1500 1000 500 0
0 500 1000 1500 2000 2500 3000 3500 4000
Simu

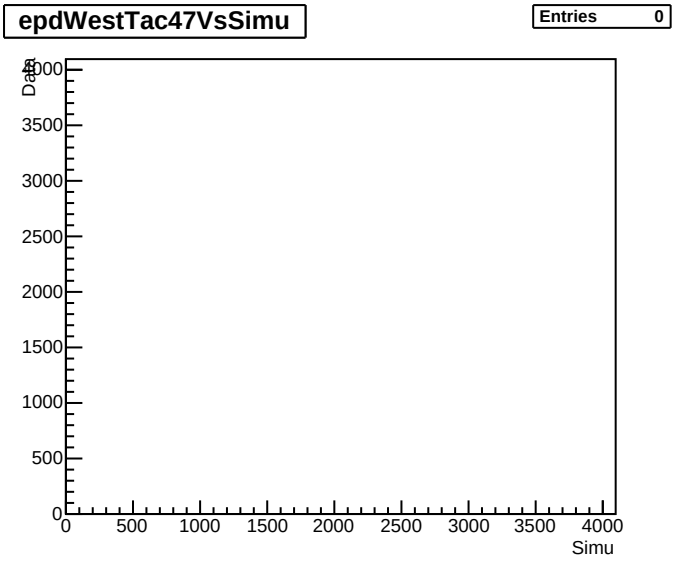

epdEastNHits
Entries 3232
10^3
10^2
10
1
0 20 40 60 80 100 120

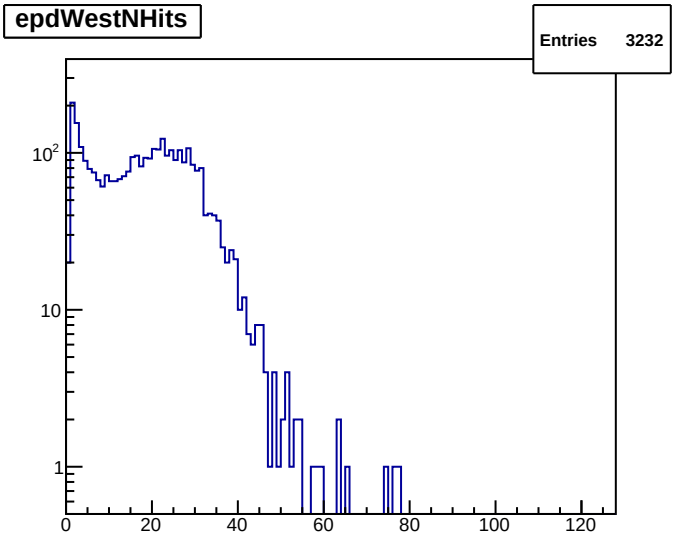

epdEastNHitsVsSimu
Entries 0
Data
120 100 80 60 40 20 0
0 20 40 60 80 100 120
Simu

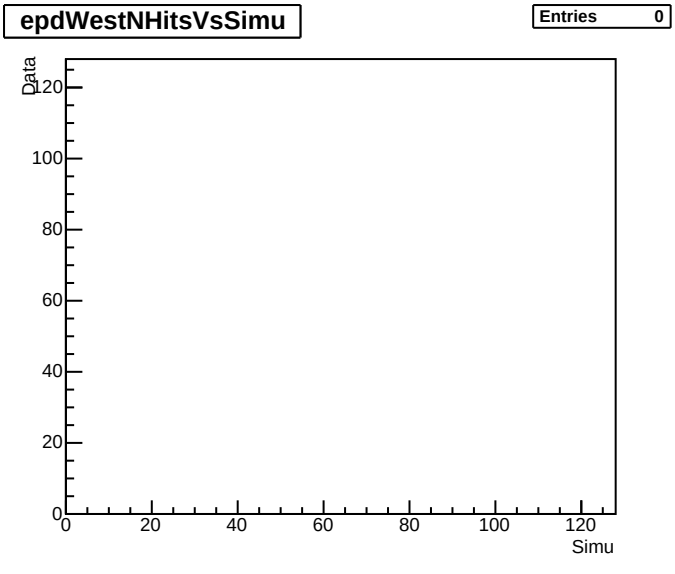

epdWestTac03
Entries 3232

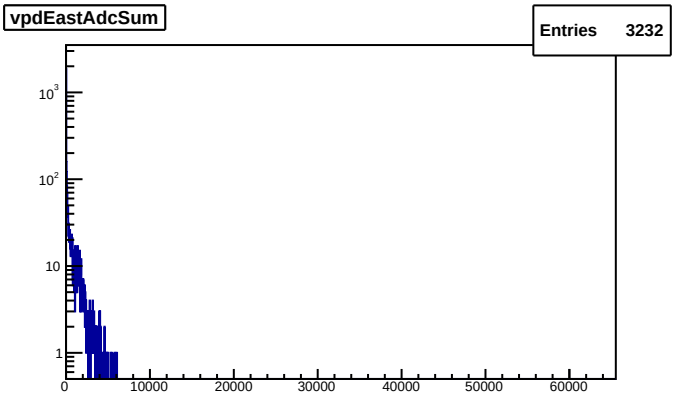

epdWestTac03VsSimu
Entries 0
Data
Simu

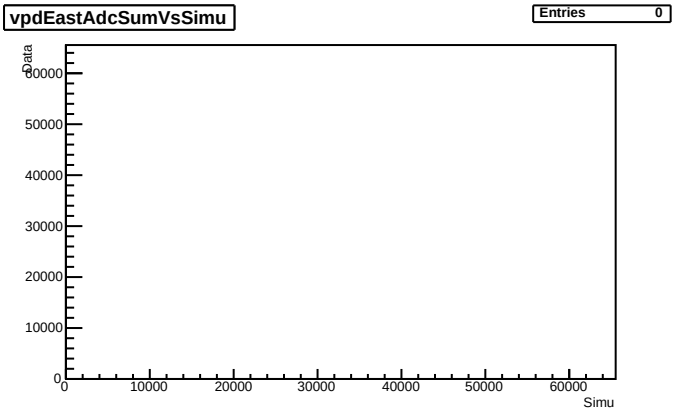

epdWestTac47
Entries 3232

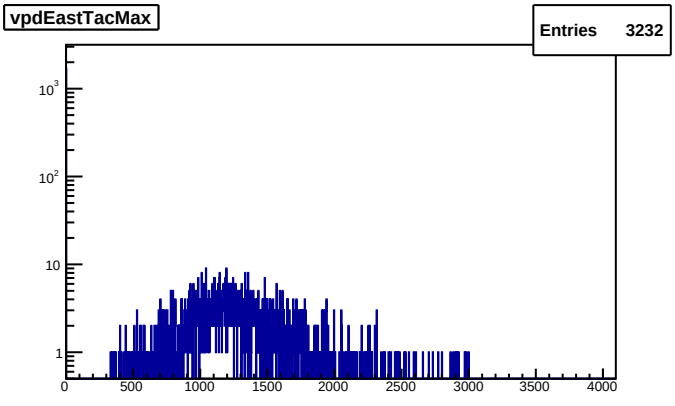

epdWestTac47VsSimu
Entries 0
Data
Simu

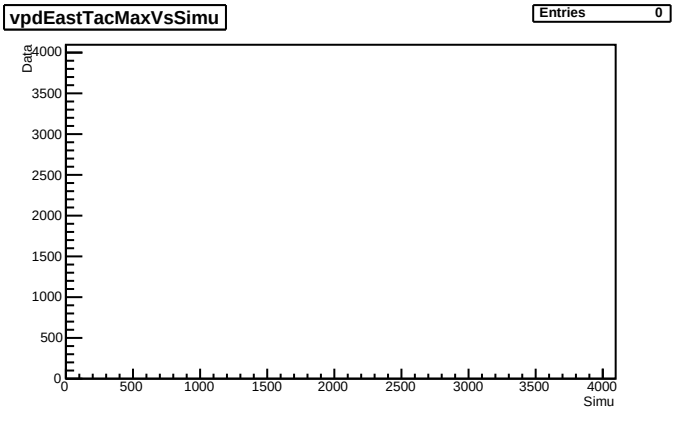

epdWestNHits
Entries 3232

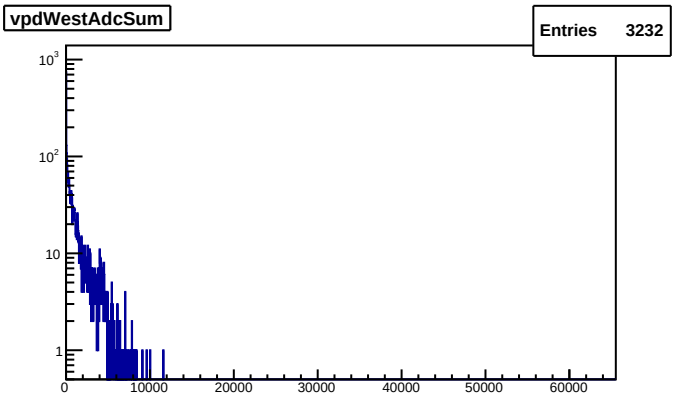

epdWestNHitsVsSimu
Entries 0
Data
Simu

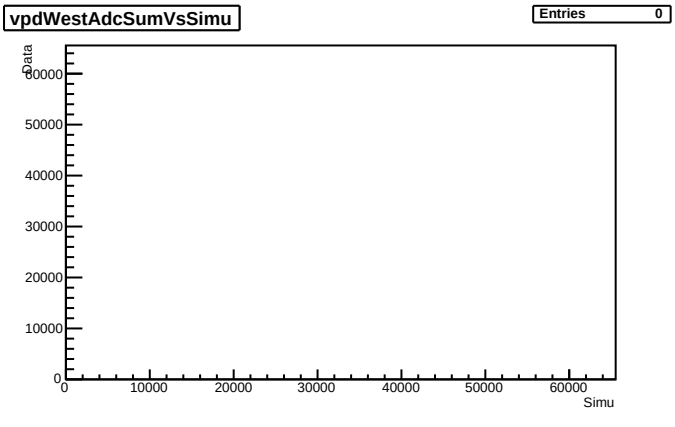

vpdEastAdcSum
Entries 3232

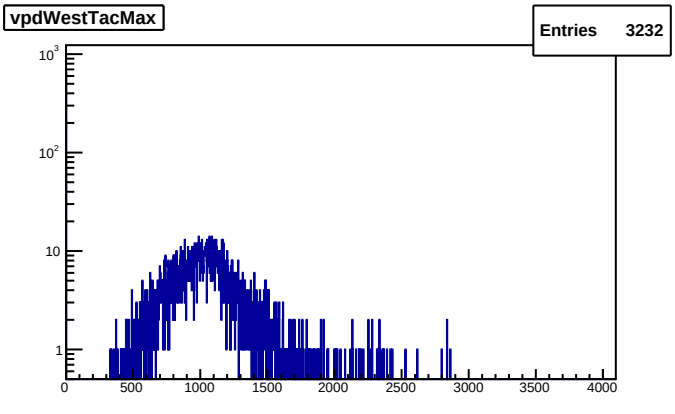

vpdEastAdcSumVsSimu
Entries 0
Data
Simu

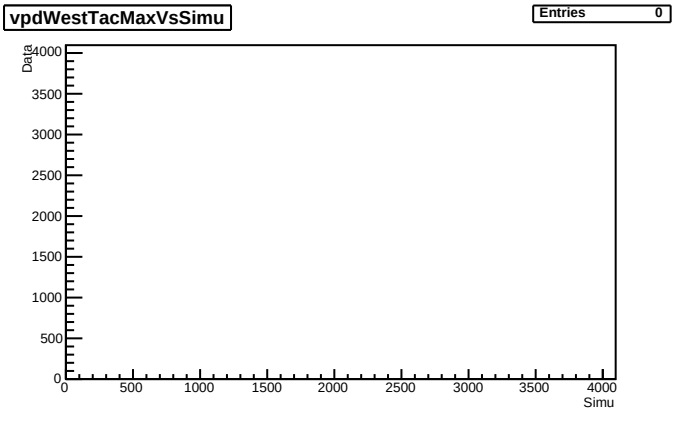

vpdEastTacMax
Entries 3232

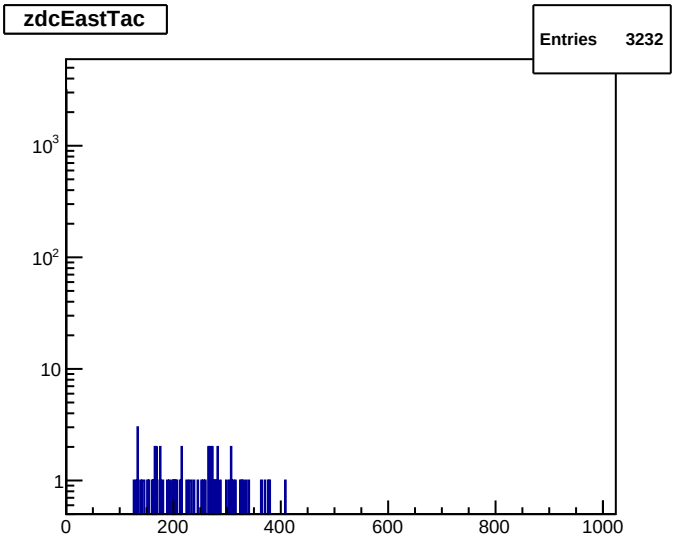

vpdEastTacMaxVsSimu
Entries 0
Data
Simu

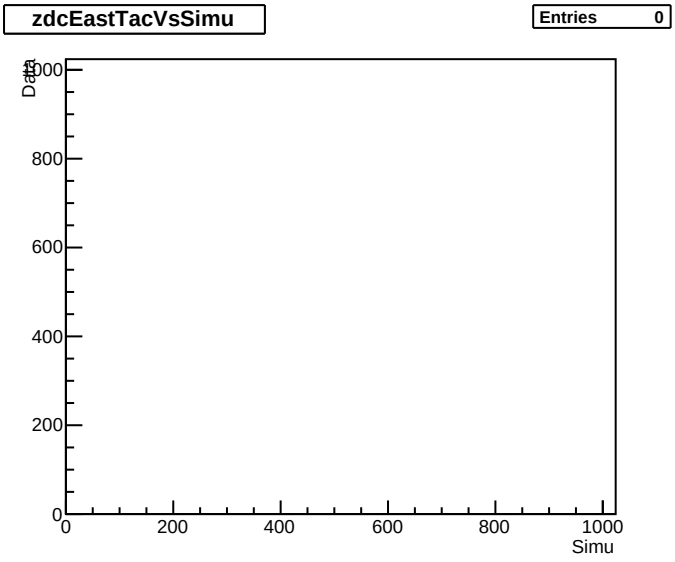

vpdWestAdcSum
Entries 3232

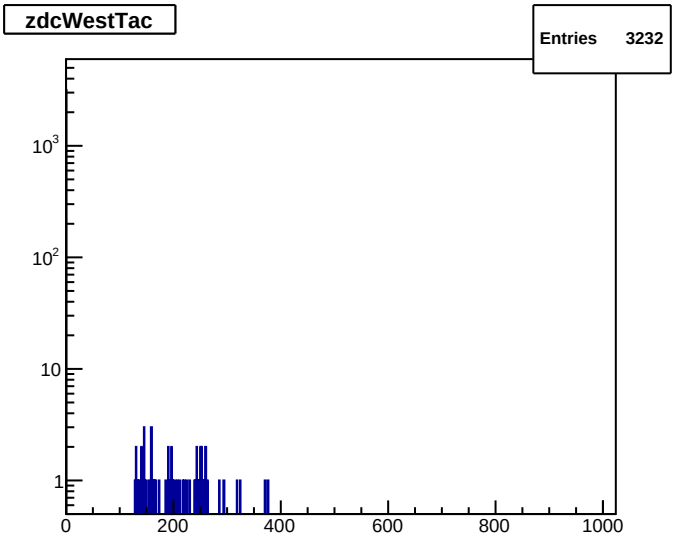

vpdWestAdcSumVsSimu
Entries 0
Data
Simu

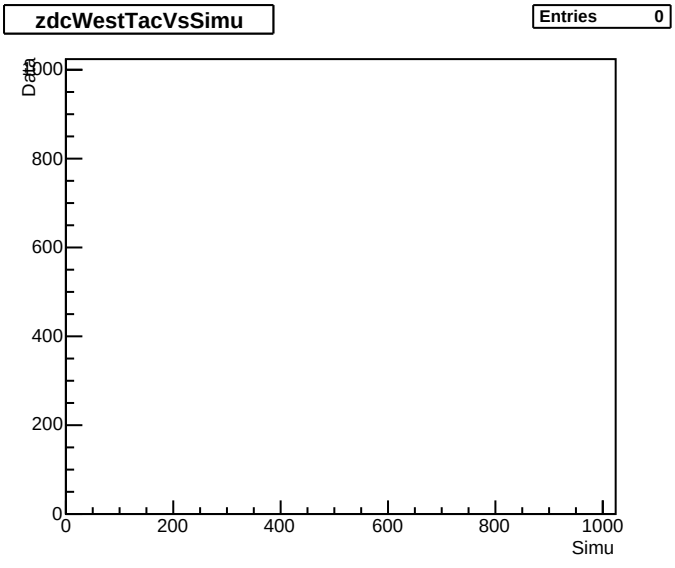

vpdWestTacMax
Entries 3232

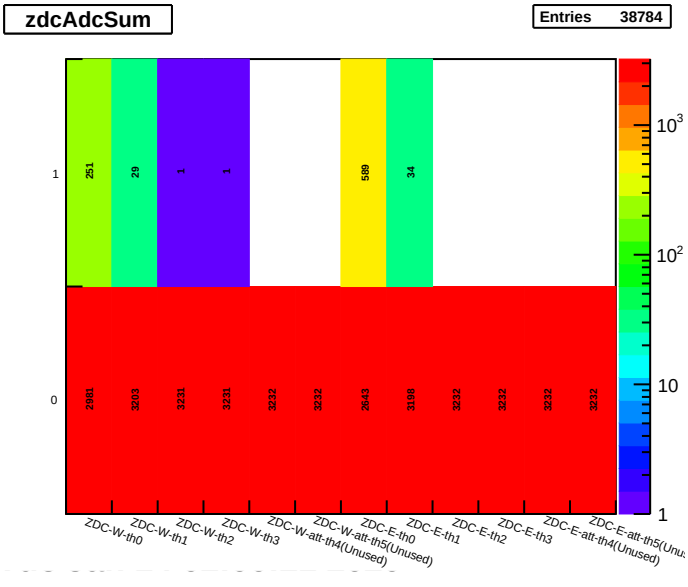

vpdWestTacMaxVsSimu
Entries 0
Data
Simu

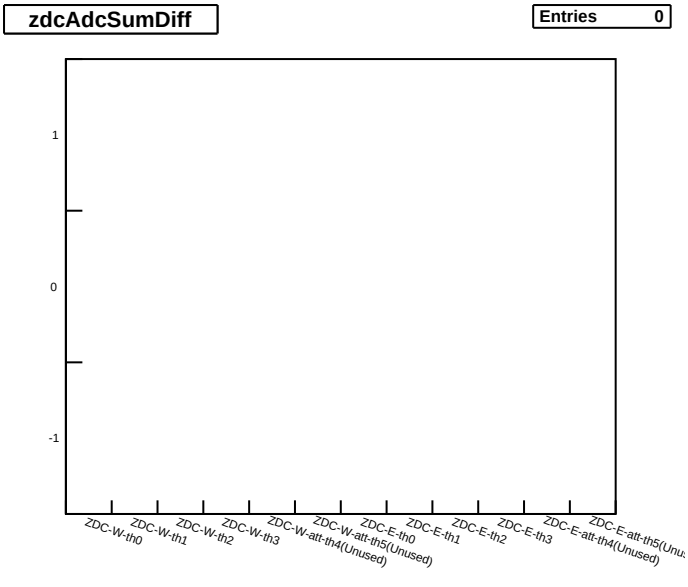

zdcEastTac
Entries 3232
10^3
10^2
10
1
0
200
400
600
800
1000

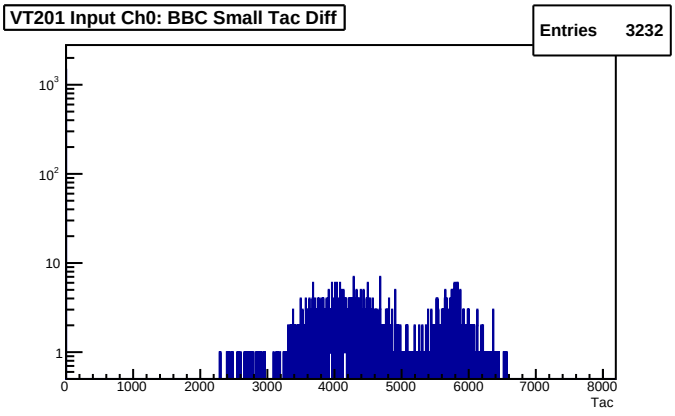

zdcEastTacVsSimu
Entries 0
Data
1000
800
600
400
200
0
0
200
400
600
800
1000
Simu

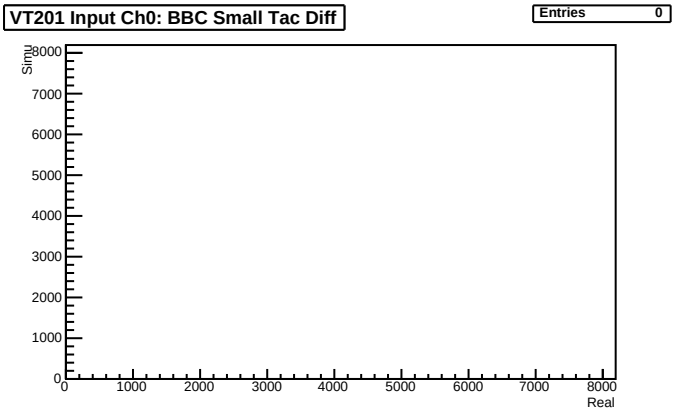

zdcWestTac
Entries 3232
10^3
10^2
10
1
0
200
400
600
800
1000

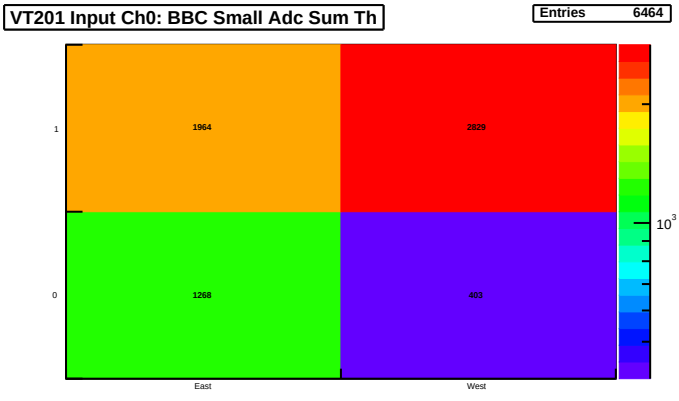

zdcWestTacVsSimu
Entries 0
Data
1000
800
600
400
200
0
0
200
400
600
800
1000
Simu

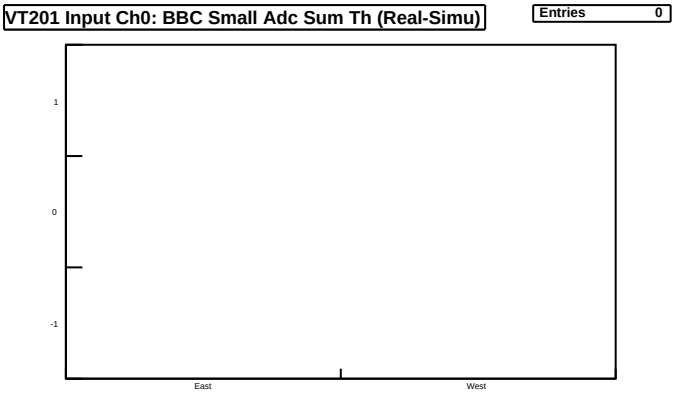

zdcAdcSum
Entries 38784
1
0
251
29
1
1
589
34
2981
3203
3231
3231
3232
3232
2643
3198
3232
3232
3232
3232
10^3
10^2
10
1
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)
ZDC-E-att-th5(Unu

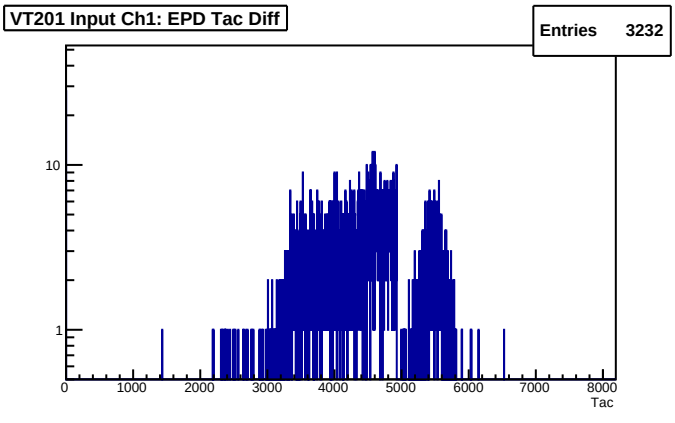

zdcAdcSumDiff
Entries 0
1
0
-1
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)
ZDC-E-att-th5(Unu

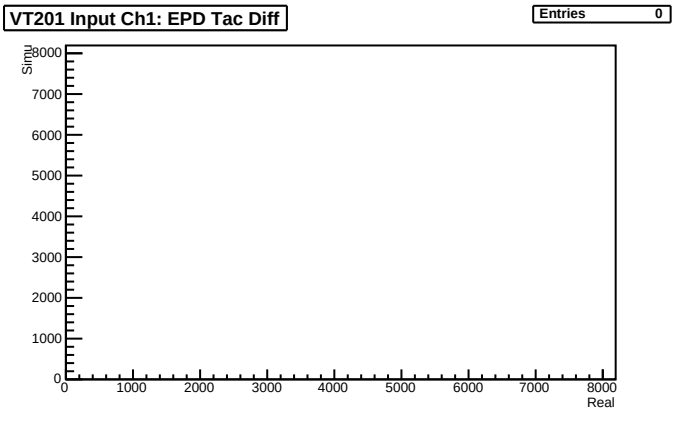

VT201 Input Ch0: BBC Small Tac Diff
Entries 3232
Tac

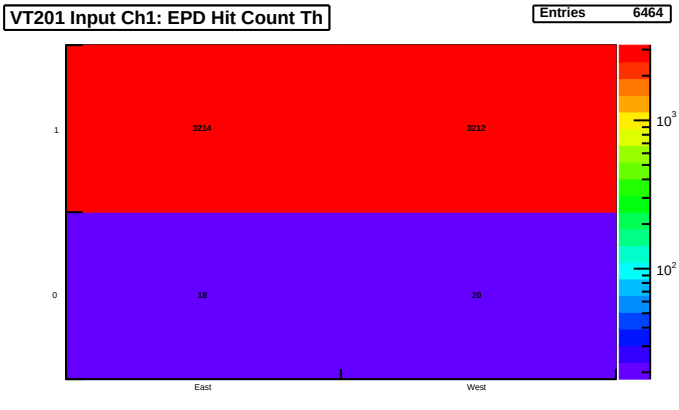

VT201 Input Ch0: BBC Small Tac Diff
Entries 0
Simu
Real

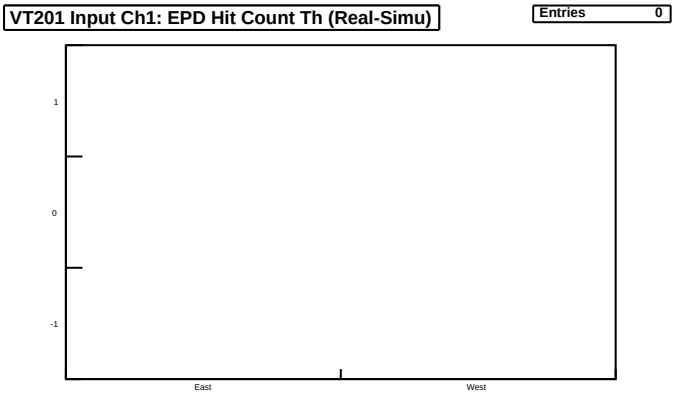

VT201 Input Ch0: BBC Small Adc Sum Th
Entries 6464
1964
2829
1268
403
East
West

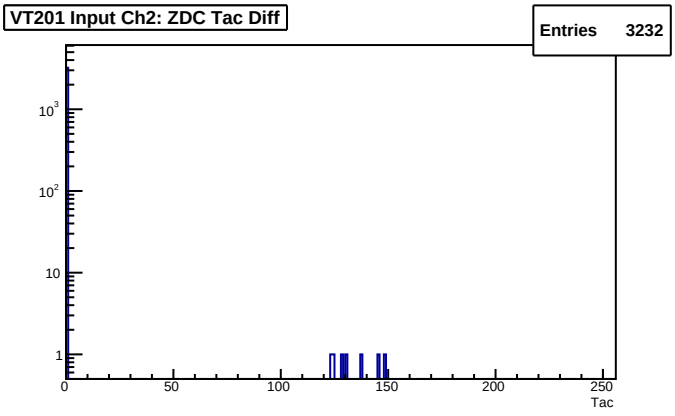

VT201 Input Ch0: BBC Small Adc Sum Th (Real-Simu)
Entries 0
East
West

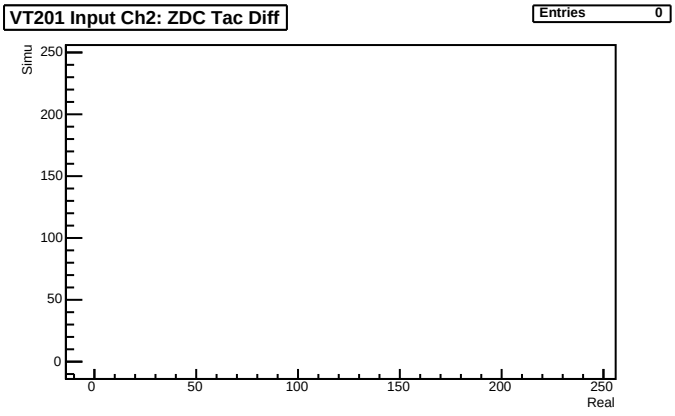

VT201 Input Ch1: EPD Tac Diff
Entries 3232
Tac

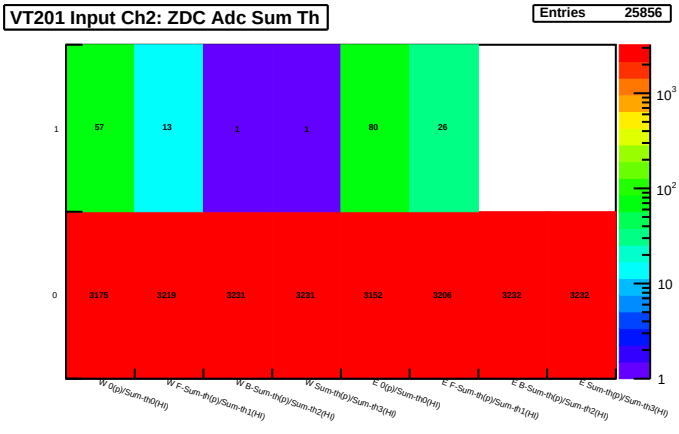

VT201 Input Ch1: EPD Tac Diff
Entries 0
Simu
Real

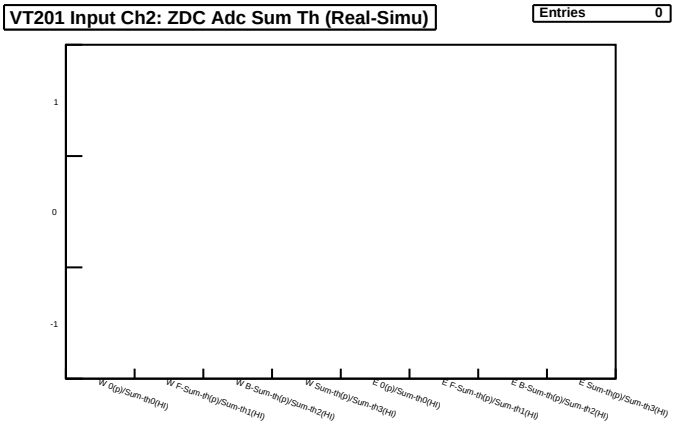

VT201 Input Ch1: EPD Hit Count Th
Entries 6464
3214
3212
18
20
East
West

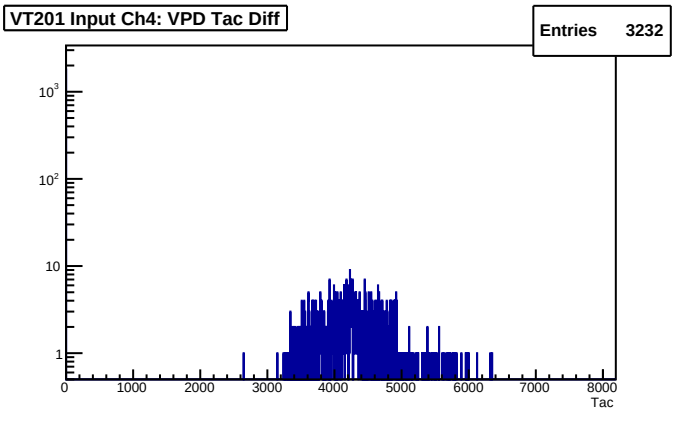

VT201 Input Ch1: EPD Hit Count Th (Real-Simu)
Entries 0
East
West

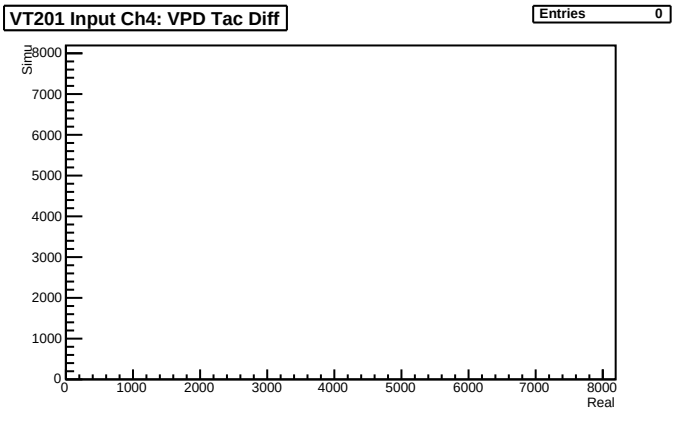

VT201 Input Ch2: ZDC Tac Diff
Entries 3232
Tac

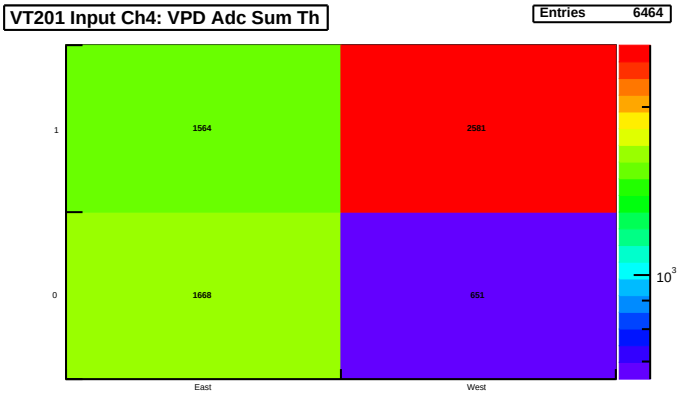

VT201 Input Ch2: ZDC Tac Diff
Entries 0
Simu
Real

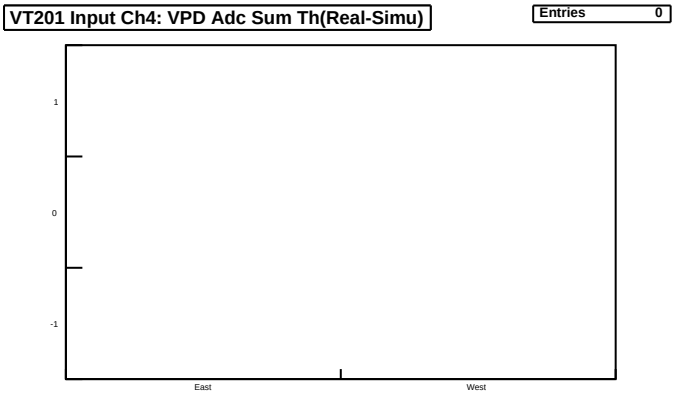

VT201 Input Ch2: ZDC Adc Sum Th
Entries 25856
57
13
1
1
80
26
3175
3219
3231
3231
3152
3206
3232
3232
W 0(p)/Sum-th0(H)
W F-Sum-th(p)/Sum-th1(H)
W B-Sum-th(p)/Sum-th2(H)
W Sum-th(p)/Sum-th3(H)
E 0(p)/Sum-th0(H)
E F-Sum-th(p)/Sum-th1(H)
E B-Sum-th(p)/Sum-th2(H)
E Sum-th(p)/Sum-th3(H)

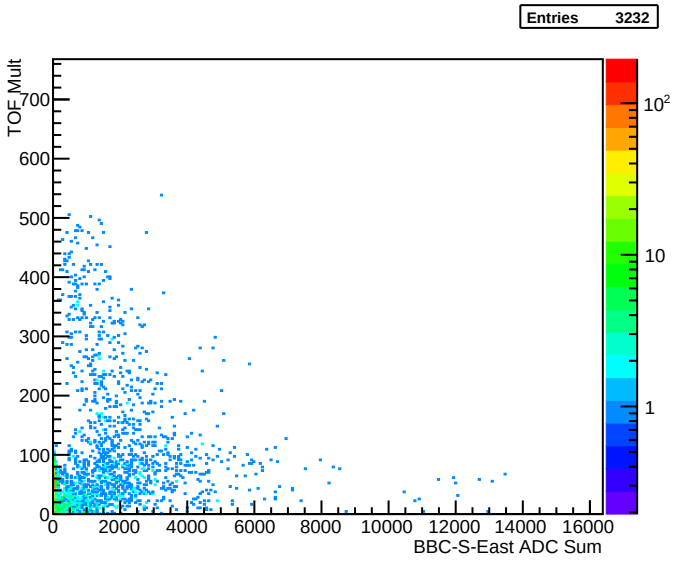

VT201 Input Ch2: ZDC Adc Sum Th (Real-Simu)
Entries 0
W 0(p)/Sum-th0(H)
W F-Sum-th(p)/Sum-th1(H)
W B-Sum-th(p)/Sum-th2(H)
W Sum-th(p)/Sum-th3(H)
E 0(p)/Sum-th0(H)
E F-Sum-th(p)/Sum-th1(H)
E B-Sum-th(p)/Sum-th2(H)
E Sum-th(p)/Sum-th3(H)

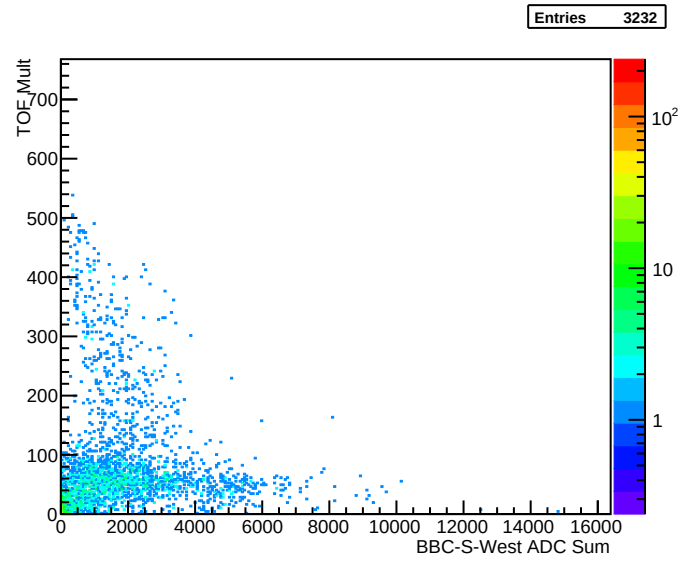

VT201 Input Ch4: VPD Tac Diff
Entries 3232
Tac

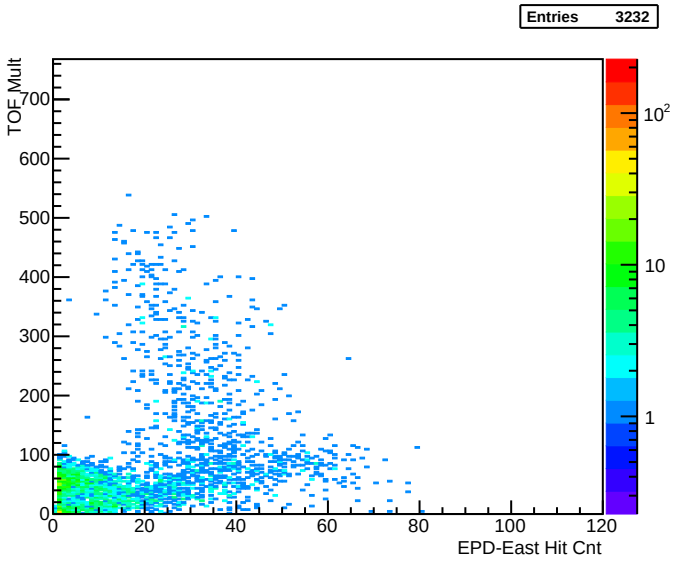

VT201 Input Ch4: VPD Tac Diff
Entries 0
Simu
Real

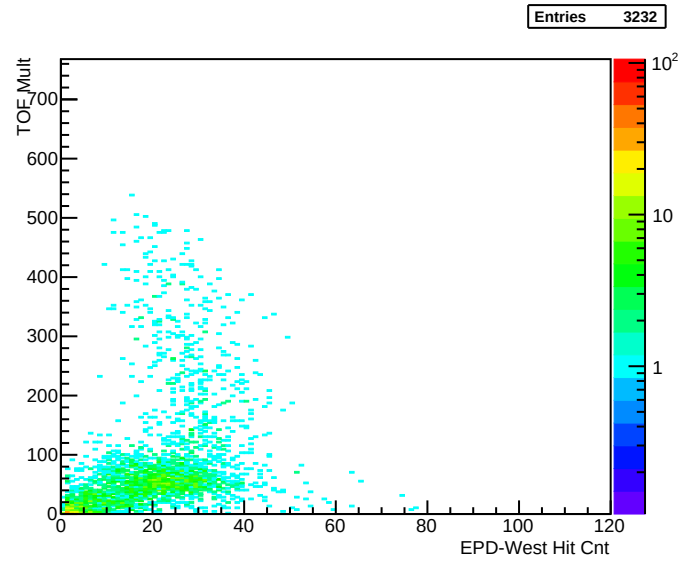

VT201 Input Ch4: VPD Adc Sum Th
Entries 6464
1564
2581
1668
651
East
West

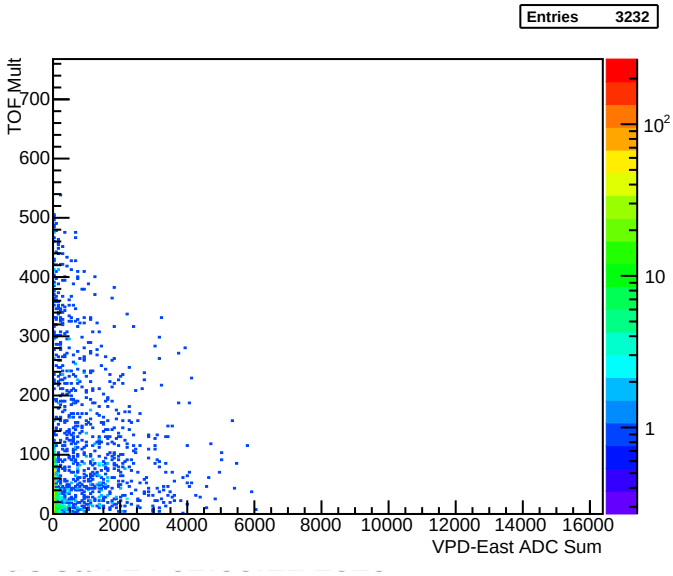

VT201 Input Ch4: VPD Adc Sum Th(Real-Simu)
Entries 0
East
West

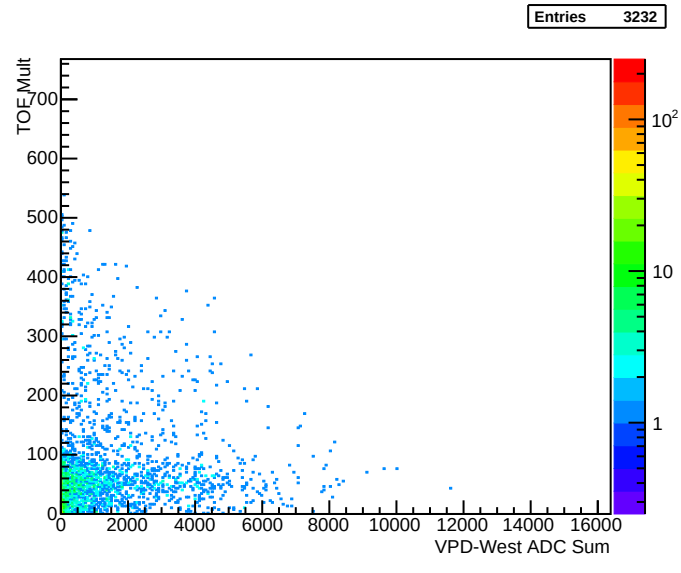

Entries 3232
TOF_Mult
BBC-S-East ADC Sum

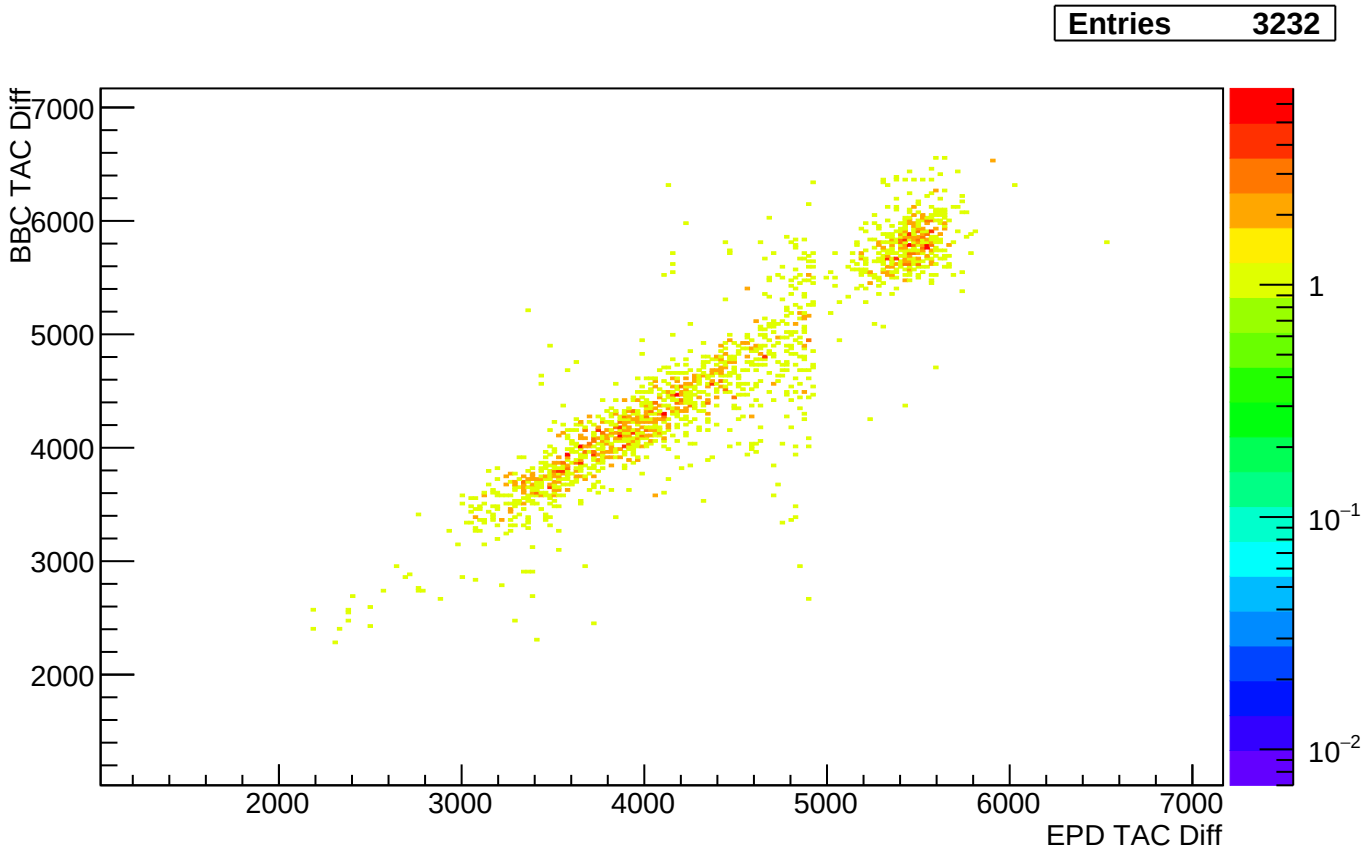

Entries 3232
TOF_Mult
BBC-S-West ADC Sum

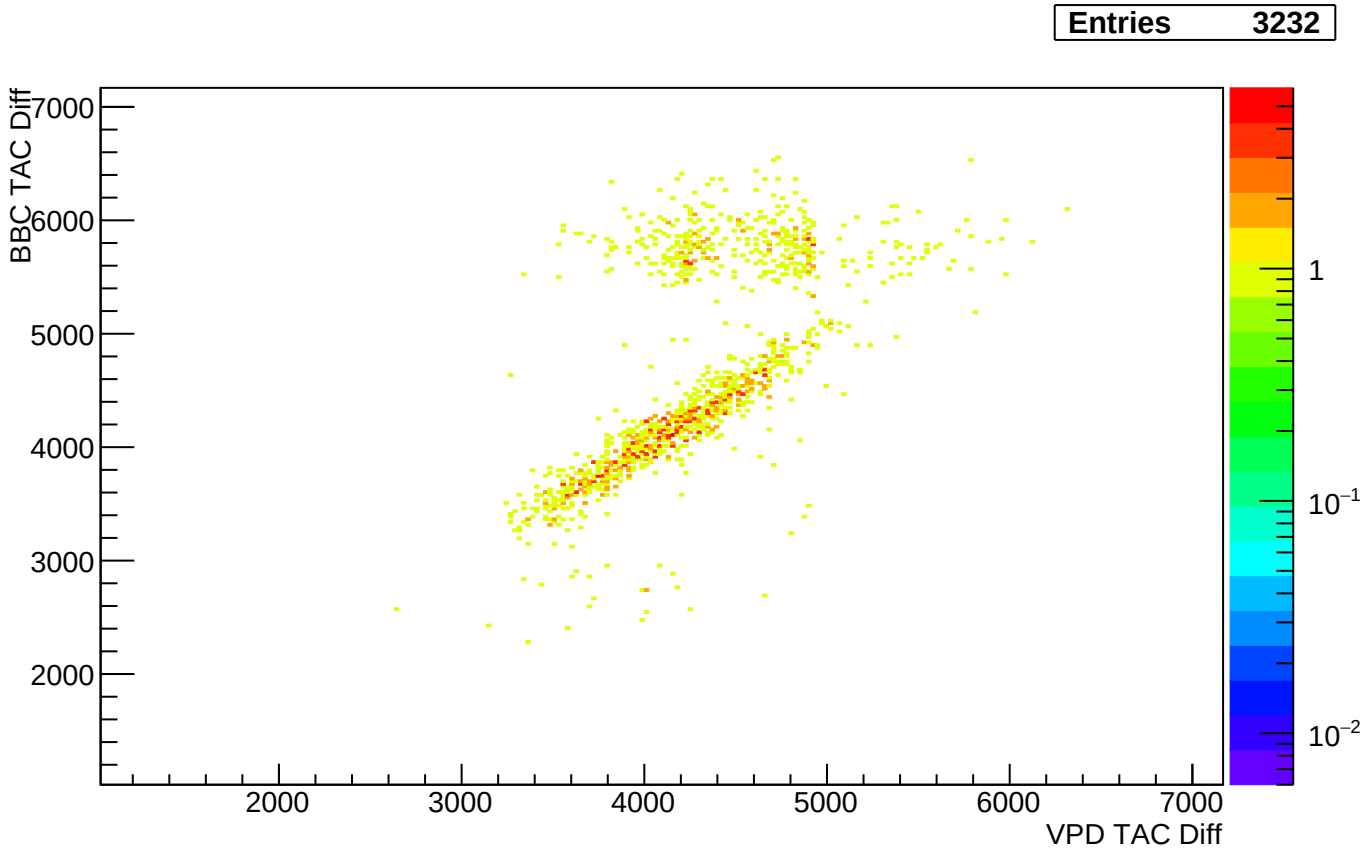

Entries 3232
TOF_Mult
EPD-East Hit Cnt

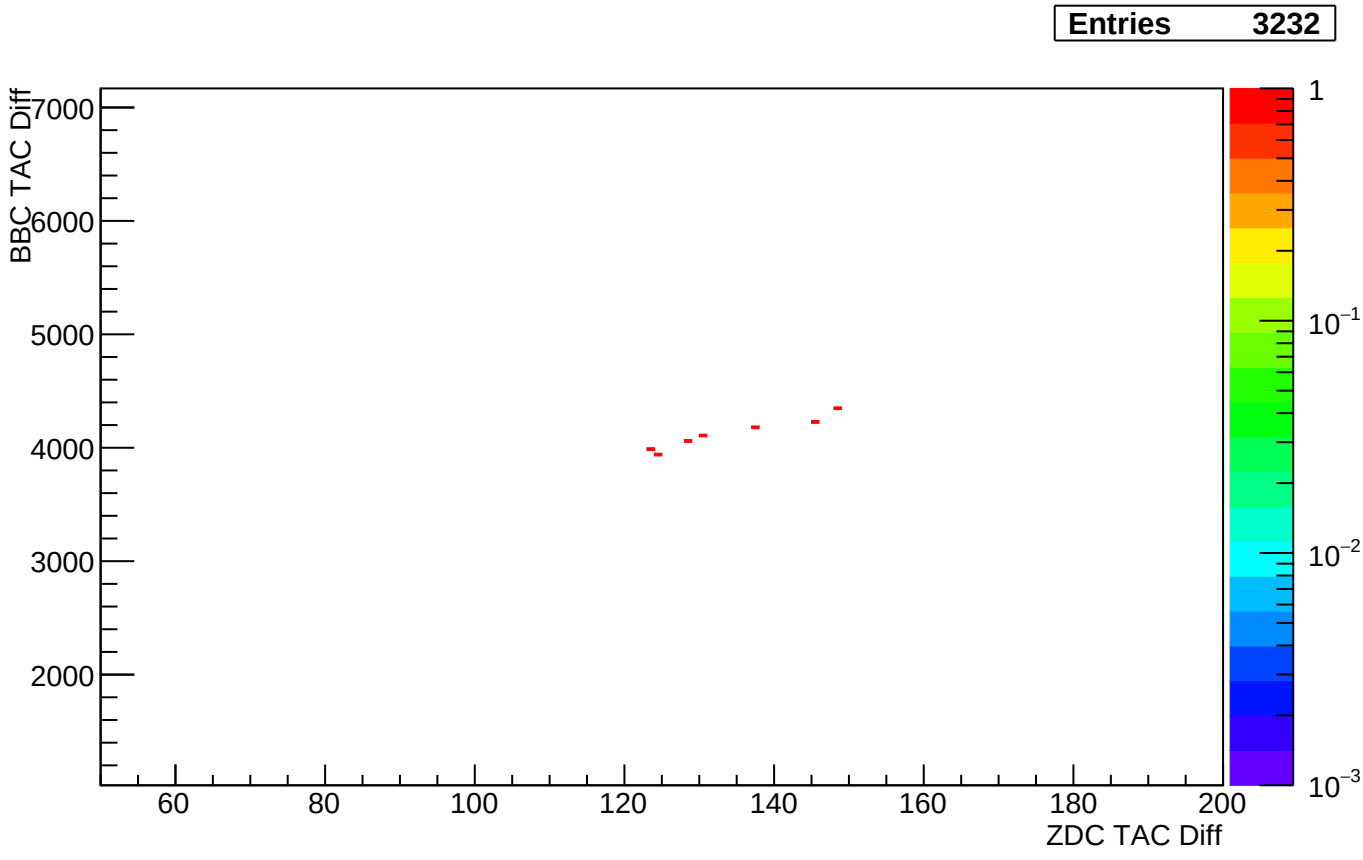

Entries 3232
TOF_Mult
EPD-West Hit Cnt

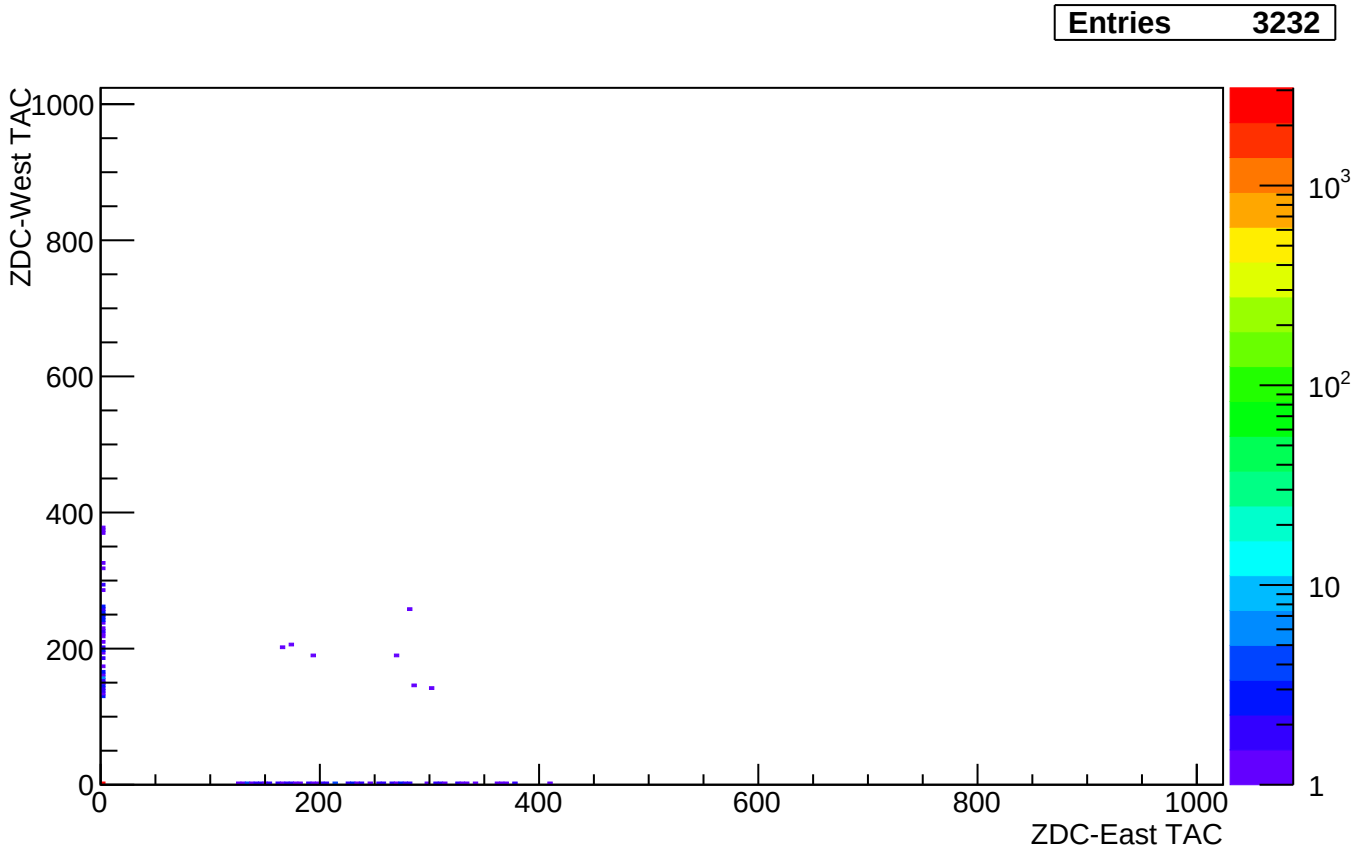

Entries 3232
TOF_Mult
VPD-East ADC Sum

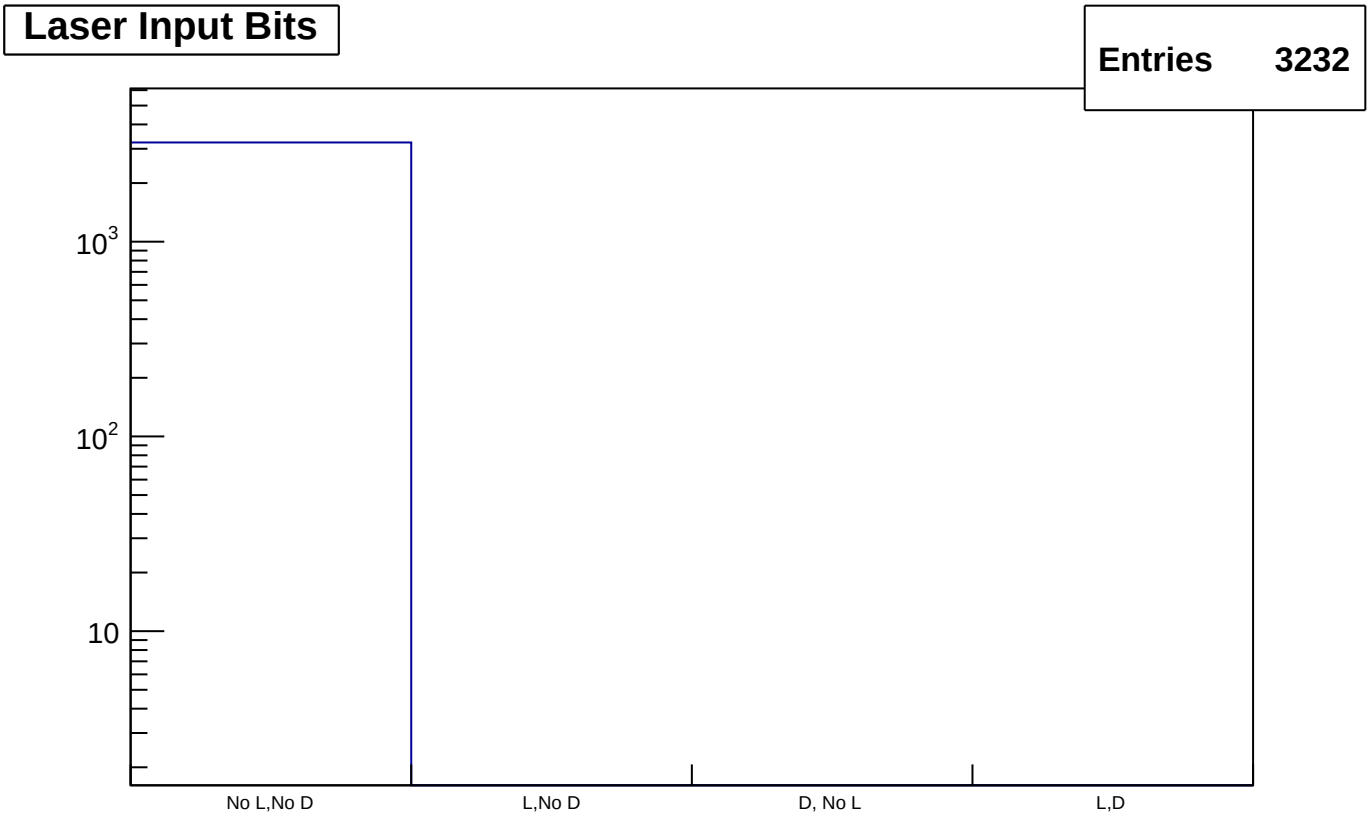

Entries 3232
TOF_Mult
VPD-West ADC Sum

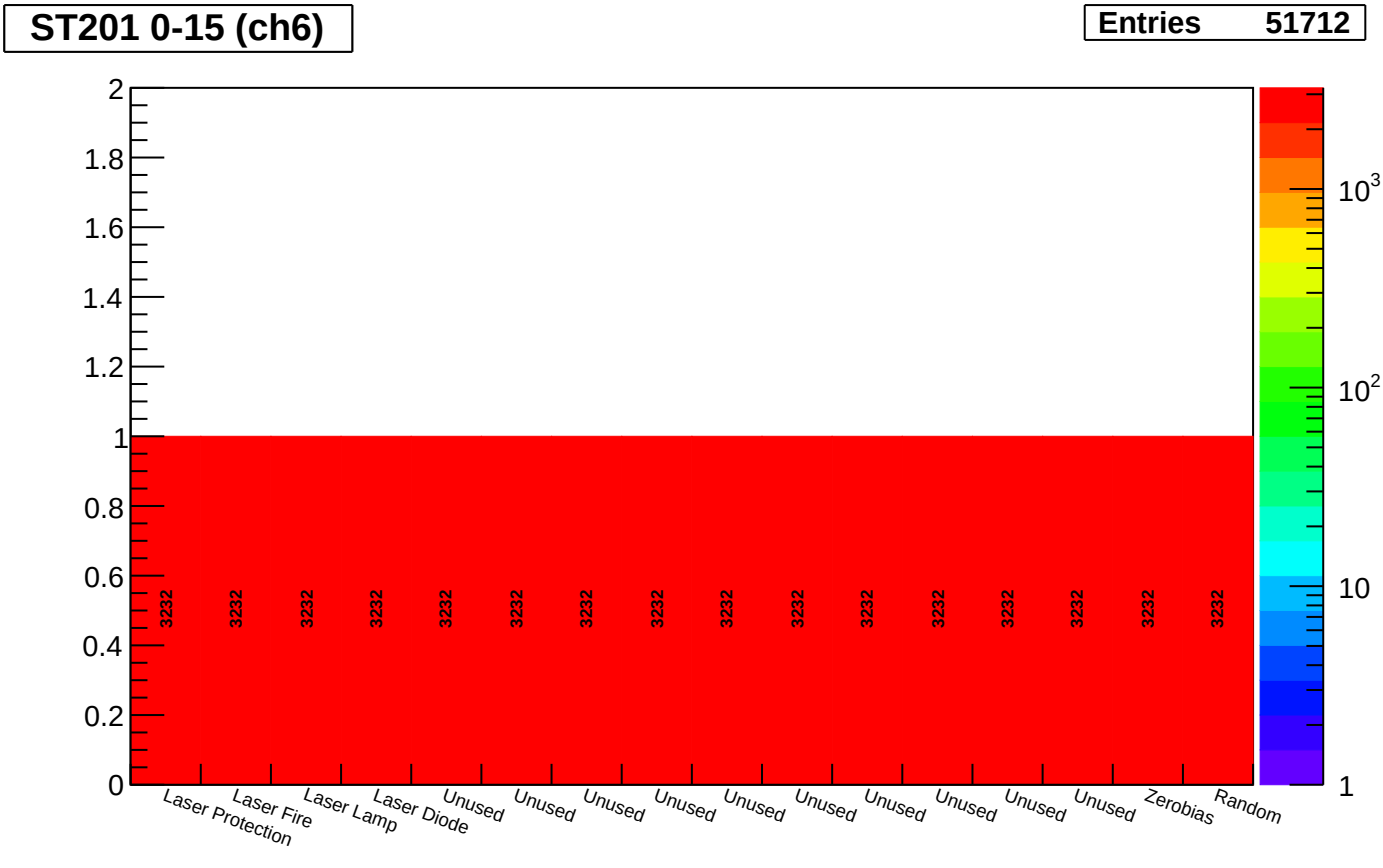

Entries 3232
BBC TAC Diff
7000
6000
5000
4000
3000
2000
2000
3000
4000
5000
6000
7000
EPD TAC Diff
1
10^{-1}
10^{-2}

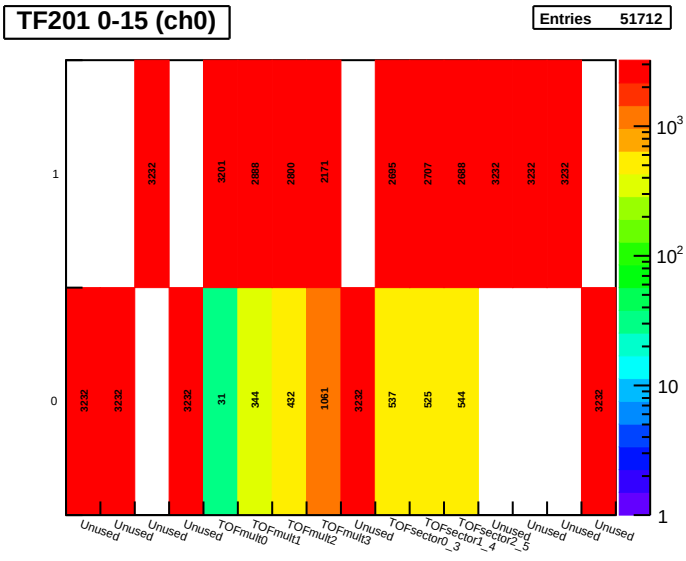

Entries 3232
BBC TAC Diff
7000
6000
5000
4000
3000
2000
2000
3000
4000
5000
6000
7000
VPD TAC Diff
1
10^{-1}
10^{-2}

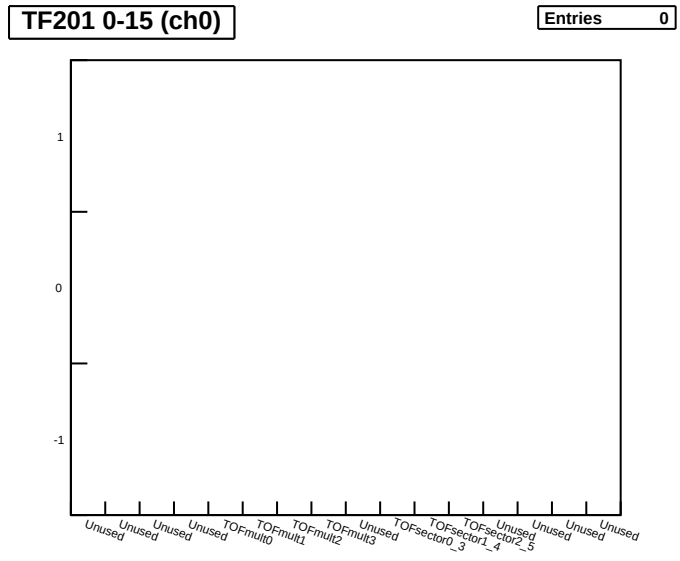

Entries 3232
BBC TAC Diff
7000
6000
5000
4000
3000
2000
60
80
100
120
140
160
180
200
ZDC TAC Diff
1
10^{-1}
10^{-2}
10^{-3}

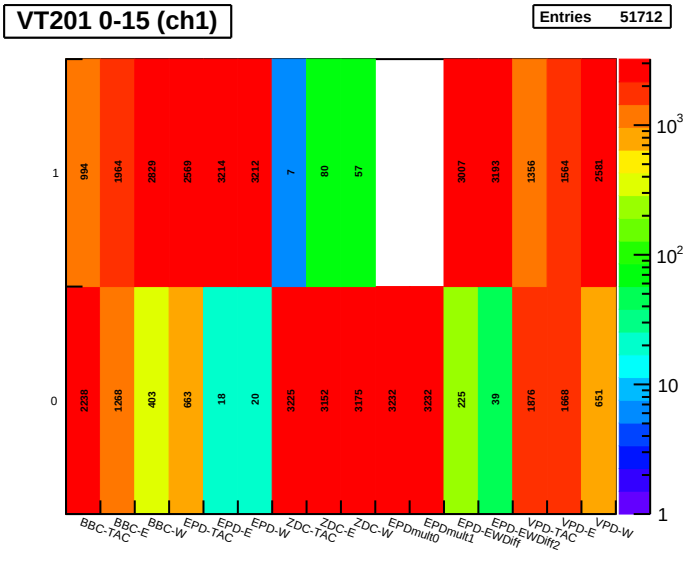

Entries 3232
ZDC-West TAC
1000
800
600
400
200
0
0
200
400
600
800
1000
ZDC-East TAC
10^3
10^2
10
1

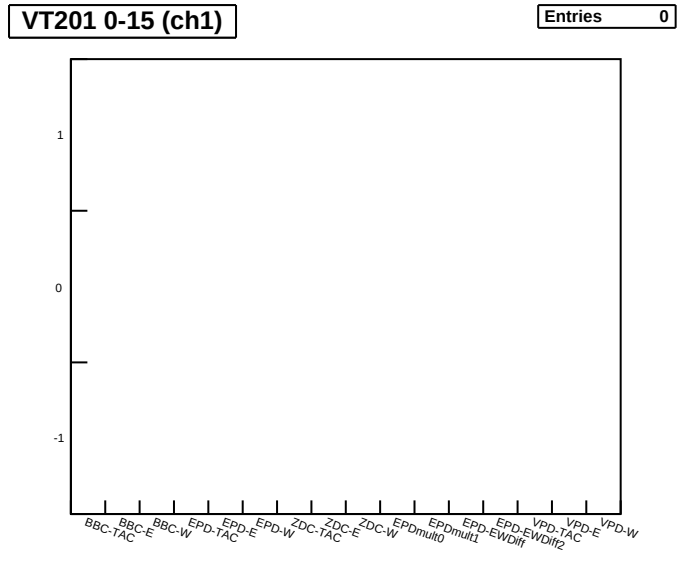

Laser Input Bits
Entries 3232
10^3
10^2
10
No L,No D
L,No D
D, No L
L,D

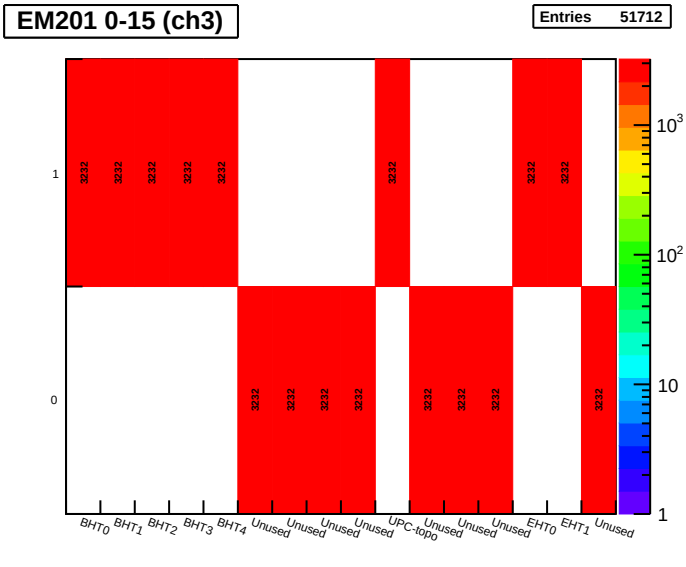

ST201 0-15 (ch6)
Entries 51712
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
3232
Laser Protection
Laser Fire
Laser Lamp
Laser Diode
Unused
Zerobias
Random
10^3
10^2
10
1

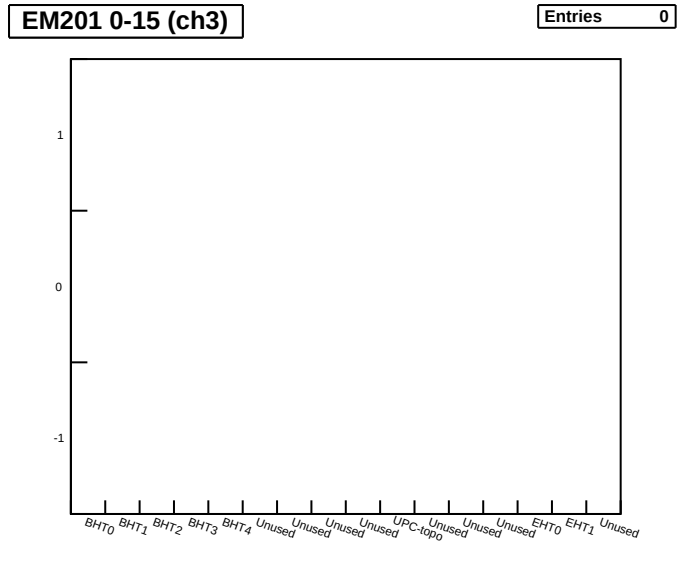

TF201 0-15 (ch0)
Entries 51712
1
0
3232 3201 2888 2800 2171 2695 2707 2688 3232 3232 3232
3232 3232 3232 31 344 432 1061 3232 537 525 544 3232
Unused Unused Unused Unused TOFmult0 TOFmult1 TOFmult2 TOFmult3 Unused TOFsector0_3 TOFsector1_4 TOFsector2_5 Unused Unused Unused Unused
10^3
10^2
10
1


TF201 0-15 (ch0)
Entries 0
1
0
-1
Unused Unused Unused Unused TOFmult0 TOFmult1 TOFmult2 TOFmult3 Unused TOFsector0_3 TOFsector1_4 TOFsector2_5 Unused Unused Unused


VT201 0-15 (ch1)
Entries 51712
1
0
994 1964 2829 2569 3214 3212 7 80 57 3007 3193 1356 1564 2581
2238 1268 403 663 18 20 3225 3152 3175 3232 3232 225 39 1876 1668 651
BBC-TAC BBC-E BBC-W EPD-TAC EPD-E EPD-W ZDC-TAC ZDC-E ZDC-W EPDmult0 EPDmult1 EPD-EWDiff EPD-EWDiff2 VPD-TAC VPD-E VPD-W
10^3
10^2
10
1


VT201 0-15 (ch1)
Entries 0
1
0
-1
BBC-TAC BBC-E BBC-W EPD-TAC EPD-E EPD-W ZDC-TAC ZDC-E ZDC-W EPDmult0 EPDmult1 EPD-EWDiff EPD-EWDiff2 VPD-TAC VPD-E VPD-W


EM201 0-15 (ch3)
Entries 51712
1
0
3232 3232 3232 3232 3232 3232 3232 3232
3232 3232 3232 3232 3232 3232 3232 3232
BHT0 BHT1 BHT2 BHT3 BHT4 Unused Unused Unused Unused UPC-topo Unused Unused Unused EHT0 EHT1 Unused
10^3
10^2
10
1


EM201 0-15 (ch3)
Entries 0
1
0
-1
BHT0 BHT1 BHT2 BHT3 BHT4 Unused Unused Unused Unused UPC-topo Unused Unused Unused EHT0 EHT1 Unused