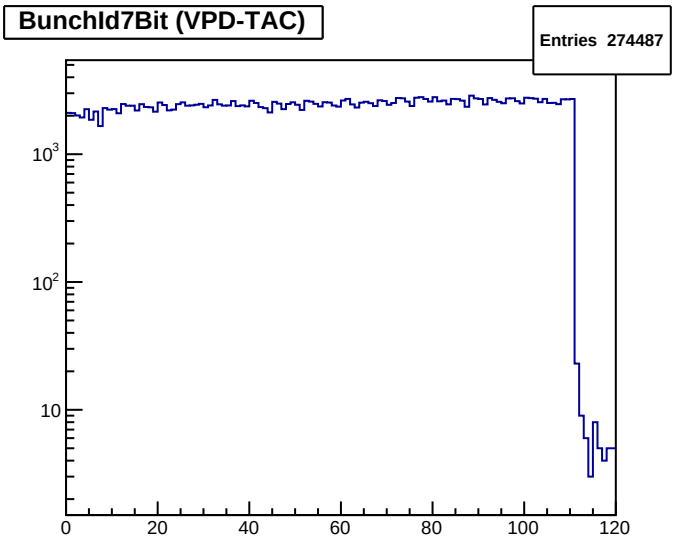
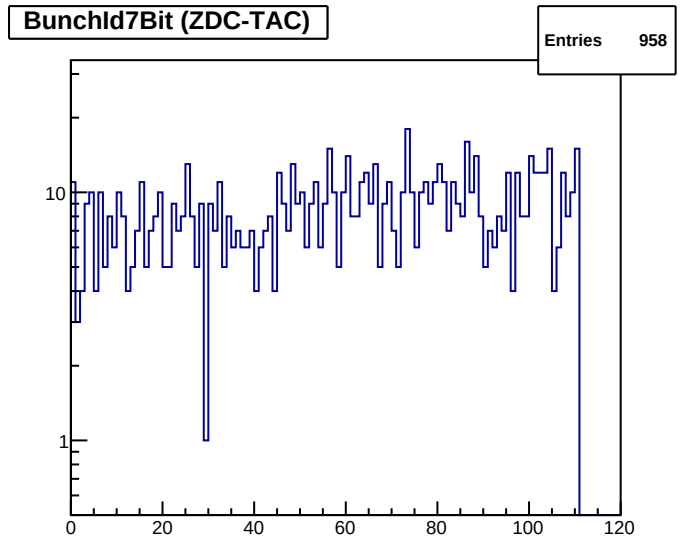
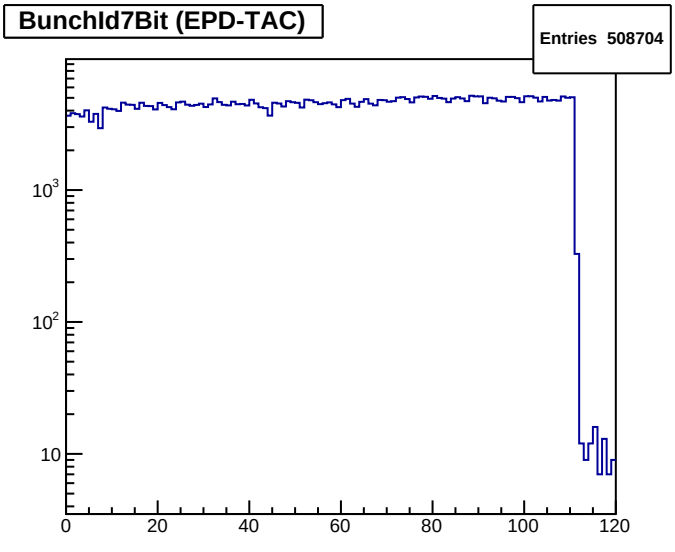
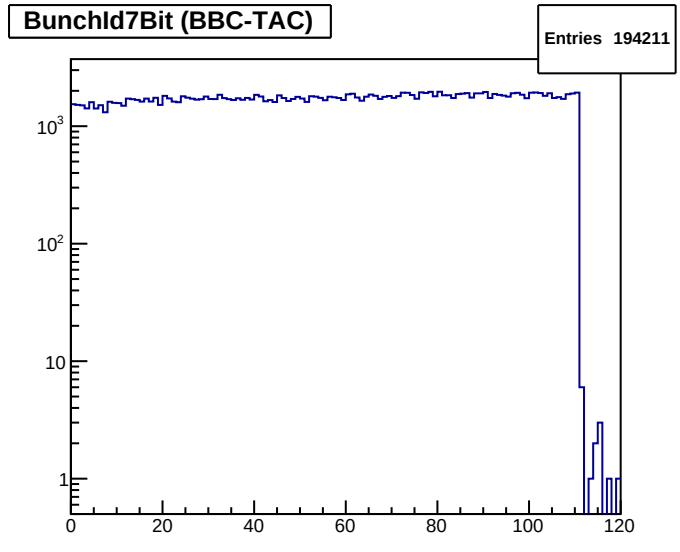
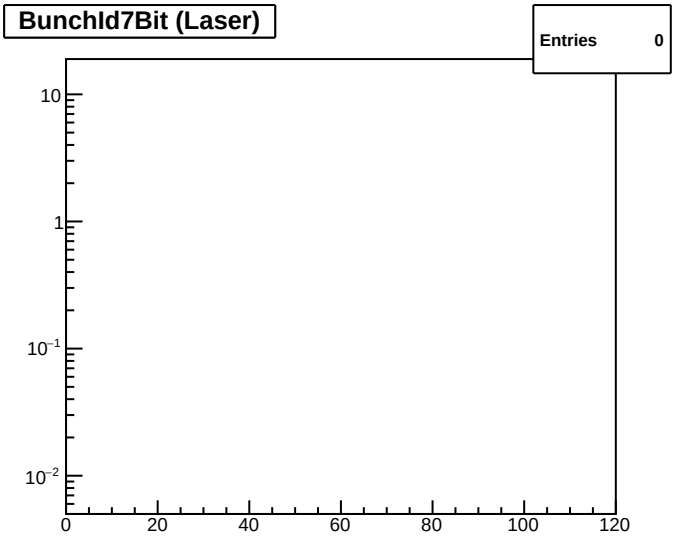
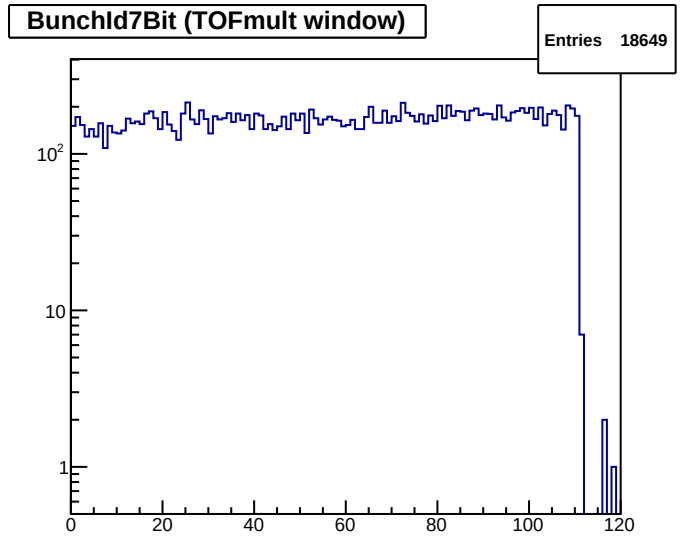

BunchId7Bit (VPD-TAC)
Entries 274487

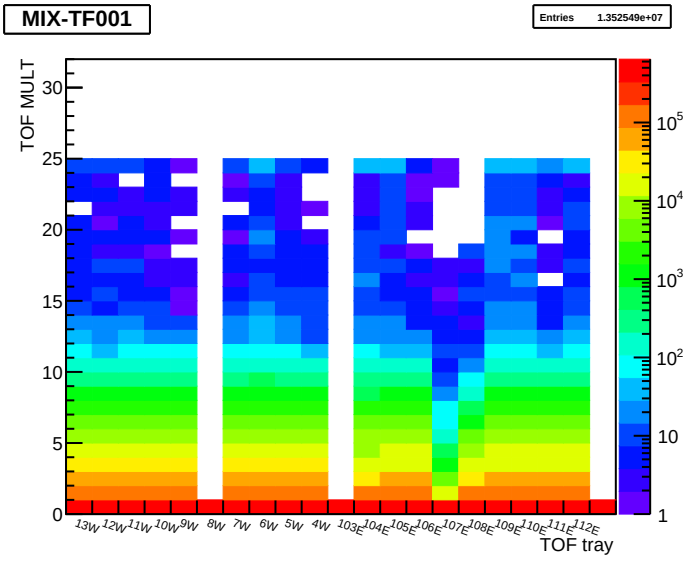

BunchId7Bit (ZDC-TAC)
Entries 958

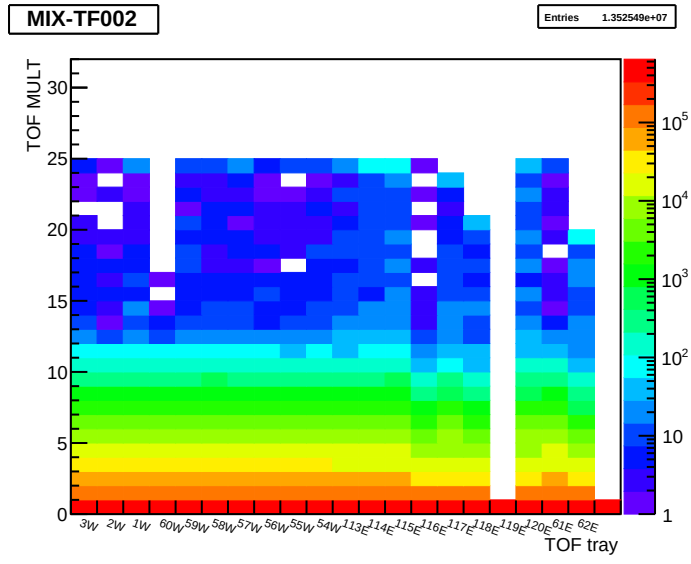

BunchId7Bit (EPD-TAC)
Entries 508704

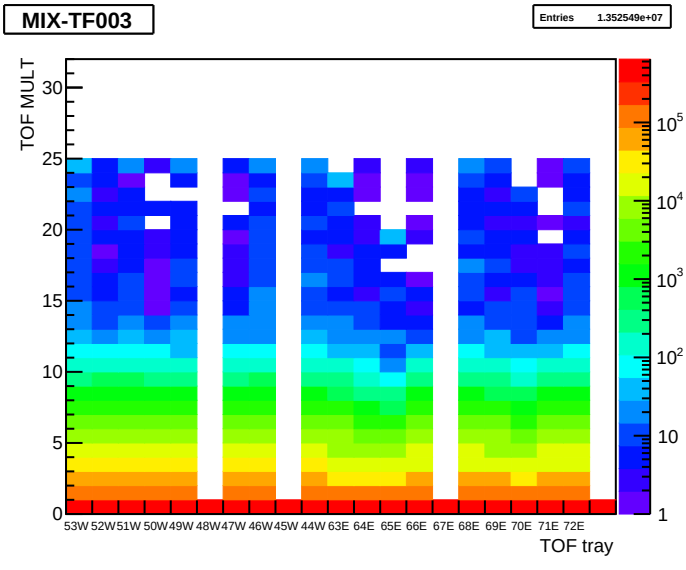

BunchId7Bit (BBC-TAC)
Entries 194211

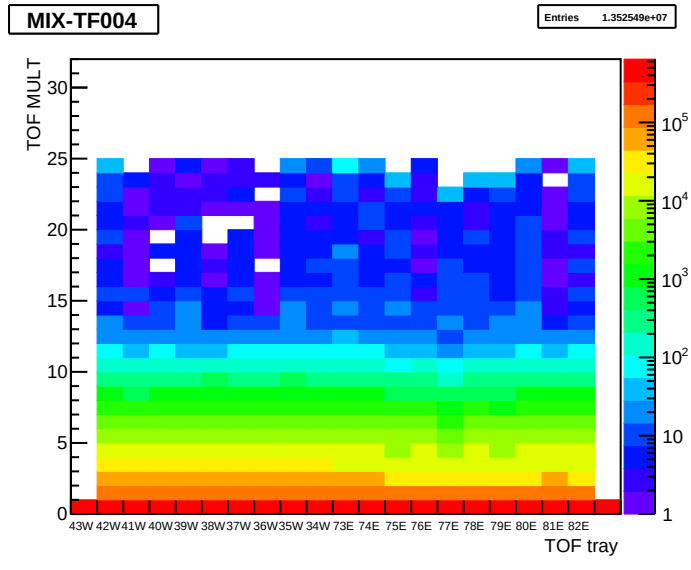

BunchId7Bit (Laser)
Entries 0

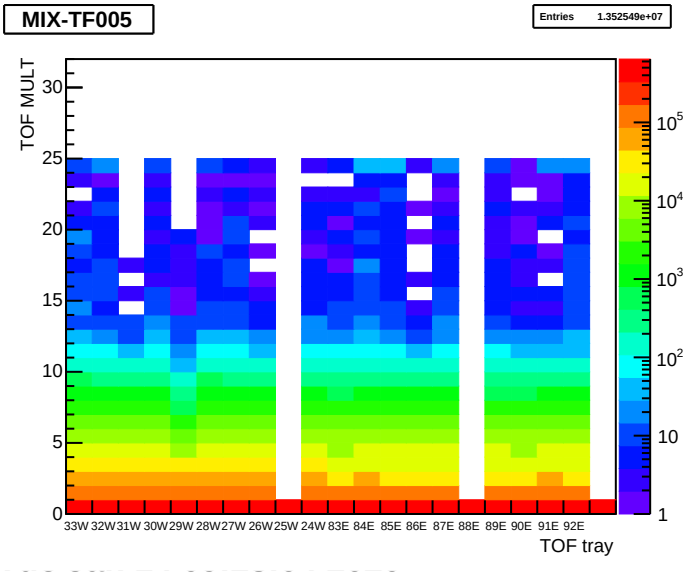

BunchId7Bit (TOFmult window)
Entries 18649

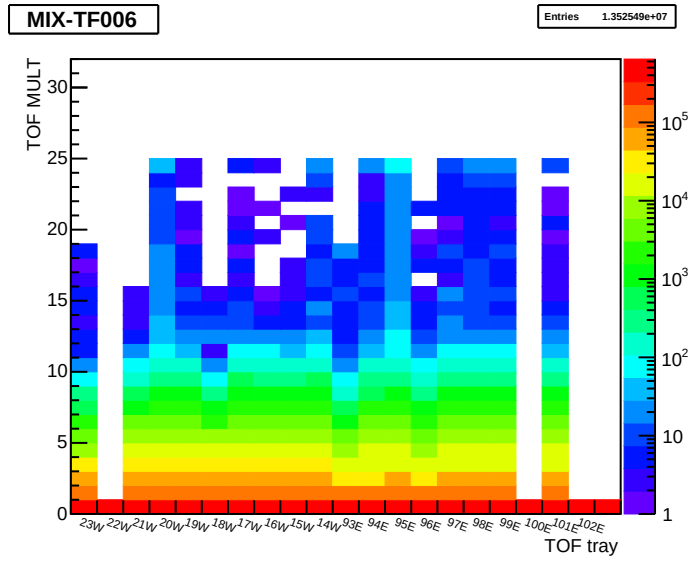

MIX-TF001
Entries 1.352549e+07
TOF MULT
TOF tray
13W 12W 11W 10W 9W 8W 7W 6W 5W 4W 103E 104E 105E 106E 107E 108E 109E 110E 111E 112E

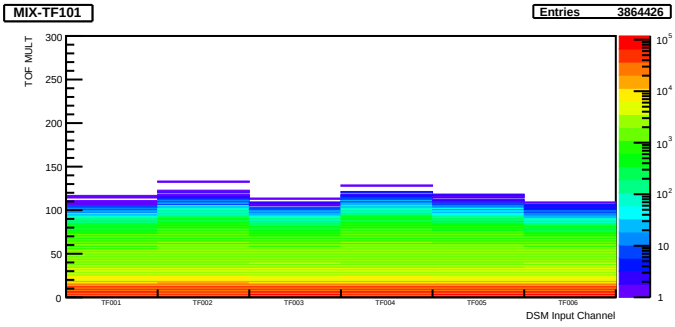

MIX-TF002
Entries 1.352549e+07
TOF MULT
TOF tray
3W 2W 1W 60W 59W 58W 57W 56W 55W 54W 113E 114E 115E 116E 117E 118E 119E 120E 61E 62E

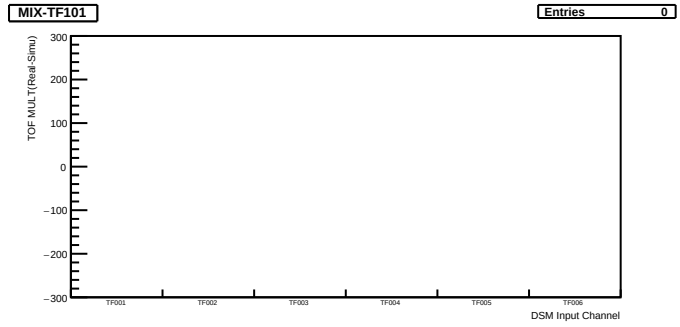

MIX-TF003
Entries 1.352549e+07
TOF MULT
TOF tray
53W 52W 51W 50W 49W 48W 47W 46W 45W 44W 63E 64E 65E 66E 67E 68E 69E 70E 71E 72E

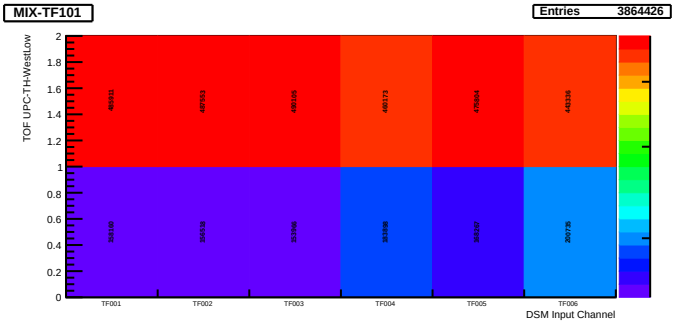

MIX-TF004
Entries 1.352549e+07
TOF MULT
TOF tray
43W 42W 41W 40W 39W 38W 37W 36W 35W 34W 73E 74E 75E 76E 77E 78E 79E 80E 81E 82E

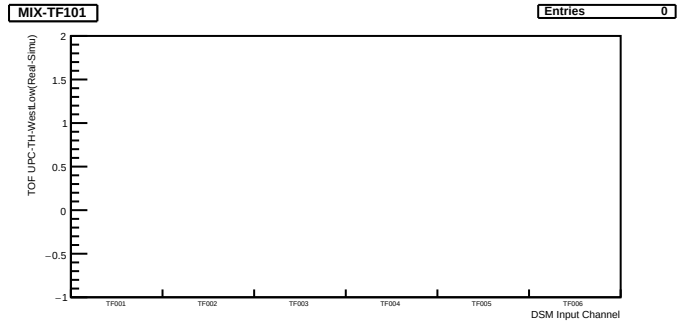

MIX-TF005
Entries 1.352549e+07
TOF MULT
TOF tray
33W 32W 31W 30W 29W 28W 27W 26W 25W 24W 83E 84E 85E 86E 87E 88E 89E 90E 91E 92E

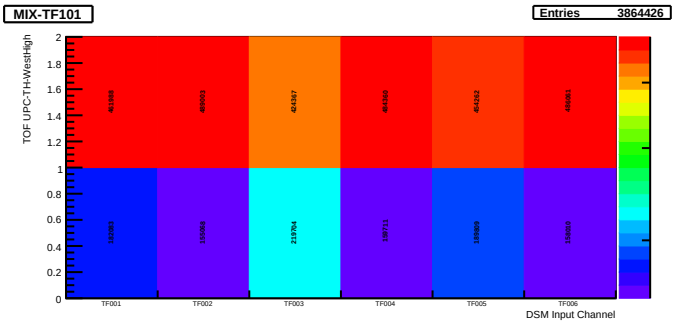

MIX-TF006
Entries 1.352549e+07
TOF MULT
TOF tray
23W 22W 21W 20W 19W 18W 17W 16W 15W 14W 93E 94E 95E 96E 97E 98E 99E 100E 101E 102E

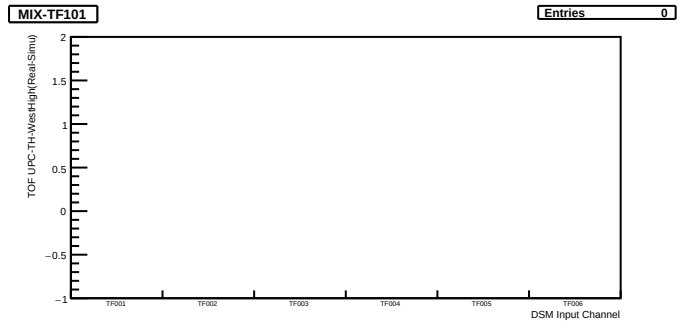

MIX-TF101
Entries 3864426
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

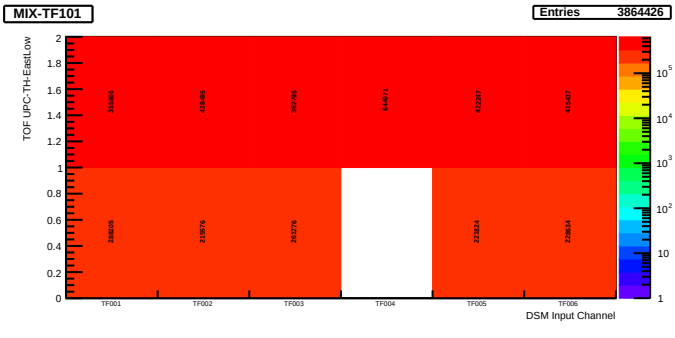

MIX-TF101
Entries 0
TOF MULT(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

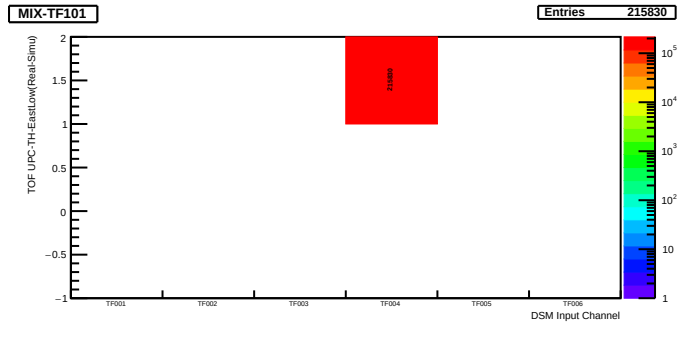

MIX-TF101
Entries 3864426
TOF UPC-TH-WestLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

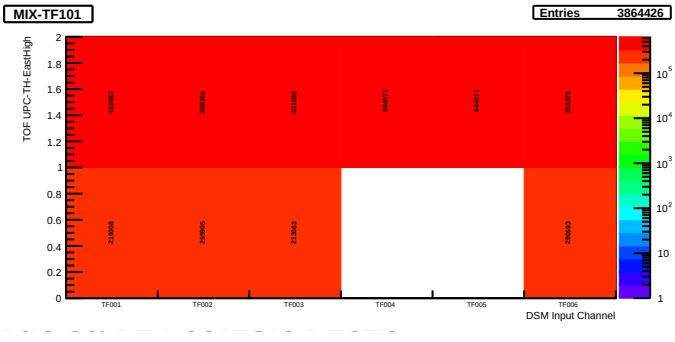

MIX-TF101
Entries 0
TOF UPC-TH-WestLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

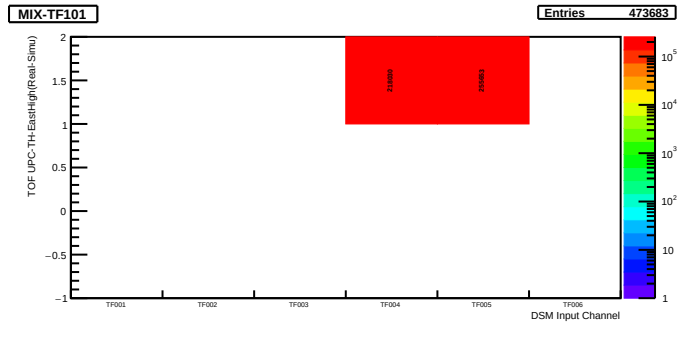

MIX-TF101
Entries 3864426
TOF UPC-TH-WestHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

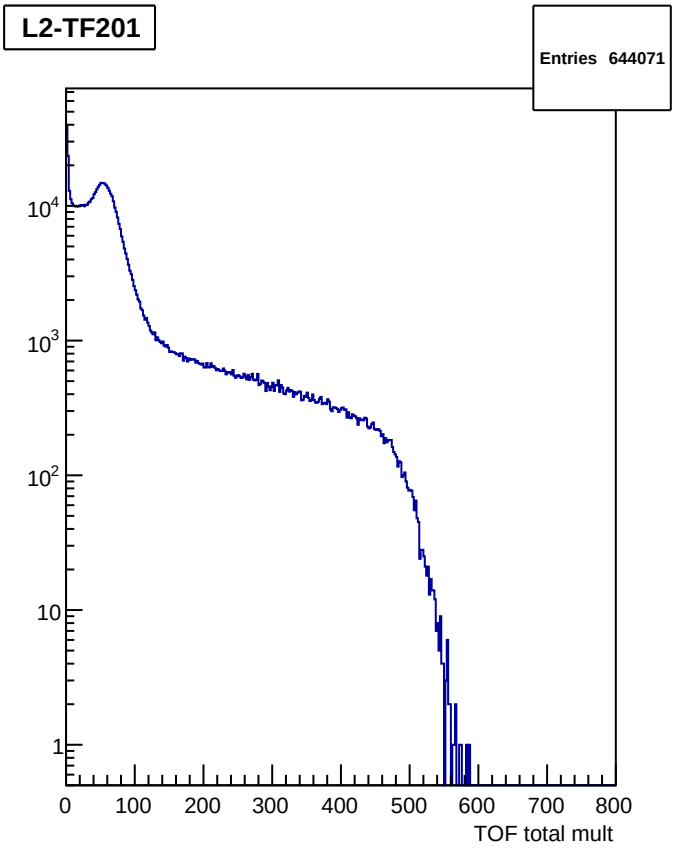

MIX-TF101
Entries 0
TOF UPC-TH-WestHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

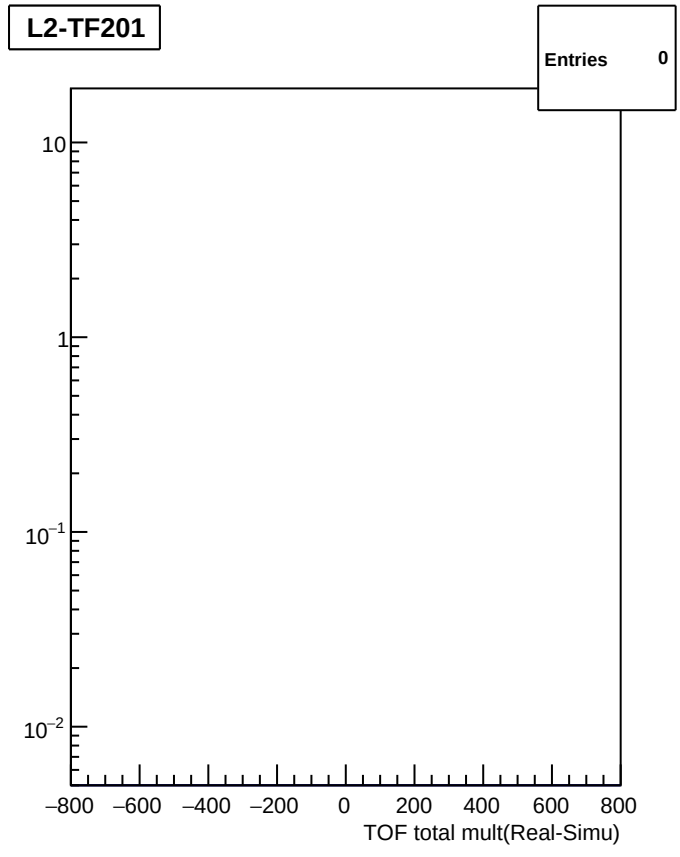

MIX-TF101
Entries 3864426
TOF UPC-TH-EastLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

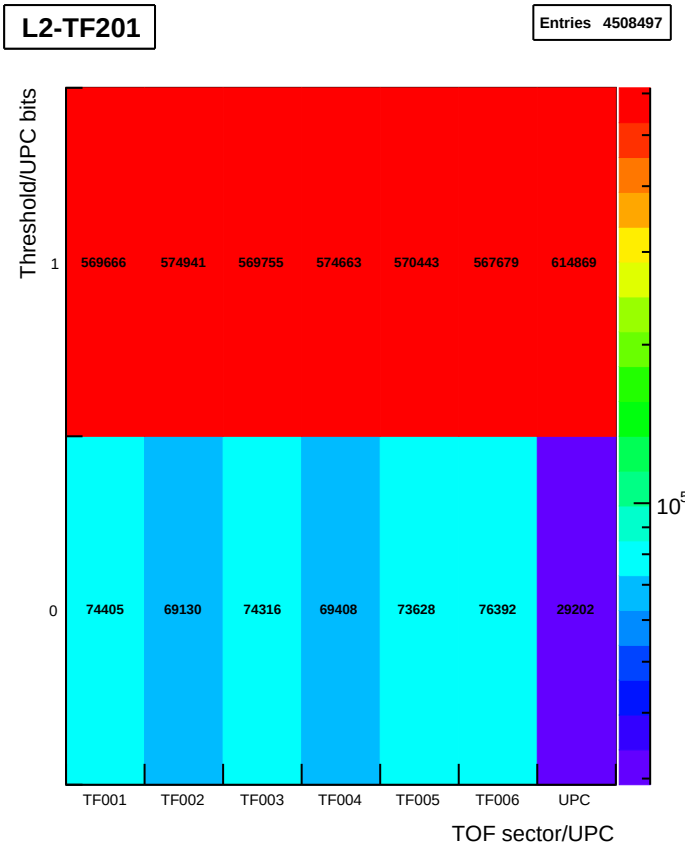

MIX-TF101
Entries 215830
TOF UPC-TH-EastLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

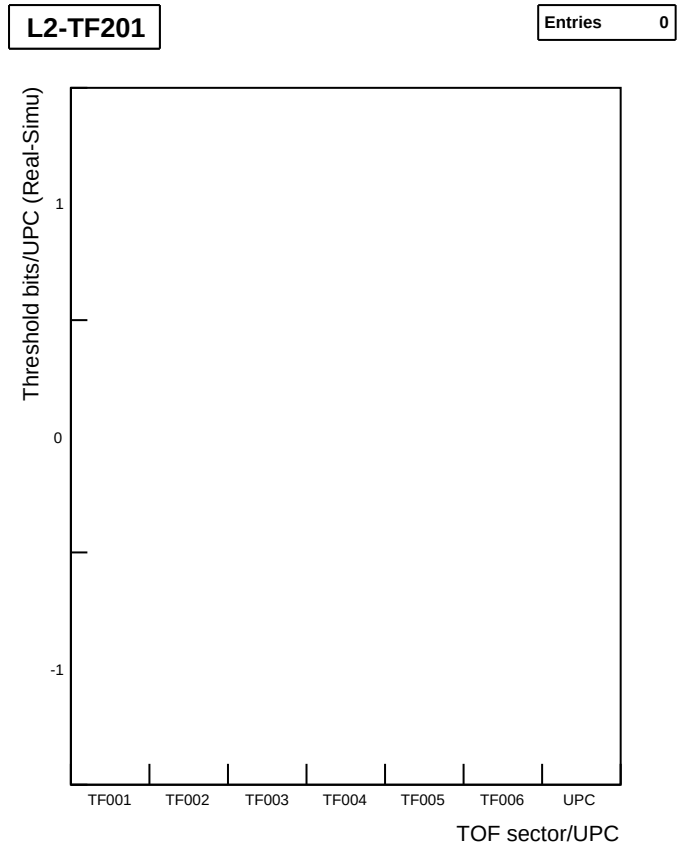

MIX-TF101
Entries 3864426
TOF UPC-TH-EastHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

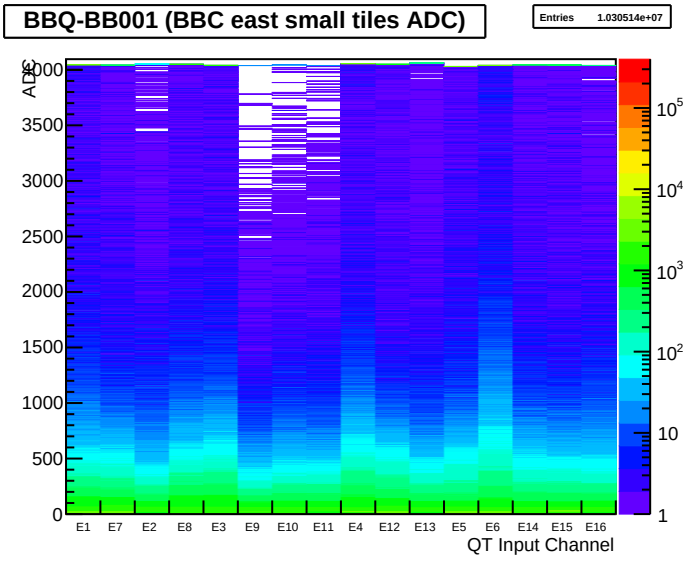

MIX-TF101
Entries 473683
TOF UPC-TH-EastHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

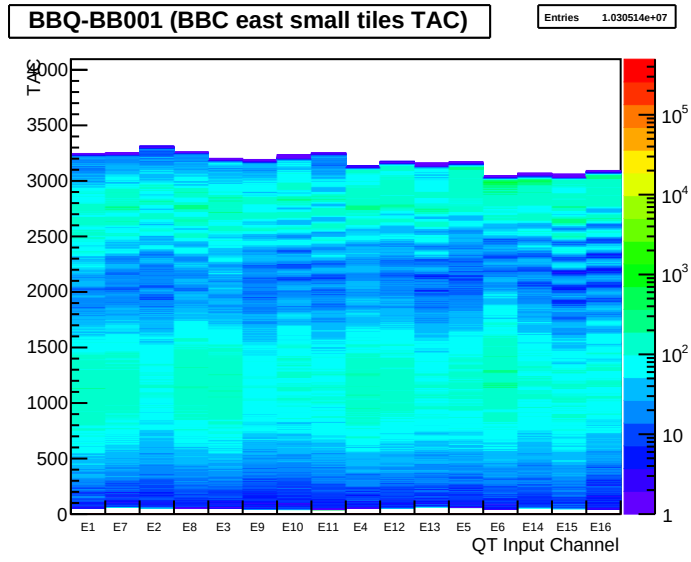

L2-TF201
Entries 644071
TOF total mult

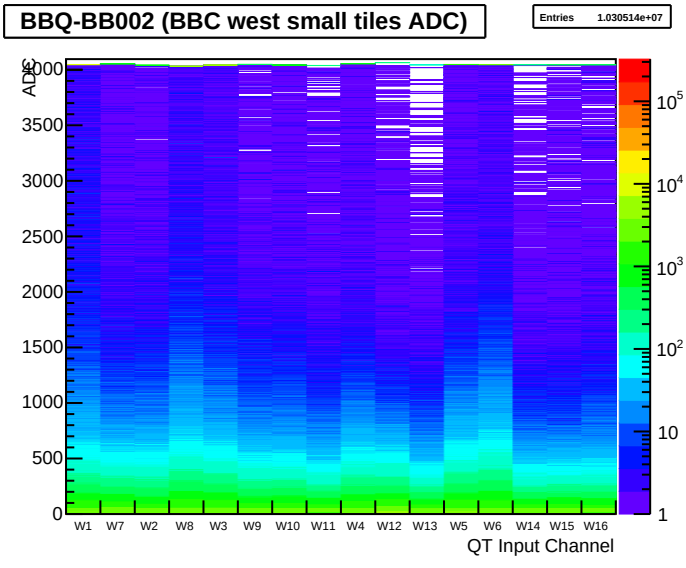

L2-TF201
Entries 0
TOF total mult(Real-Simu)

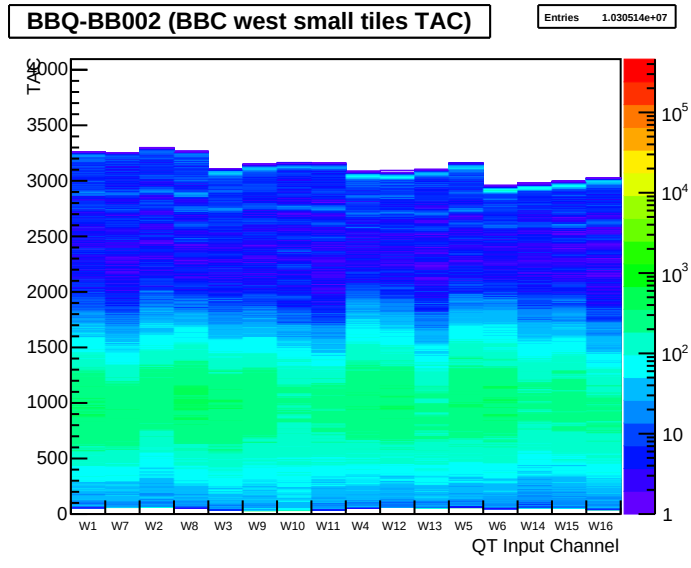

L2-TF201
Entries 4508497
Threshold/UPC bits
569666
574941
569755
574663
570443
567679
614869
74405
69130
74316
69408
73628
76392
29202
TF001
TF002
TF003
TF004
TF005
TF006
UPC
TOF sector/UPC

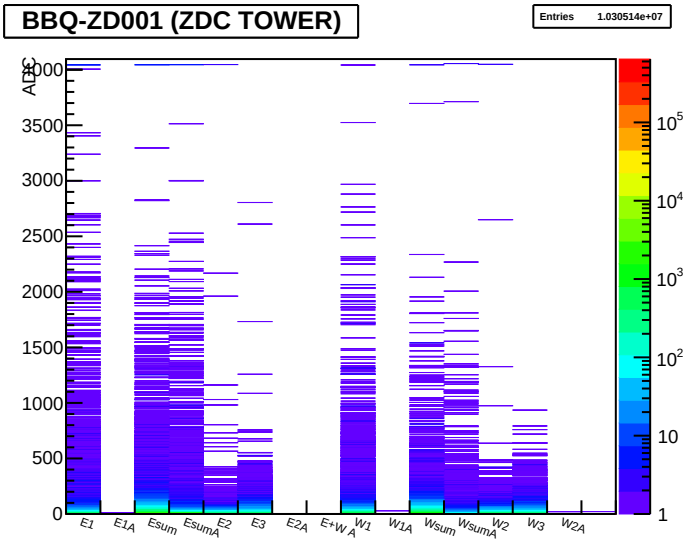

L2-TF201
Entries 0
Threshold bits/UPC (Real-Simu)
TF001
TF002
TF003
TF004
TF005
TF006
UPC
TOF sector/UPC

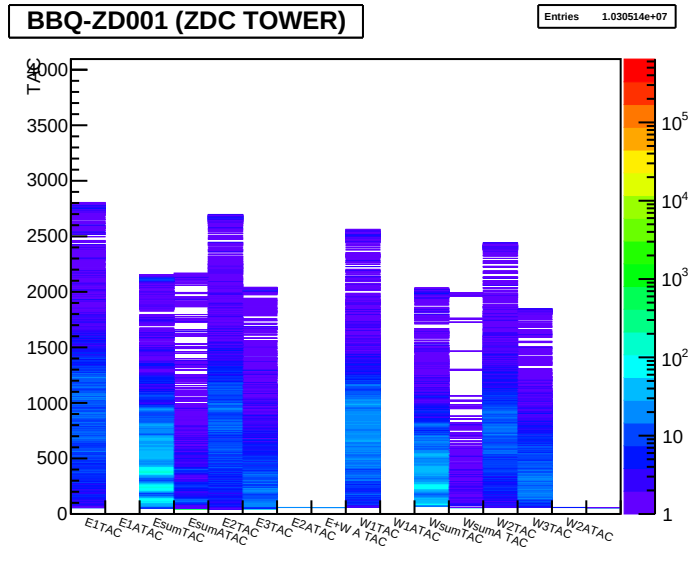

BBQ-BB001 (BBC east small tiles ADC)
Entries 1.030514e+07
ADC
QT Input Channel
E1 E7 E2 E8 E3 E9 E10 E11 E4 E12 E13 E5 E6 E14 E15 E16

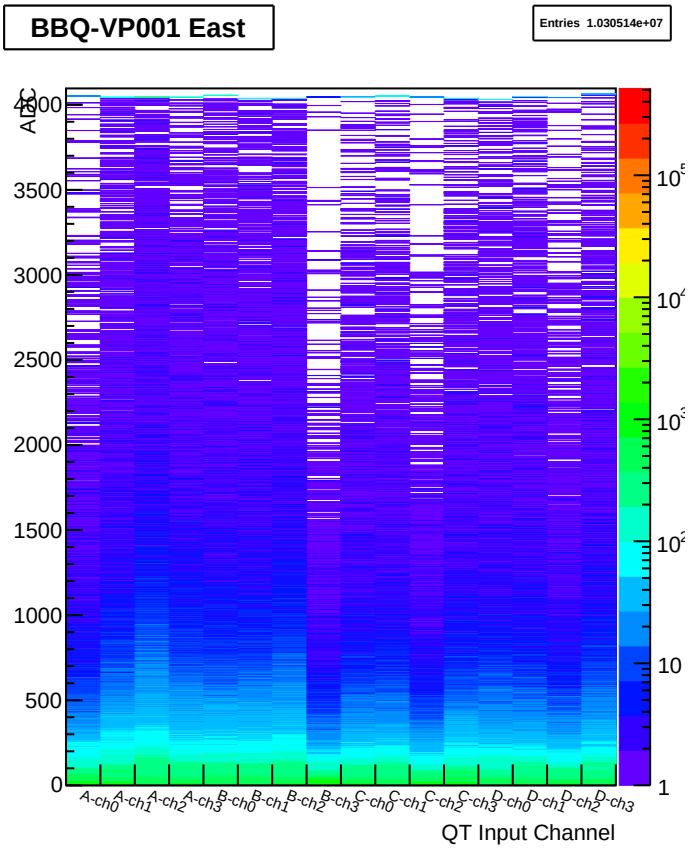

BBQ-BB001 (BBC east small tiles TAC)
Entries 1.030514e+07
TAC
QT Input Channel
E1 E7 E2 E8 E3 E9 E10 E11 E4 E12 E13 E5 E6 E14 E15 E16

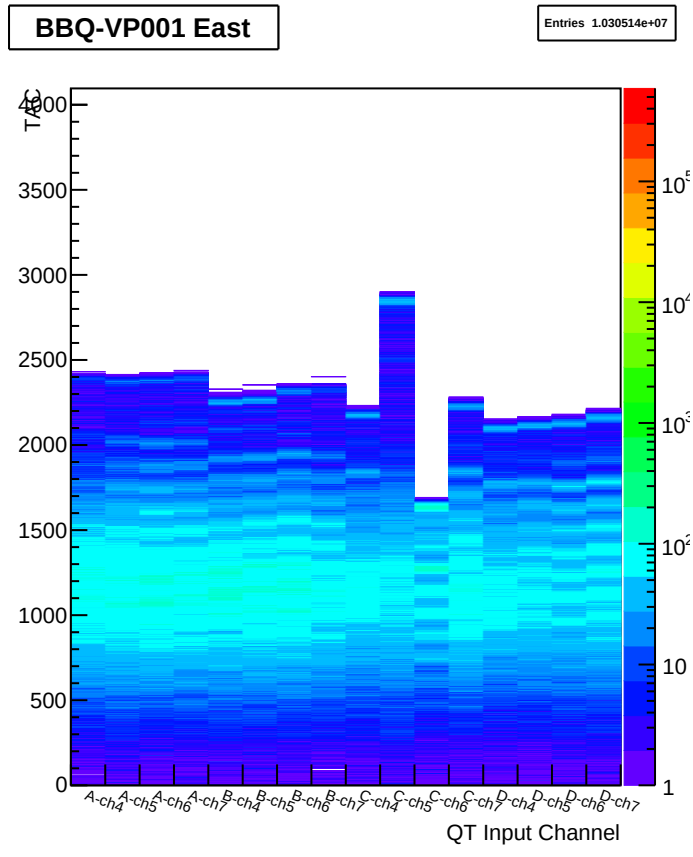

BBQ-BB002 (BBC west small tiles ADC)
Entries 1.030514e+07
ADC
QT Input Channel
W1 W7 W2 W8 W3 W9 W10 W11 W4 W12 W13 W5 W6 W14 W15 W16

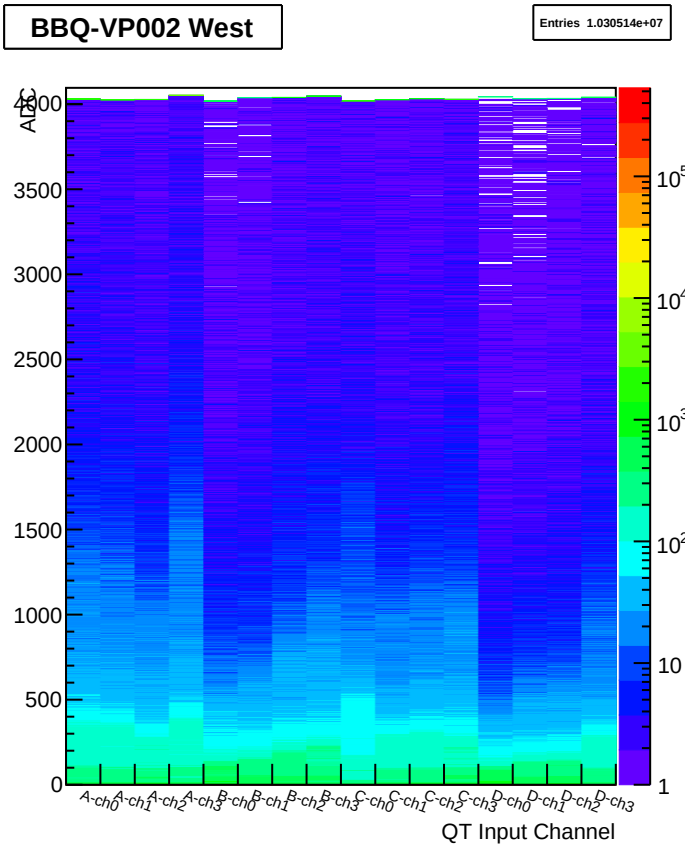

BBQ-BB002 (BBC west small tiles TAC)
Entries 1.030514e+07
TAC
QT Input Channel
W1 W7 W2 W8 W3 W9 W10 W11 W4 W12 W13 W5 W6 W14 W15 W16

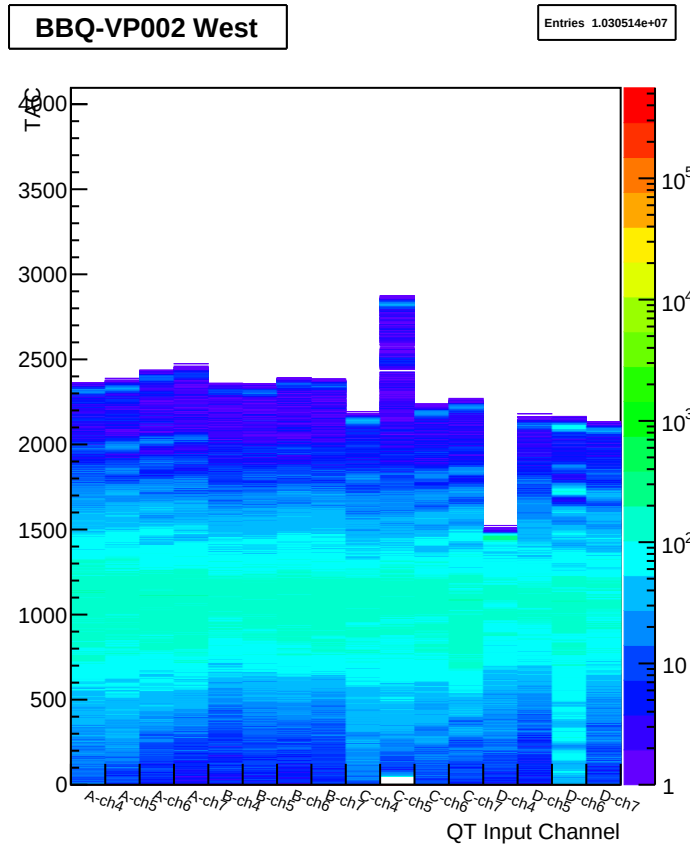

BBQ-ZD001 (ZDC TOWER)
Entries 1.030514e+07
ADC
E1 E1A Esum EsumA E2 E3 E2A E+W A W1 W1A Wsum WsumA W2 W3 W2A

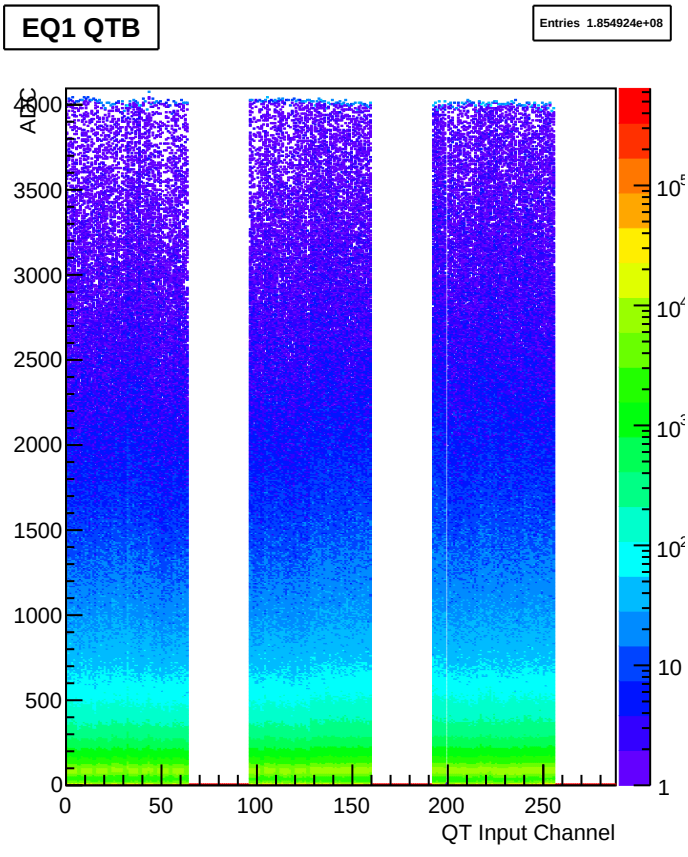

BBQ-ZD001 (ZDC TOWER)
Entries 1.030514e+07
TAC
E1TAC E1ATAC EsumTAC EsumATAC E2TAC E3TAC E2ATAC E+W A TAC W1TAC W1ATAC WsumTAC WsumA TAC W2TAC W3TAC W2ATAC

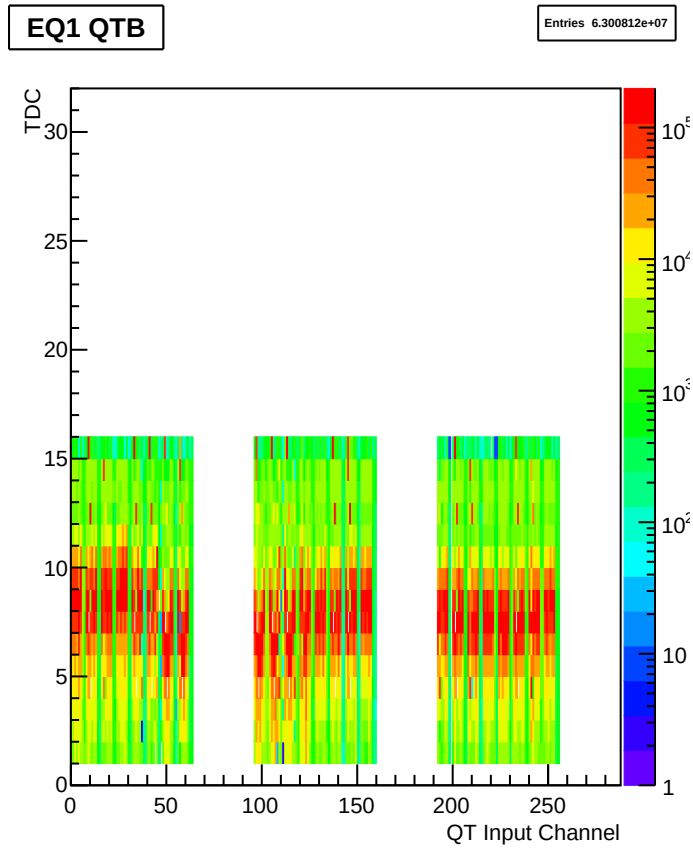

BBQ-VP001 East
Entries 1.030514e+07
ADC
QT Input Channel

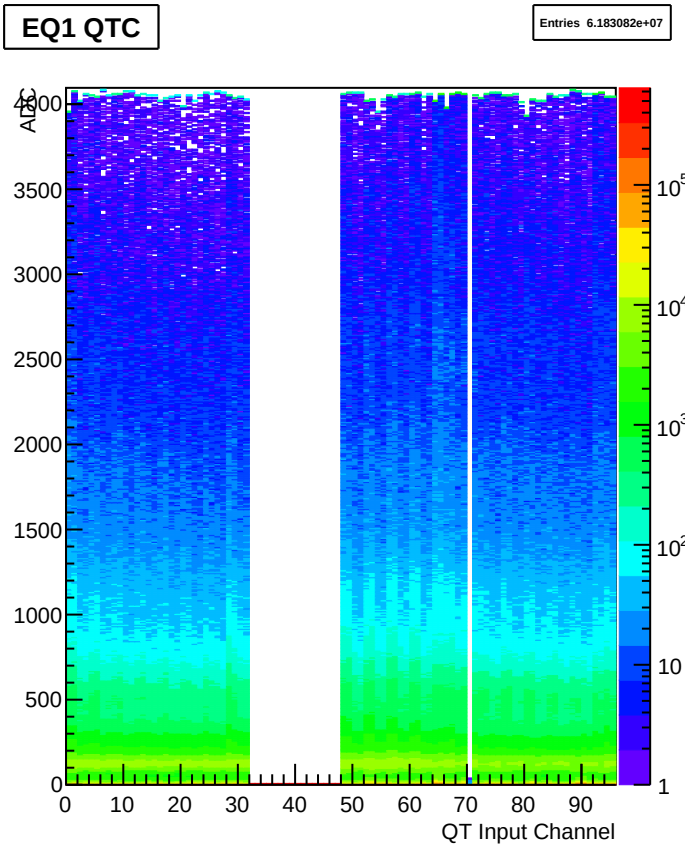

BBQ-VP001 East
Entries 1.030514e+07
TADC
QT Input Channel

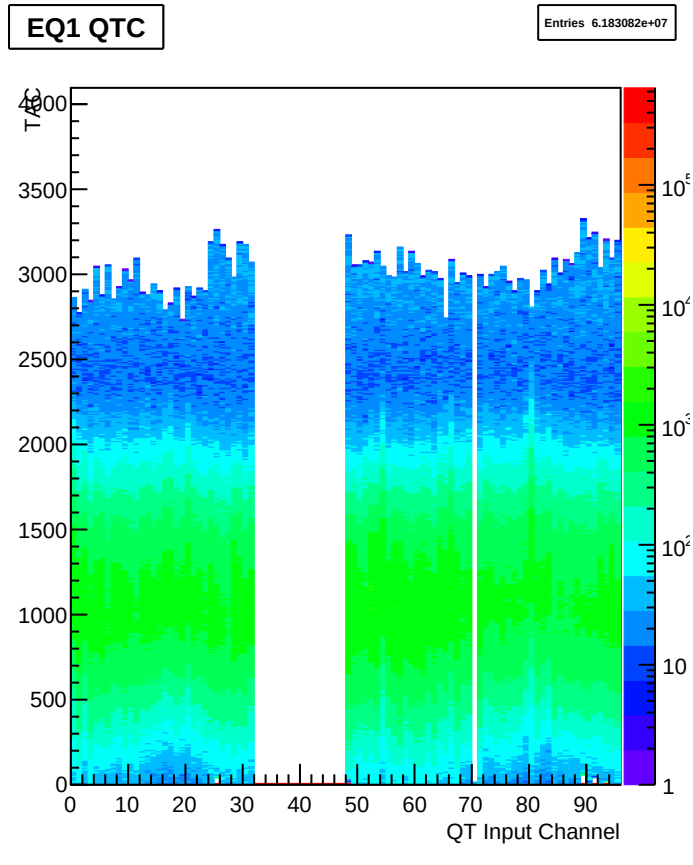

BBQ-VP002 West
Entries 1.030514e+07
ADC
QT Input Channel

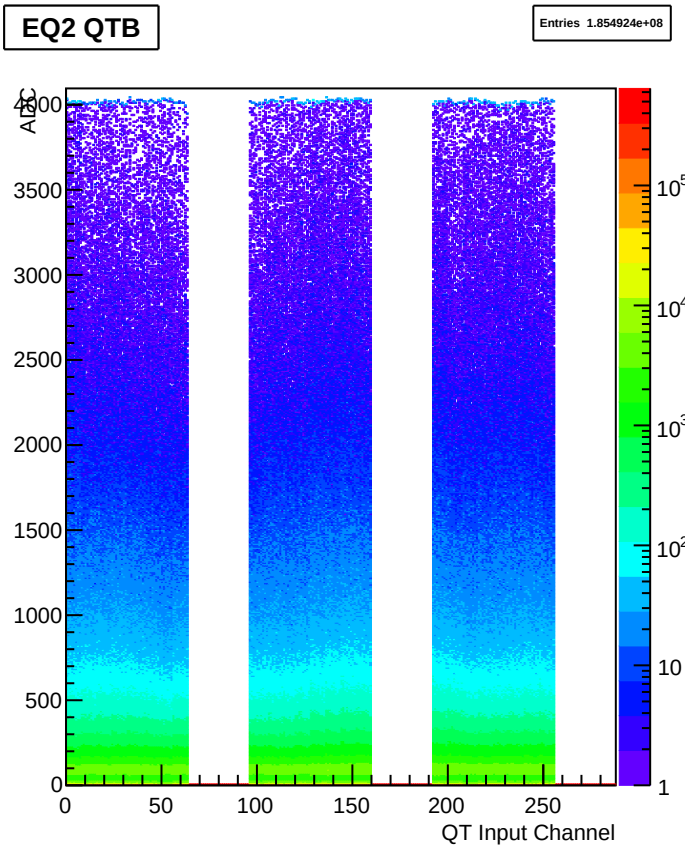

BBQ-VP002 West
Entries 1.030514e+07
TADC
QT Input Channel

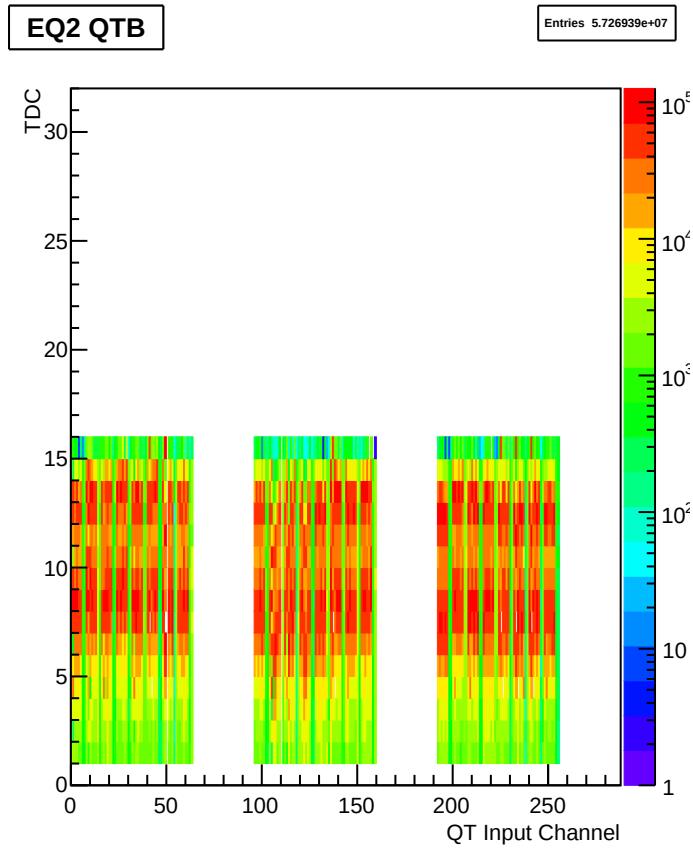

EQ1 QTB
Entries 1.854924e+08
ADC
QT Input Channel

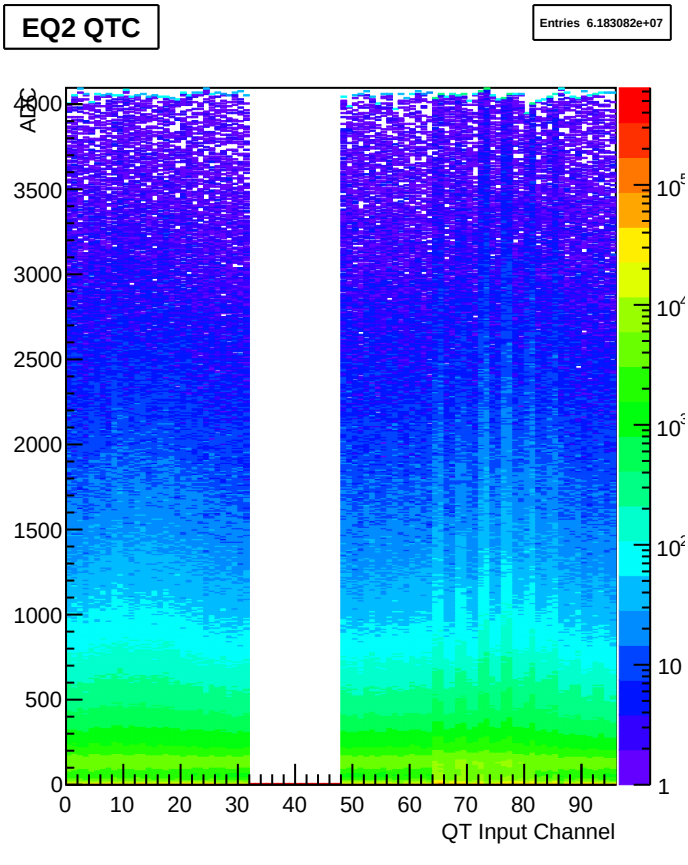

EQ1 QTB
Entries 6.300812e+07
TDC
QT Input Channel

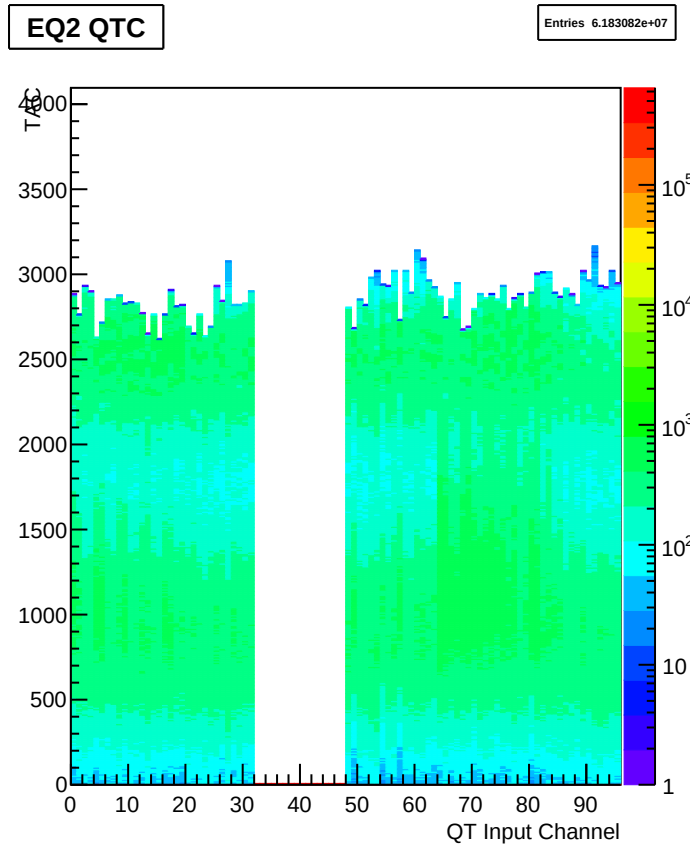

EQ1 QTC
Entries 6.183082e+07
ADC
QT Input Channel

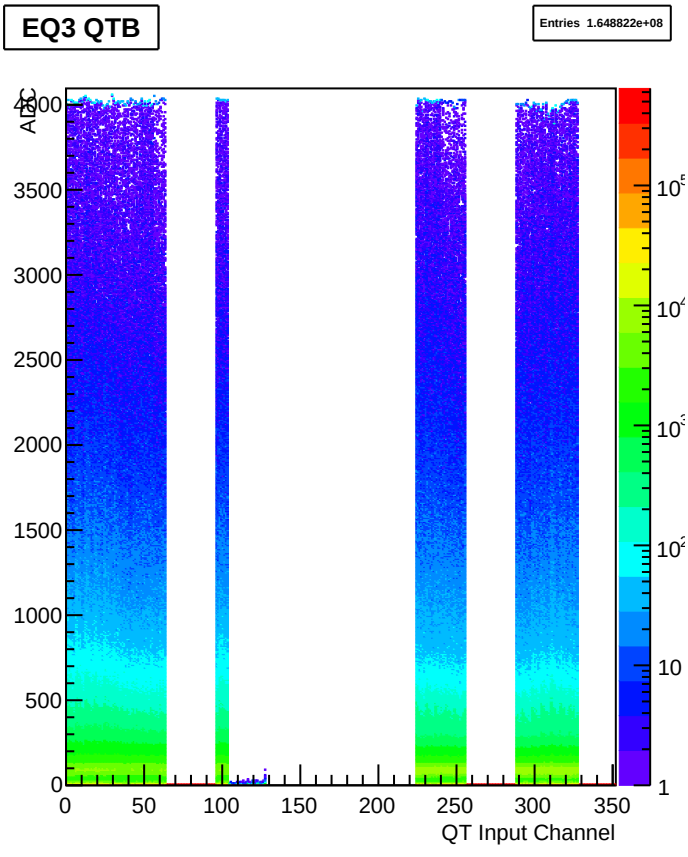

EQ1 QTC
Entries 6.183082e+07
TADC
QT Input Channel

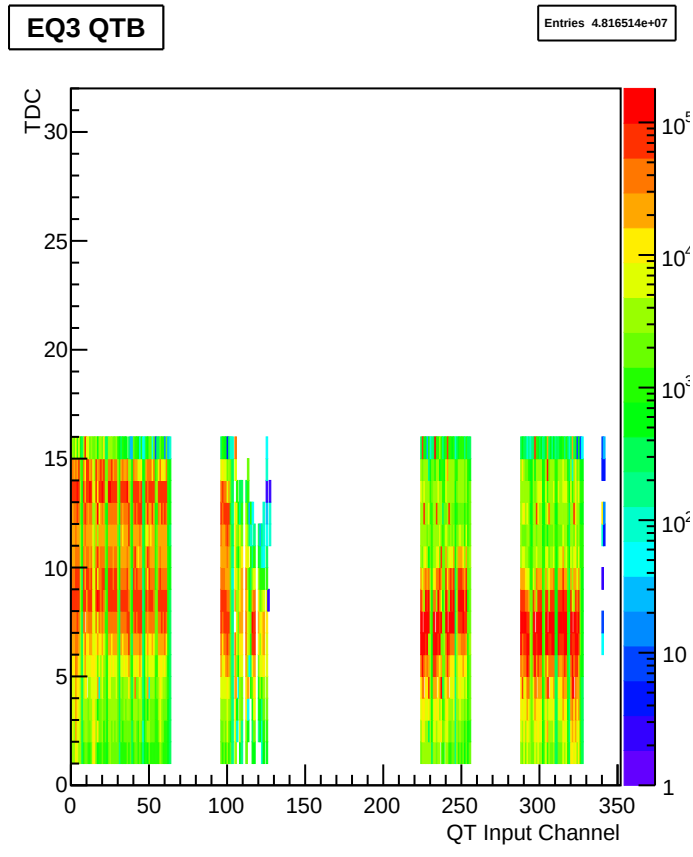

EQ2 QTB
Entries 1.854924e+08
ADC
QT Input Channel

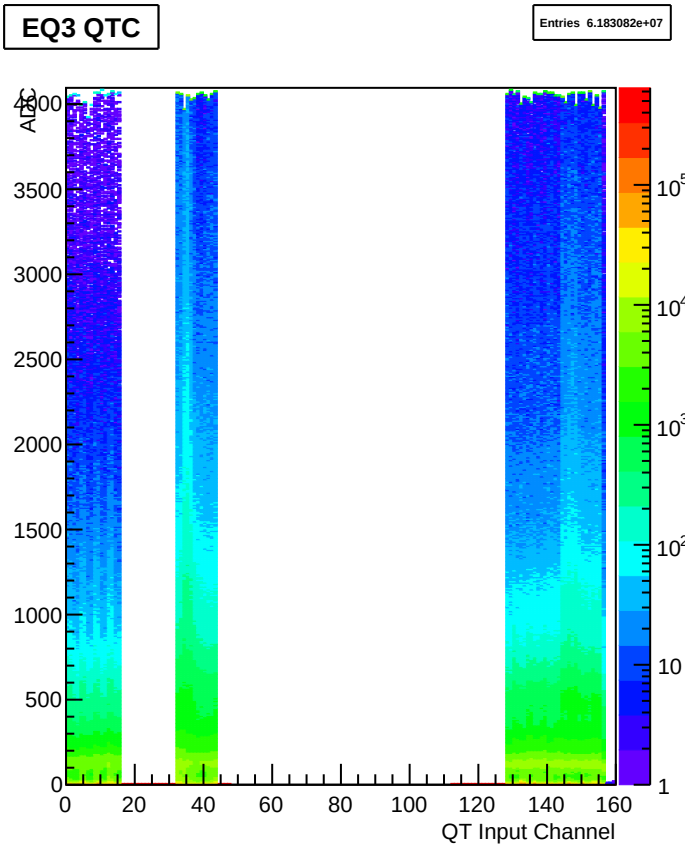

EQ2 QTB
Entries 5.726939e+07
TDC
QT Input Channel

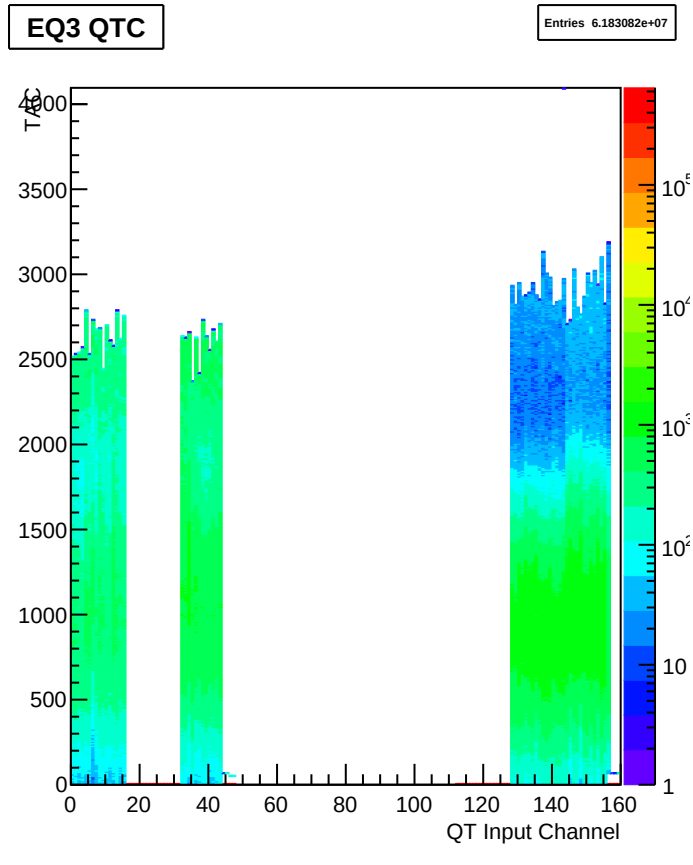

EQ2 QTC
Entries 6.183082e+07
ADC
QT Input Channel

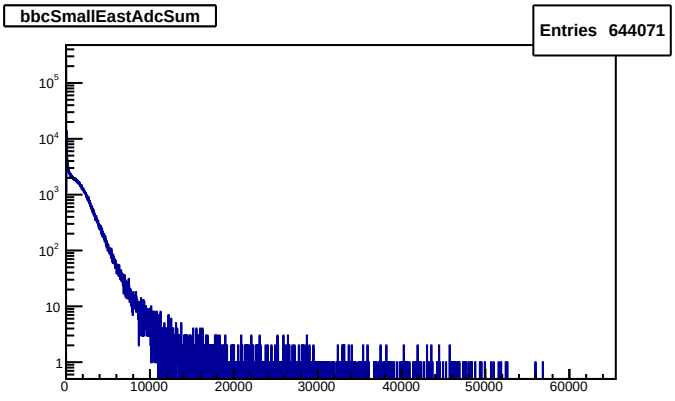

EQ2 QTC
Entries 6.183082e+07
TADC
QT Input Channel

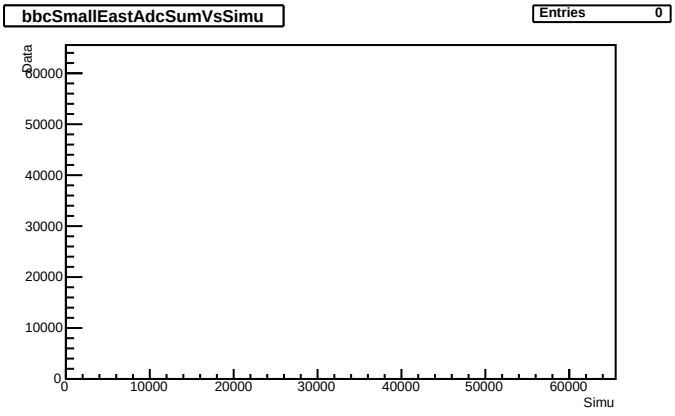

EQ3 QTB
Entries 1.648822e+08
ADC
QT Input Channel

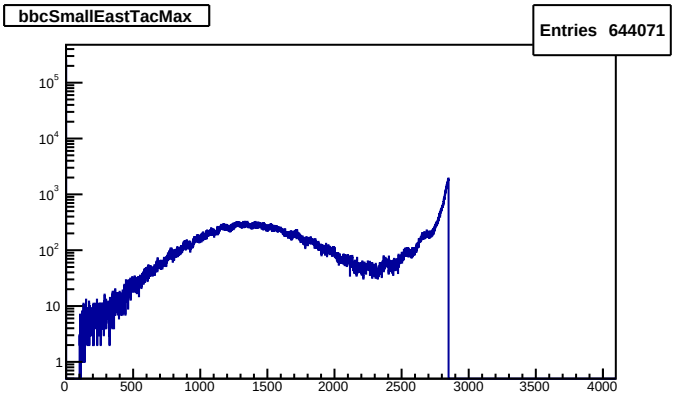

EQ3 QTB
Entries 4.816514e+07
TDC
QT Input Channel


EQ3 QTC
Entries 6.183082e+07
ADC
QT Input Channel


EQ3 QTC
Entries 6.183082e+07
TADC
QT Input Channel


bbcSmallEastAdcSum
Entries 644071


bbcSmallEastAdcSumVsSimu
Entries 0
Data
Simu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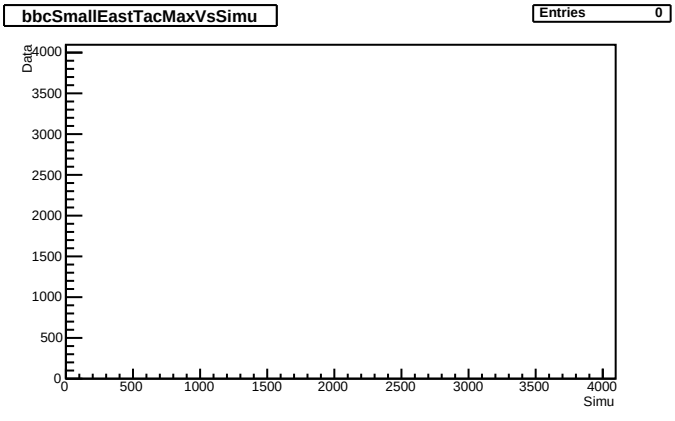
Entries 644071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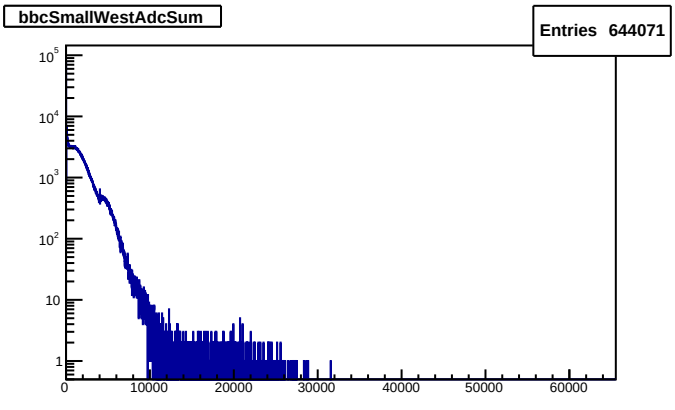
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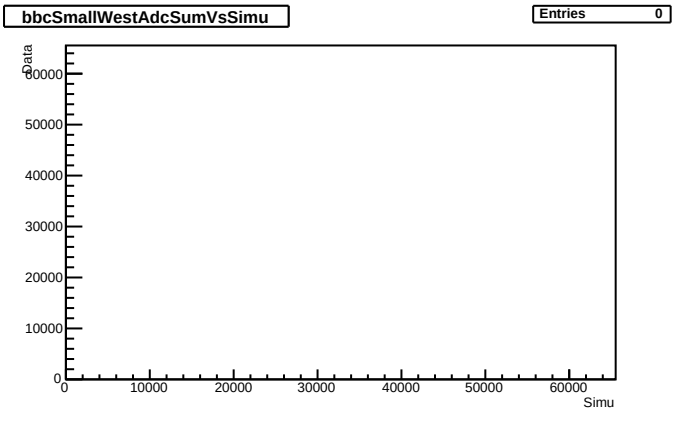
Entries 644071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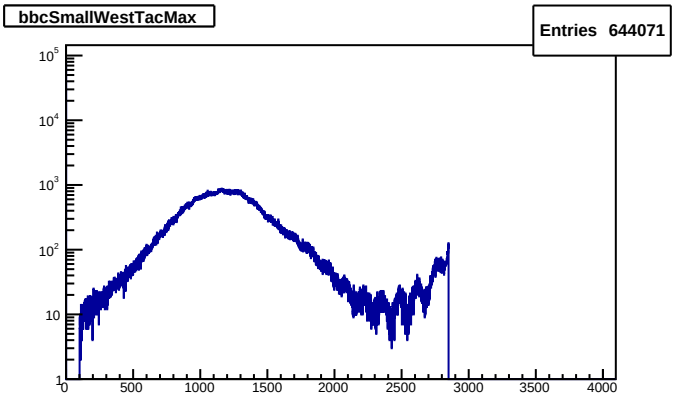
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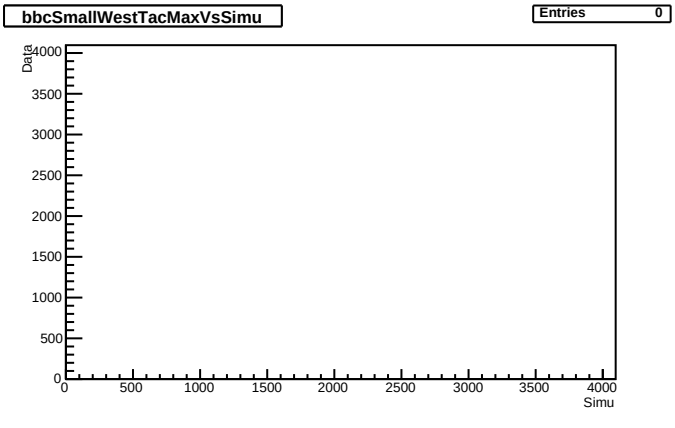
Entries 644071


bbcSmallWestTacMaxVsSimu
Entries 0
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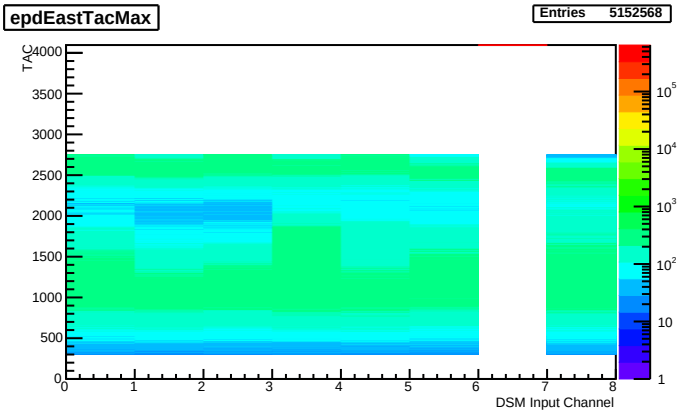
Simu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Entries 5152568
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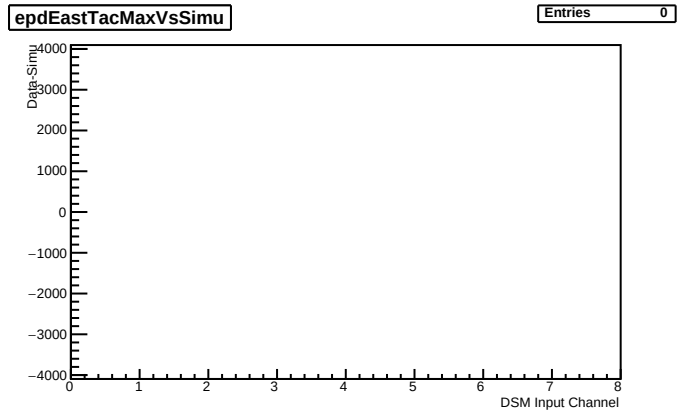
DSM Input Channel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Data-Simu
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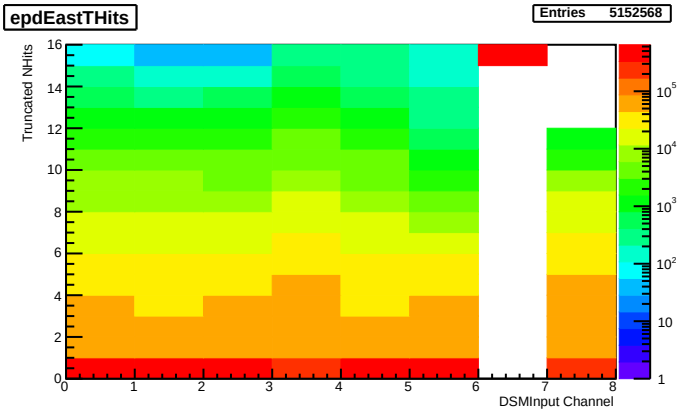
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Entries 5152568
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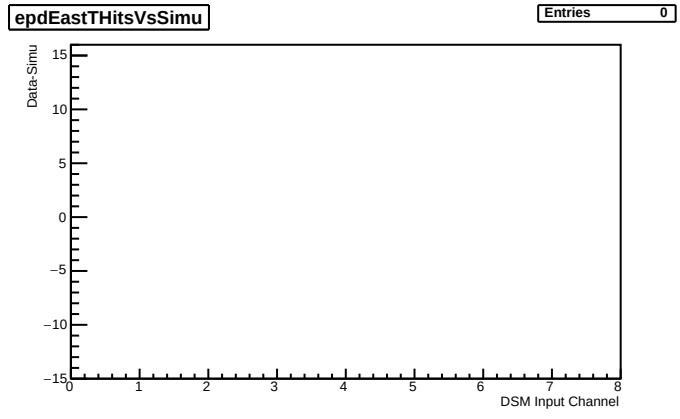
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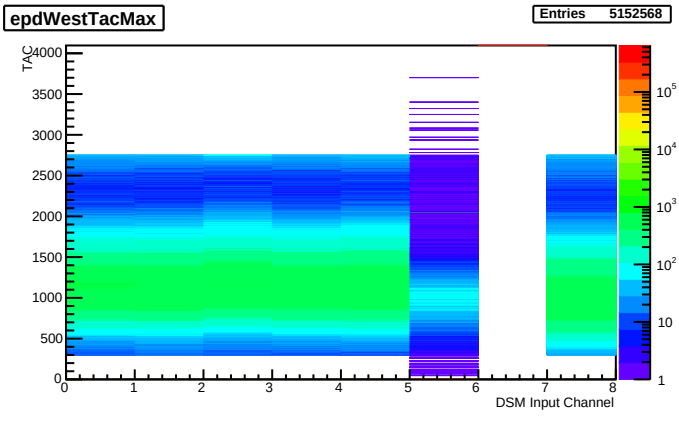
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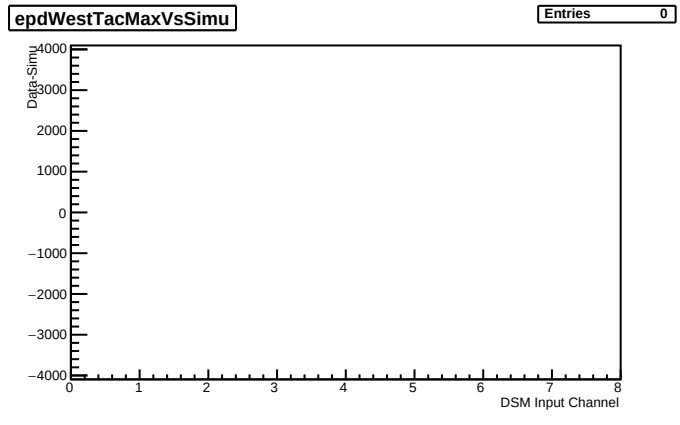
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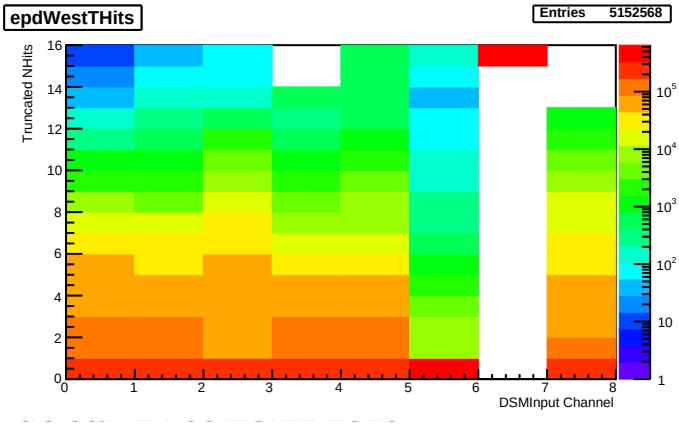
Data-Simu
DSM Input Channel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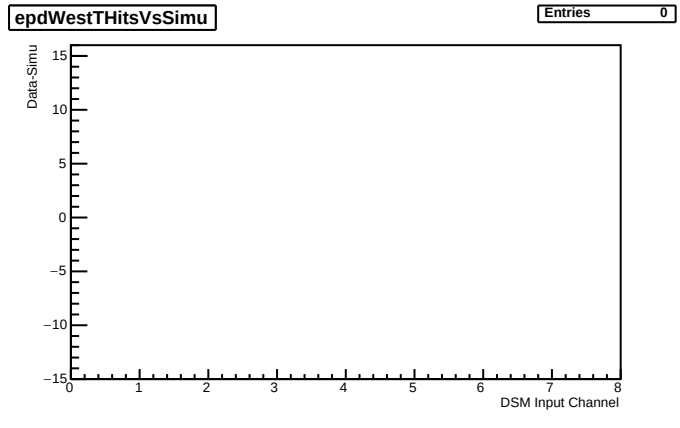
Entries 5152568
Truncated NHits
DSMInput Channel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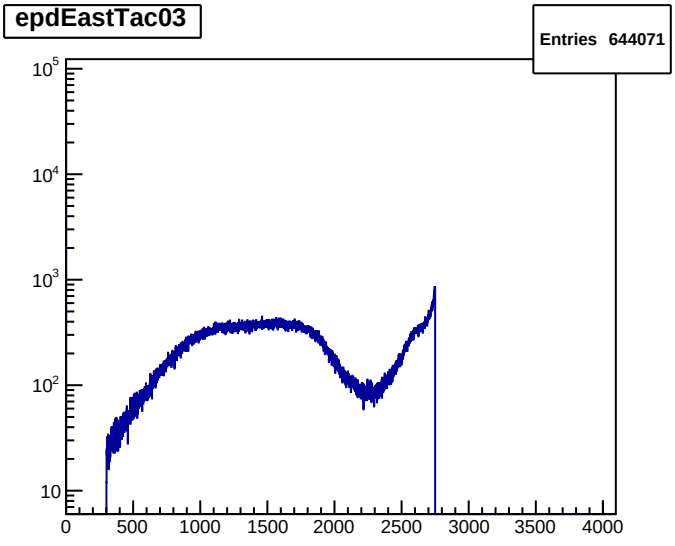
DSM Input Channel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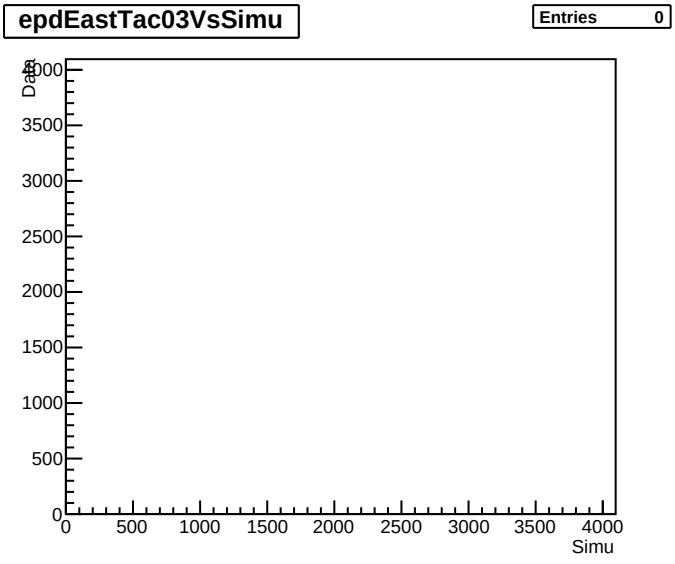
Entries 644071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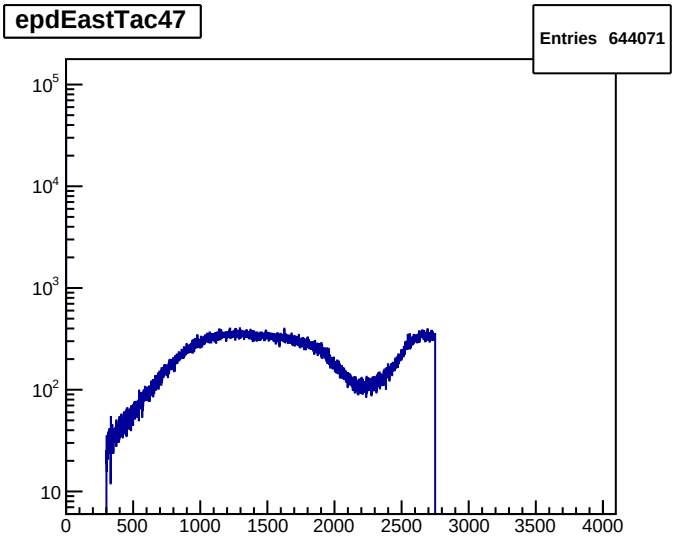
Entries 0
Data
Simu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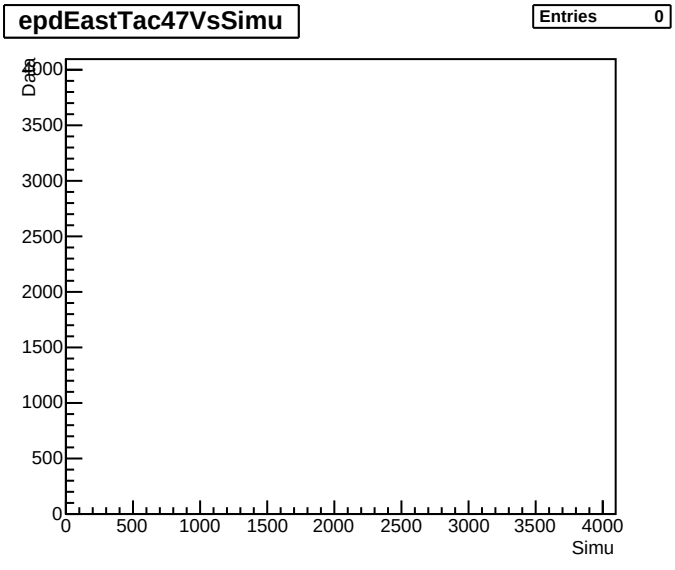
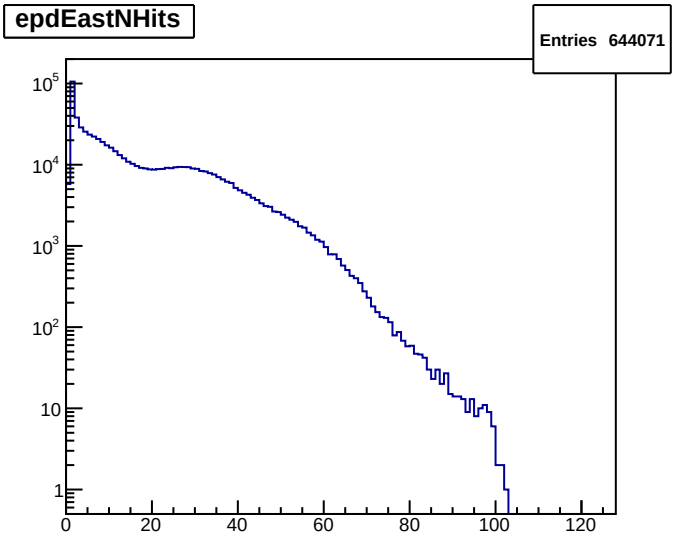
Entries 644071


epdEastTac47VsSimu
Entries 0
Data
Simu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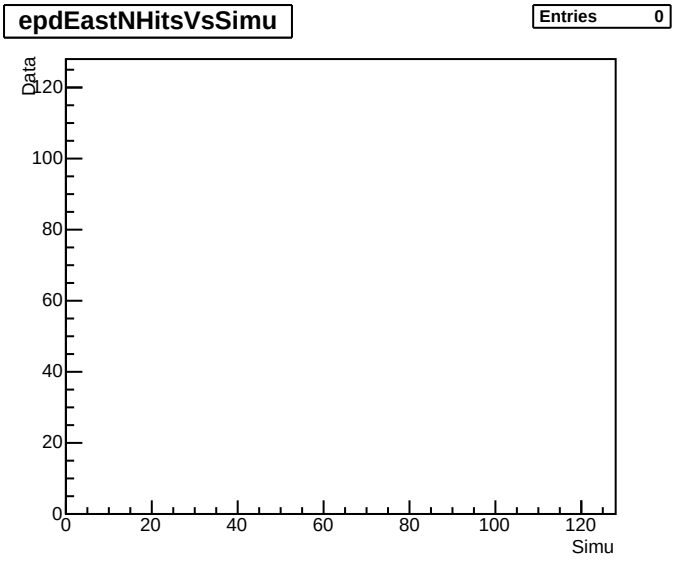
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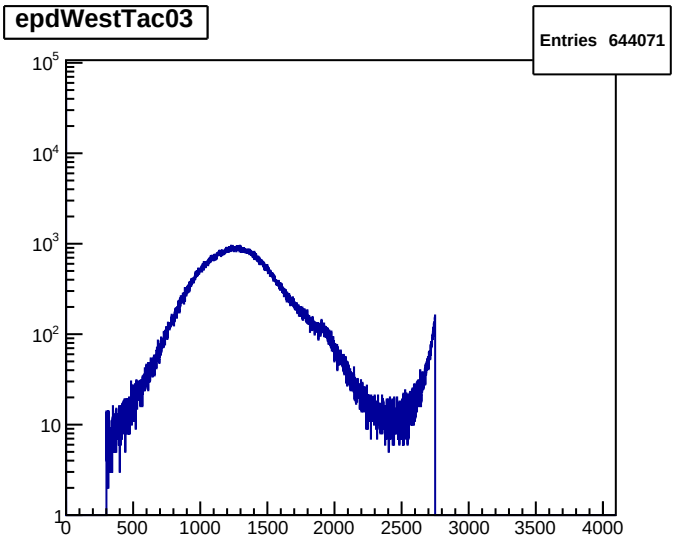
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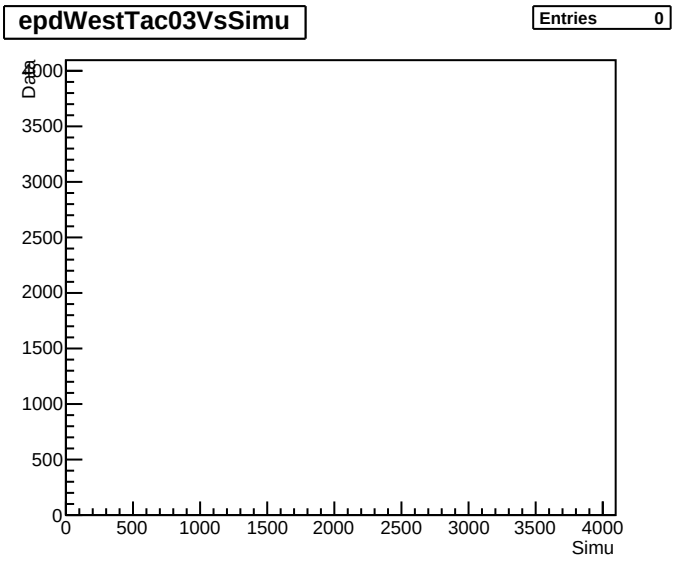
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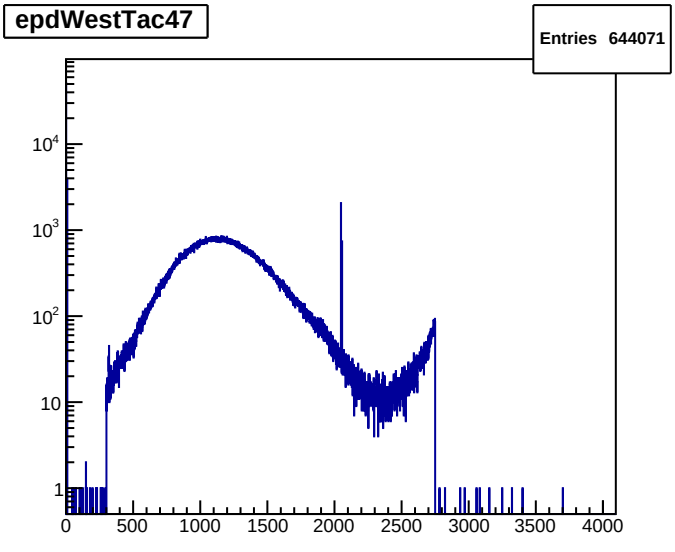
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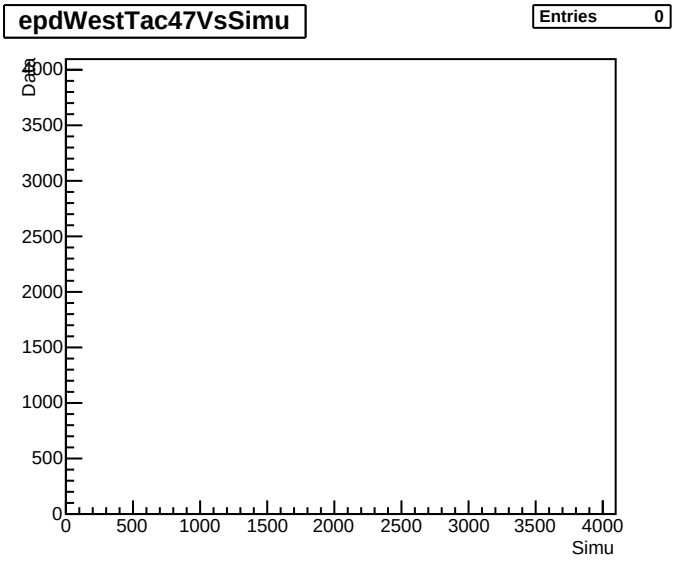
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Entries 0
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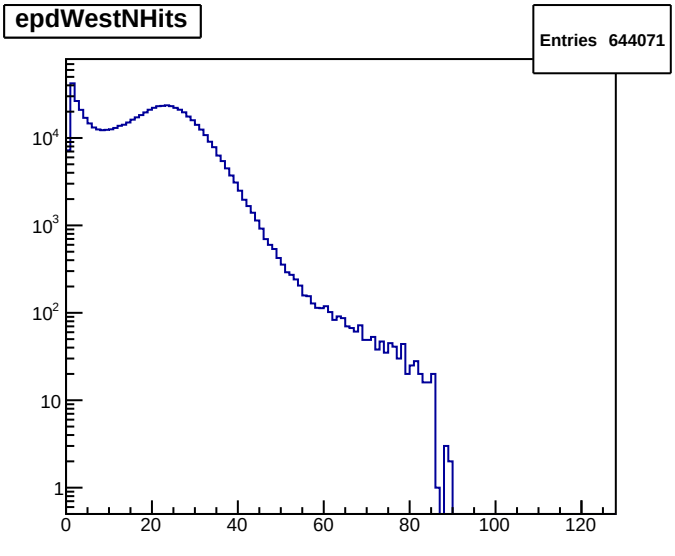
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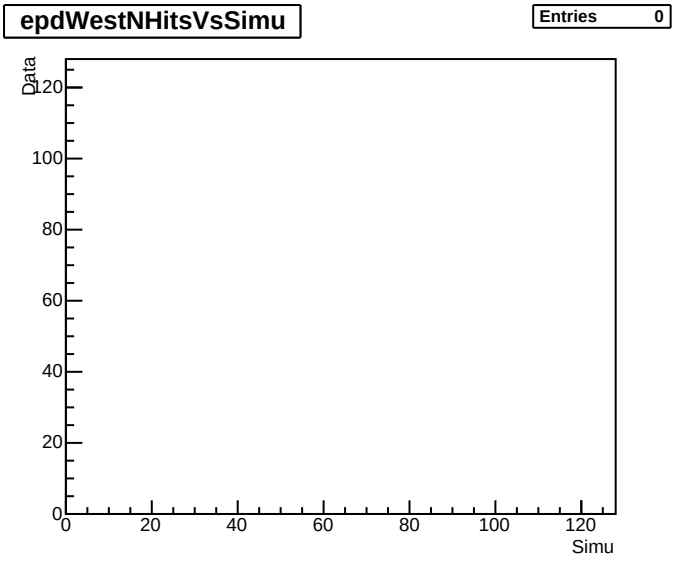
Entries 644071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Entries 0
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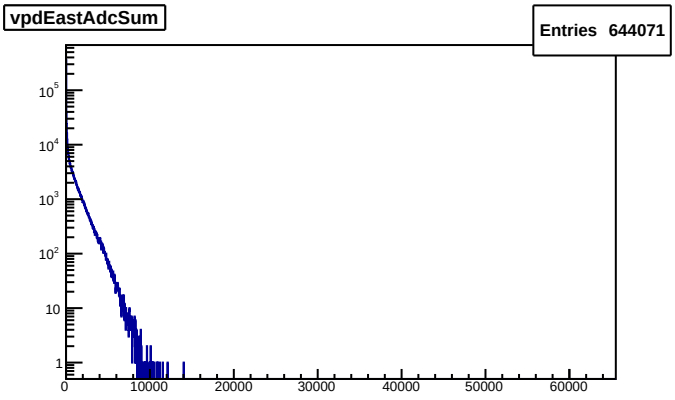
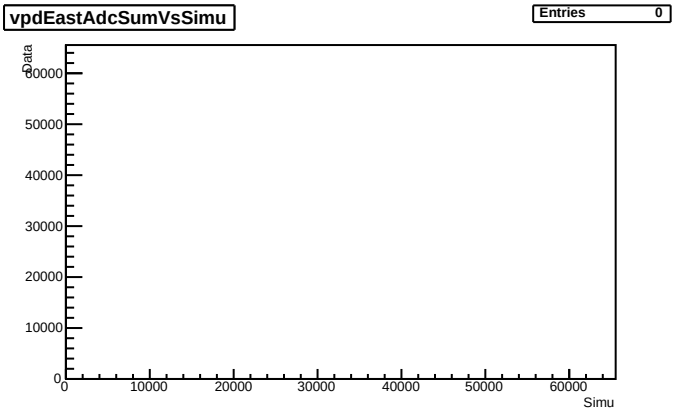

vpdEastAdcSum
Entries 644071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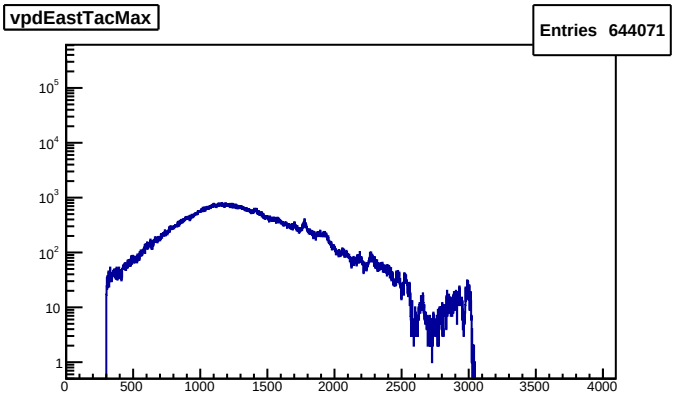
Entries 0
Data
Simu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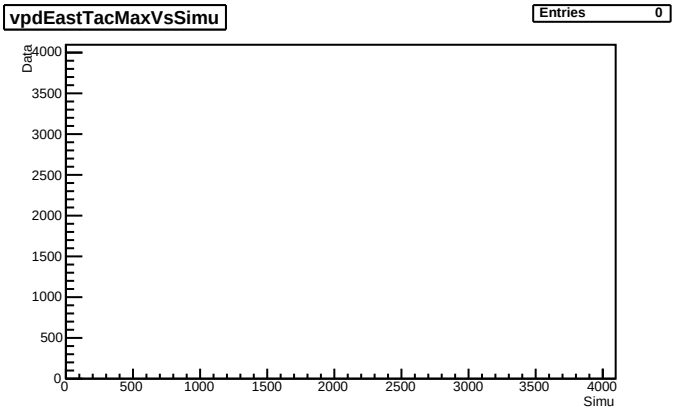
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Entries 0
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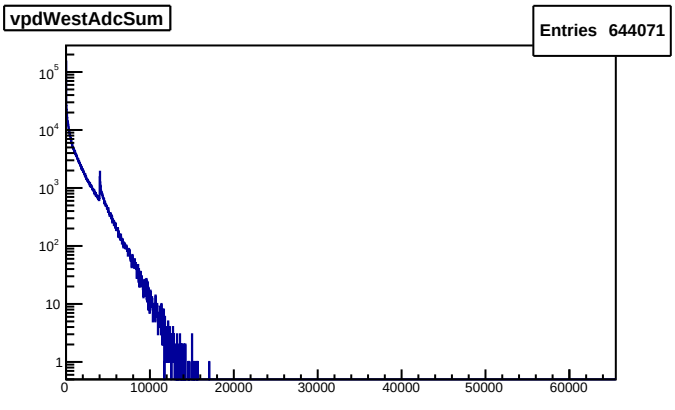
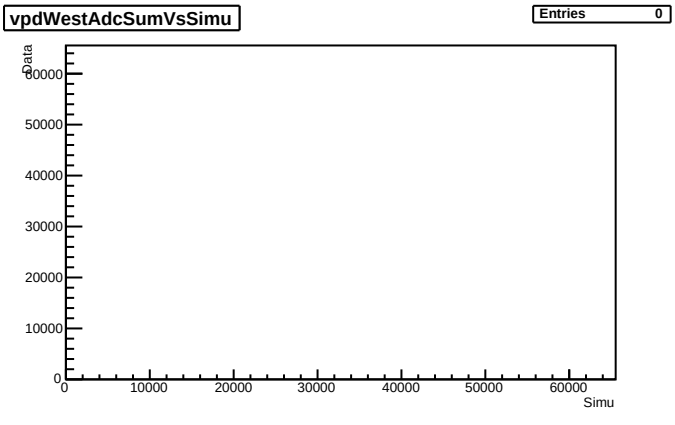

vpdWestAdcSum
Entries 644071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Entries 0
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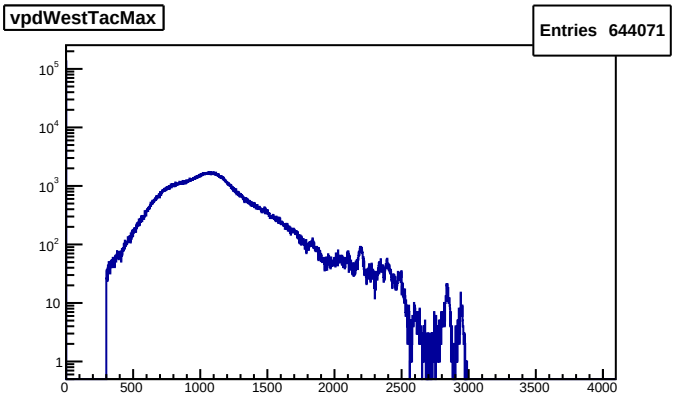
Simu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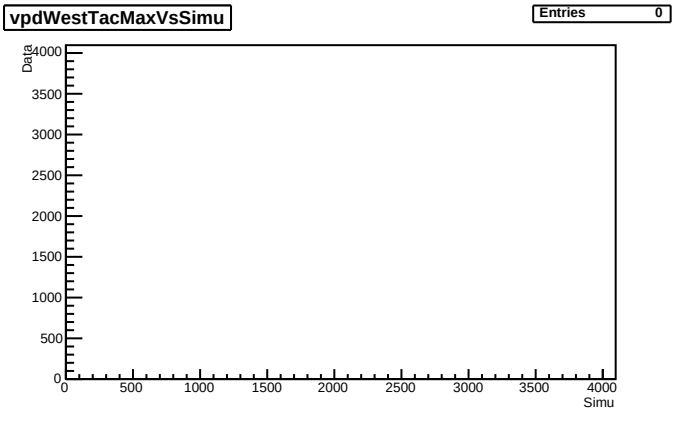
Entries 644071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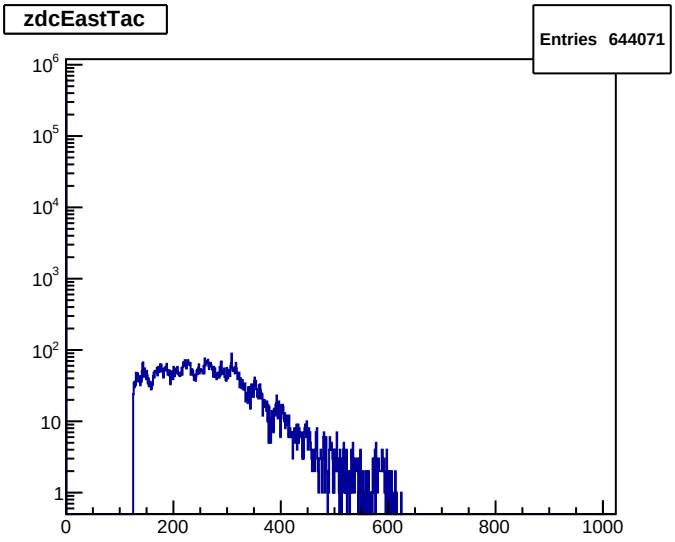
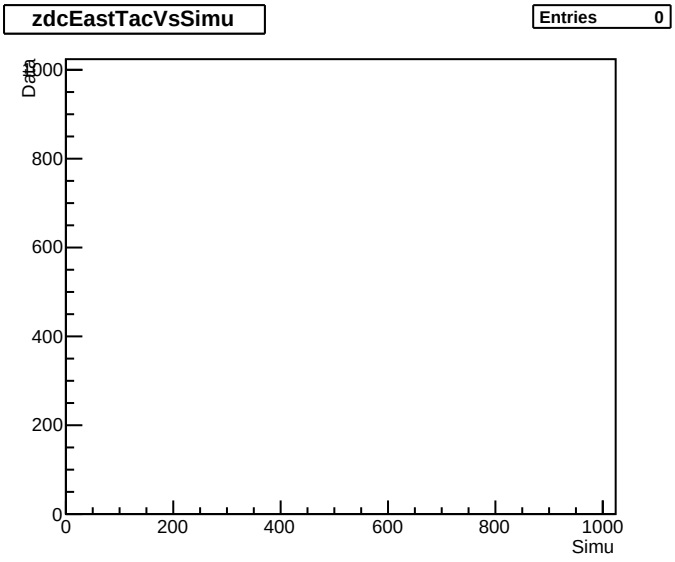
Entries 0
Data
Simu


zdcEastTac
Entries 644071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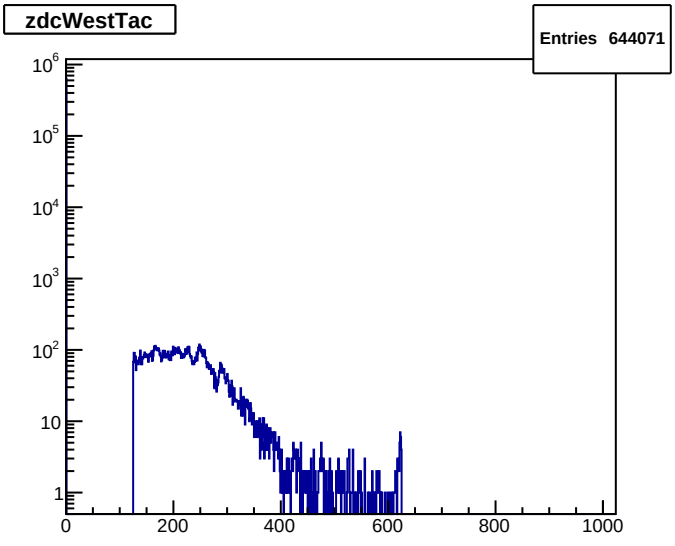
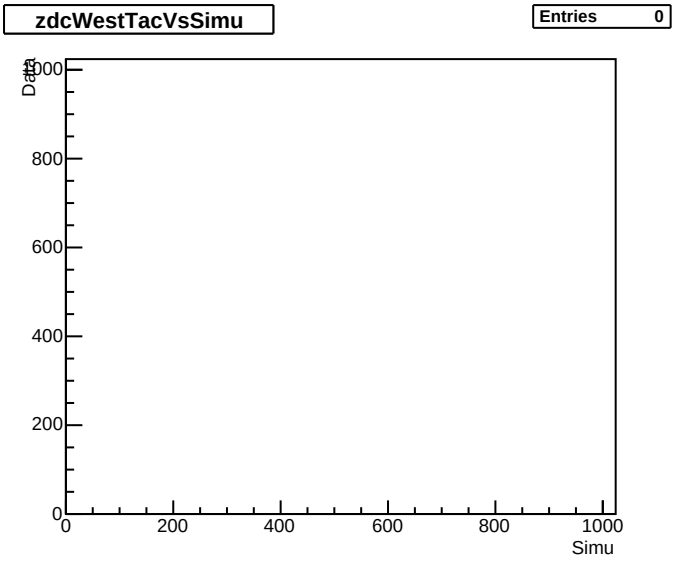
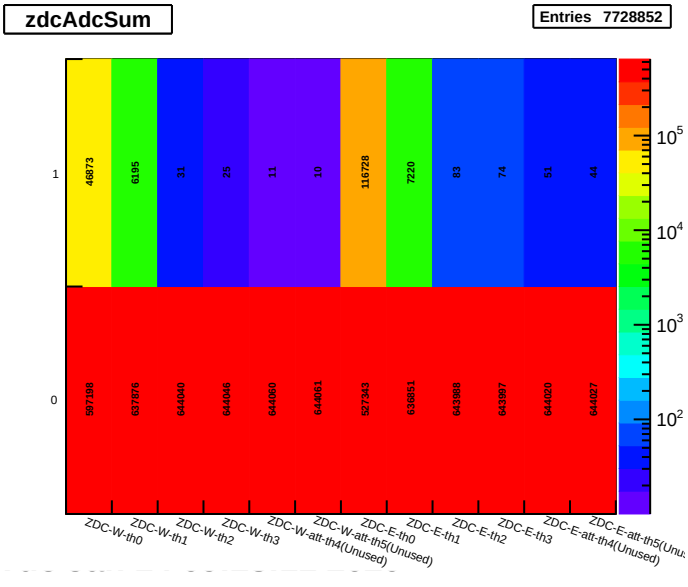
Entries 0
Data
Simu


zdcWestTac
Entries 644071


zdcWestTacVsSimu
Entries 0
Data
Simu


zdcAdcSum
Entries 7728852
46873
6195
31
25
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7220
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74
51
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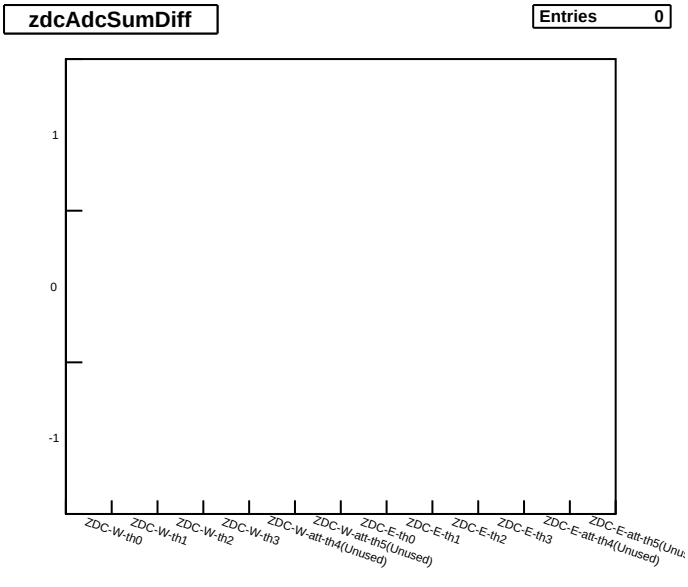
597198
637876
644040
644046
644060
644061
527343
636851
643988
643997
644020
644027
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)


zdcAdcSumDiff
Entries 0
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
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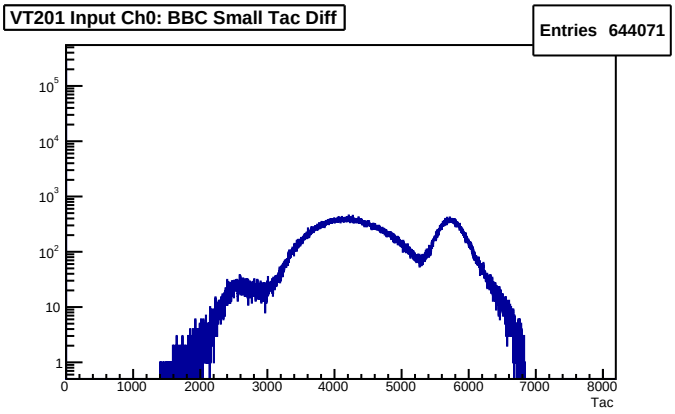
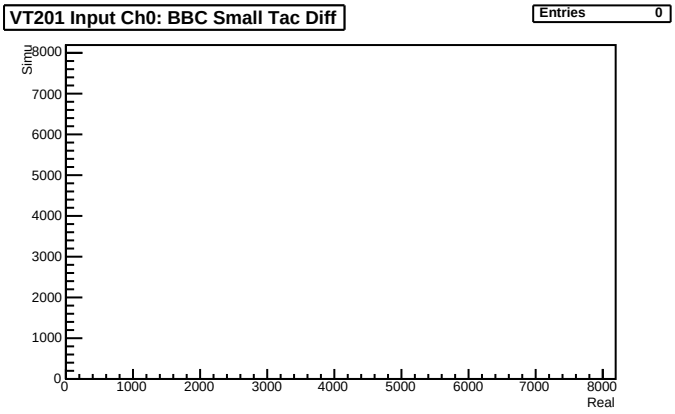
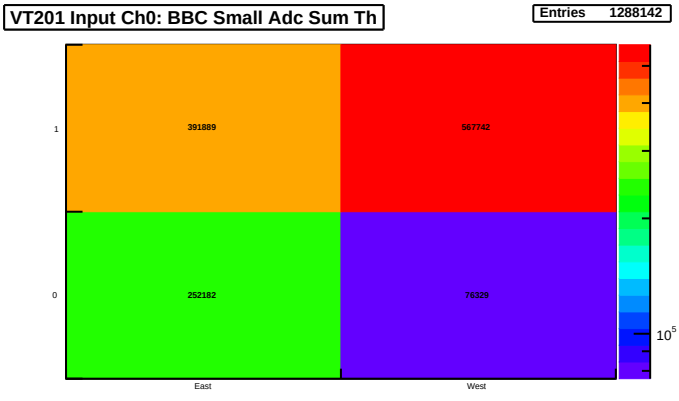
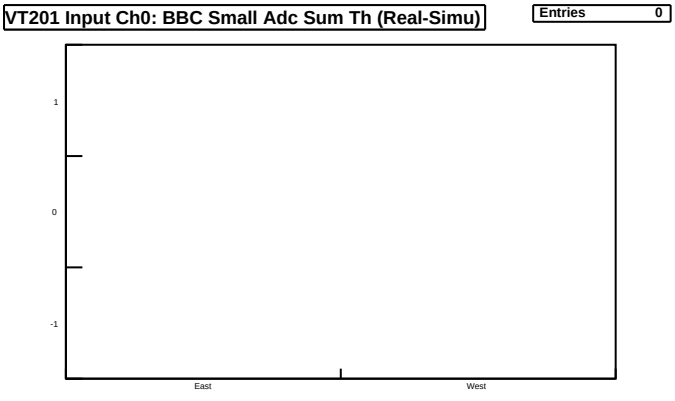
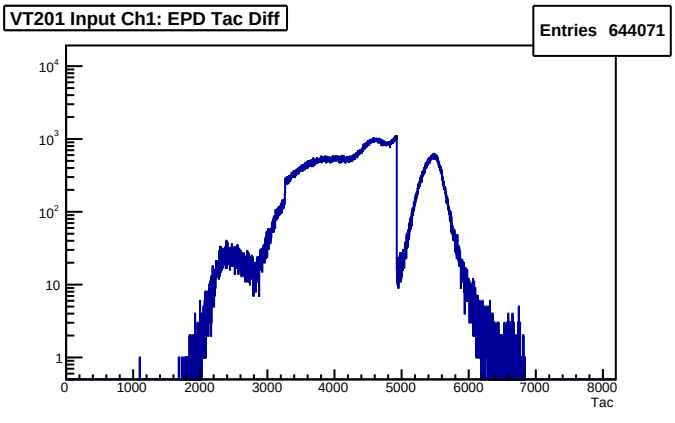
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)

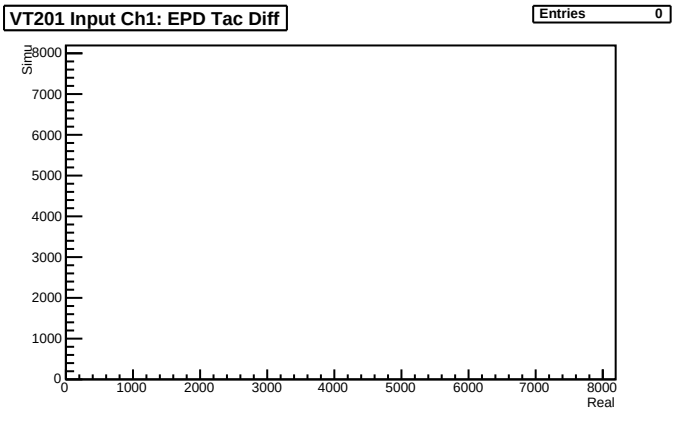

VT201 Input Ch0: BBC Small Tac Diff
Entries 644071
Tac

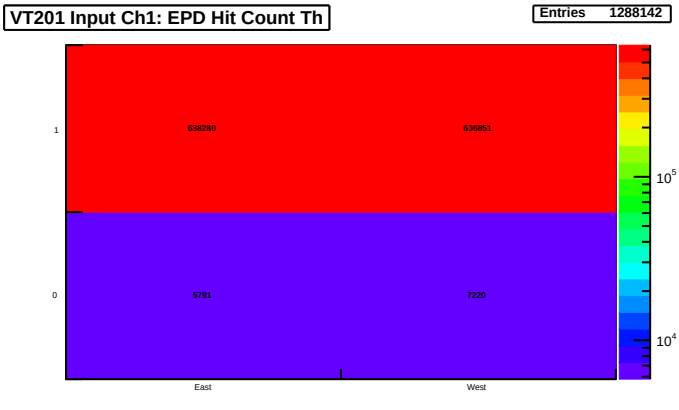

VT201 Input Ch0: BBC Small Tac Diff
Entries 0
Simu
Real

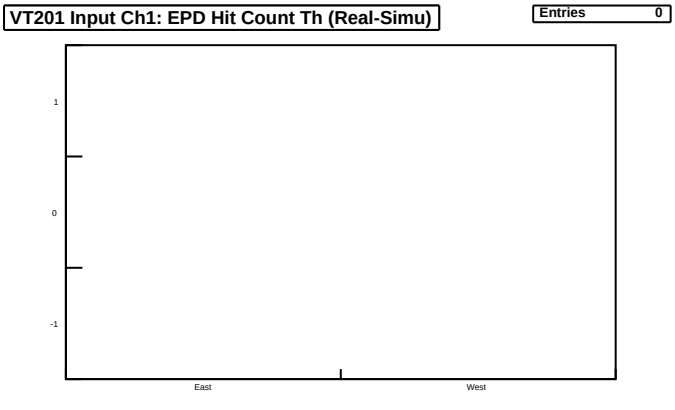

VT201 Input Ch0: BBC Small Adc Sum Th
Entries 1288142
391889
567742
252182
76329
East
West

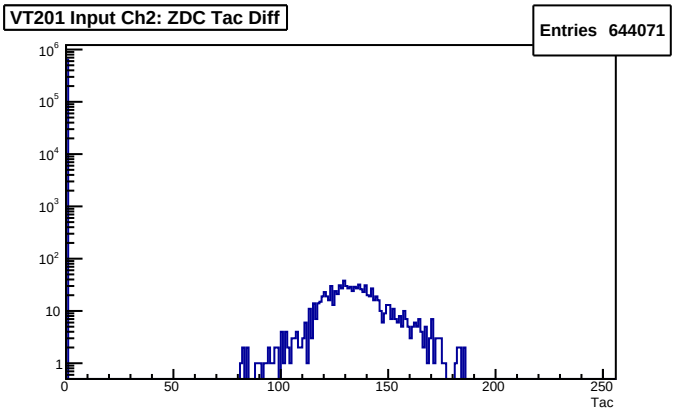

VT201 Input Ch0: BBC Small Adc Sum Th (Real-Simu)
Entries 0
East
West

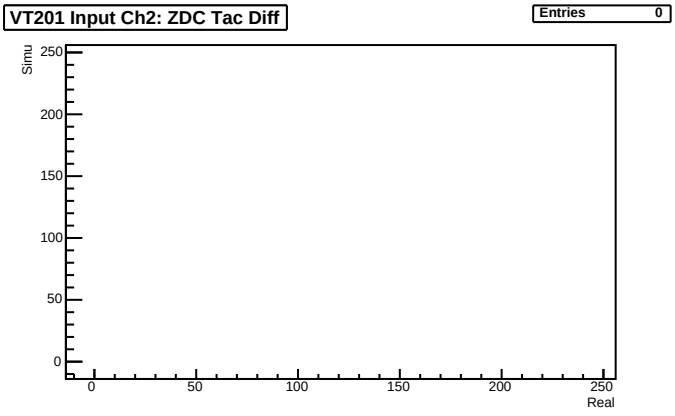

VT201 Input Ch1: EPD Tac Diff
Entries 644071
Tac

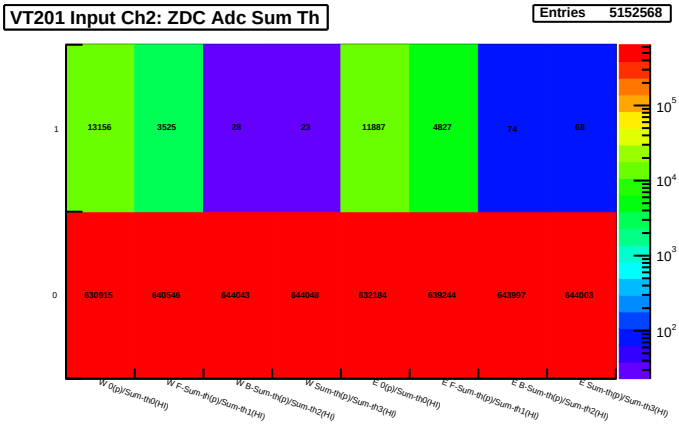

VT201 Input Ch1: EPD Tac Diff
Entries 0
Simu
Real

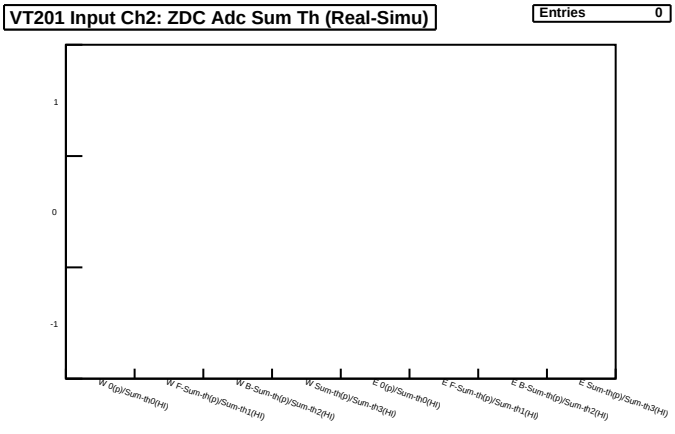

VT201 Input Ch1: EPD Hit Count Th
Entries 1288142
638280
636851
5791
7220
East
West

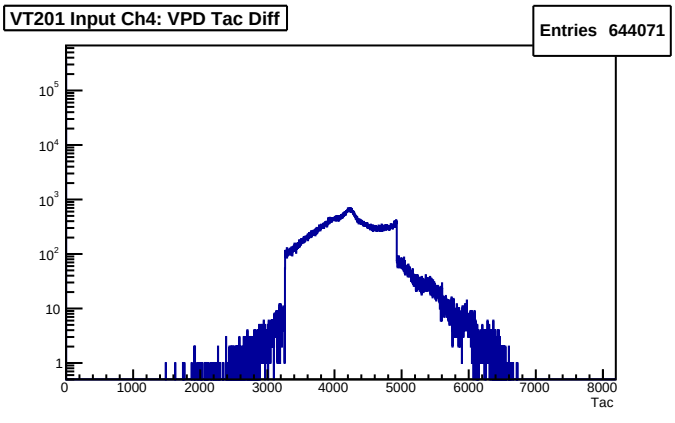

VT201 Input Ch1: EPD Hit Count Th (Real-Simu)
Entries 0
East
West

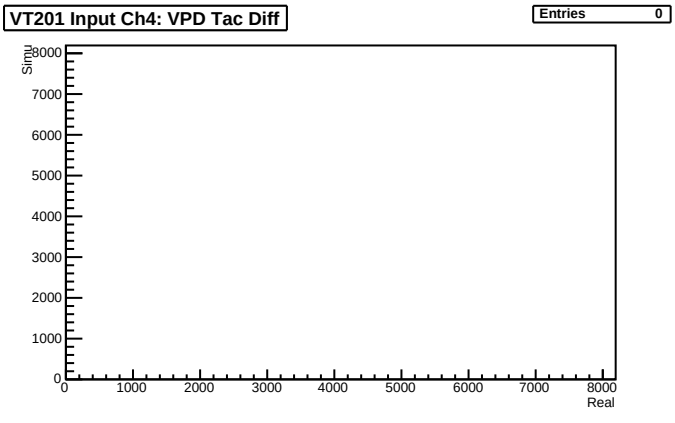

VT201 Input Ch2: ZDC Tac Diff
Entries 644071
Tac

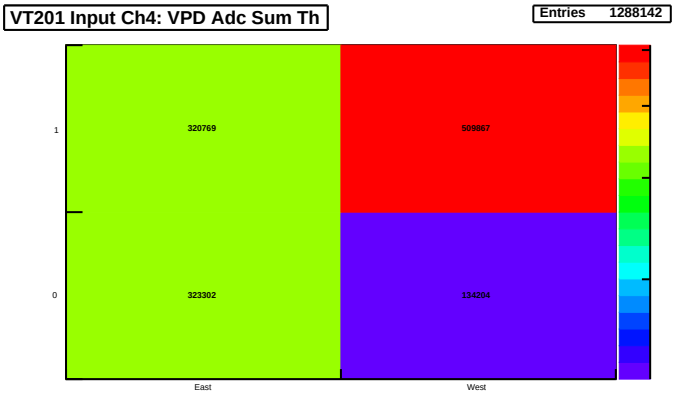

VT201 Input Ch2: ZDC Tac Diff
Entries 0
Simu
Real

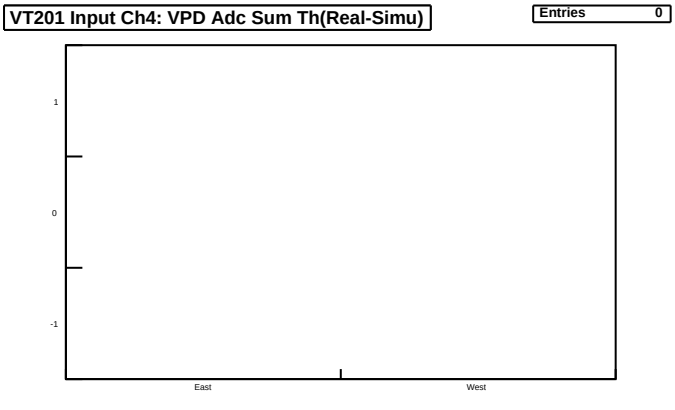

VT201 Input Ch2: ZDC Adc Sum Th
Entries 5152568
13156
3525
28
23
11887
4827
74
68
630915
640546
644043
644048
632184
639244
643997
644003
W 0(p)/Sum-th0(Hl)
W F-Sum-th(p)/Sum-th1(Hl)
W B-Sum-th(p)/Sum-th2(Hl)
W Sum-th(p)/Sum-th3(Hl)
E 0(p)/Sum-th0(Hl)
E F-Sum-th(p)/Sum-th1(Hl)
E B-Sum-th(p)/Sum-th2(Hl)
E Sum-th(p)/Sum-th3(Hl)

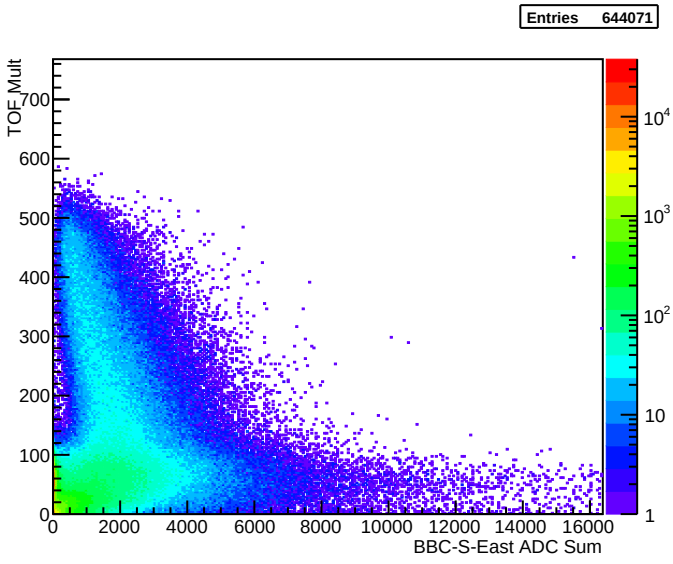

VT201 Input Ch2: ZDC Adc Sum Th (Real-Simu)
Entries 0
W 0(p)/Sum-th0(Hl)
W F-Sum-th(p)/Sum-th1(Hl)
W B-Sum-th(p)/Sum-th2(Hl)
W Sum-th(p)/Sum-th3(Hl)
E 0(p)/Sum-th0(Hl)
E F-Sum-th(p)/Sum-th1(Hl)
E B-Sum-th(p)/Sum-th2(Hl)
E Sum-th(p)/Sum-th3(Hl)

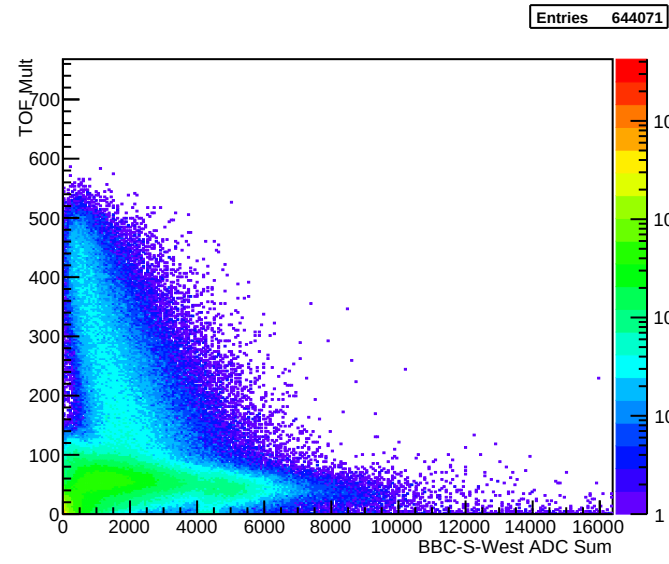

VT201 Input Ch4: VPD Tac Diff
Entries 644071
Tac

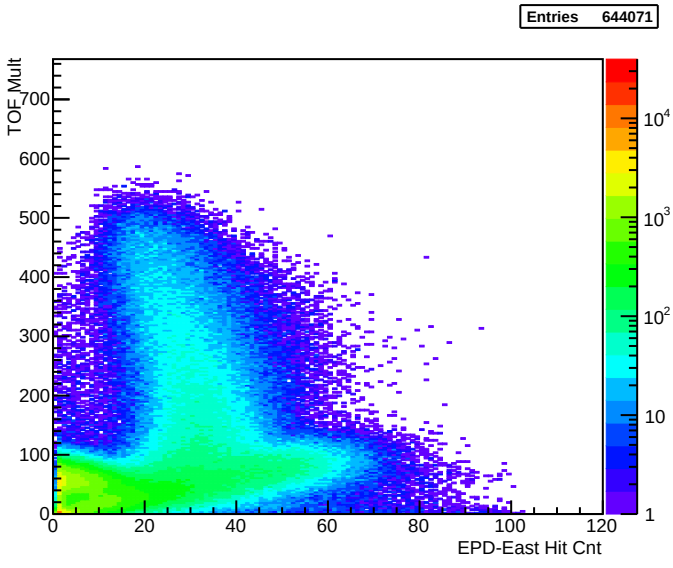

VT201 Input Ch4: VPD Tac Diff
Entries 0
Simu
Real

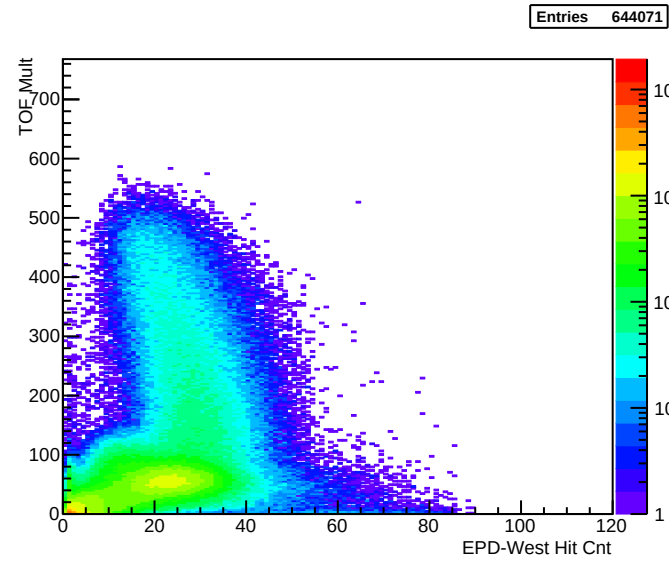

VT201 Input Ch4: VPD Adc Sum Th
Entries 1288142
320769
509867
323302
134204
East
West

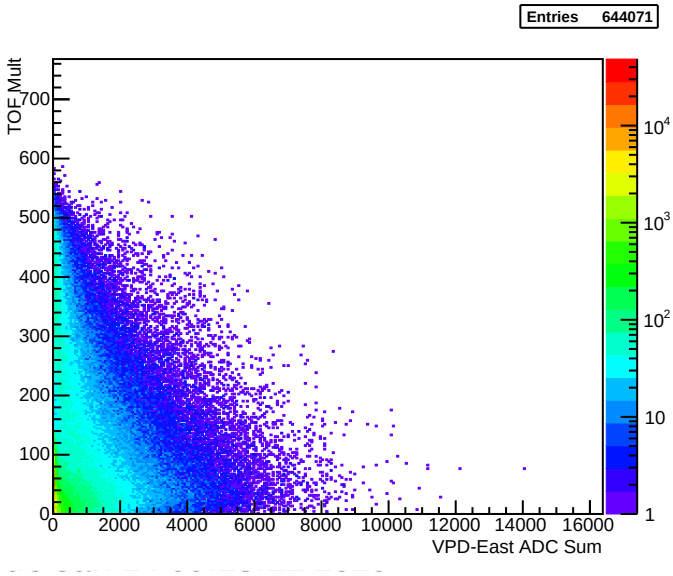

VT201 Input Ch4: VPD Adc Sum Th(Real-Simu)
Entries 0
East
West

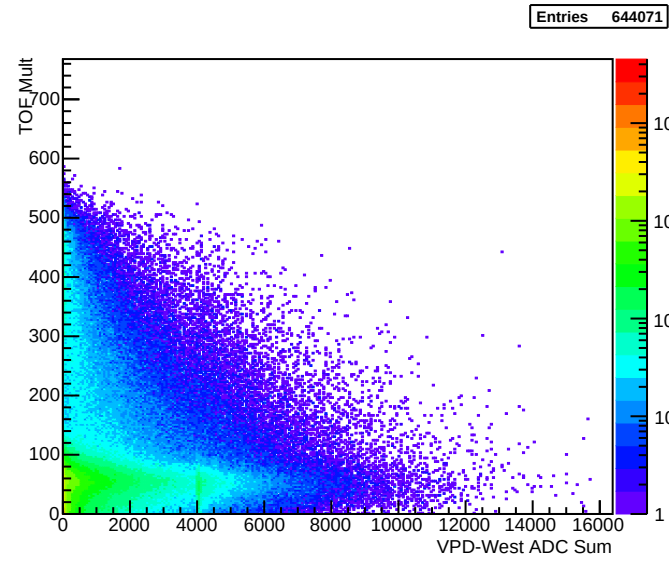

Entries 644071
TOF_Mult
BBC-S-East ADC Sum

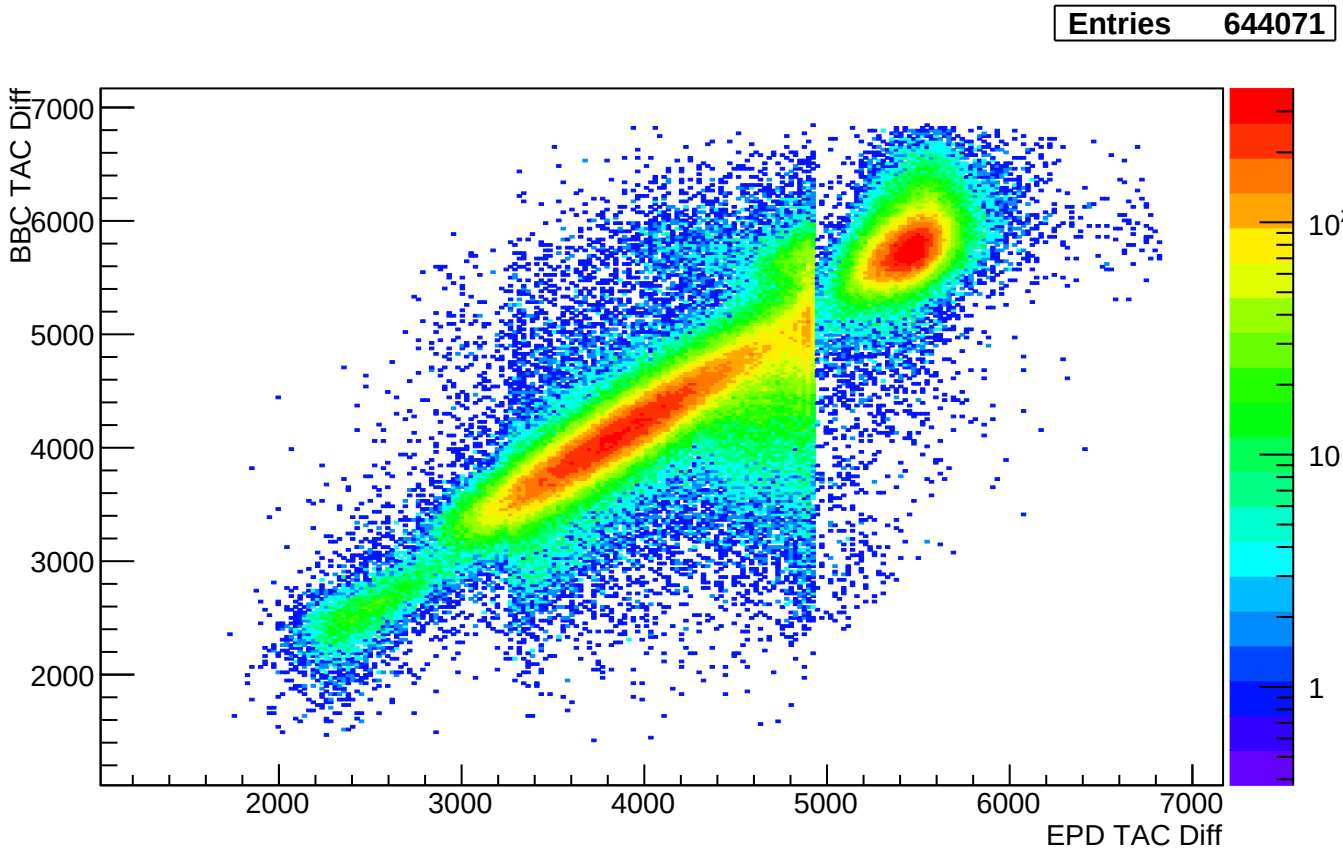

Entries 644071
TOF_Mult
BBC-S-West ADC Sum

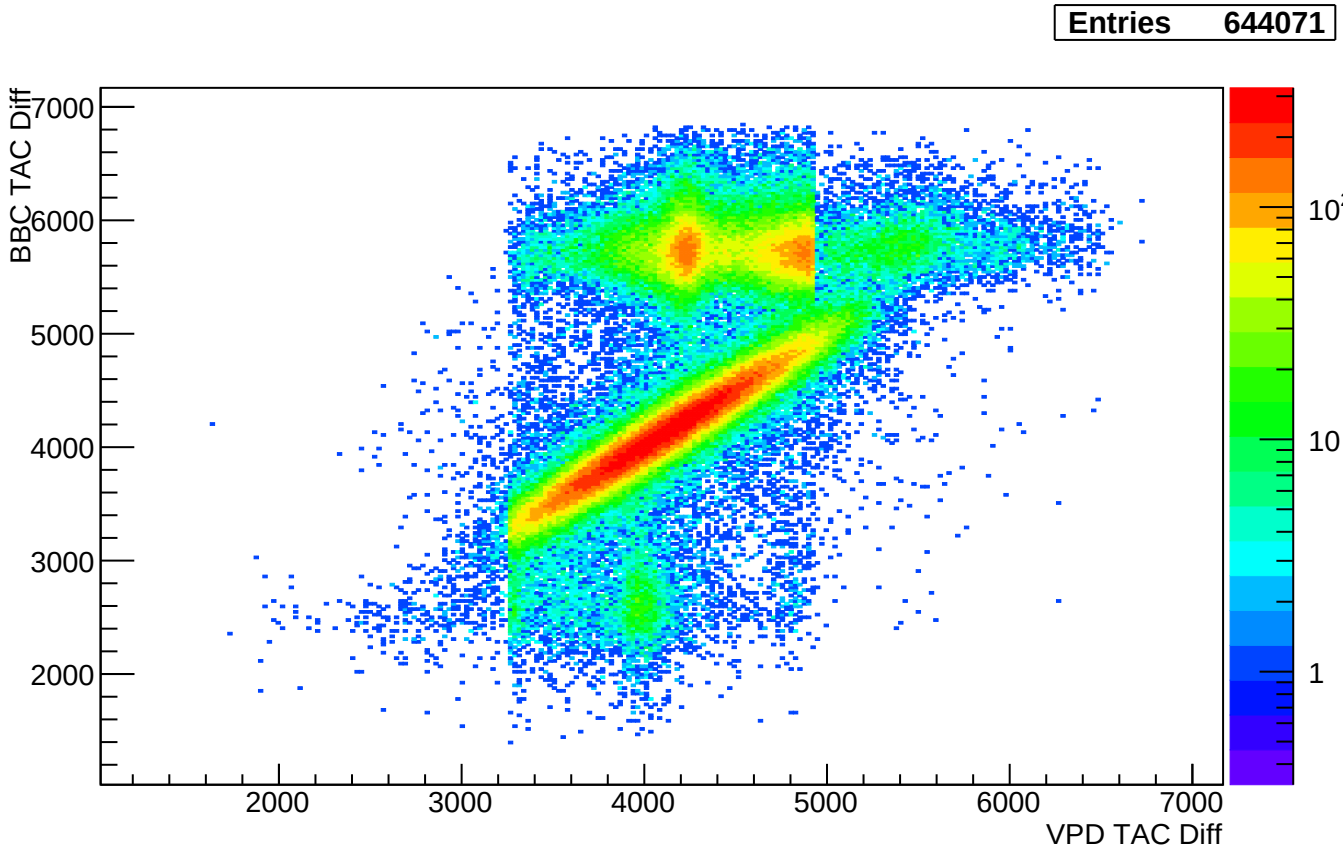

Entries 644071
TOF_Mult
EPD-East Hit Cnt

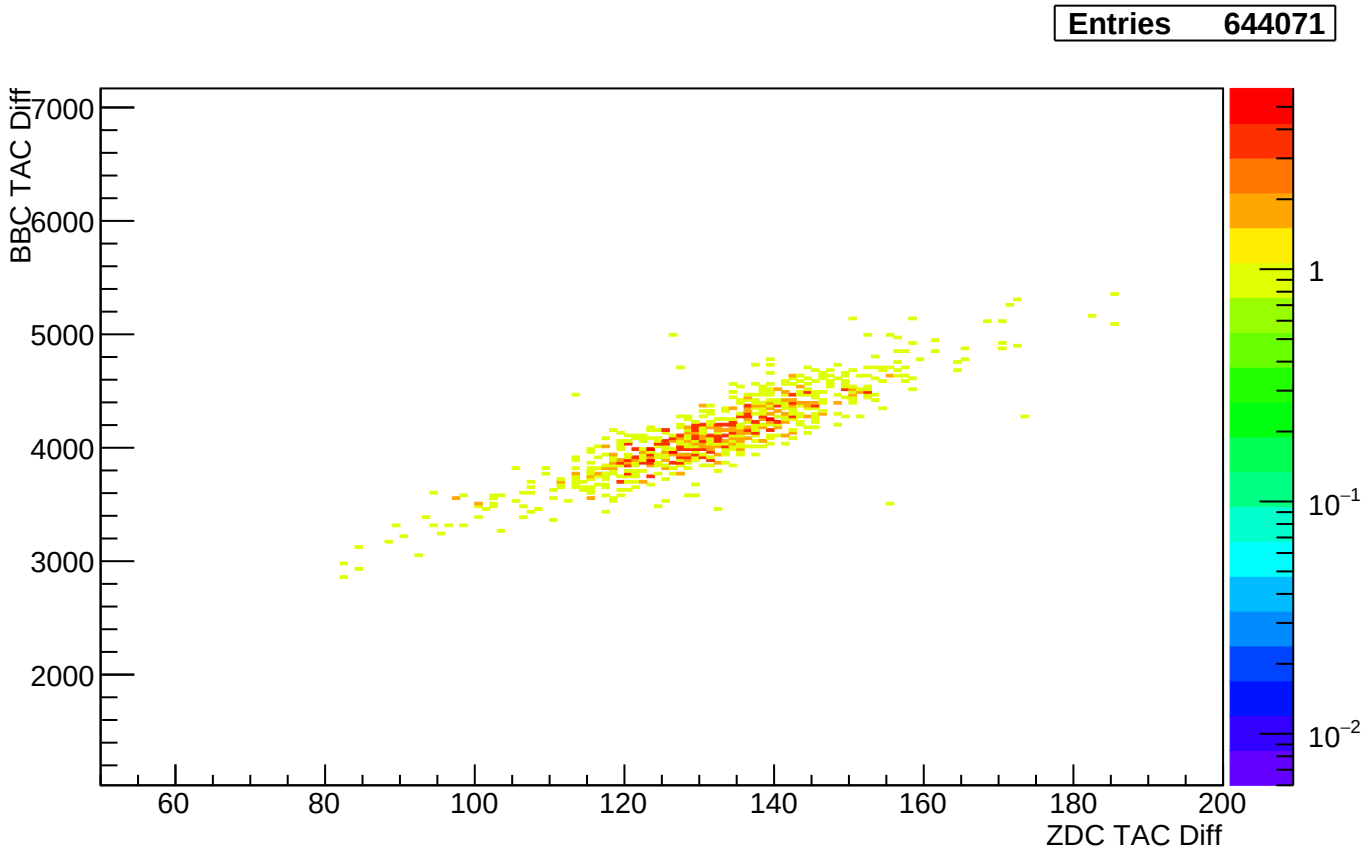

Entries 644071
TOF_Mult
EPD-West Hit Cnt

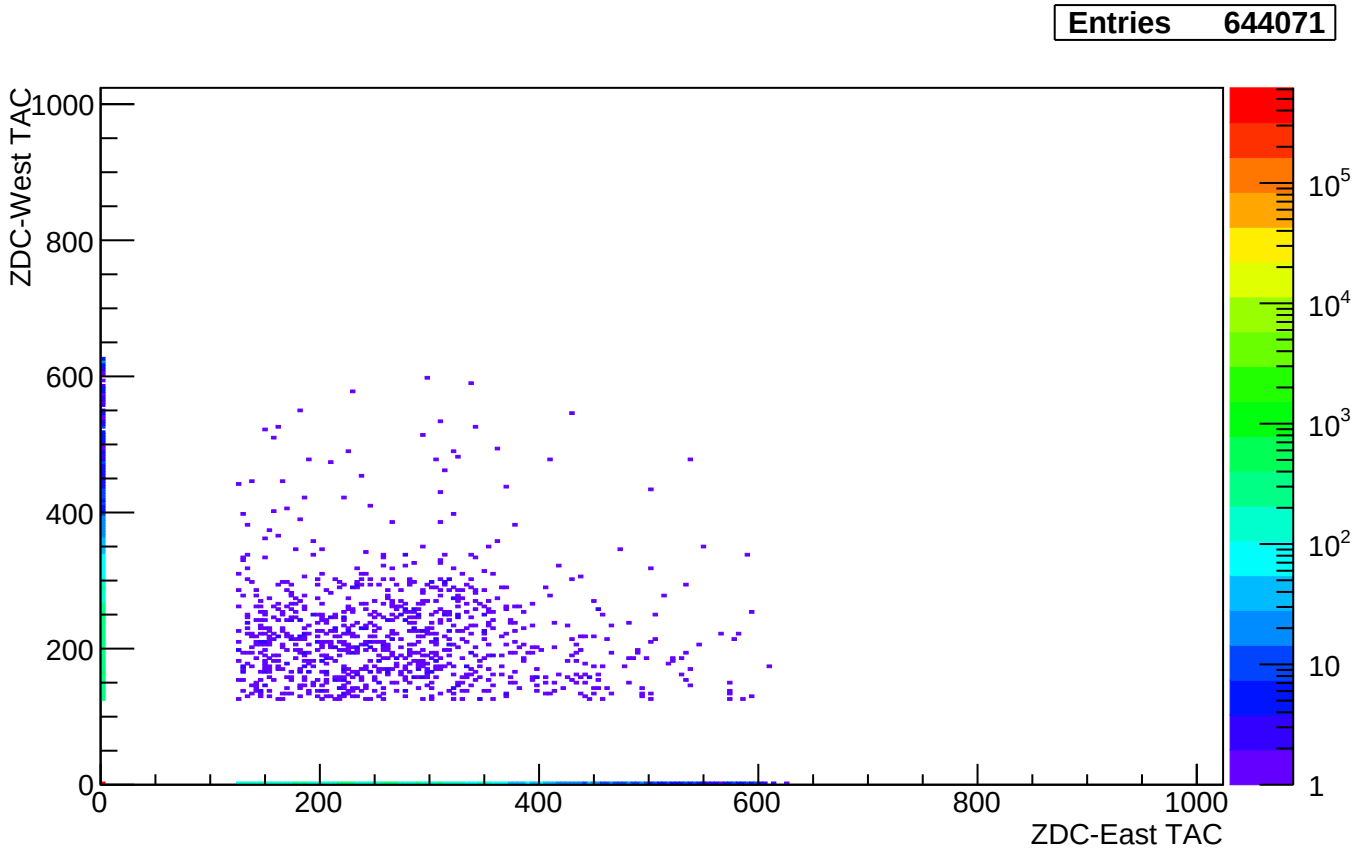

Entries 644071
TOF_Mult
VPD-East ADC Sum

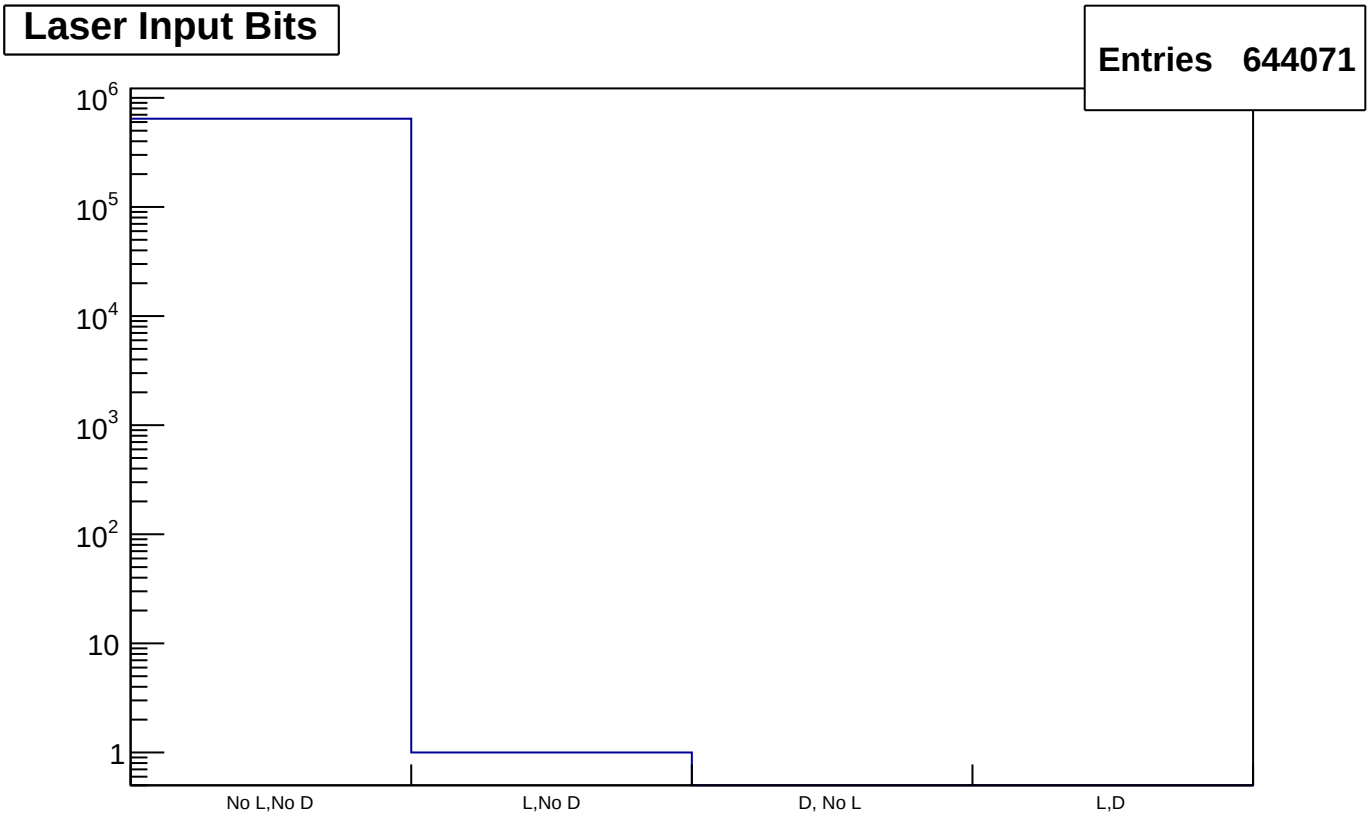

Entries 644071
TOF_Mult
VPD-West ADC Sum

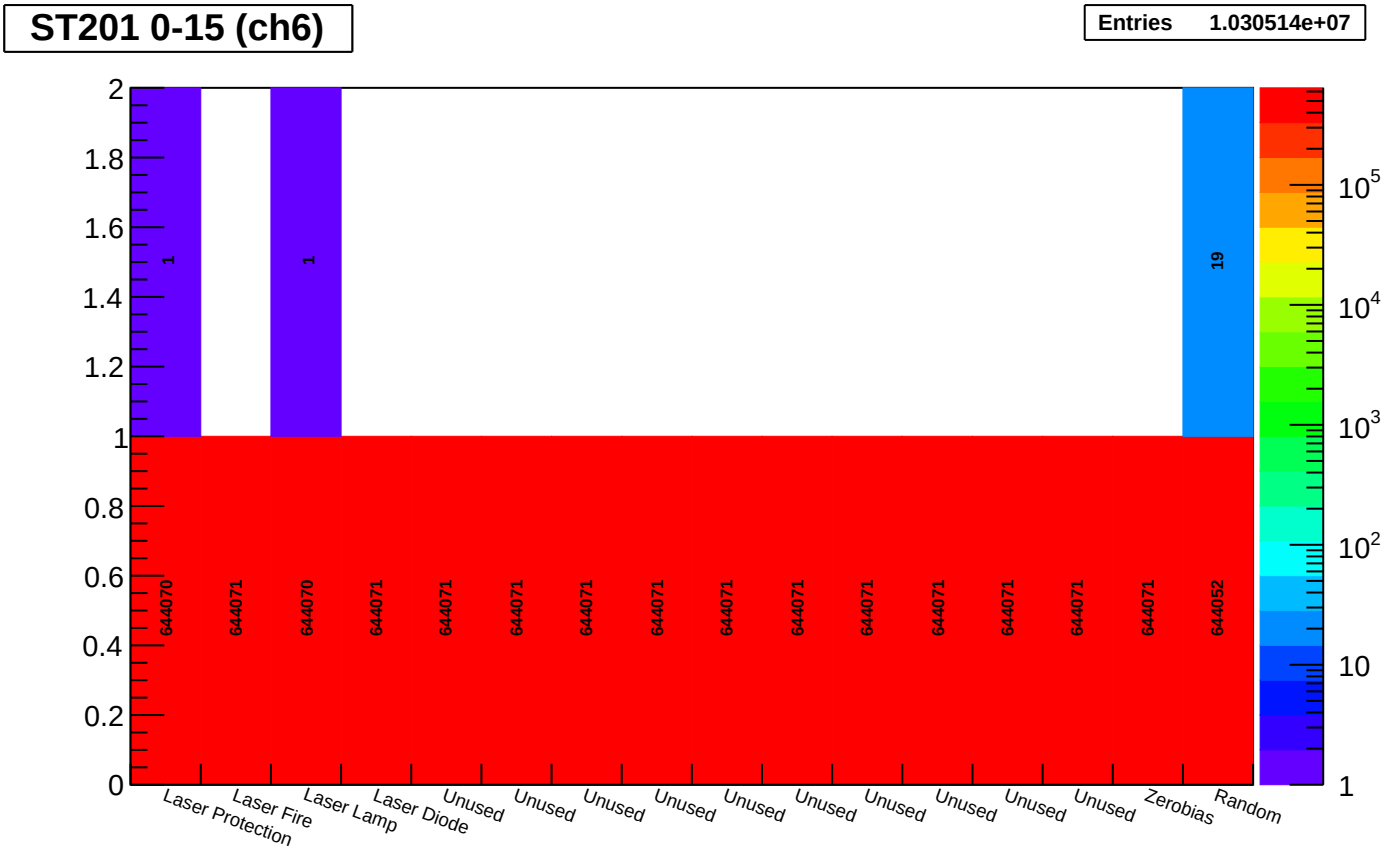

Entries 644071
BBC TAC Diff
7000
6000
5000
4000
3000
2000
2000
3000
4000
5000
6000
7000
EPD TAC Diff
10^2
10
1


Entries 644071
BBC TAC Diff
7000
6000
5000
4000
3000
2000
2000
3000
4000
5000
6000
7000
VPD TAC Diff
10^2
10
1

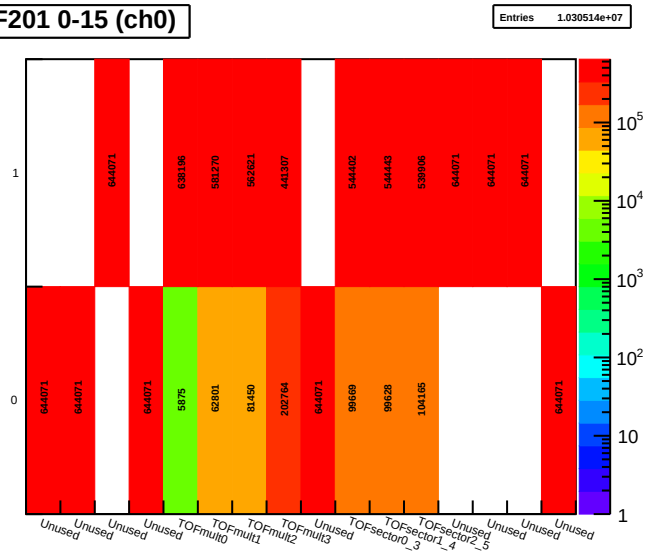

Entries 644071
BBC TAC Diff
7000
6000
5000
4000
3000
2000
60
80
100
120
140
160
180
200
ZDC TAC Diff
1
10^{-1}
10^{-2}


Entries 644071
ZDC-West TAC
1000
800
600
400
200
0
0
200
400
600
800
1000
ZDC-East TAC
10^5
10^4
10^3
10^2
10
1

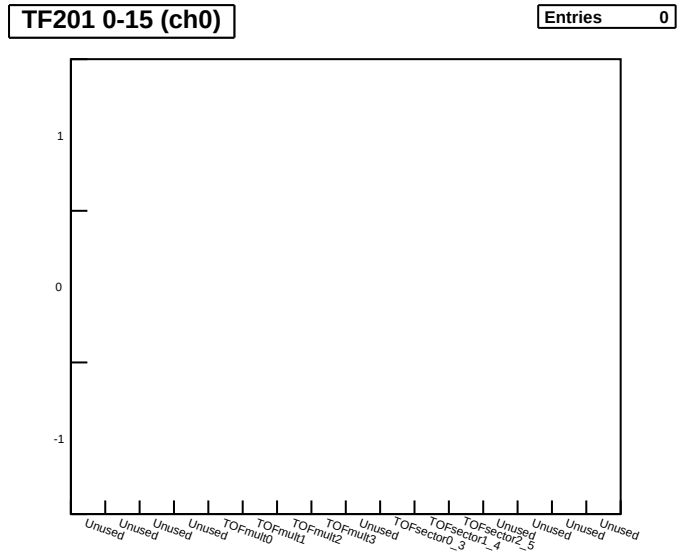

Laser Input Bits
Entries 644071
10^6
10^5
10^4
10^3
10^2
10
1
No L,No D
L,No D
D, No L
L,D


ST201 0-15 (ch6)
Entries 1.030514e+07
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
1
1
19
644070
644071
644070
644071
644071
644071
644071
644071
644071
644071
644071
644071
644071
644071
644071
644052
Laser Protection
Laser Fire
Laser Lamp
Laser Diode
Unused
Unused
Unused
Unused
Unused
Unused
Unused
Unused
Unused
Unused
Zerobias
Random
10^5
10^4
10^3
10^2
10
1

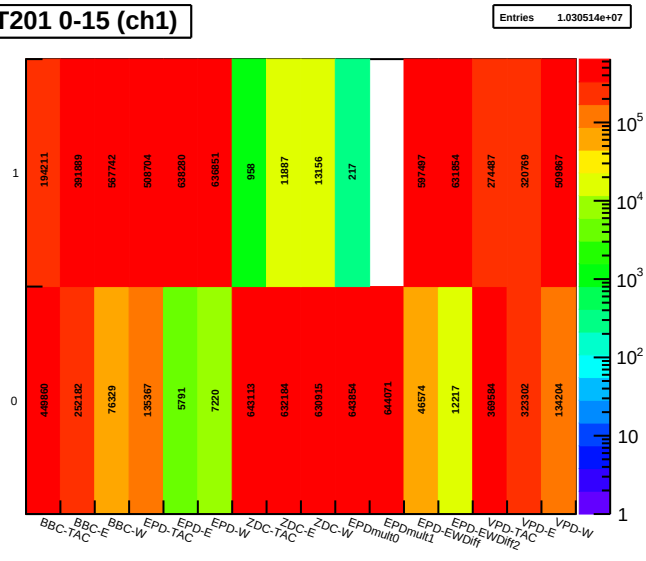
TF201 0-15 (ch0)



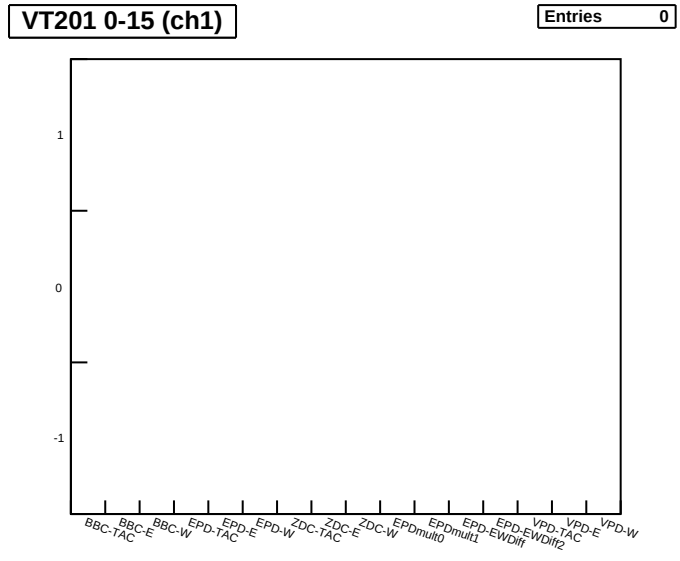
TF201 0-15 (ch0)



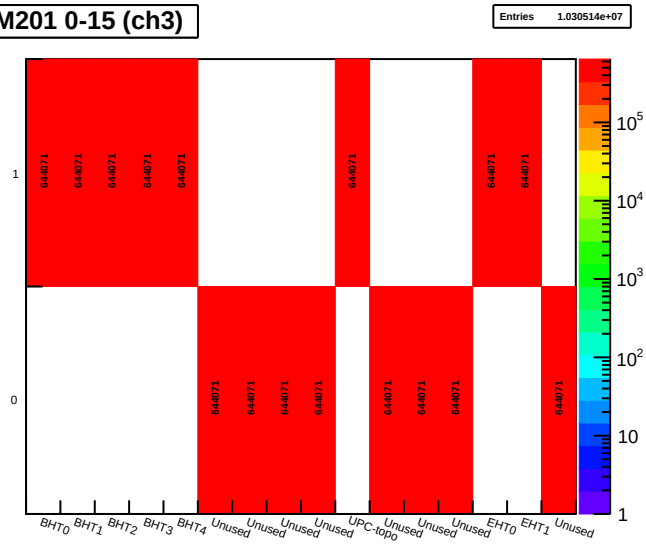
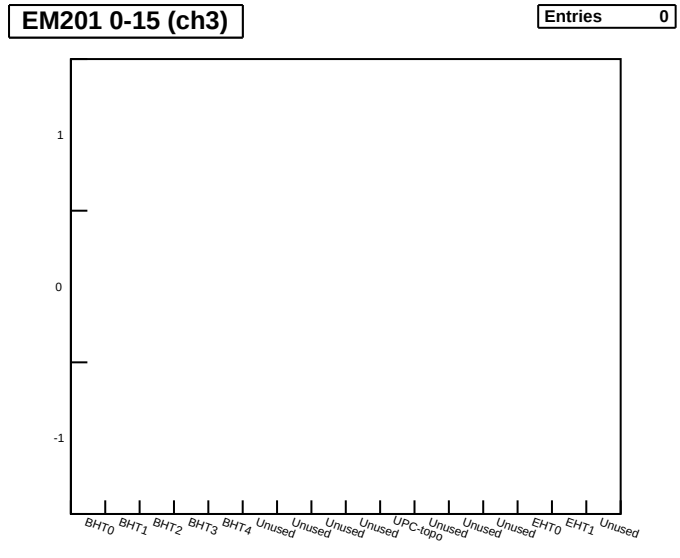
VT201 0-15 (ch1)



VT201 0-15 (ch1)



EM201 0-15 (ch3)



EM201 0-15 (ch3)