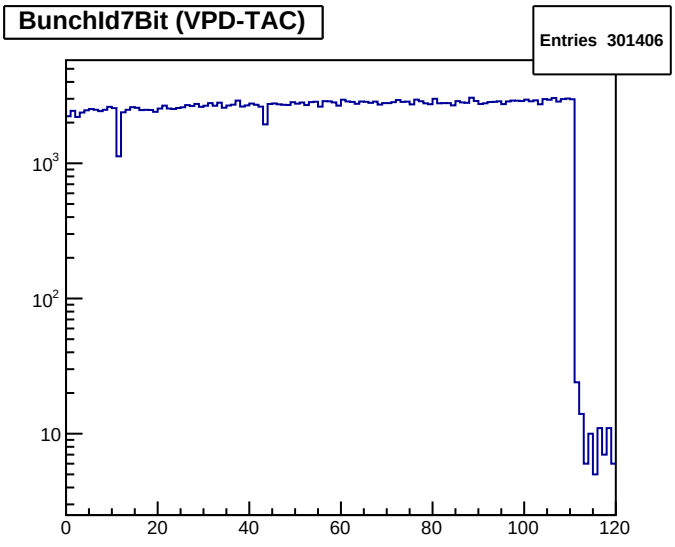
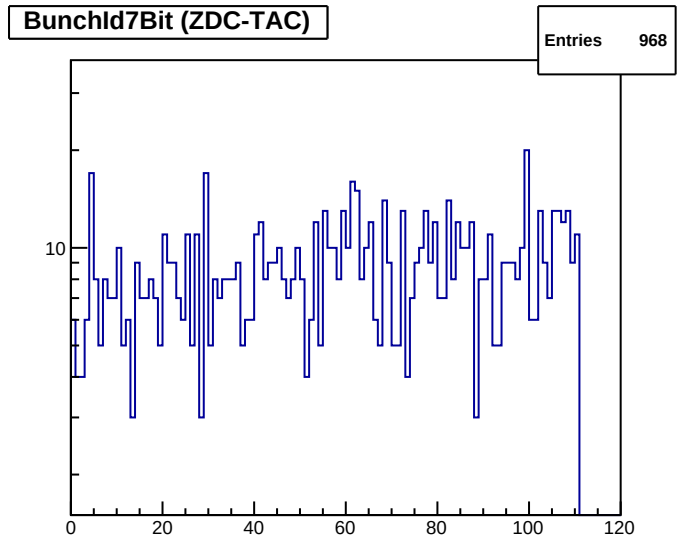
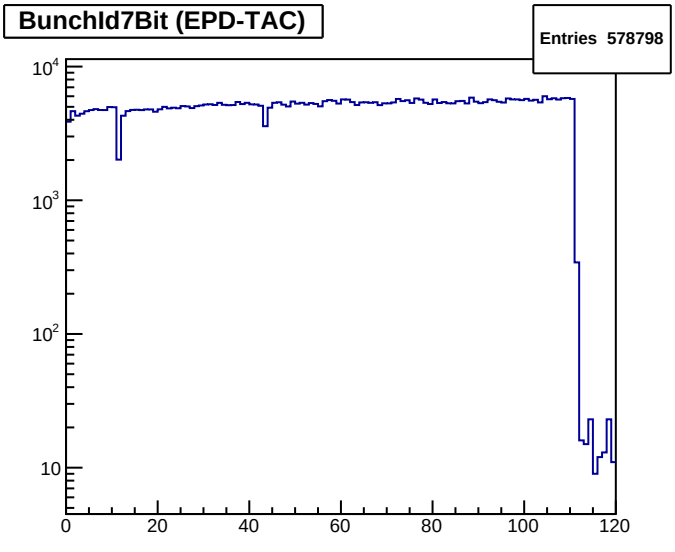
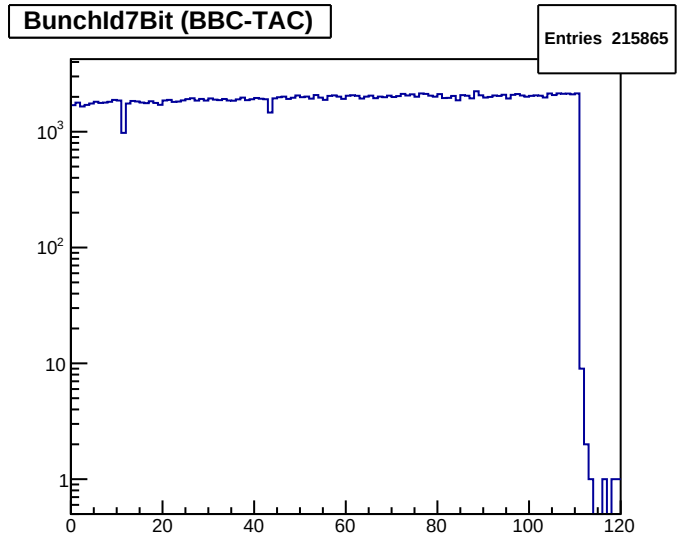
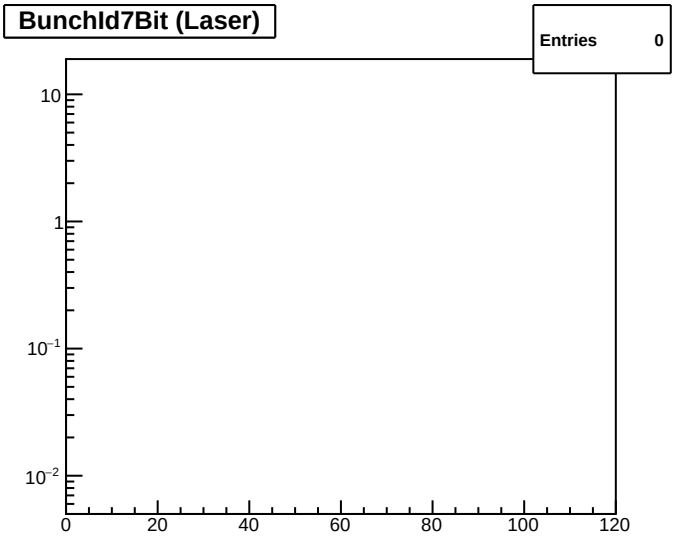
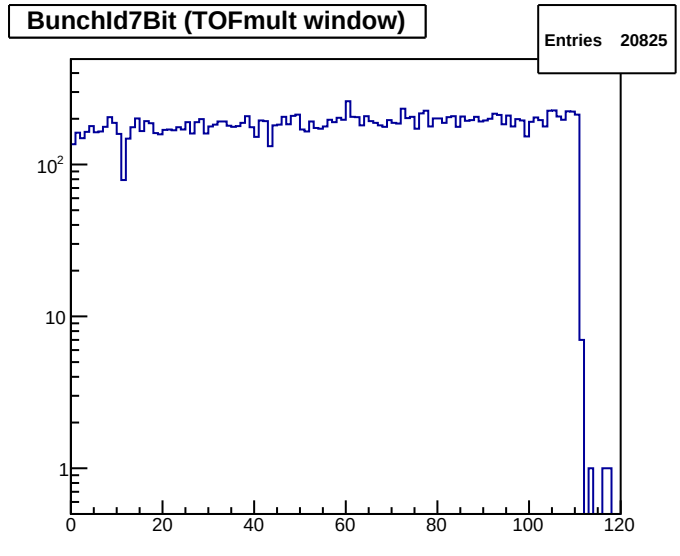

BunchId7Bit (VPD-TAC)
Entries 301406

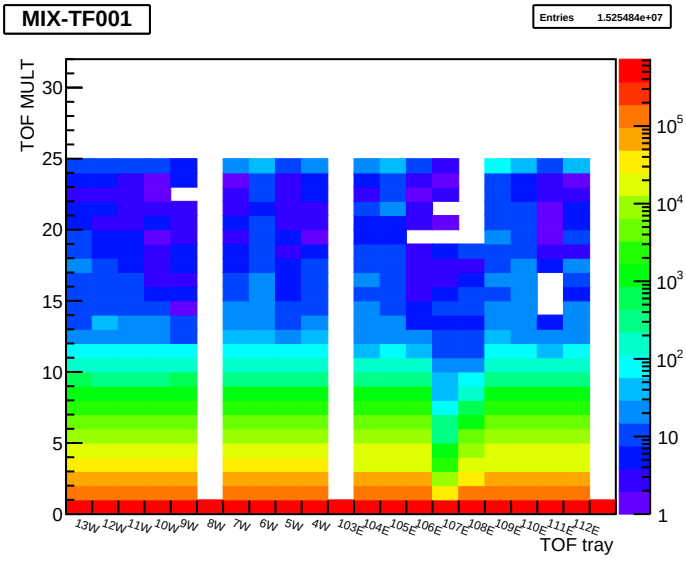

BunchId7Bit (ZDC-TAC)
Entries 968

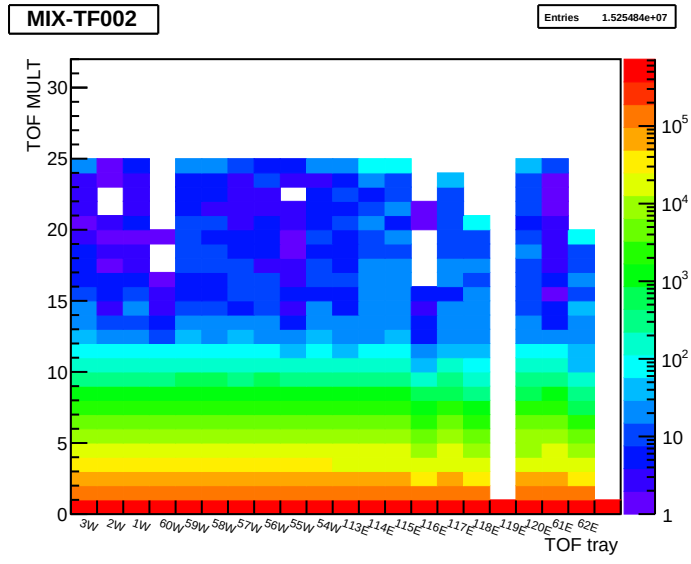

BunchId7Bit (EPD-TAC)
Entries 578798

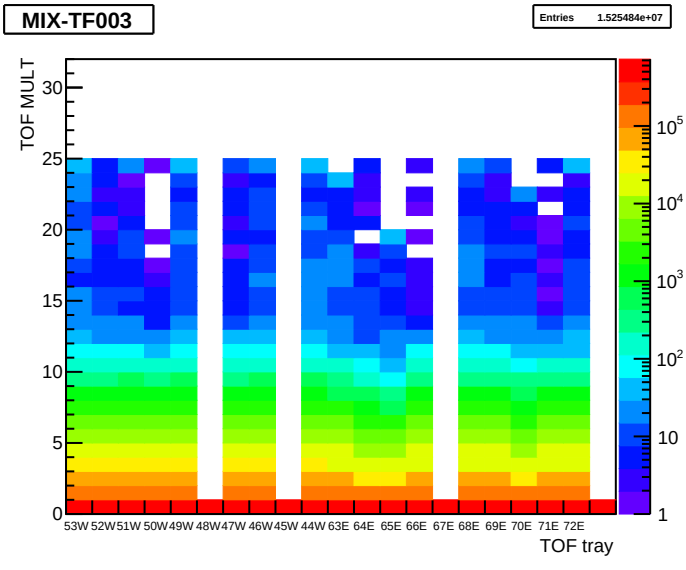

BunchId7Bit (BBC-TAC)
Entries 215865

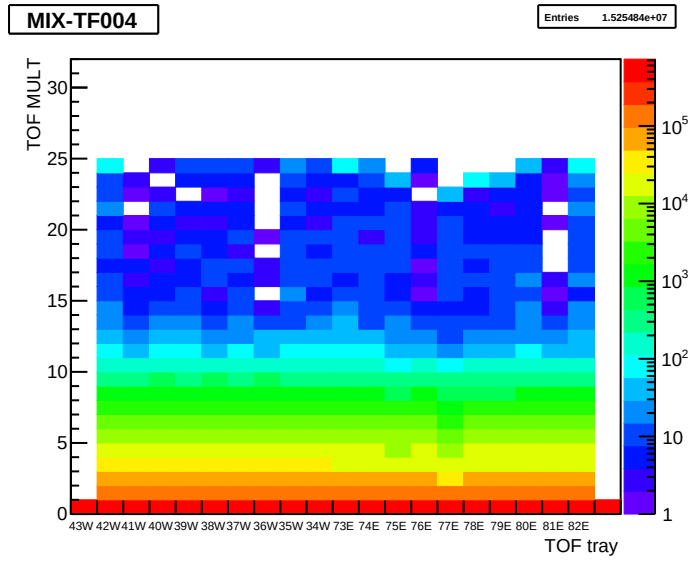

BunchId7Bit (Laser)
Entries 0

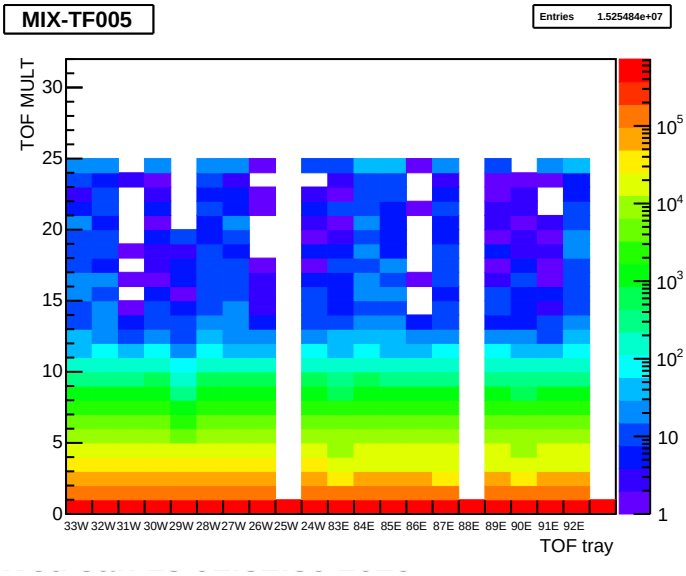

BunchId7Bit (TOFmult window)
Entries 20825

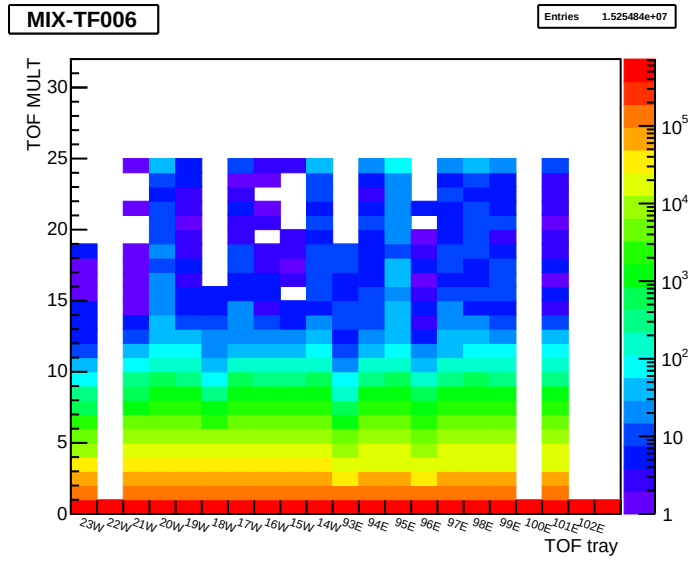

MIX-TF001
Entries 1.525484e+07
TOF MULT
TOF tray
13W 12W 11W 10W 9W 8W 7W 6W 5W 4W 103E 104E 105E 106E 107E 108E 109E 110E 111E 112E

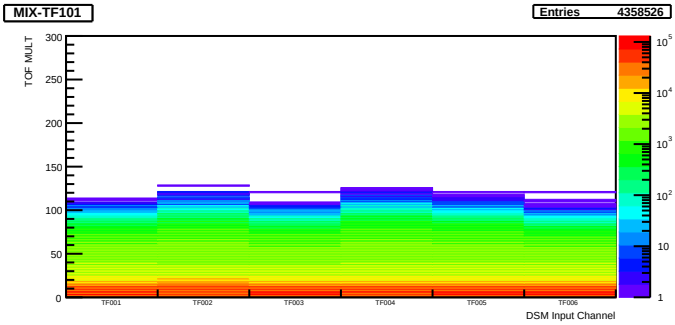

MIX-TF002
Entries 1.525484e+07
TOF MULT
TOF tray
3W 2W 1W 60W 59W 58W 57W 56W 55W 54W 113E 114E 115E 116E 117E 118E 119E 120E 61E 62E

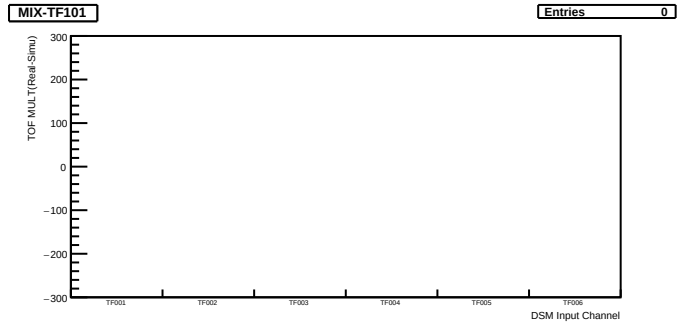

MIX-TF003
Entries 1.525484e+07
TOF MULT
TOF tray
53W 52W 51W 50W 49W 48W 47W 46W 45W 44W 63E 64E 65E 66E 67E 68E 69E 70E 71E 72E

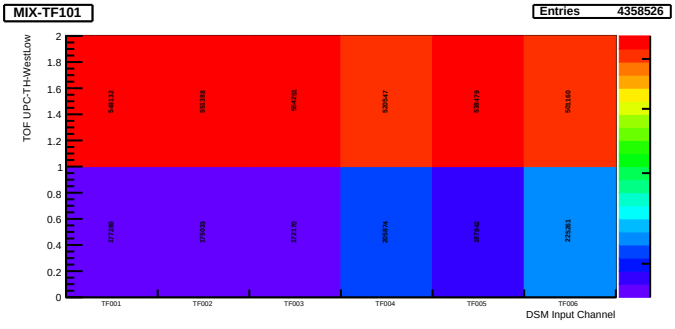

MIX-TF004
Entries 1.525484e+07
TOF MULT
TOF tray
43W 42W 41W 40W 39W 38W 37W 36W 35W 34W 73E 74E 75E 76E 77E 78E 79E 80E 81E 82E

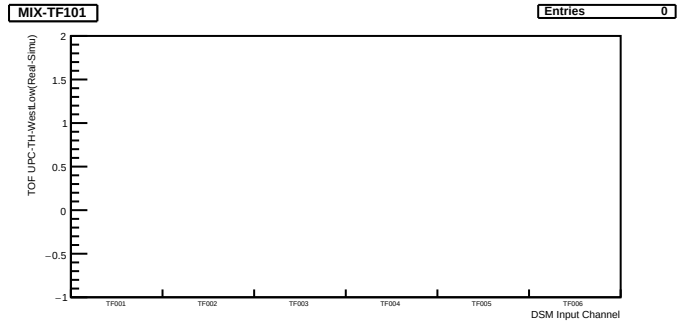

MIX-TF005
Entries 1.525484e+07
TOF MULT
TOF tray
33W 32W 31W 30W 29W 28W 27W 26W 25W 24W 83E 84E 85E 86E 87E 88E 89E 90E 91E 92E

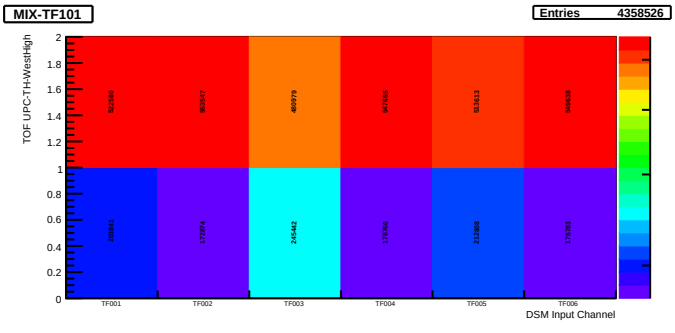

MIX-TF006
Entries 1.525484e+07
TOF MULT
TOF tray
23W 22W 21W 20W 19W 18W 17W 16W 15W 14W 93E 94E 95E 96E 97E 98E 99E 100E 101E 102E

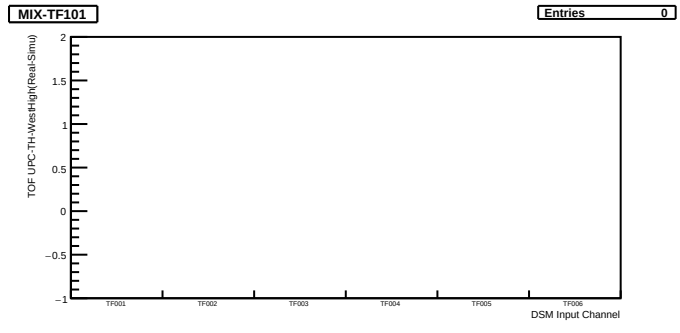

MIX-TF101
Entries 4358526
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

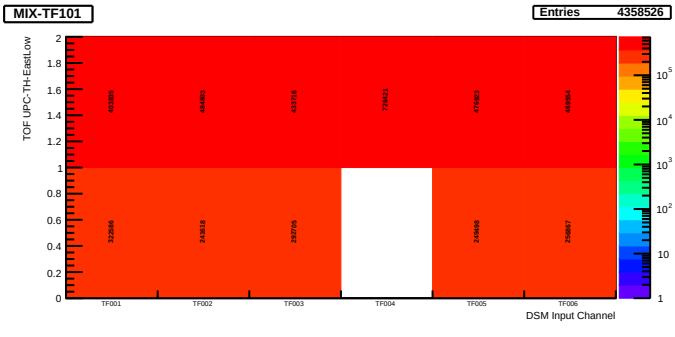

MIX-TF101
Entries 0
TOF MULT(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

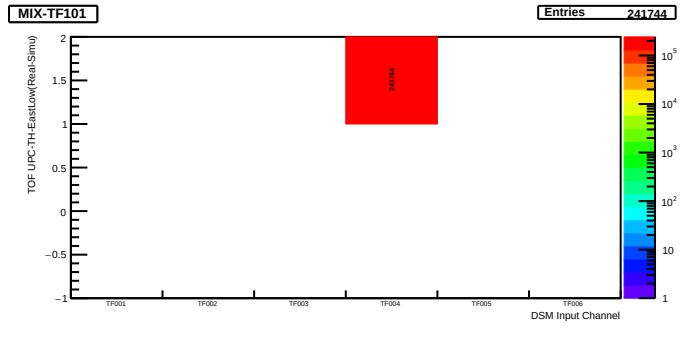

MIX-TF101
Entries 4358526
TOF UPC-TH-WestLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

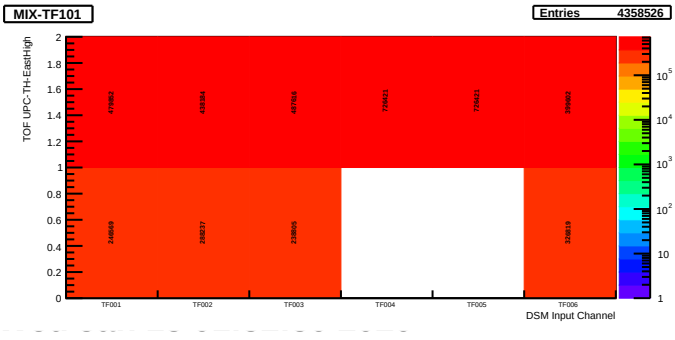

MIX-TF101
Entries 0
TOF UPC-TH-WestLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

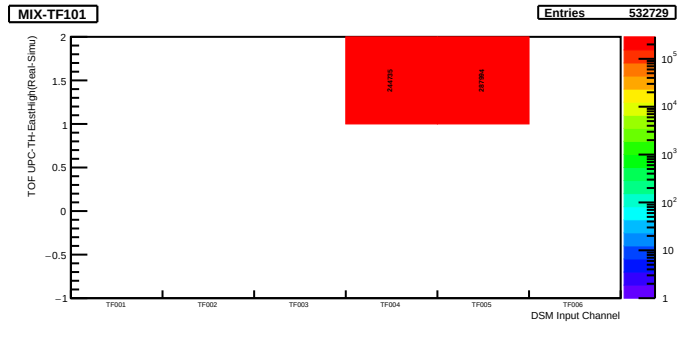

MIX-TF101
Entries 4358526
TOF UPC-TH-WestHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

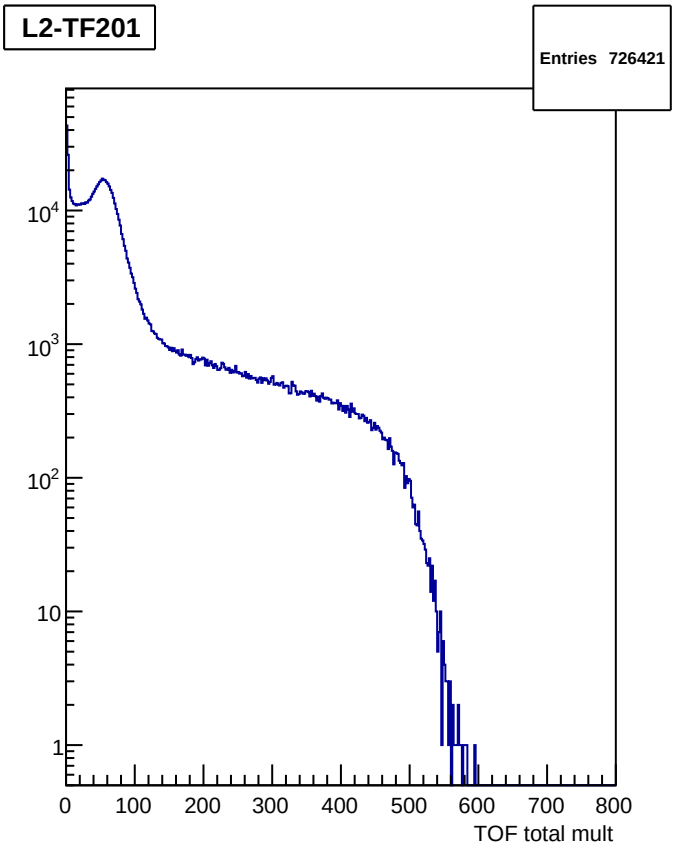

MIX-TF101
Entries 0
TOF UPC-TH-WestHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

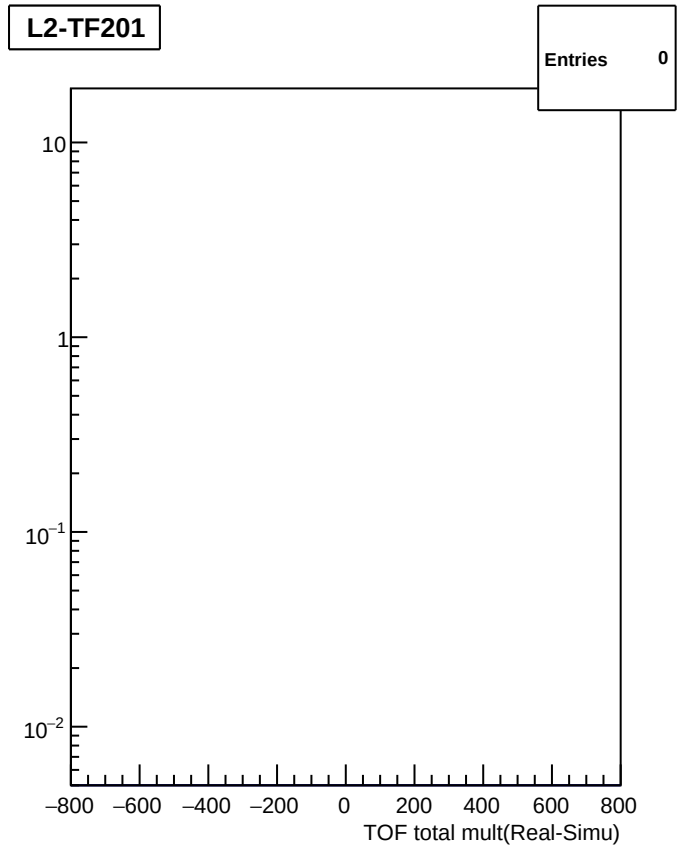

MIX-TF101
Entries 4358526
TOF UPC-TH-EastLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

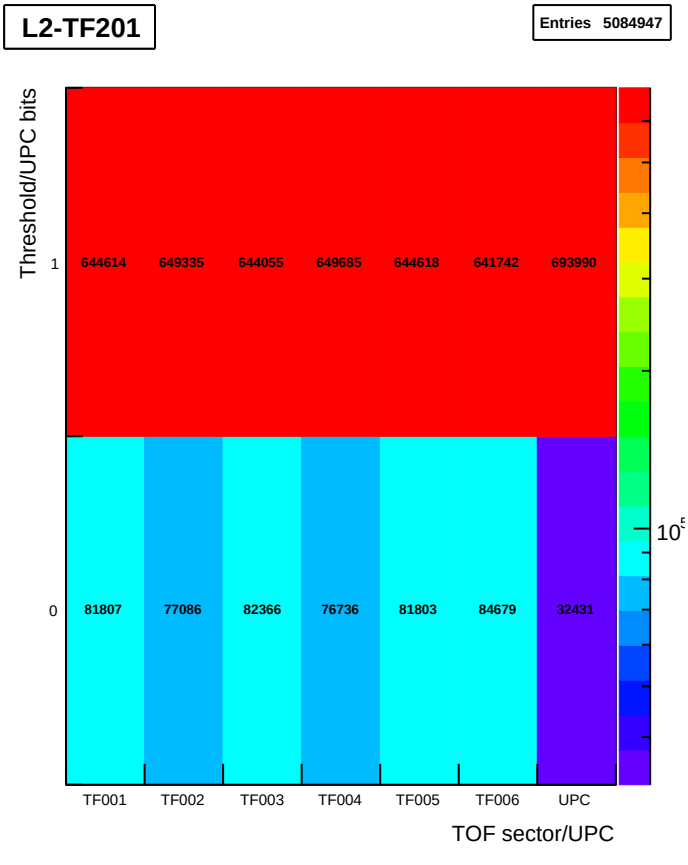

MIX-TF101
Entries 241744
TOF UPC-TH-EastLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

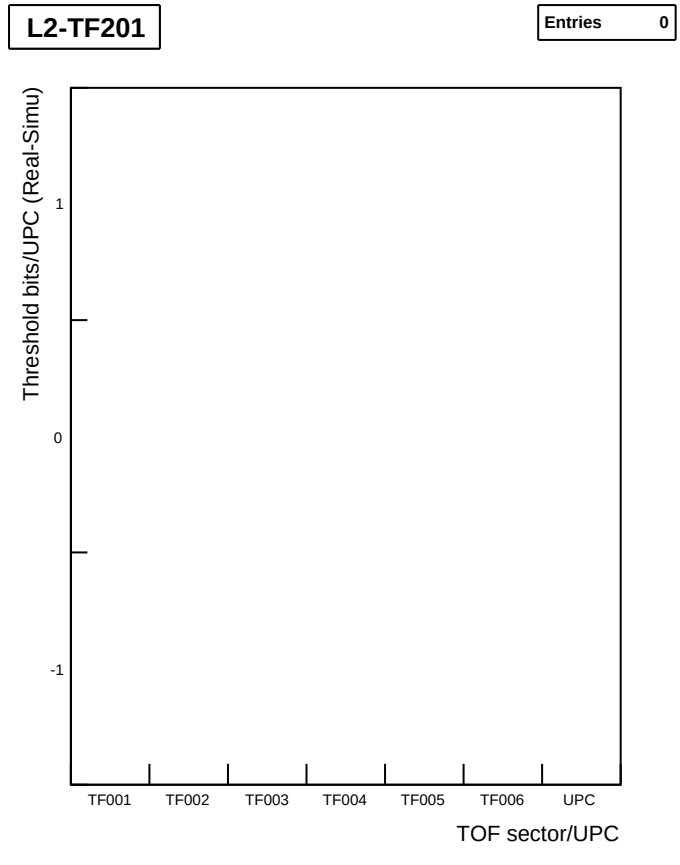

MIX-TF101
Entries 4358526
TOF UPC-TH-EastHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

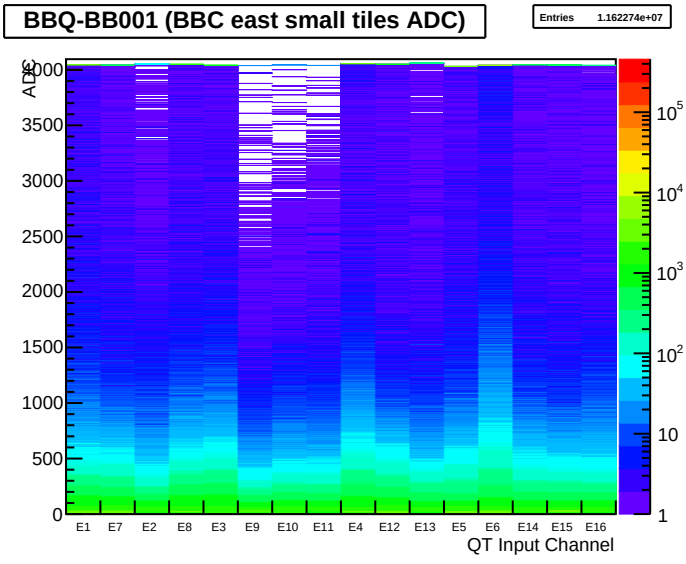

MIX-TF101
Entries 532729
TOF UPC-TH-EastHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

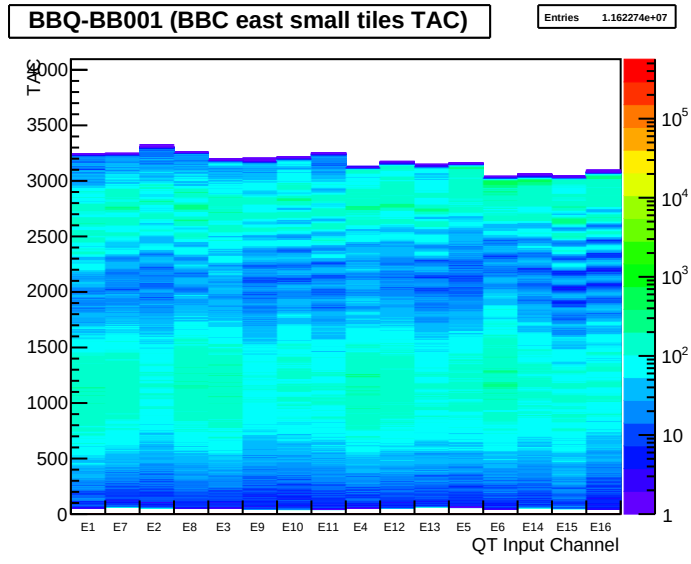

L2-TF201
Entries 726421
TOF total mult

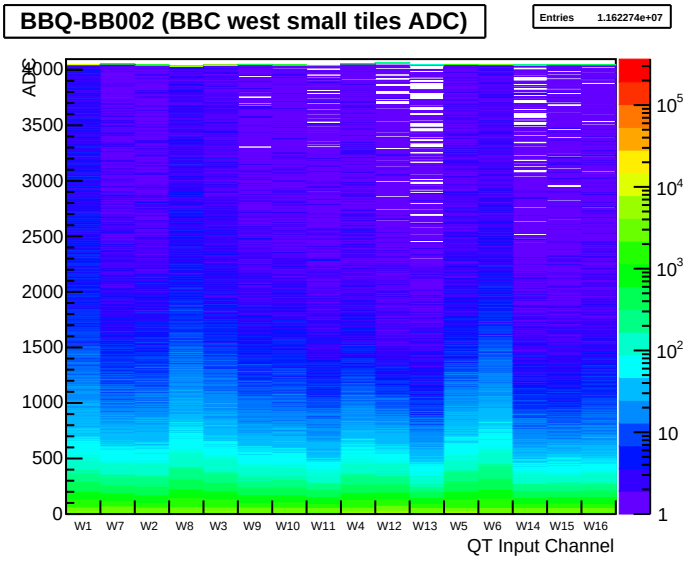

L2-TF201
Entries 0
TOF total mult(Real-Simu)

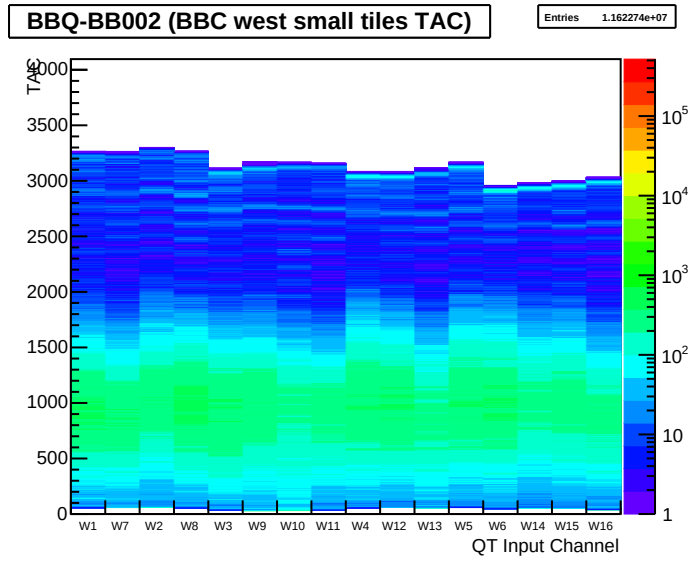

L2-TF201
Entries 5084947
Threshold/UPC bits
644614
649335
644055
649685
644618
641742
693990
81807
77086
82366
76736
81803
84679
32431
TF001
TF002
TF003
TF004
TF005
TF006
UPC
TOF sector/UPC

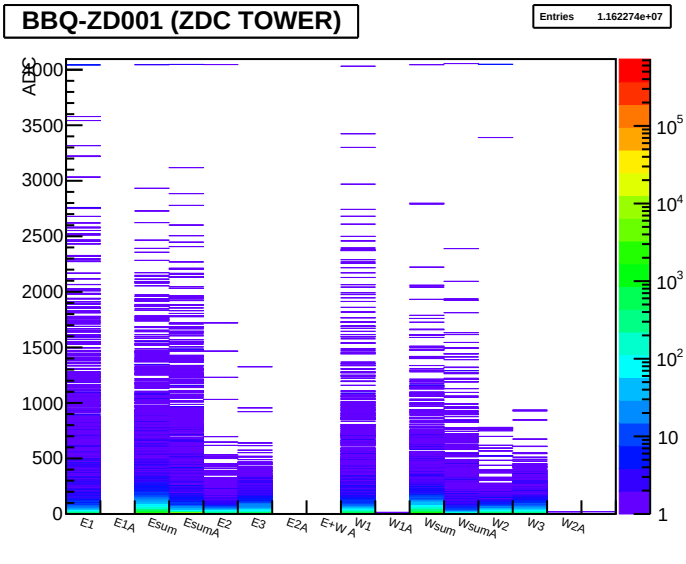

L2-TF201
Entries 0
Threshold bits/UPC (Real-Simu)
TF001
TF002
TF003
TF004
TF005
TF006
UPC
TOF sector/UPC

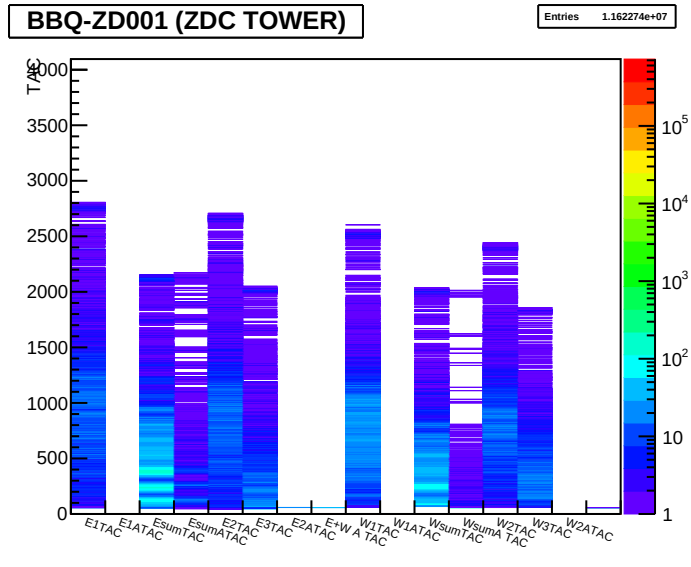

BBQ-BB001 (BBC east small tiles ADC)
Entries 1.162274e+07
ADC
QT Input Channel
E1 E7 E2 E8 E3 E9 E10 E11 E4 E12 E13 E5 E6 E14 E15 E16

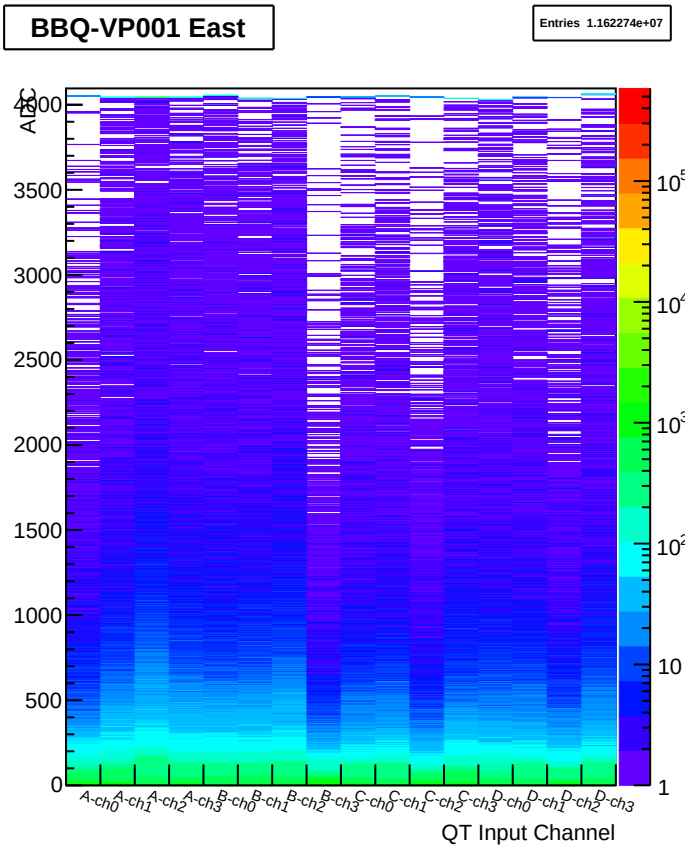

BBQ-BB001 (BBC east small tiles TAC)
Entries 1.162274e+07
TAC
QT Input Channel
E1 E7 E2 E8 E3 E9 E10 E11 E4 E12 E13 E5 E6 E14 E15 E16

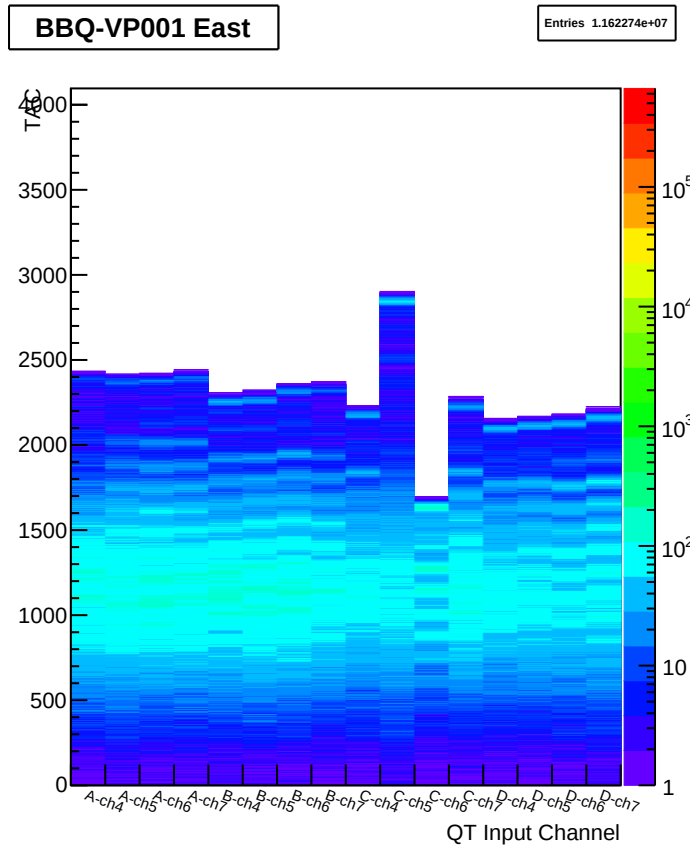

BBQ-BB002 (BBC west small tiles ADC)
Entries 1.162274e+07
ADC
QT Input Channel
W1 W7 W2 W8 W3 W9 W10 W11 W4 W12 W13 W5 W6 W14 W15 W16

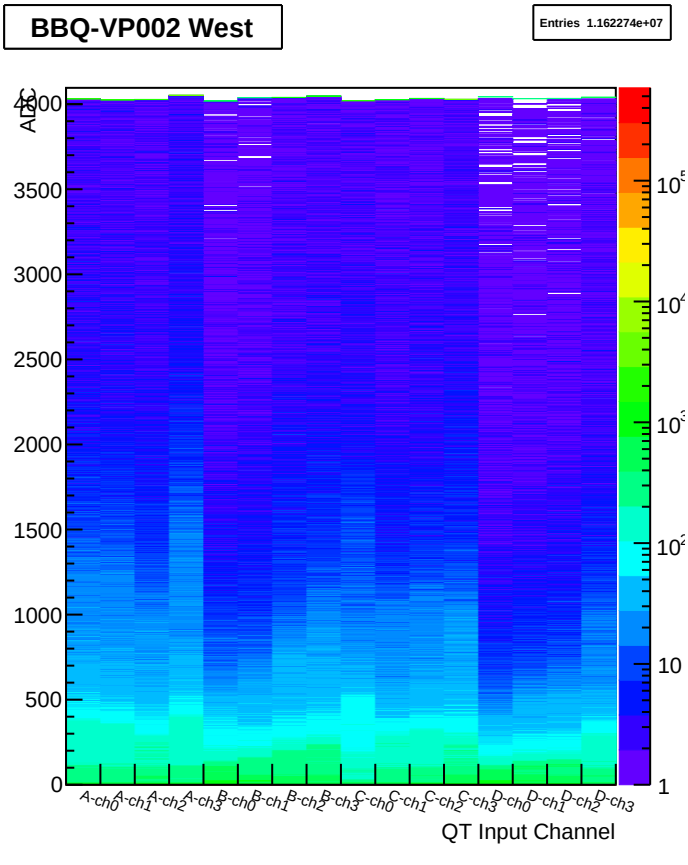

BBQ-BB002 (BBC west small tiles TAC)
Entries 1.162274e+07
TAC
QT Input Channel
W1 W7 W2 W8 W3 W9 W10 W11 W4 W12 W13 W5 W6 W14 W15 W16

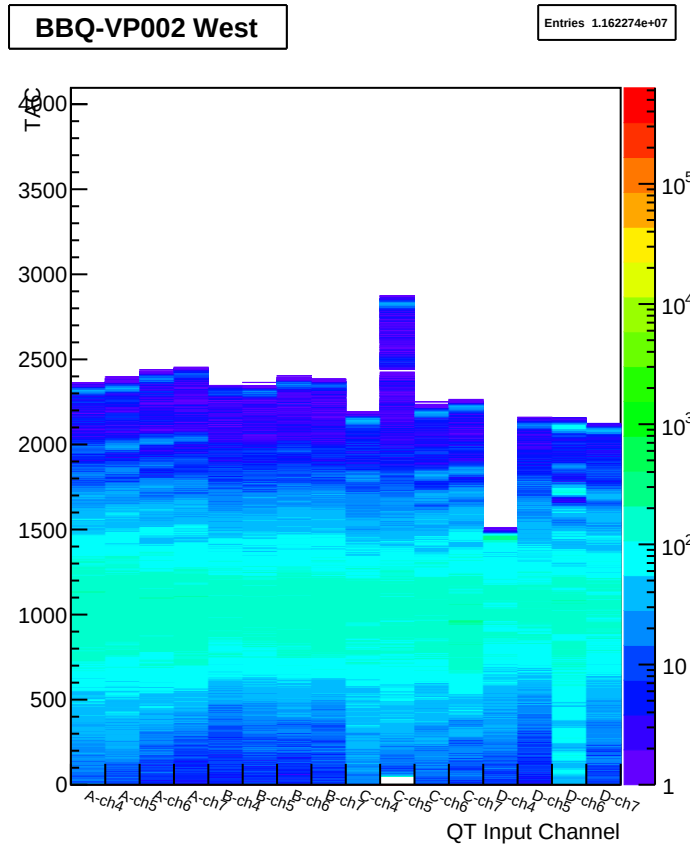

BBQ-ZD001 (ZDC TOWER)
Entries 1.162274e+07
ADC
E1 E1A Esum EsumA E2 E3 E2A E+W A W1 W1A Wsum WsumA W2 W3 W2A

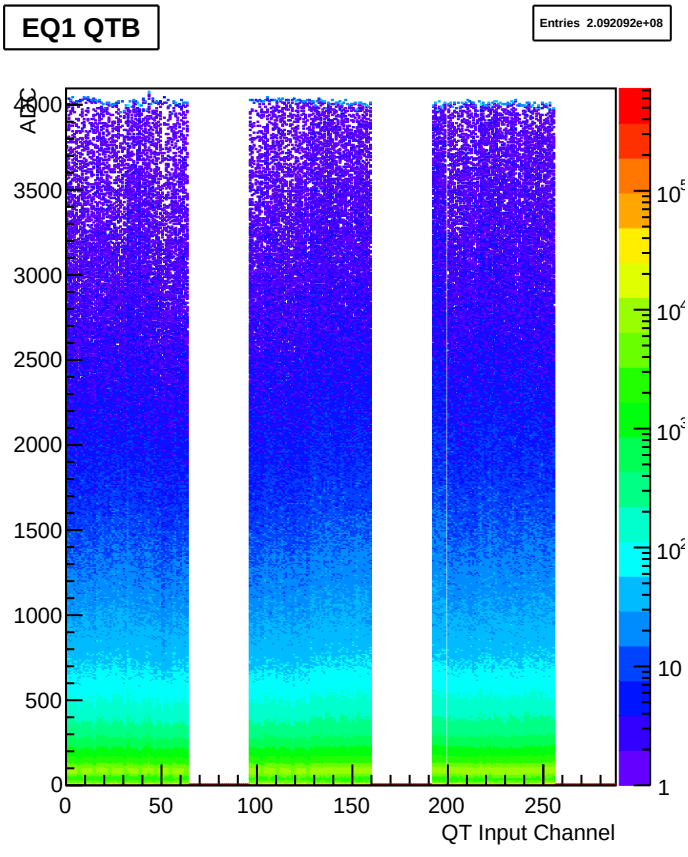

BBQ-ZD001 (ZDC TOWER)
Entries 1.162274e+07
TAC
E1TAC E1ATAC EsumTAC EsumATAC E2TAC E3TAC E2ATAC E+W A TAC W1TAC W1ATAC WsumTAC WsumA TAC W2TAC W3TAC W2ATAC

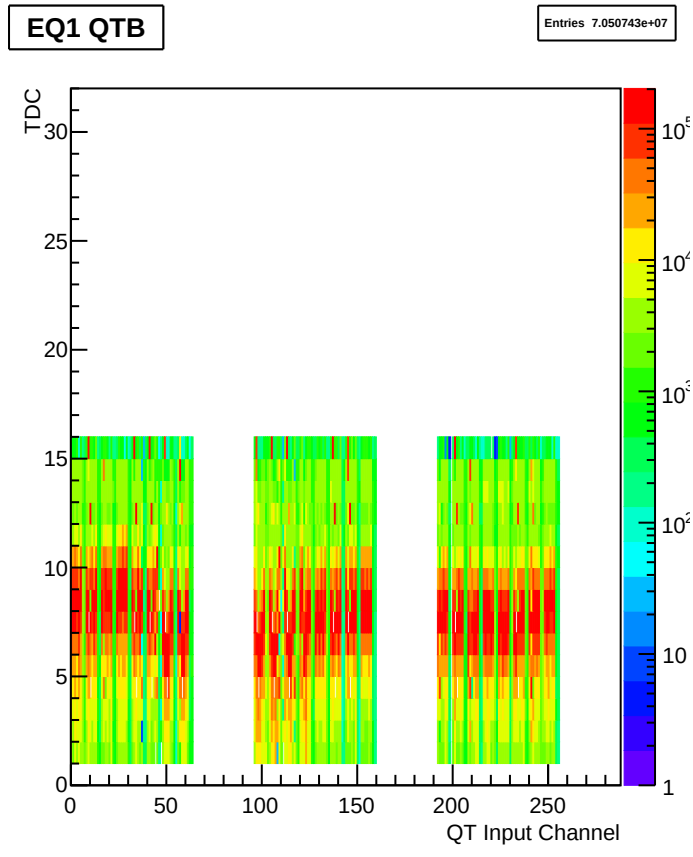

BBQ-VP001 East
Entries 1.162274e+07
ADC
QT Input Channel

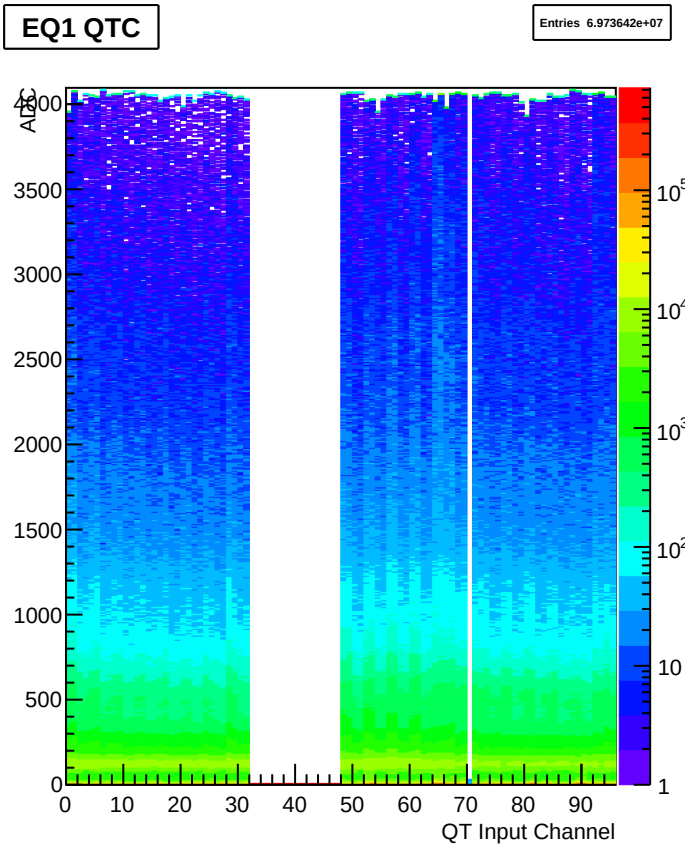

BBQ-VP001 East
Entries 1.162274e+07
TADC
QT Input Channel

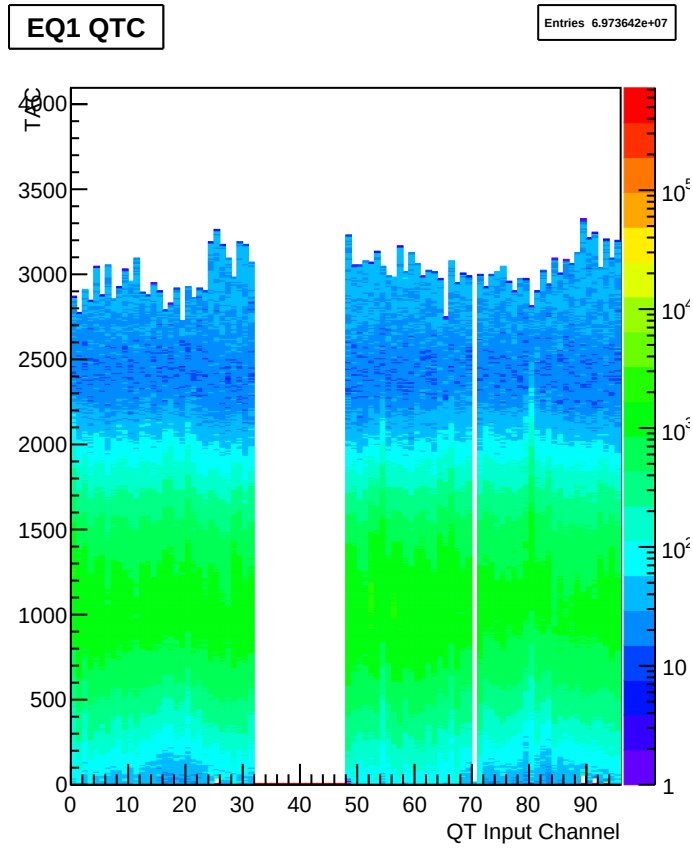

BBQ-VP002 West
Entries 1.162274e+07
ADC
QT Input Channel

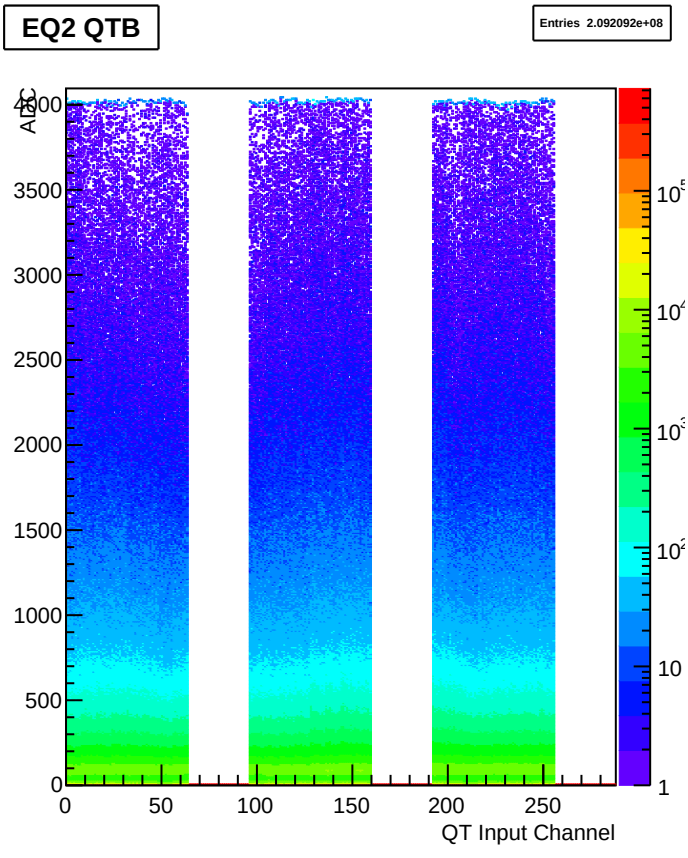

BBQ-VP002 West
Entries 1.162274e+07
TADC
QT Input Channel

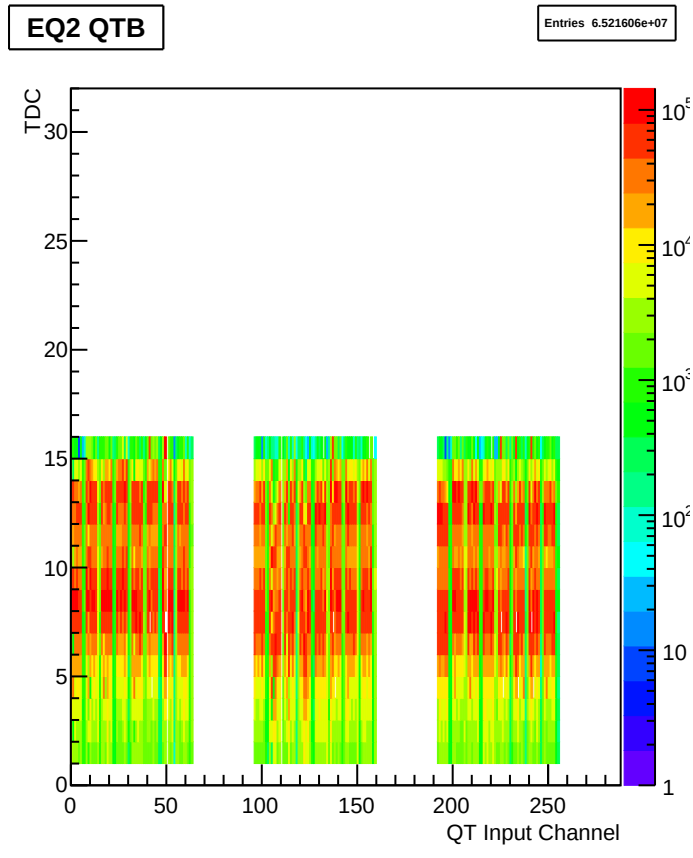

EQ1 QTB
Entries 2.092092e+08
ADC
QT Input Channel

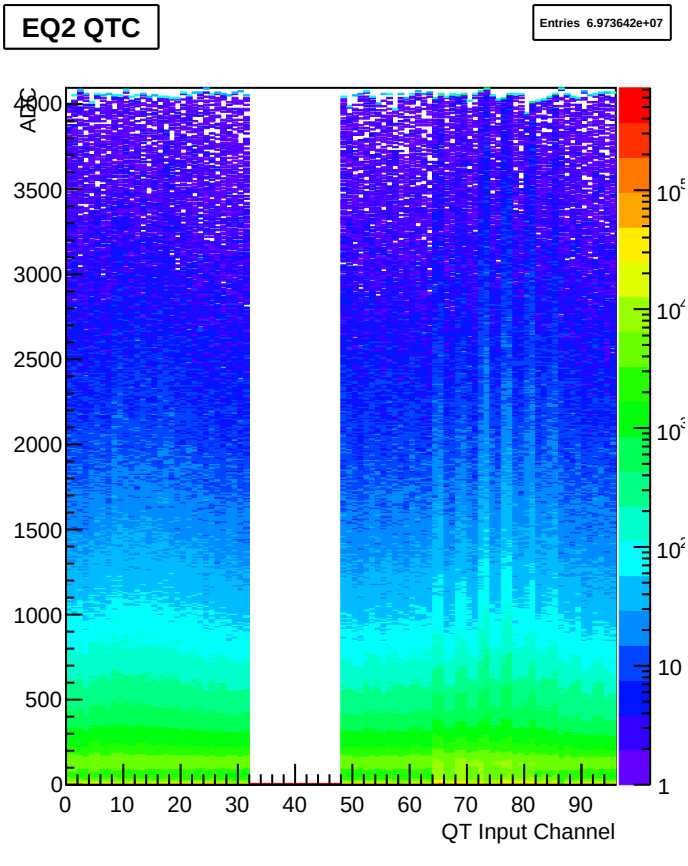

EQ1 QTB
Entries 7.050743e+07
TDC
QT Input Channel

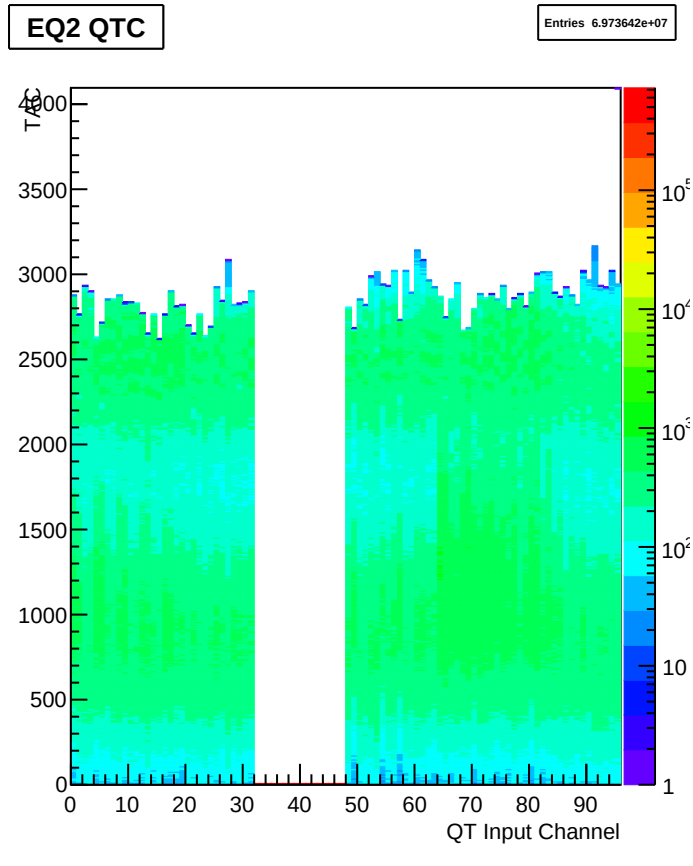

EQ1 QTC
Entries 6.973642e+07
ADC
QT Input Channel

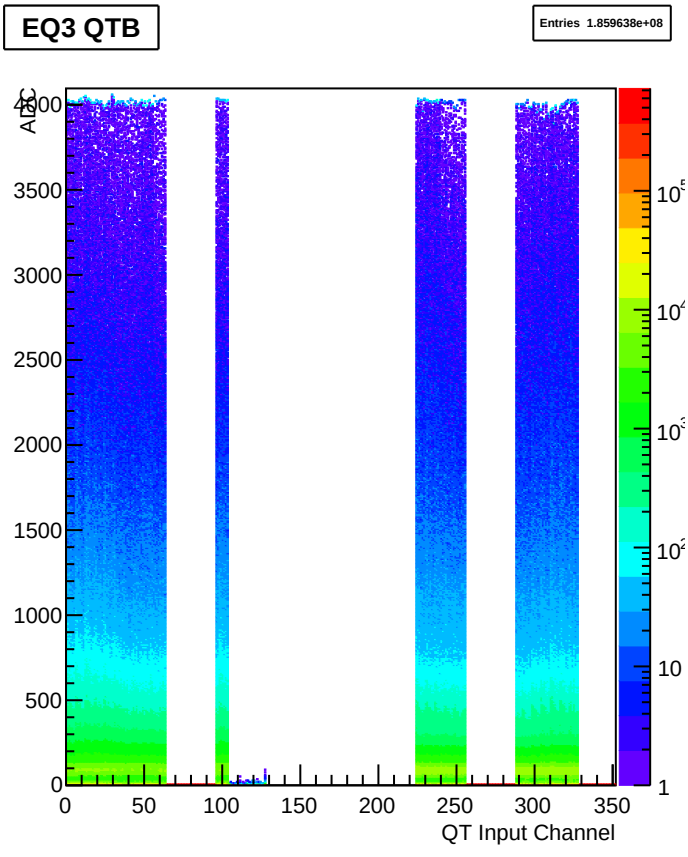

EQ1 QTC
Entries 6.973642e+07
TADC
QT Input Channel

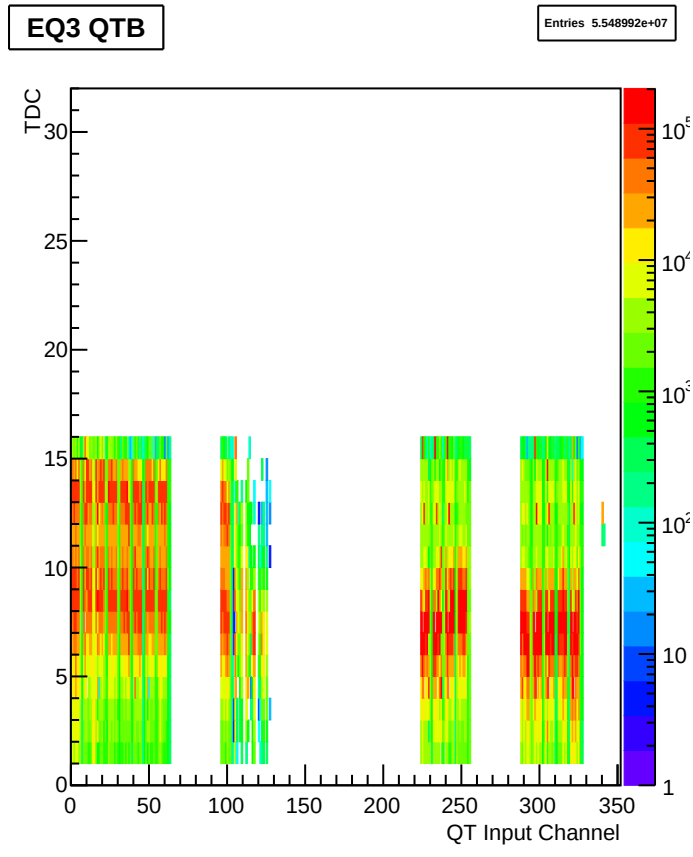

EQ2 QTB
Entries 2.092092e+08
ADC
QT Input Channel

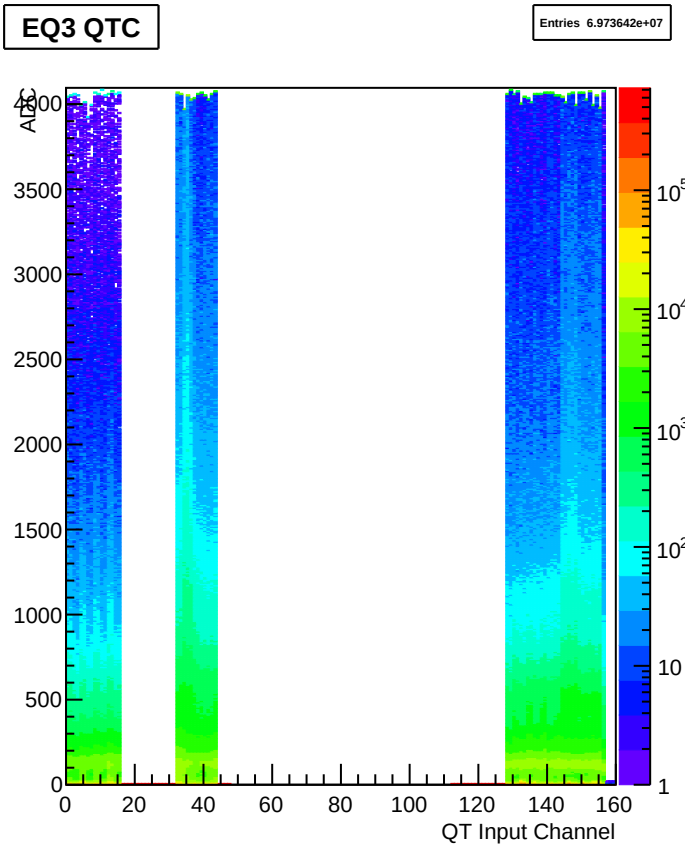

EQ2 QTB
Entries 6.521606e+07
TDC
QT Input Channel

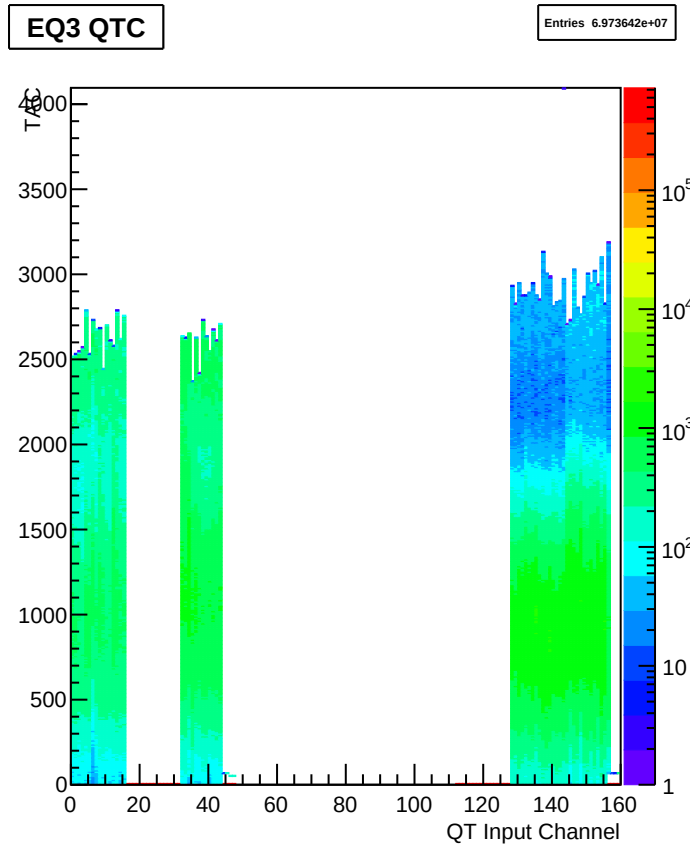

EQ2 QTC
Entries 6.973642e+07
ADC
QT Input Channel


EQ2 QTC
Entries 6.973642e+07
TADC
QT Input Channel


EQ3 QTB
Entries 1.859638e+08
ADC
QT Input Channel


EQ3 QTB
Entries 5.548992e+07
TDC
QT Input Channel


EQ3 QTC
Entries 6.973642e+07
ADC
QT Input Channel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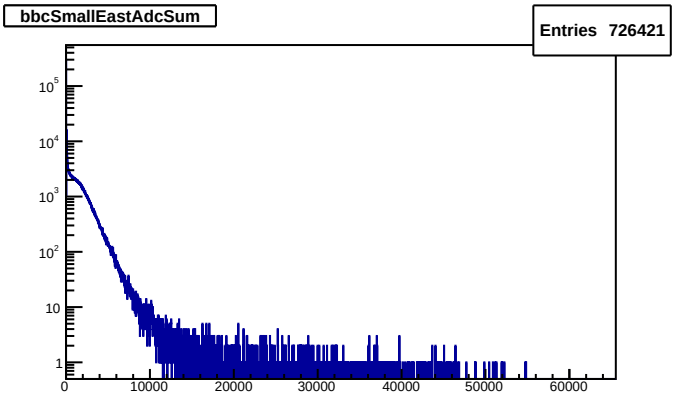
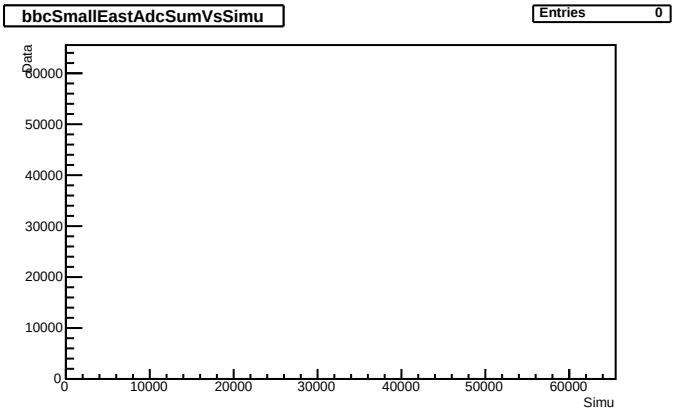
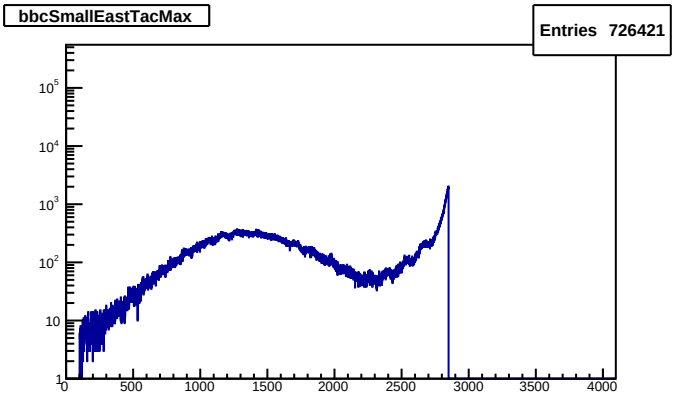
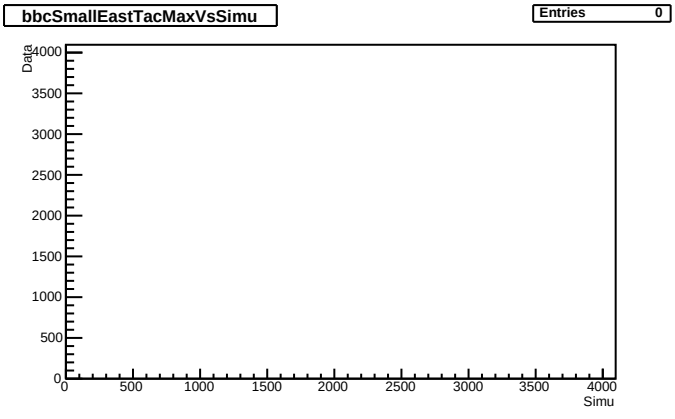
QT Input Channel


bbcSmallEastAdcSum
Entries 726421


bbcSmallEastAdcSumVsSimu
Entries 0
Data
Simu


bbcSmallEastTacMax
Entries 726421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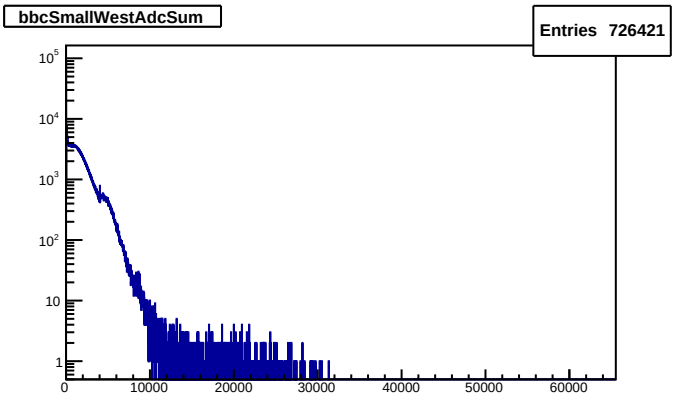
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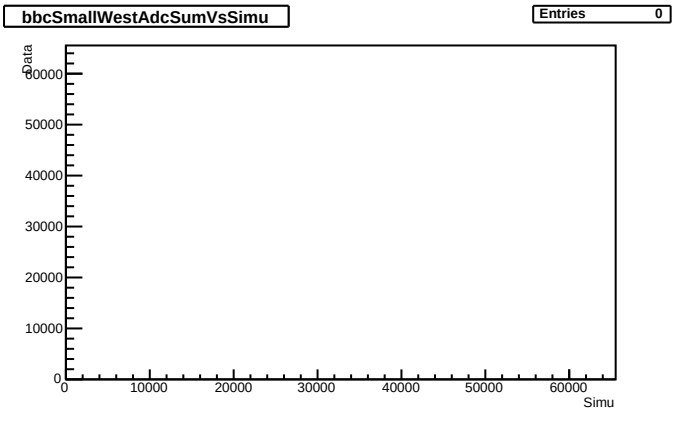
Entries 726421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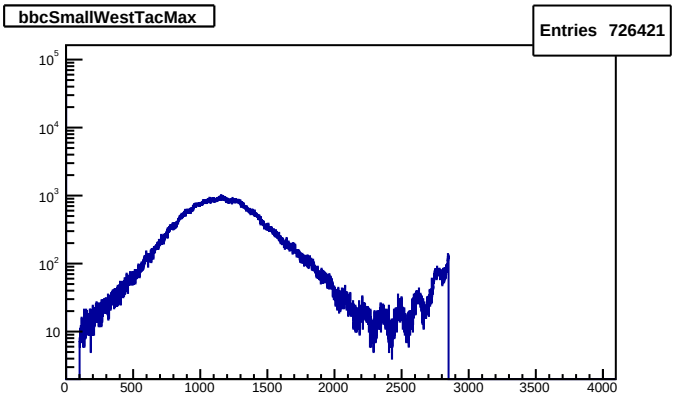
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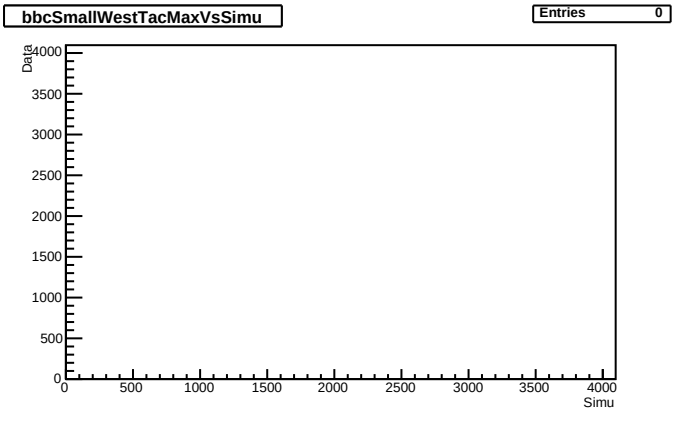
Entries 726421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Entries 0
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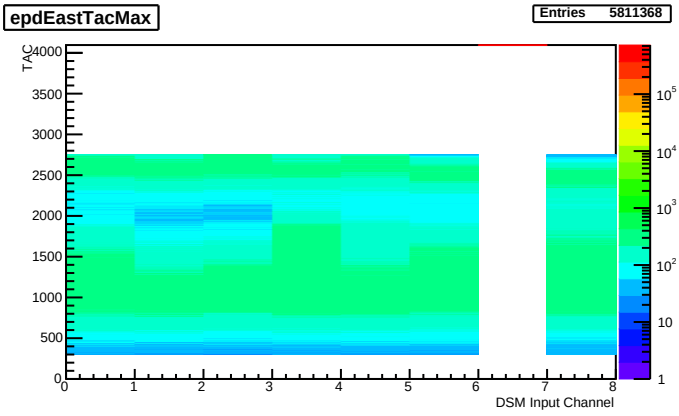
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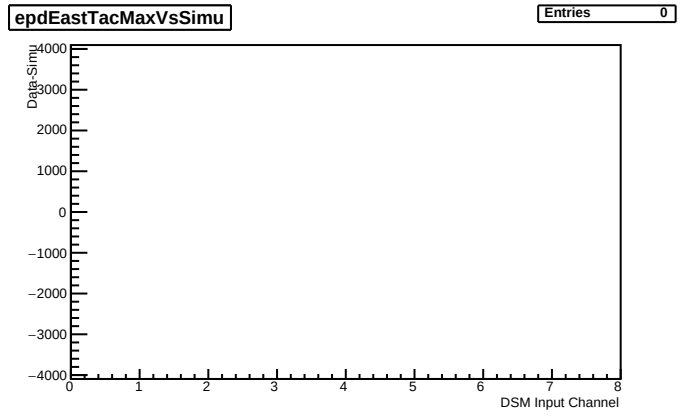
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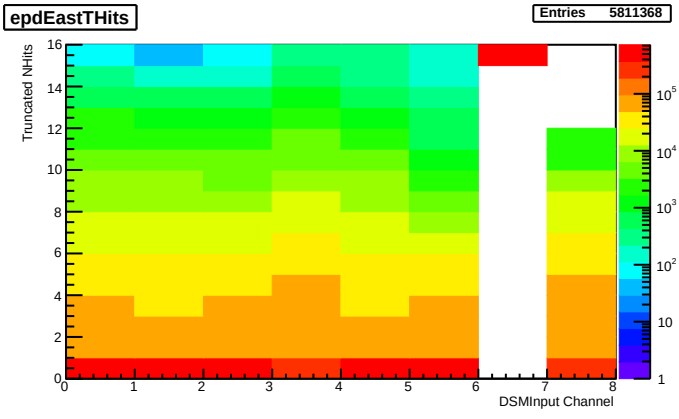
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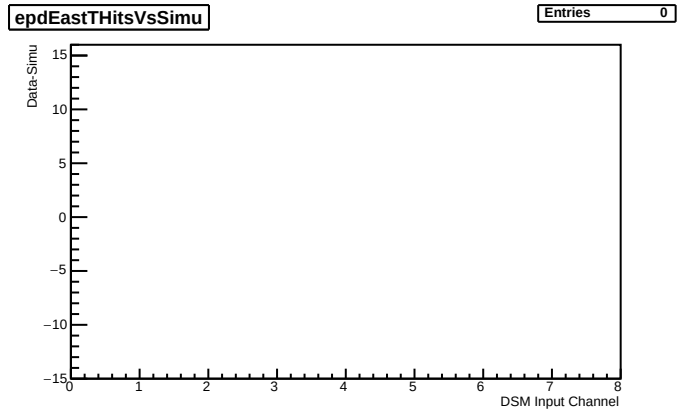
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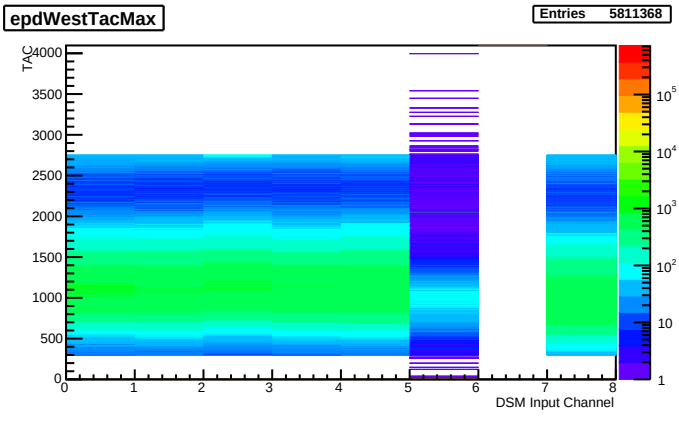
DSM Input Channel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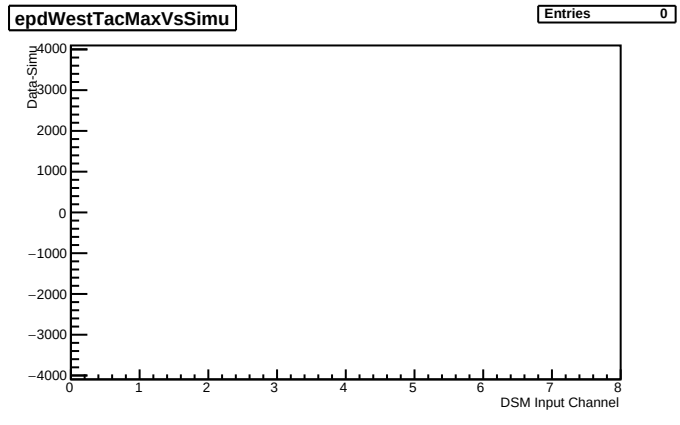
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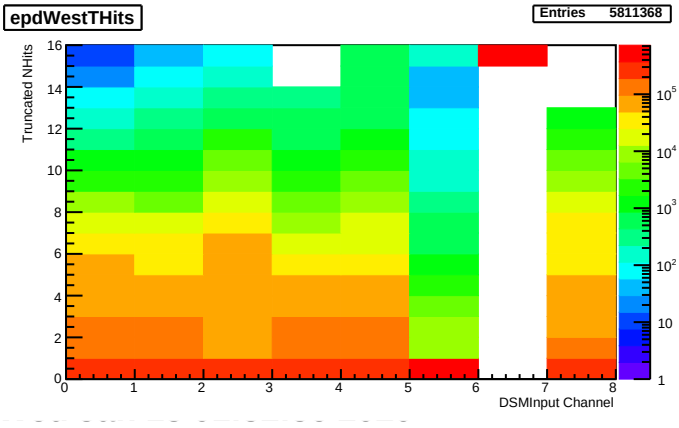
DSM Input Channel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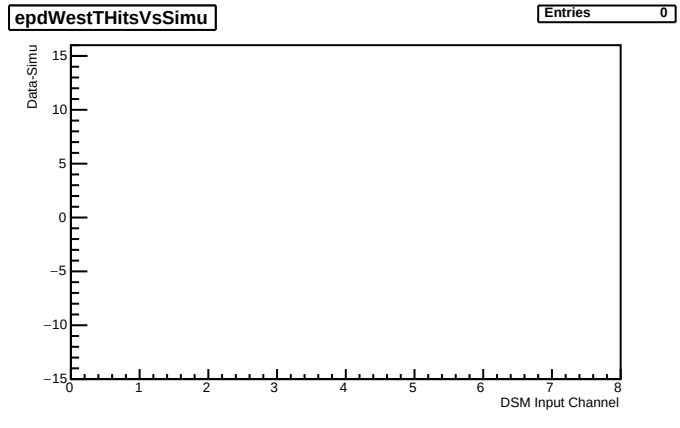
Entries 5811368
Truncated NHits
DSMInput Channel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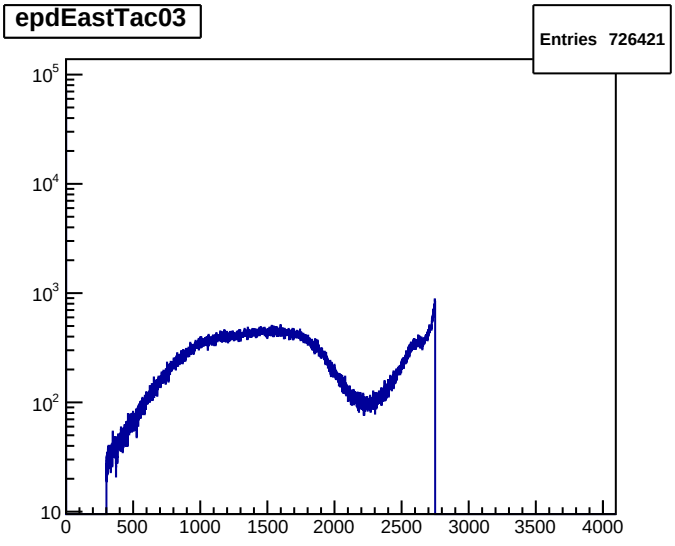
DSM Input Channel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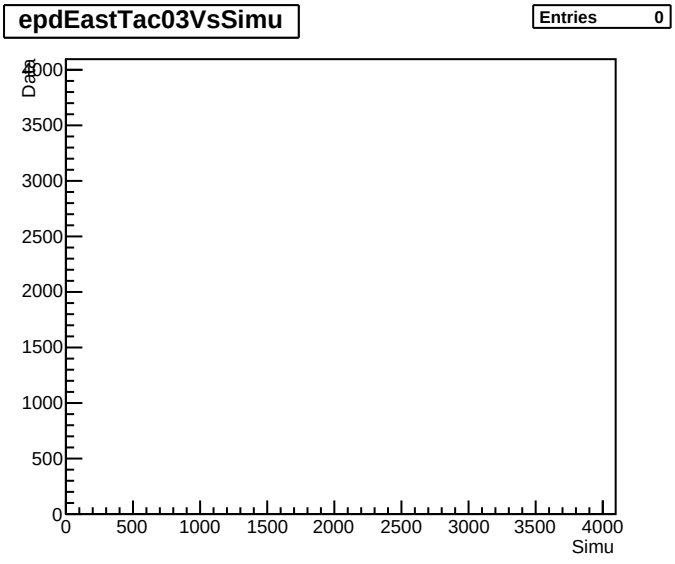
Entries 726421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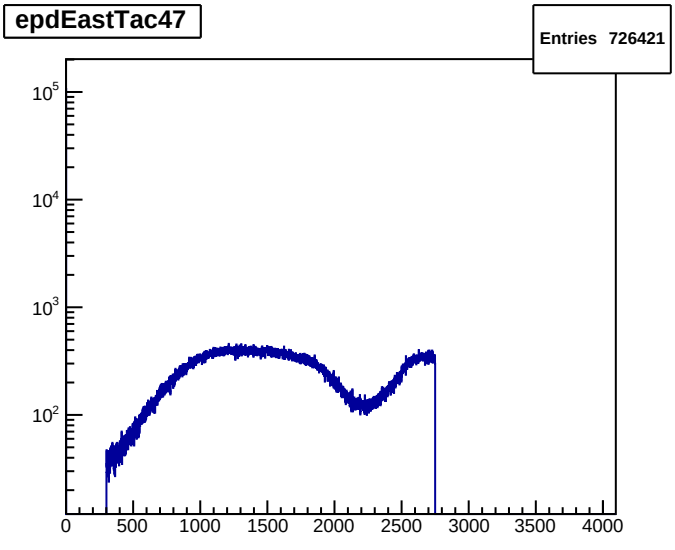
Entries 0
Data
Simu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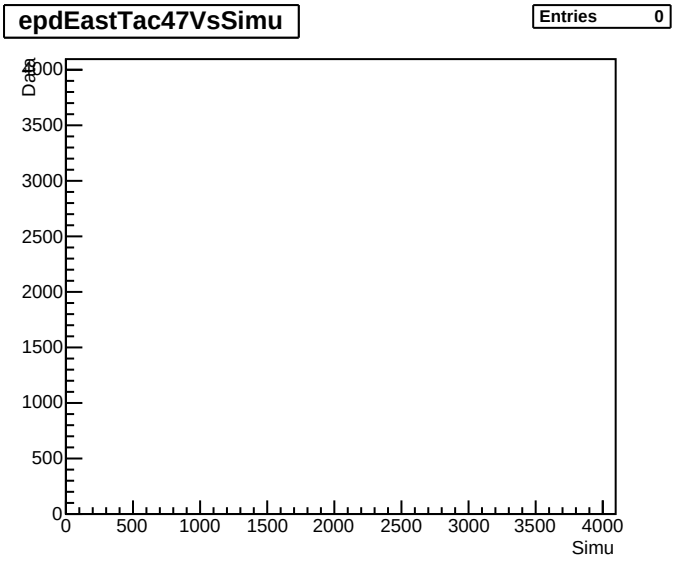
Entries 726421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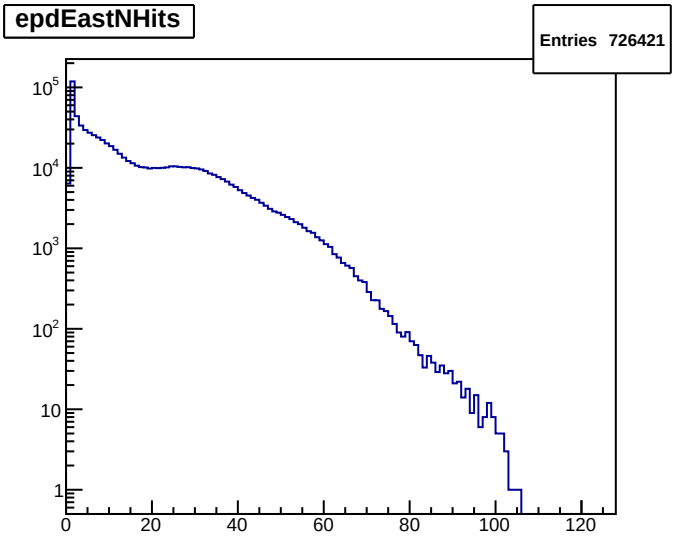
Entries 0
Data
Simu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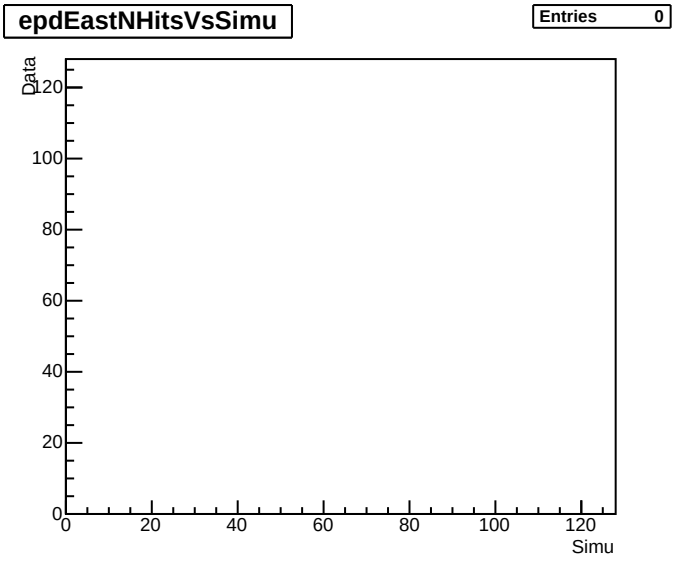
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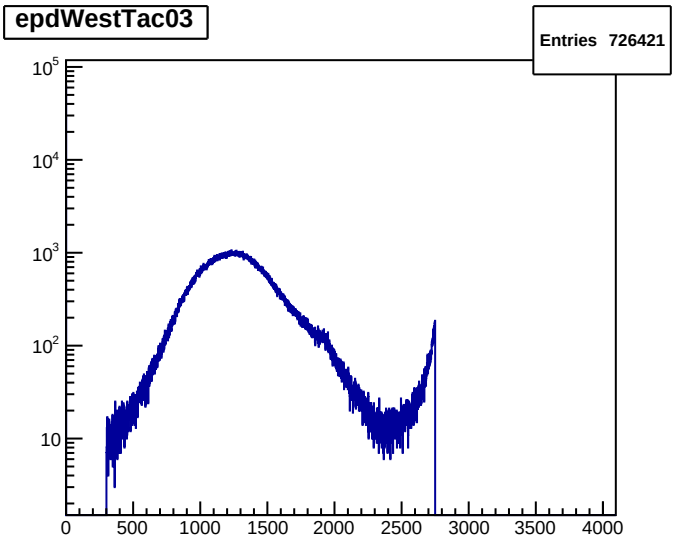
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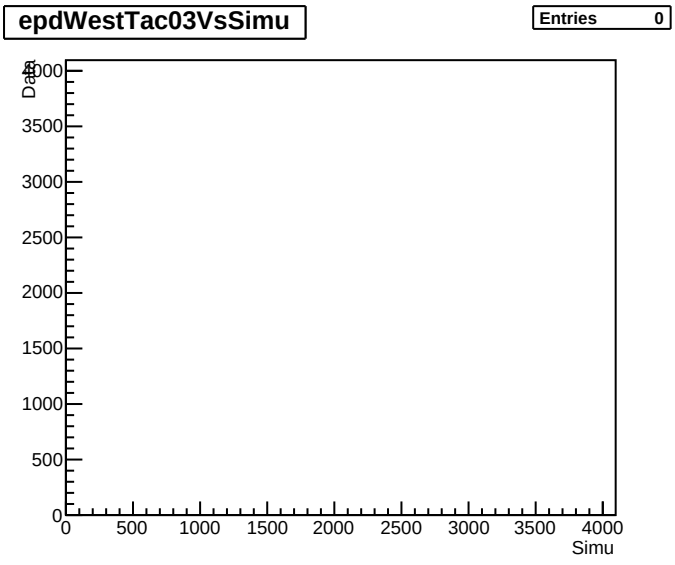
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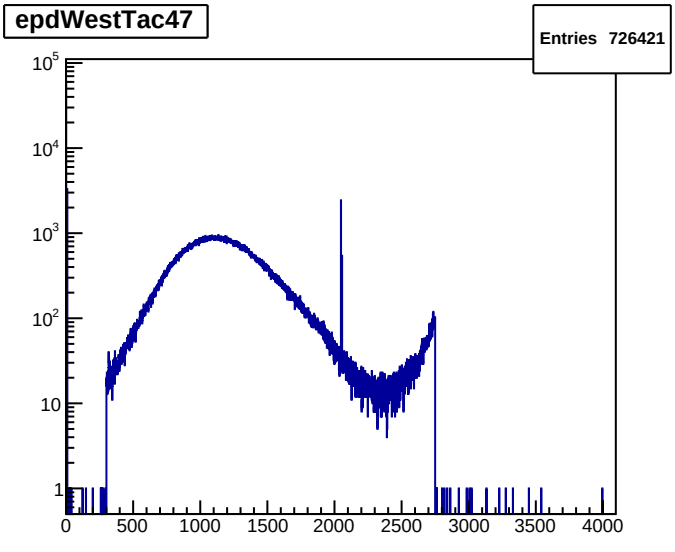
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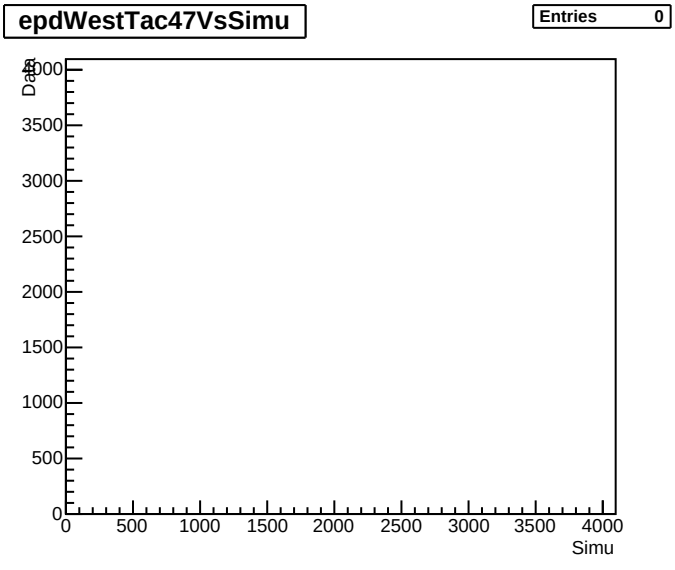
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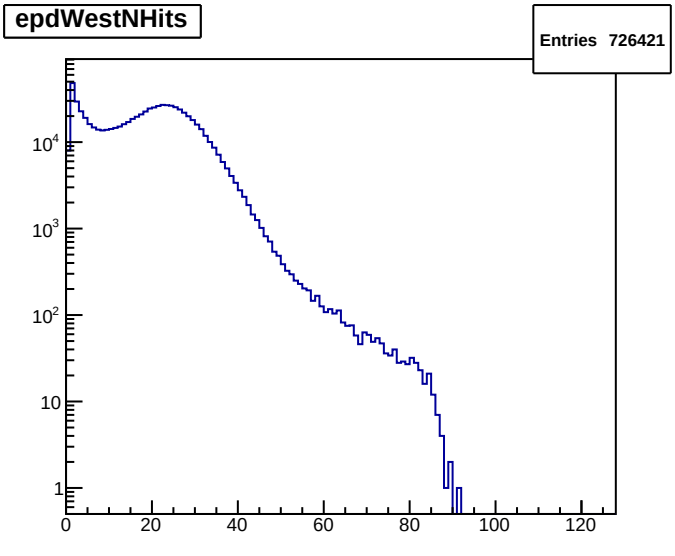
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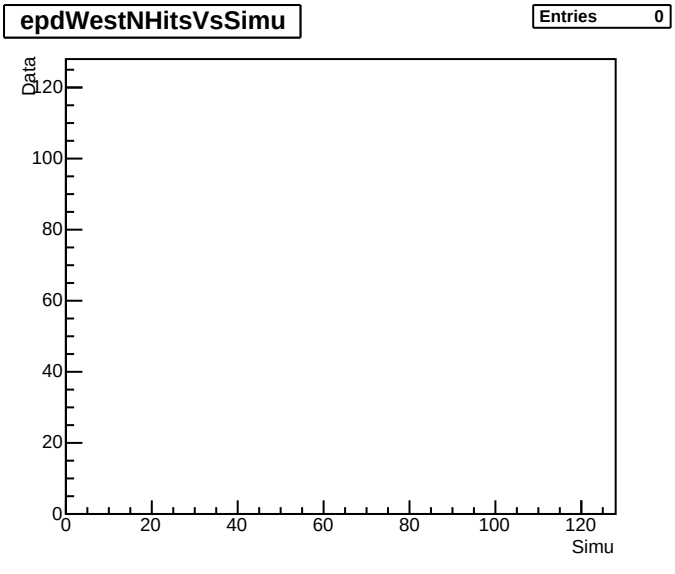
Entries 726421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Entries 0
Data
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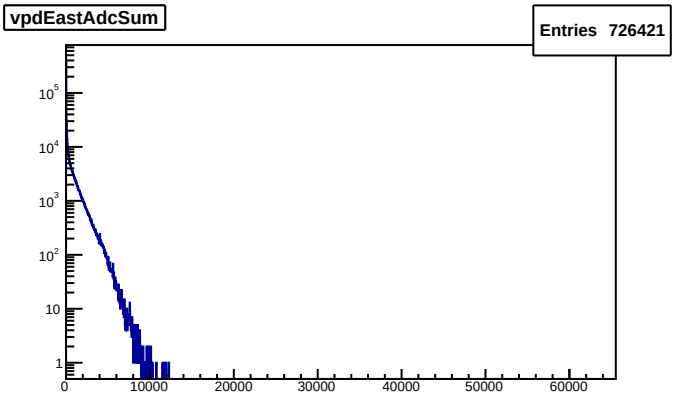
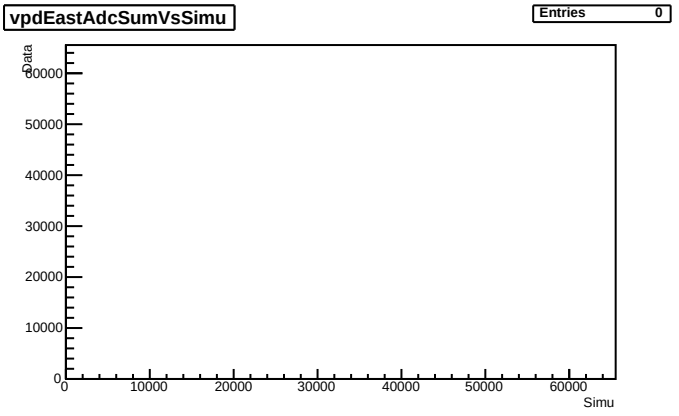

vpdEastAdcSum
Entries 726421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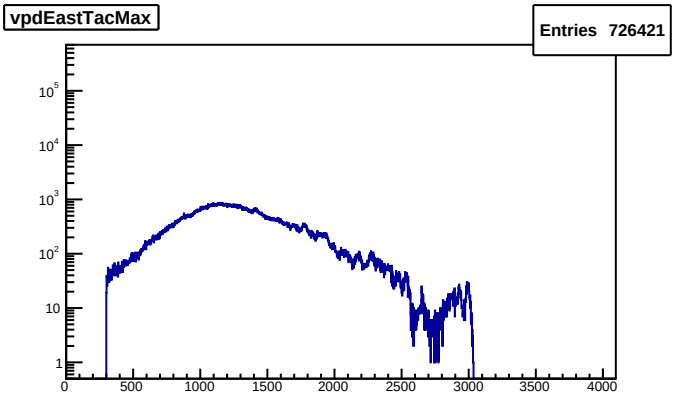
Entries 0
Data
Simu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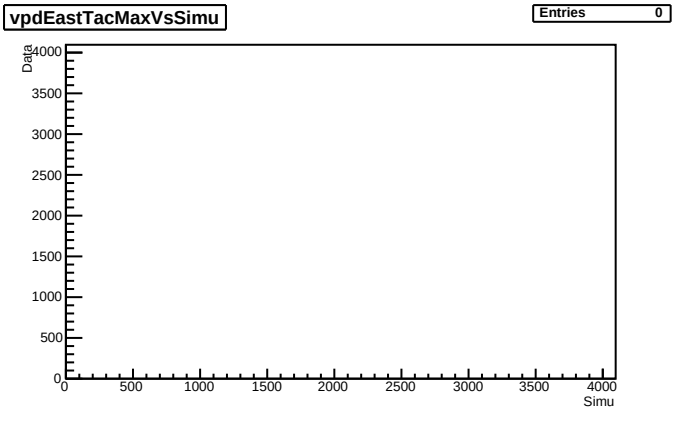
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Data
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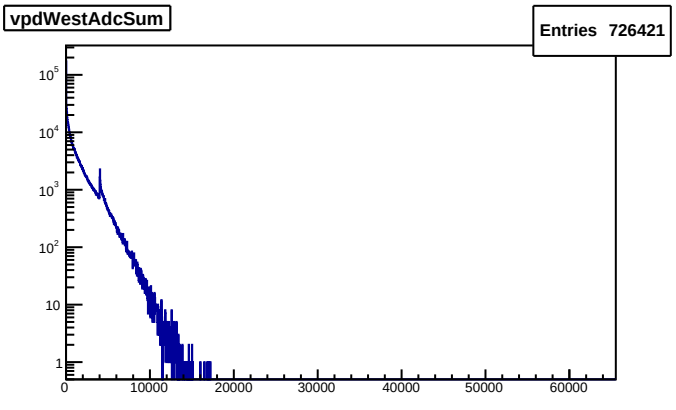
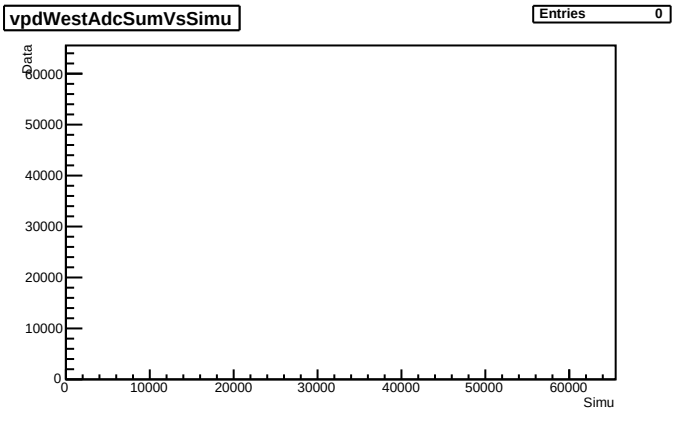

vpdWestAdcSum
Entries 726421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Entries 0
Data
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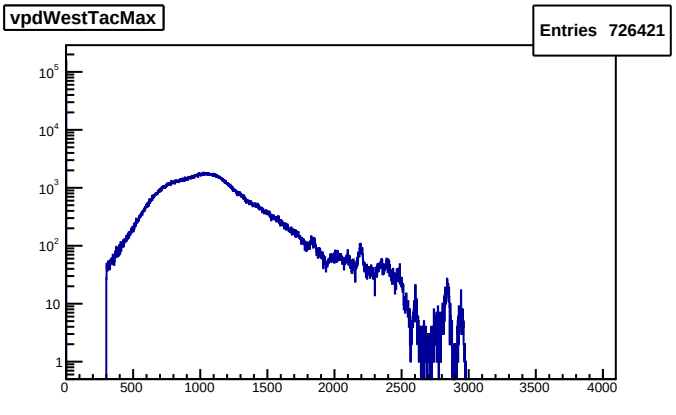
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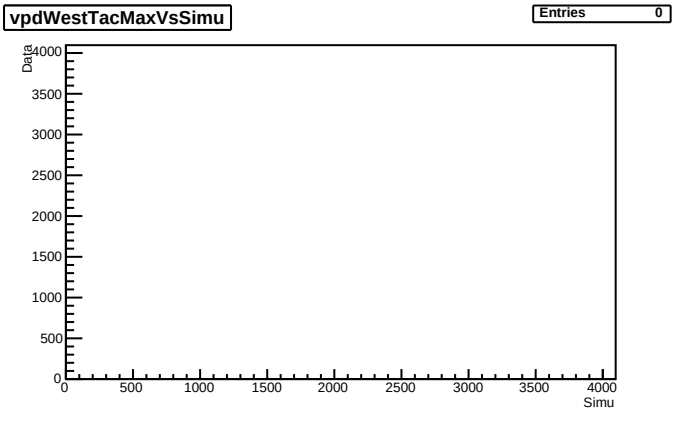
Entries 726421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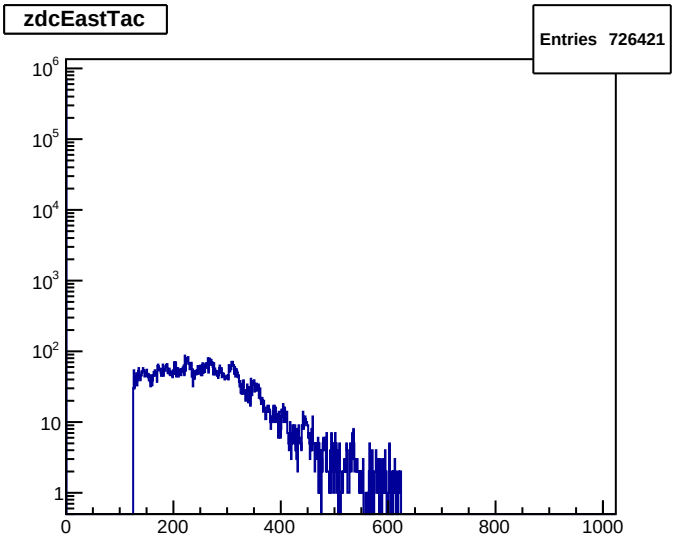
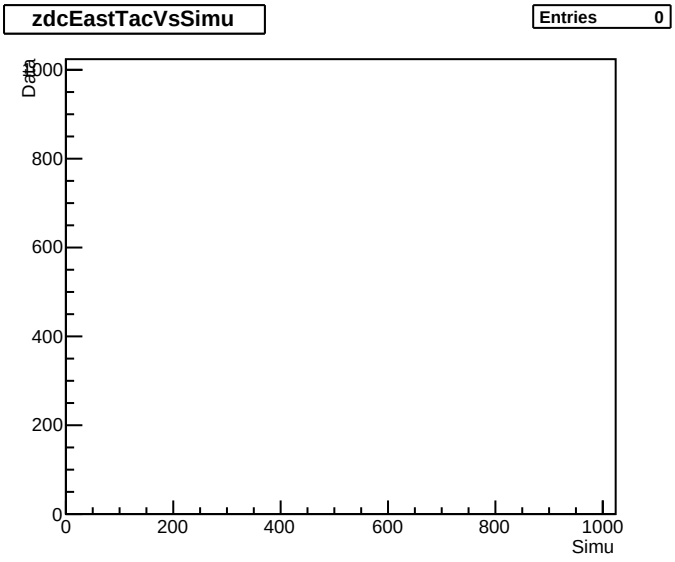
Entries 0
Data
Simu


zdcEastTac
Entries 726421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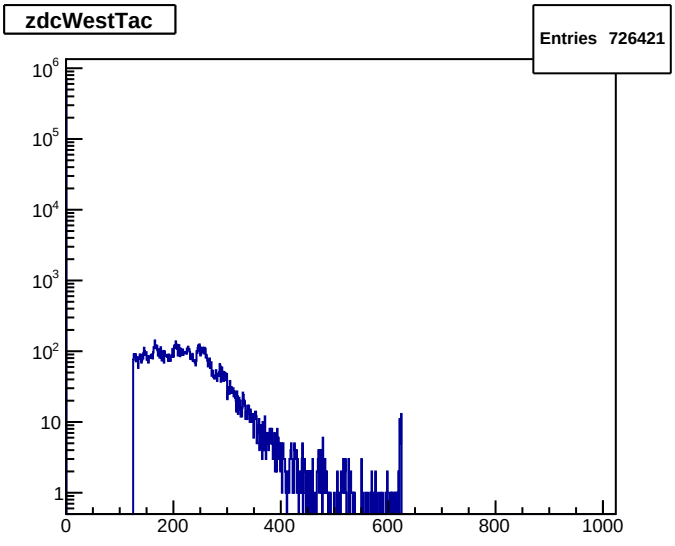
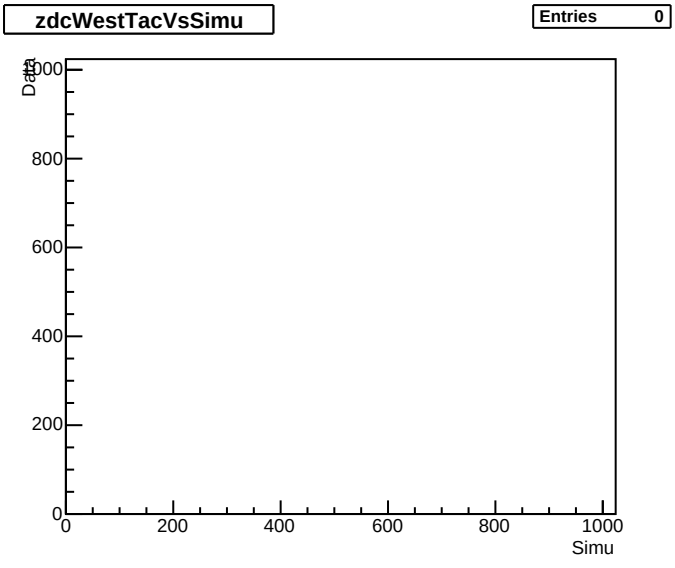
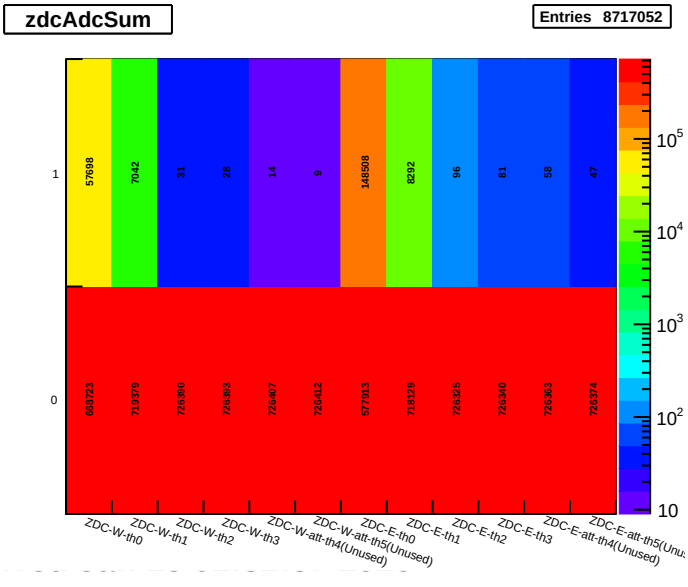
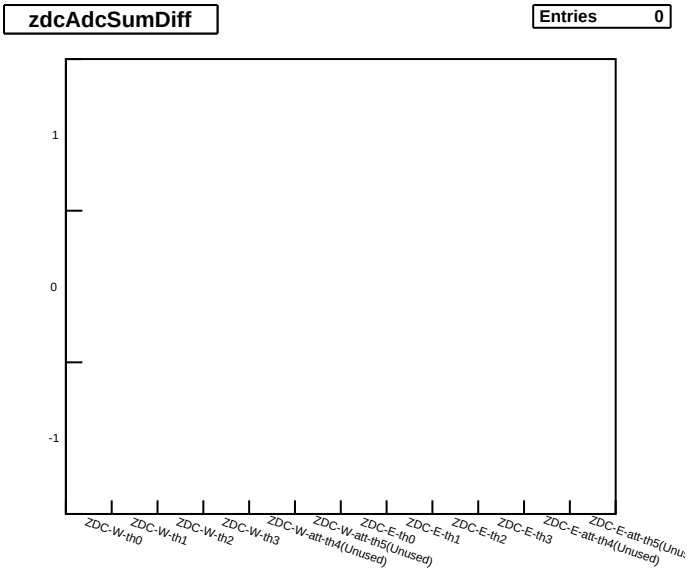
Entries 0
Data
Simu


zdcWestTac
Entries 726421


zdcWestTacVsSimu
Entries 0
Data
Simu


zdcAdcSum
Entries 8717052
57698
7042
31
28
14
9
148508
8292
96
81
58
47
668723
719379
726390
726393
726407
726412
577913
718129
726325
726340
726363
726374
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)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Entries 0
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
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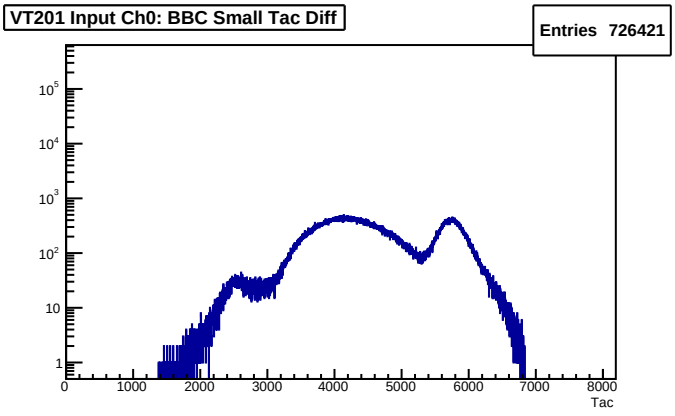
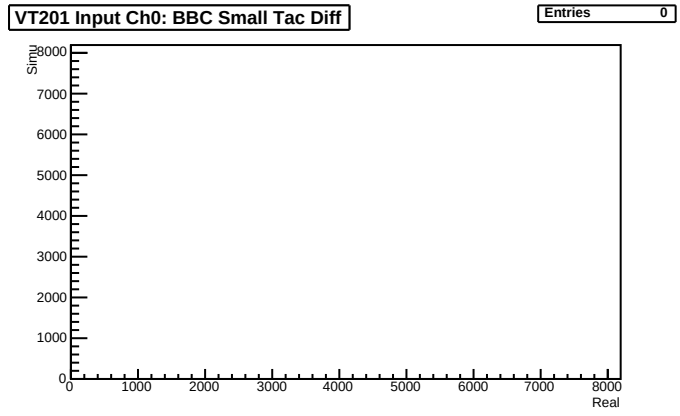
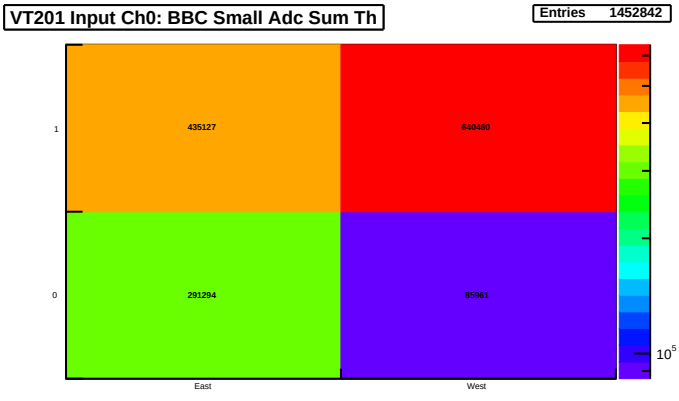
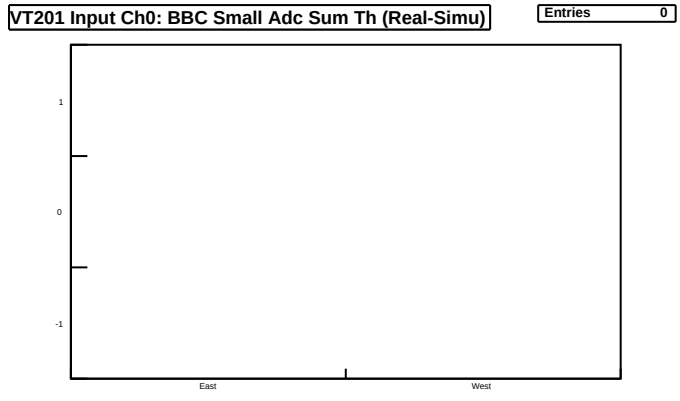
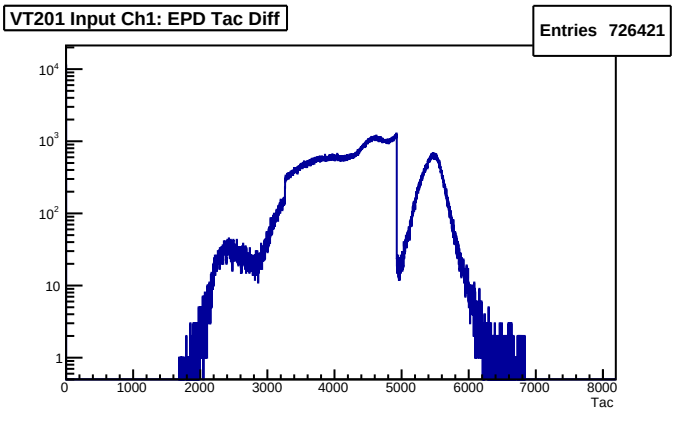
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)

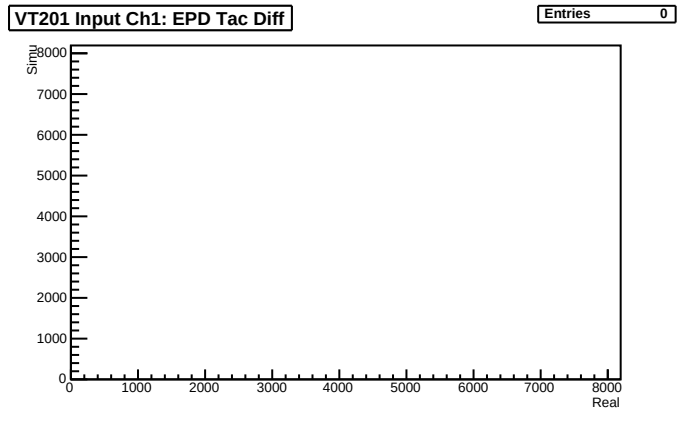

VT201 Input Ch0: BBC Small Tac Diff
Entries 726421
Tac

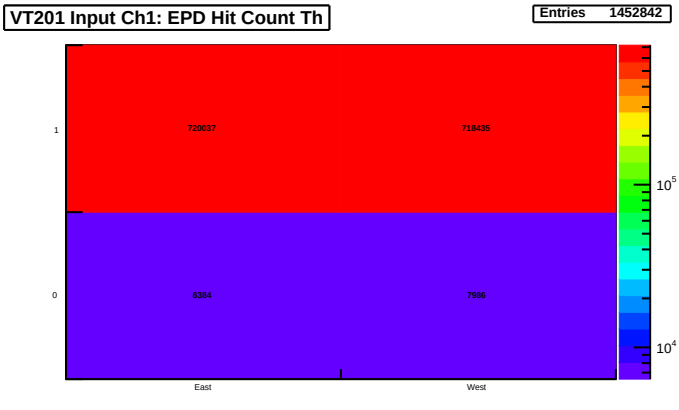

VT201 Input Ch0: BBC Small Tac Diff
Entries 0
Simu
Real

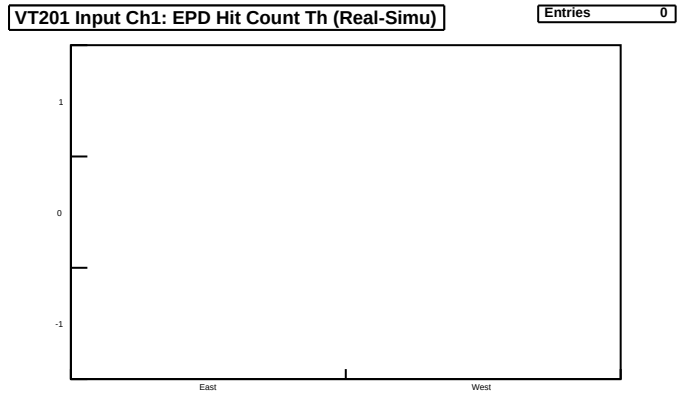

VT201 Input Ch0: BBC Small Adc Sum Th
Entries 1452842
435127
640460
291294
85961
East
West

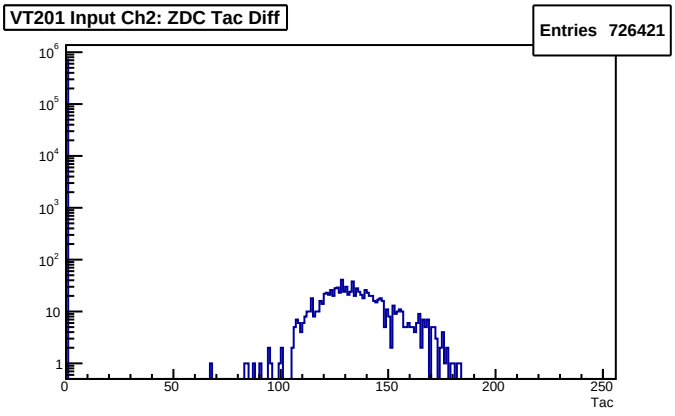

VT201 Input Ch0: BBC Small Adc Sum Th (Real-Simu)
Entries 0
East
West

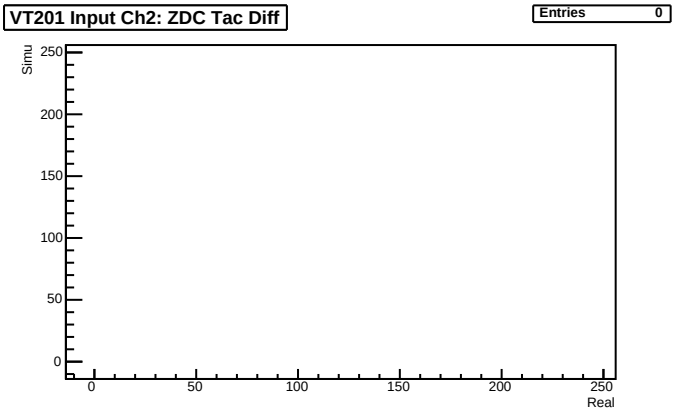

VT201 Input Ch1: EPD Tac Diff
Entries 726421
Tac

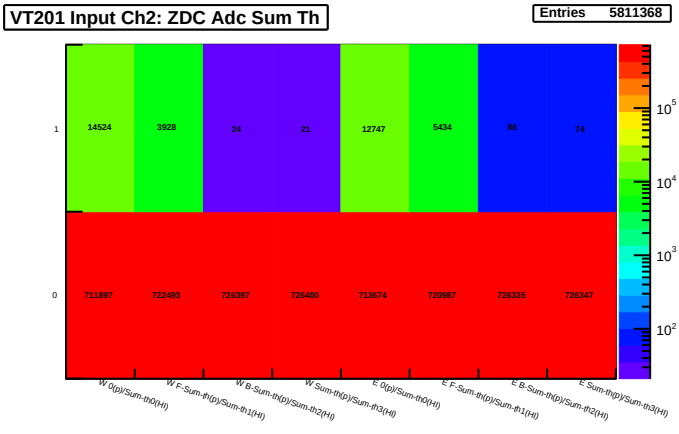

VT201 Input Ch1: EPD Tac Diff
Entries 0
Simu
Real

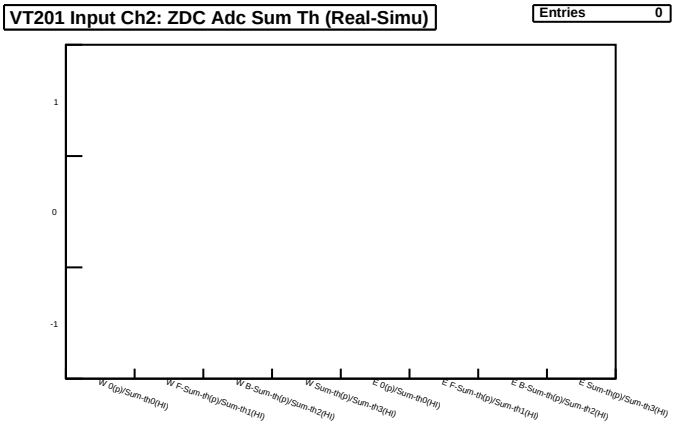

VT201 Input Ch1: EPD Hit Count Th
Entries 1452842
720037
718435
6384
7986
East
West

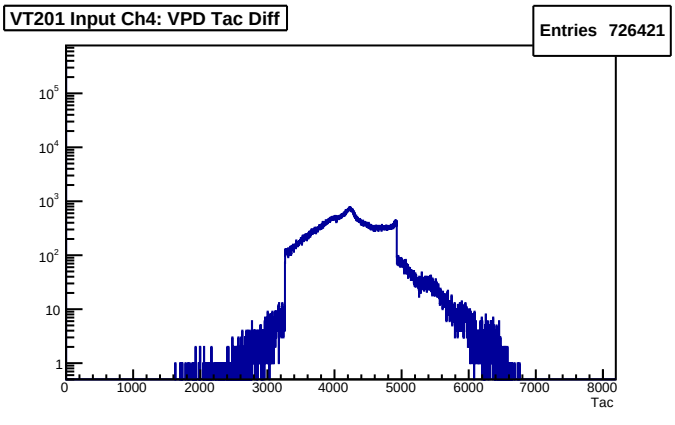

VT201 Input Ch1: EPD Hit Count Th (Real-Simu)
Entries 0
East
West

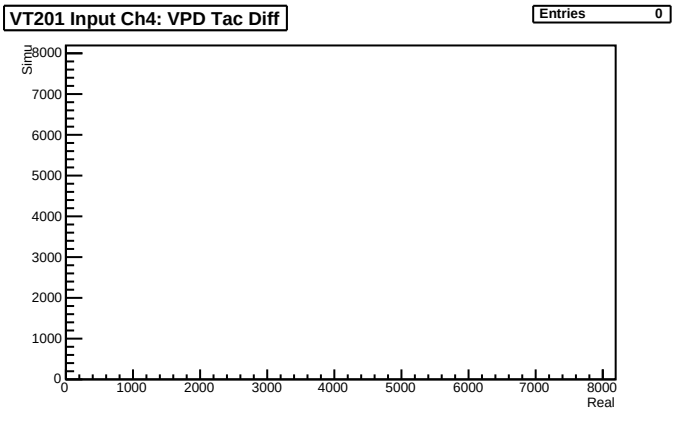

VT201 Input Ch2: ZDC Tac Diff
Entries 726421
Tac

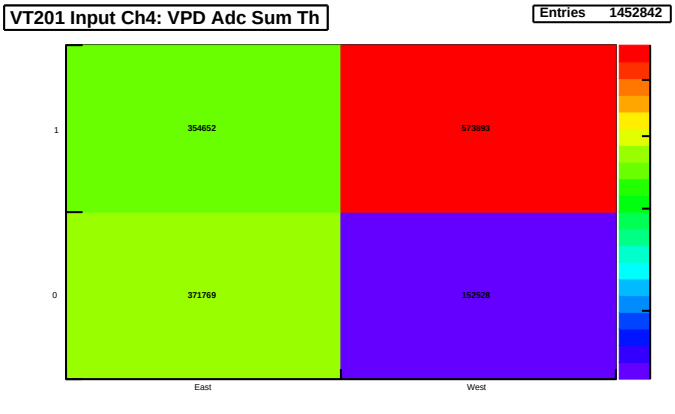

VT201 Input Ch2: ZDC Tac Diff
Entries 0
Simu
Real

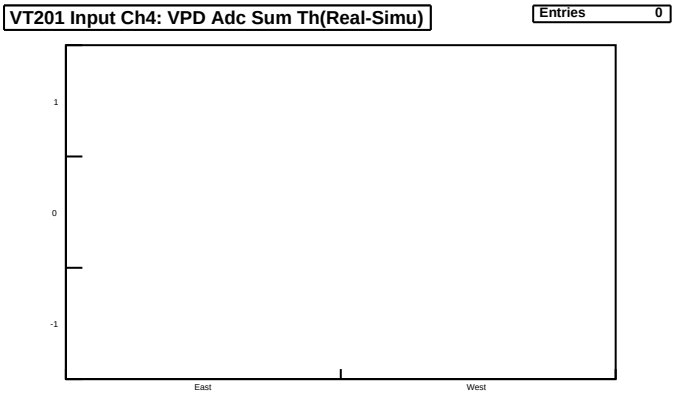

VT201 Input Ch2: ZDC Adc Sum Th
Entries 5811368
14524
3928
24
21
12747
5434
86
74
711897
722493
726397
726400
713674
720987
726335
726347
W 0(p)/Sum-th0(H)
W F-Sum-th(p)/Sum-th1(H)
W B-Sum-th(p)/Sum-th2(H)
W Sum-th(p)/Sum-th3(H)
E 0(p)/Sum-th0(H)
E F-Sum-th(p)/Sum-th1(H)
E B-Sum-th(p)/Sum-th2(H)
E Sum-th(p)/Sum-th3(H)

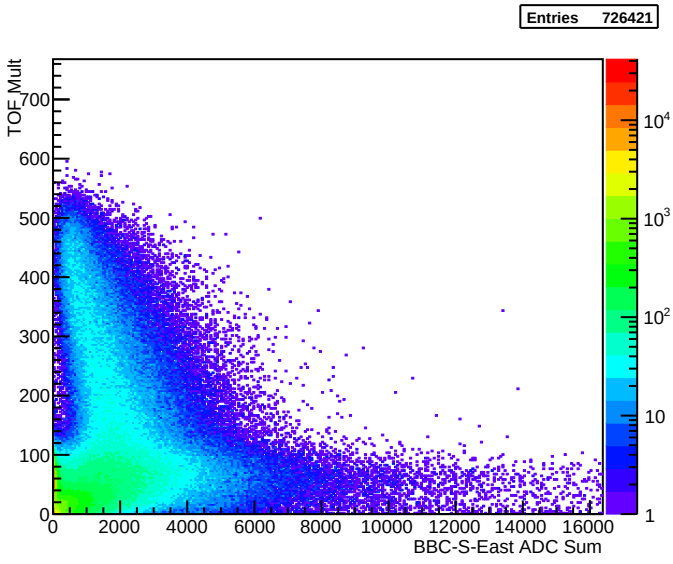

VT201 Input Ch2: ZDC Adc Sum Th (Real-Simu)
Entries 0
W 0(p)/Sum-th0(H)
W F-Sum-th(p)/Sum-th1(H)
W B-Sum-th(p)/Sum-th2(H)
W Sum-th(p)/Sum-th3(H)
E 0(p)/Sum-th0(H)
E F-Sum-th(p)/Sum-th1(H)
E B-Sum-th(p)/Sum-th2(H)
E Sum-th(p)/Sum-th3(H)

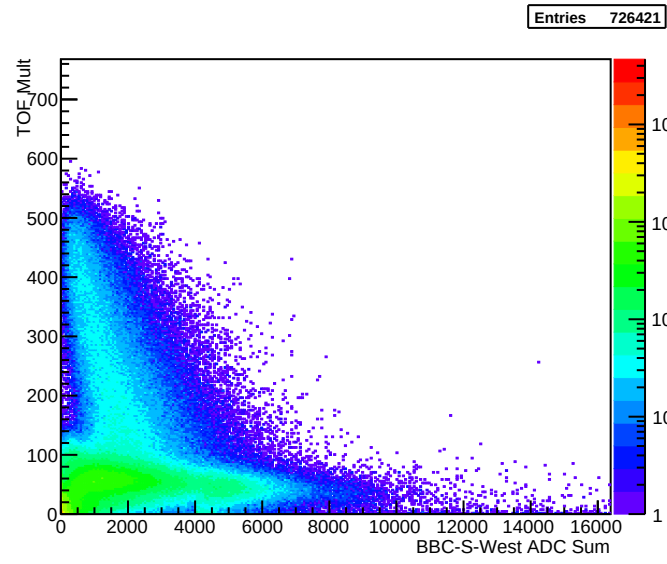

VT201 Input Ch4: VPD Tac Diff
Entries 726421
Tac

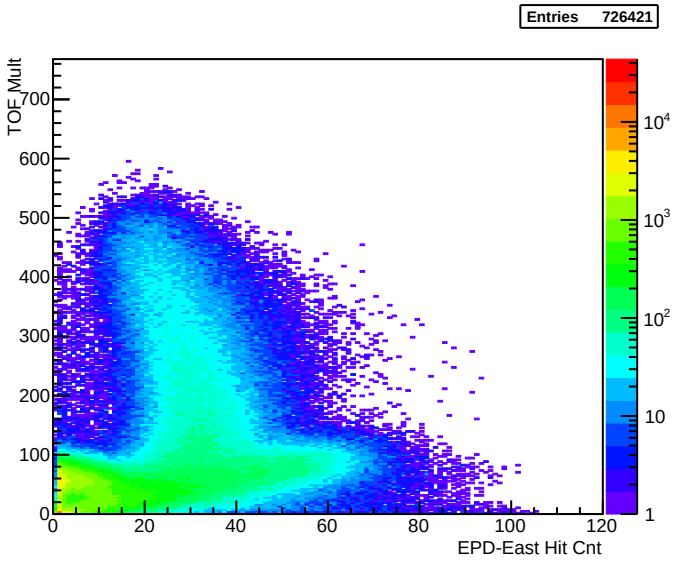

VT201 Input Ch4: VPD Tac Diff
Entries 0
Simu
Real

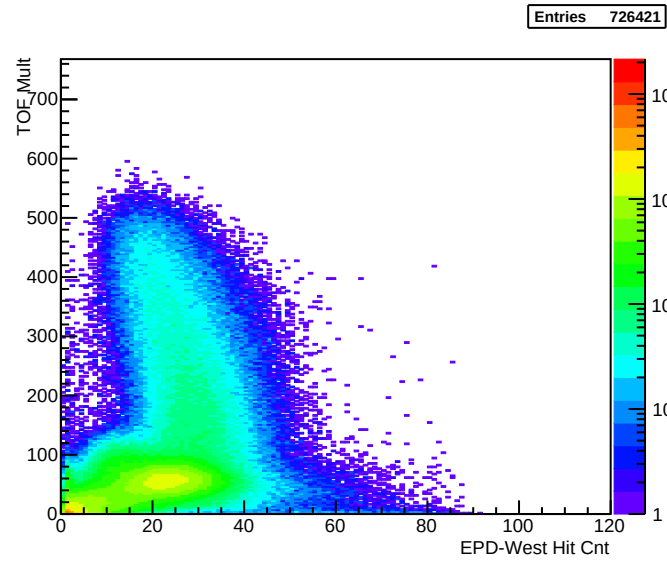

VT201 Input Ch4: VPD Adc Sum Th
Entries 1452842
354652
573893
371769
152528
East
West

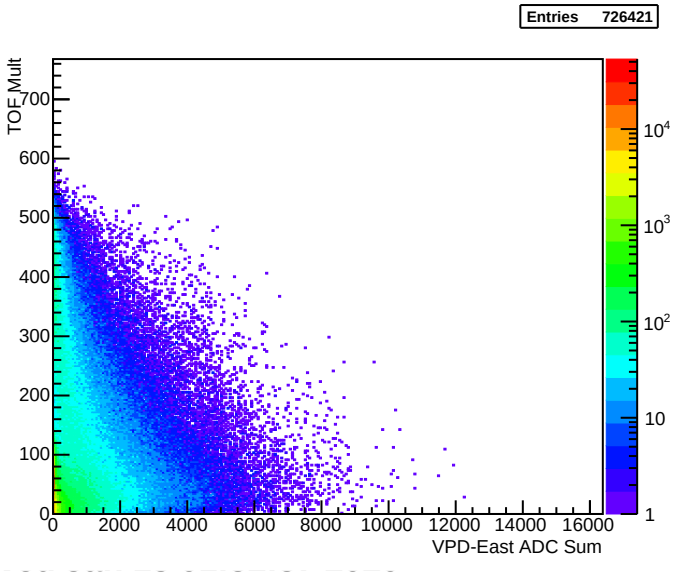

VT201 Input Ch4: VPD Adc Sum Th(Real-Simu)
Entries 0
East
West

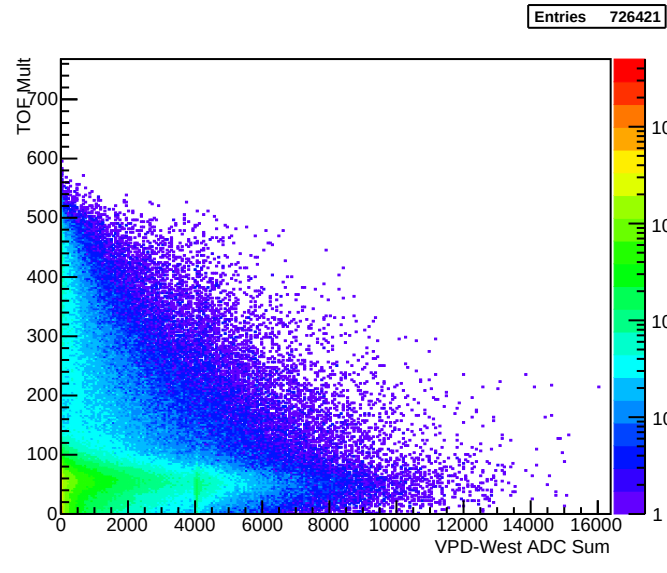

Entries 726421
TOF_Mult
BBC-S-East ADC Sum

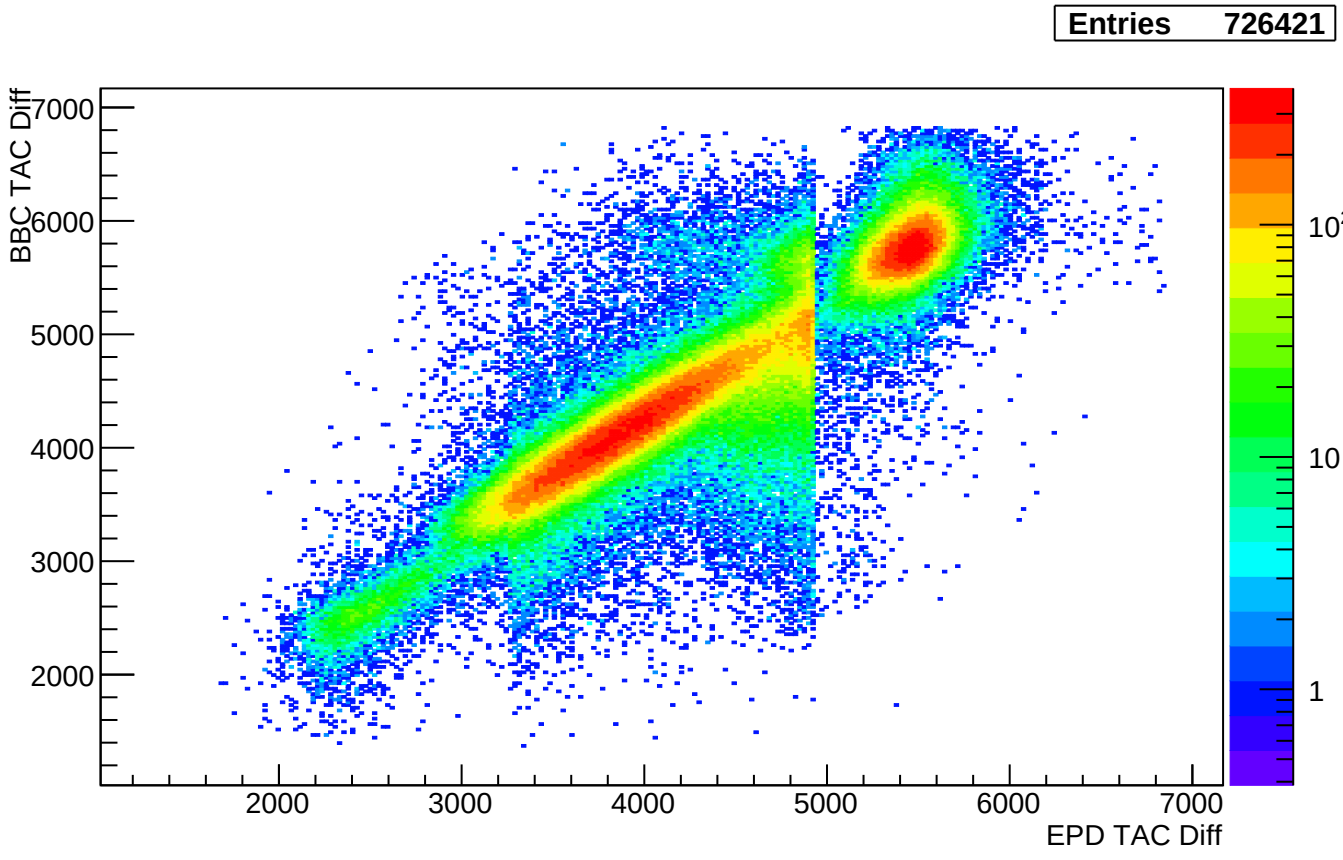

Entries 726421
TOF_Mult
BBC-S-West ADC Sum

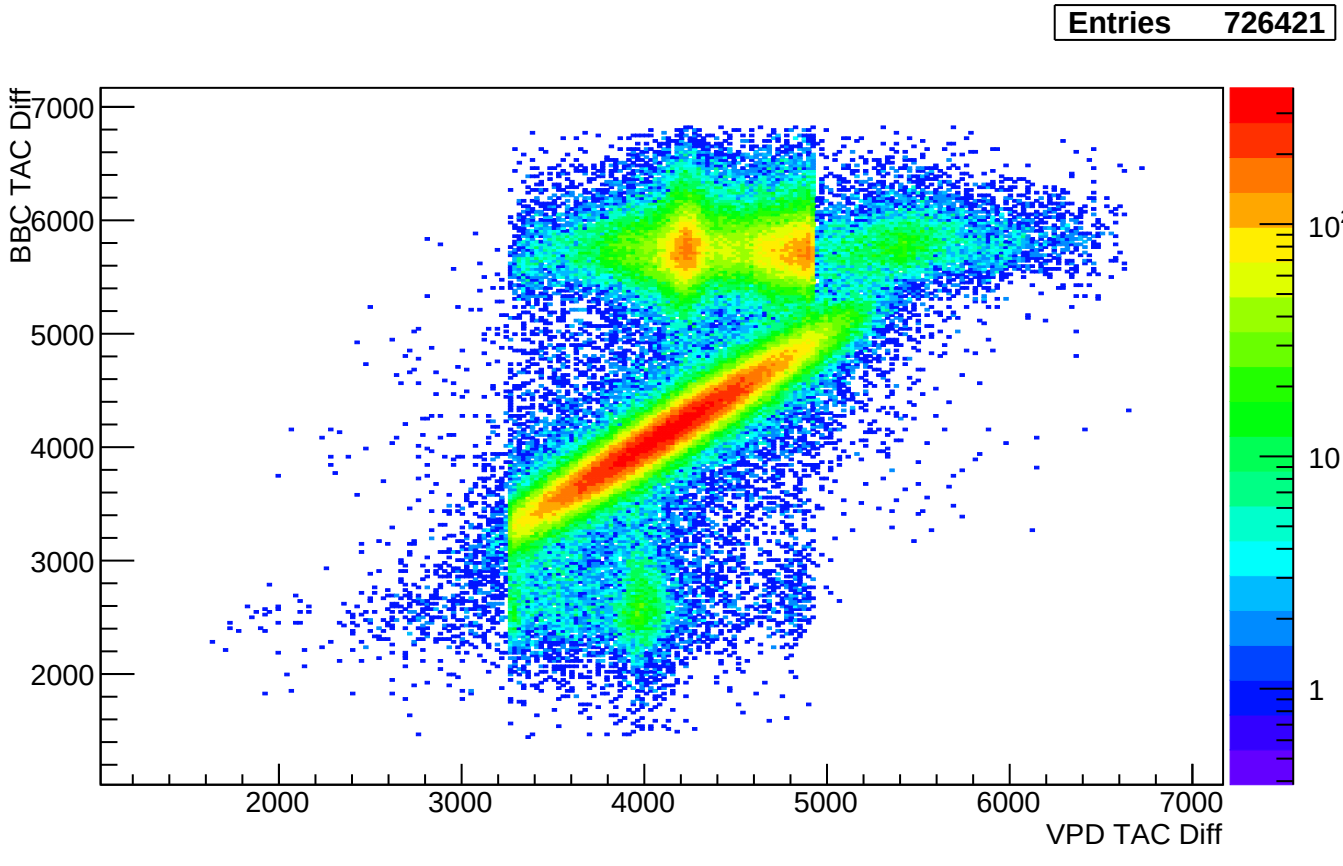

Entries 726421
TOF_Mult
EPD-East Hit Cnt

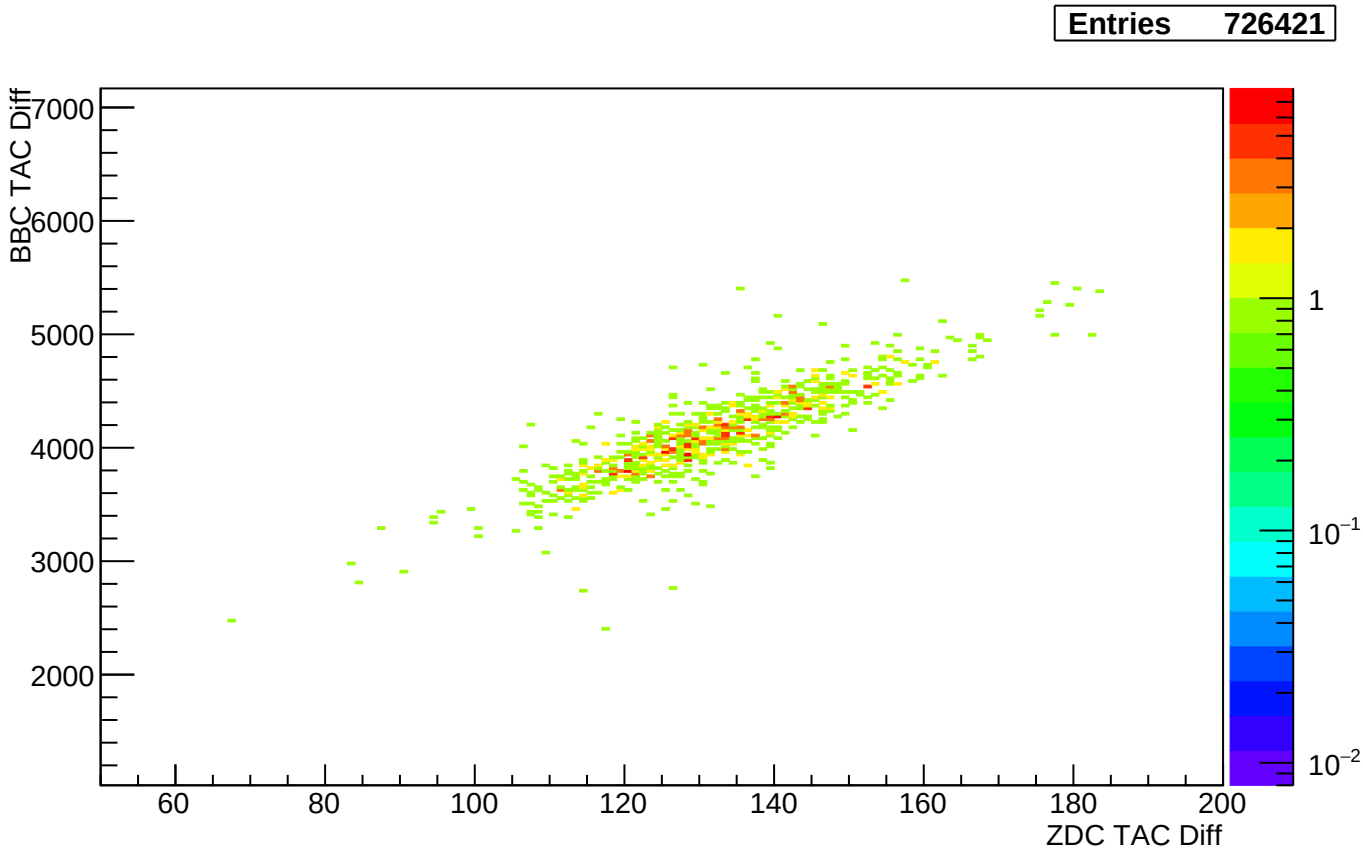

Entries 726421
TOF_Mult
EPD-West Hit Cnt

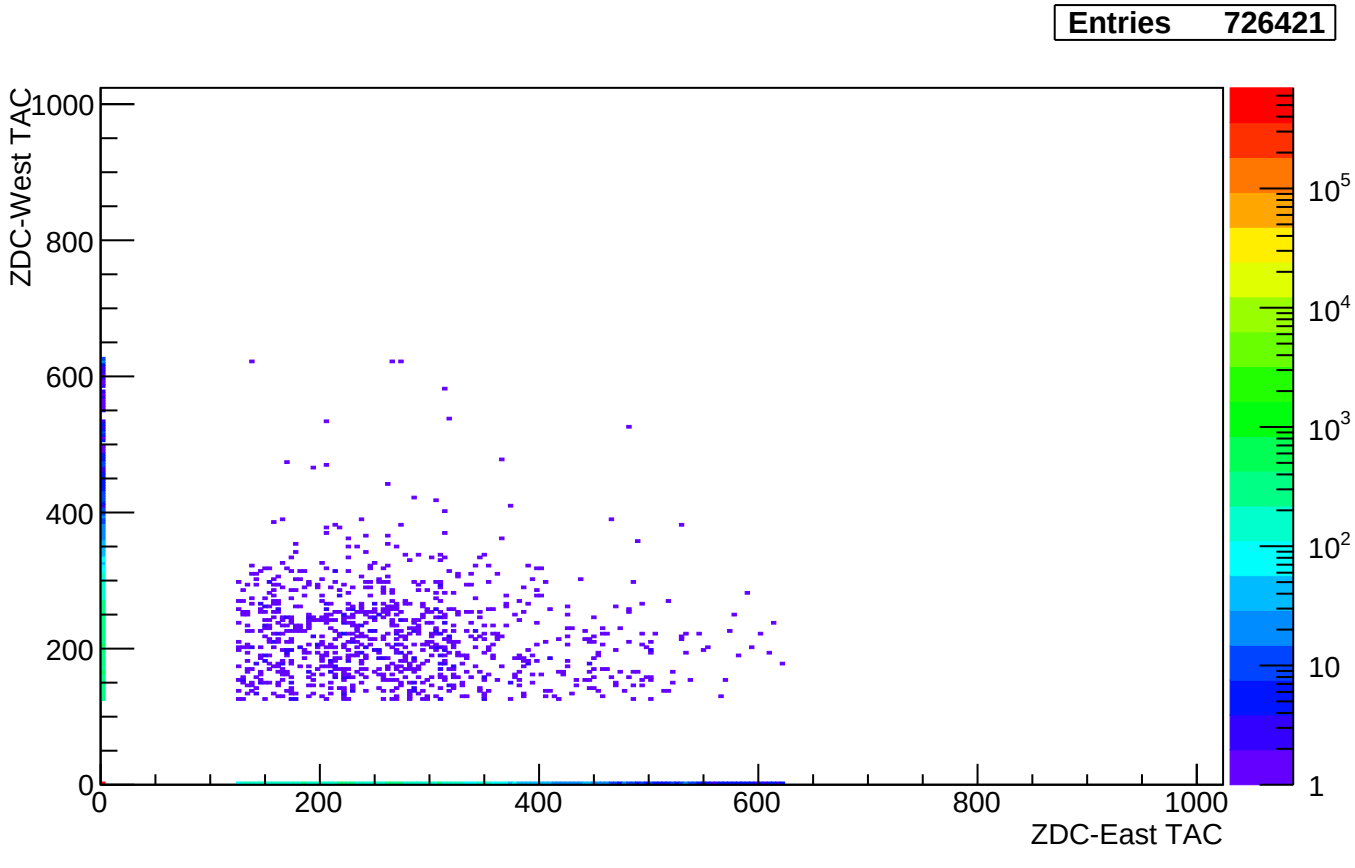

Entries 726421
TOF_Mult
VPD-East ADC Sum

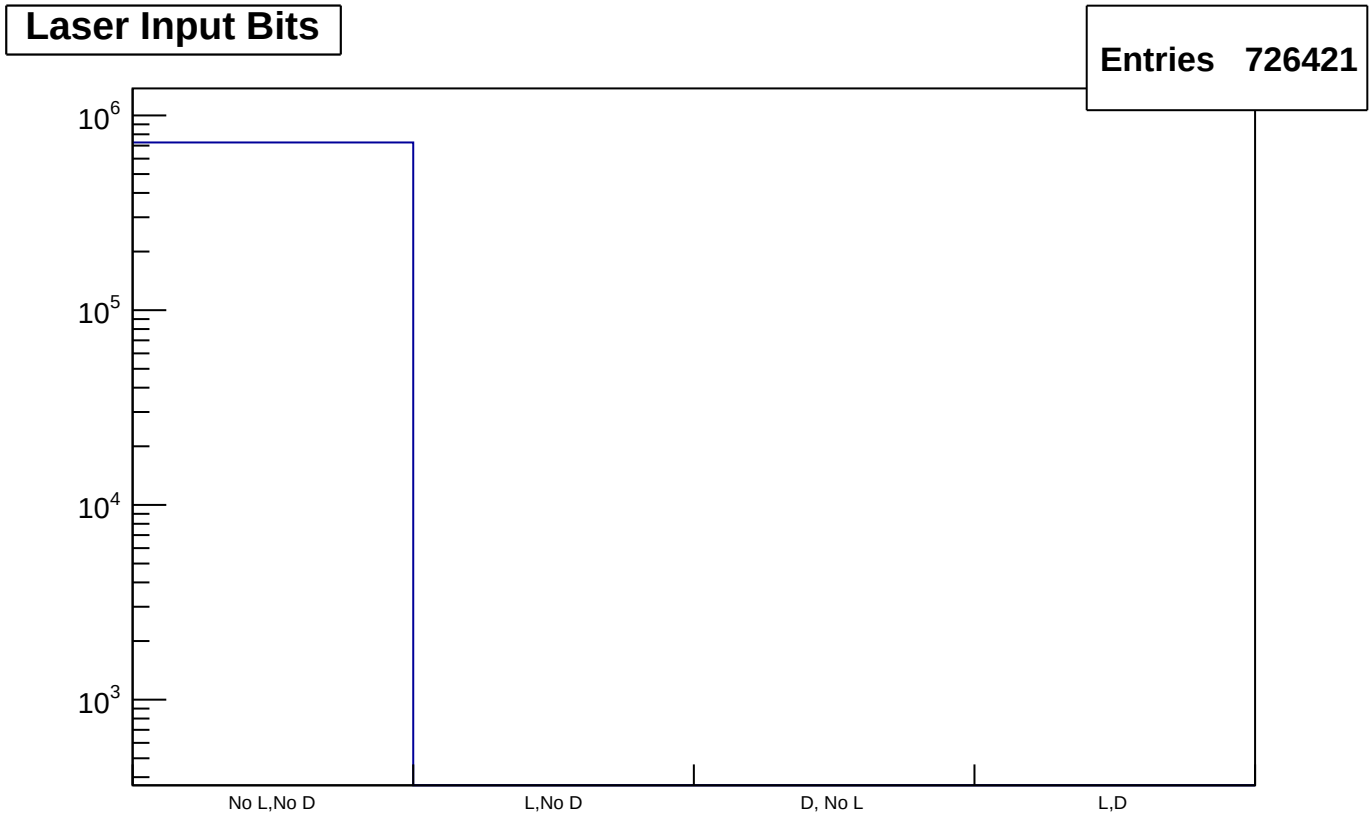

Entries 726421
TOF_Mult
VPD-West ADC Sum

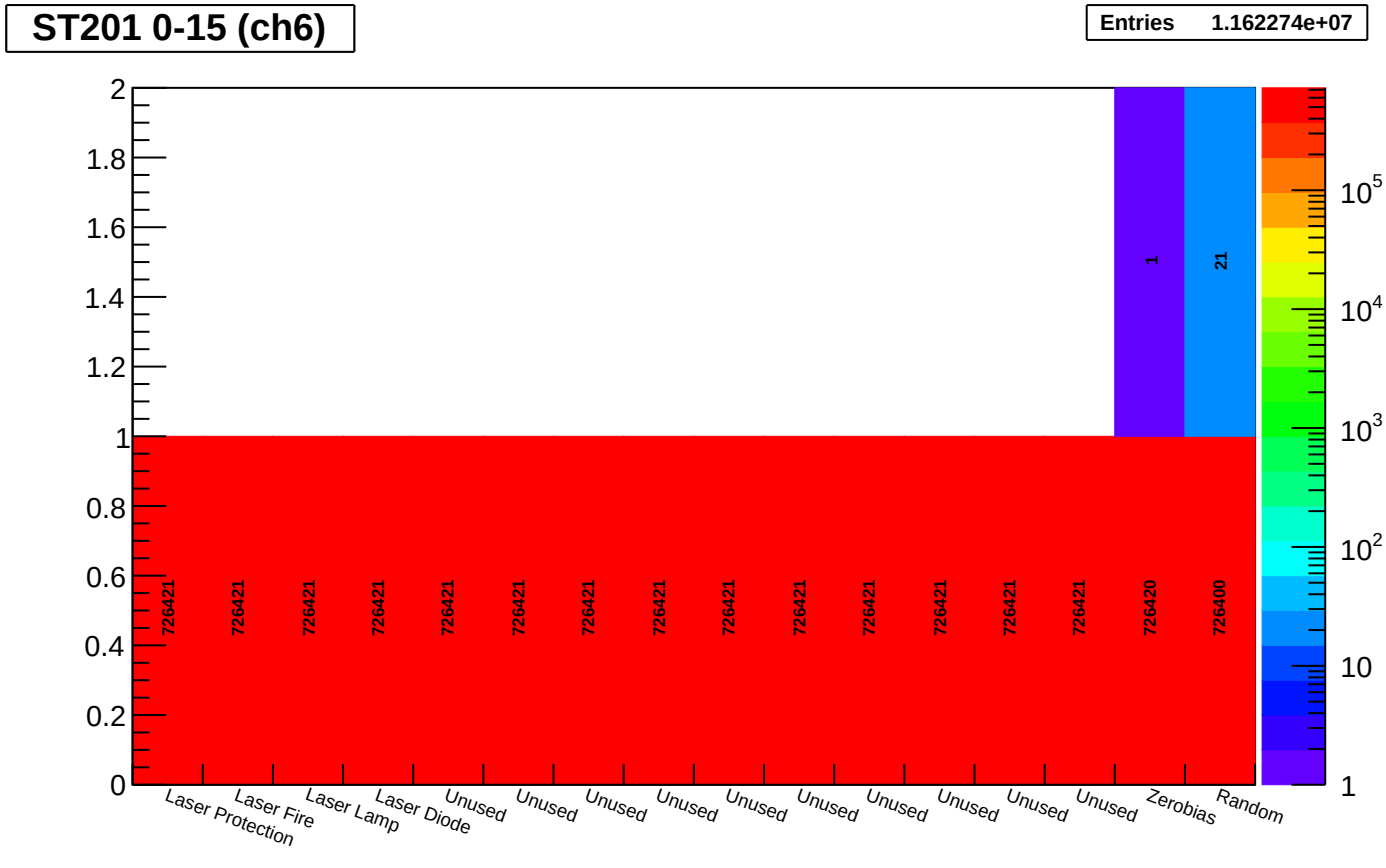

Entries 726421
BBC TAC Diff
7000
6000
5000
4000
3000
2000
2000
3000
4000
5000
6000
7000
EPD TAC Diff
$10^2$
10
1


Entries 726421
BBC TAC Diff
7000
6000
5000
4000
3000
2000
2000
3000
4000
5000
6000
7000
VPD TAC Diff
$10^2$
10
1

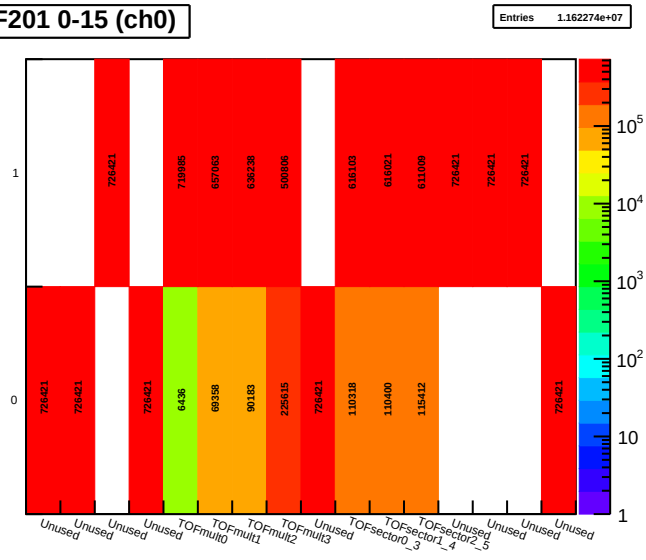

Entries 726421
BBC TAC Diff
7000
6000
5000
4000
3000
2000
60
80
100
120
140
160
180
200
ZDC TAC Diff
1
$10^{-1}$
$10^{-2}$


Entries 726421
ZDC-West TAC
1000
800
600
400
200
0
0
200
400
600
800
1000
ZDC-East TAC
$10^5$
$10^4$
$10^3$
$10^2$
10
1

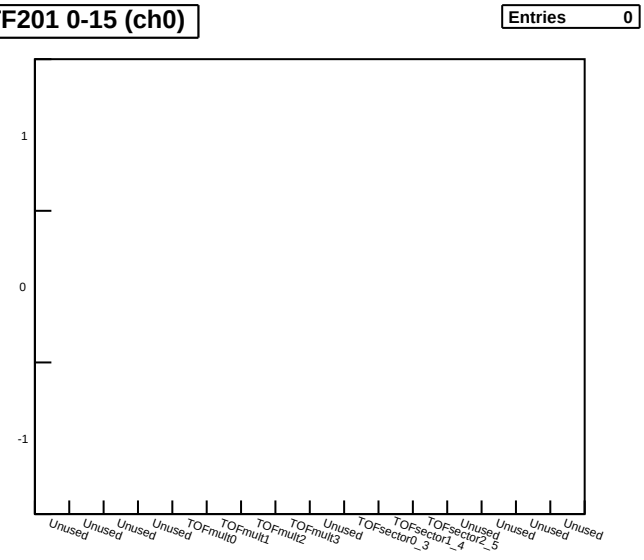

Laser Input Bits
Entries 726421
$10^6$
$10^5$
$10^4$
$10^3$
No L,No D
L,No D
D, No L
L,D


ST201 0-15 (ch6)
Entries 1.162274e+07
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
726421
726421
726421
726421
726421
726421
726421
726421
726421
726421
726421
726421
726421
726421
726420
726400
1
21
Laser Protection
Laser Fire
Laser Lamp
Laser Diode
Unused
Unused
Unused
Unused
Unused
Unused
Unused
Unused
Unused
Unused
Zerobias
Random
$10^5$
$10^4$
$10^3$
$10^2$
10
1

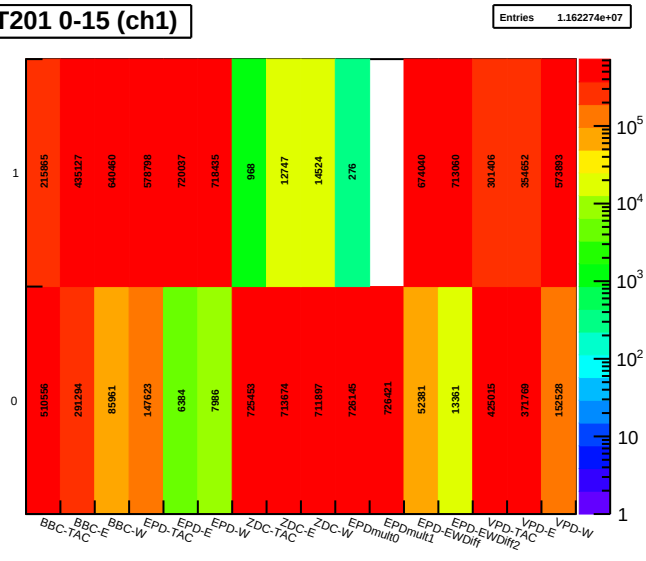
## TF201 0-15 (ch0)



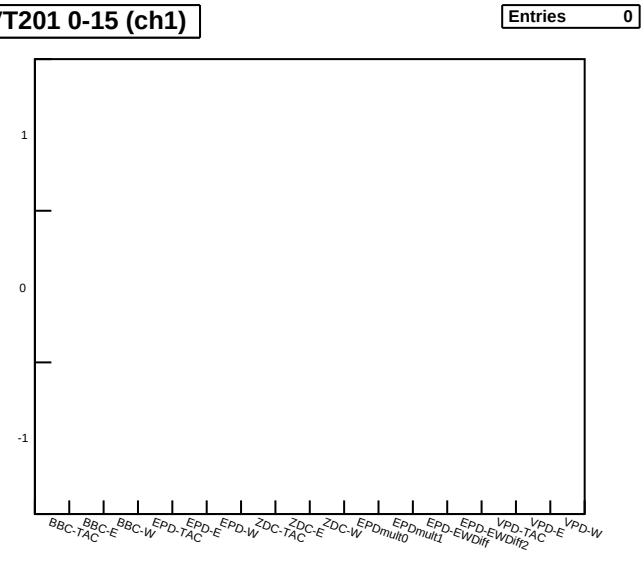

## TF201 0-15 (ch0)



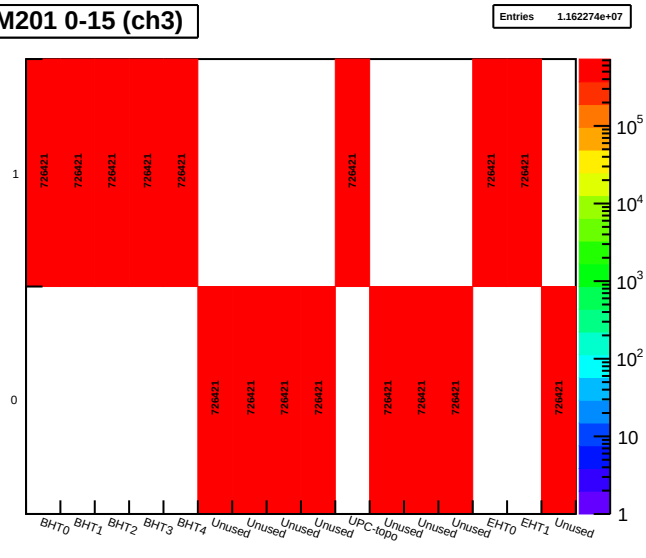

## VT201 0-15 (ch1)

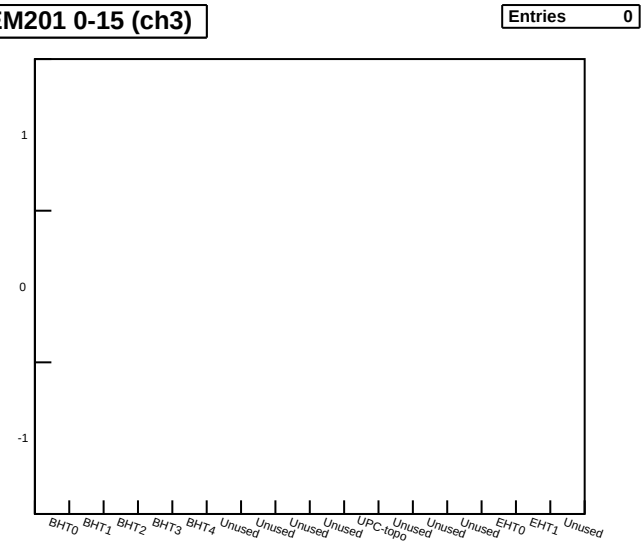



## VT201 0-15 (ch1)




## EM201 0-15 (ch3)




## EM201 0-15 (ch3)