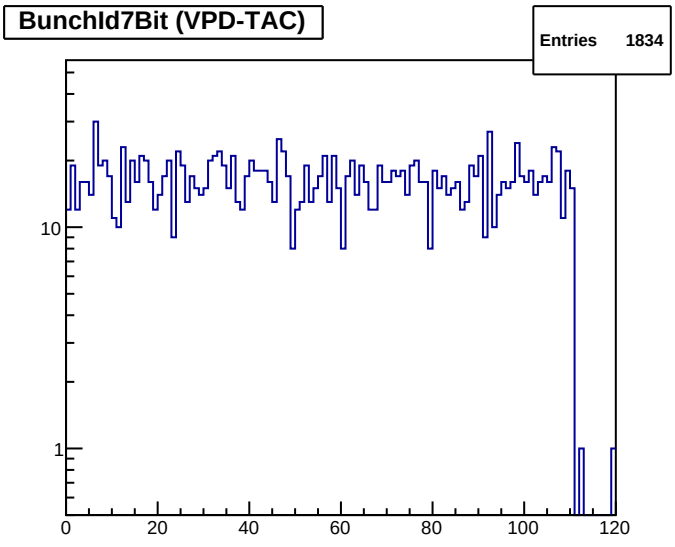
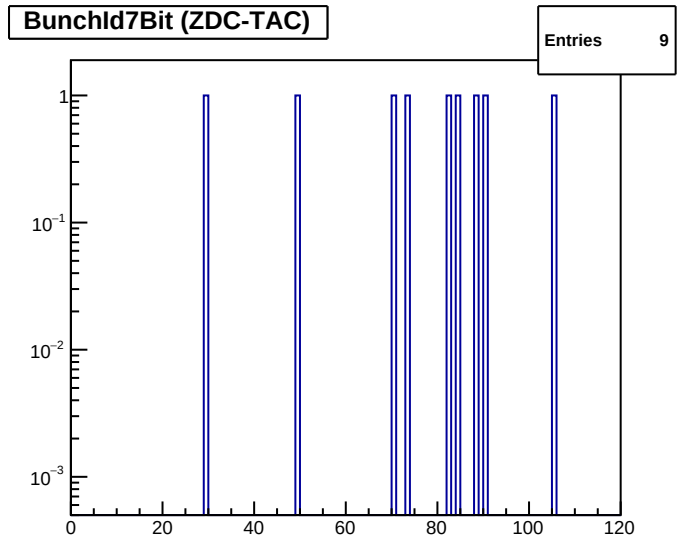
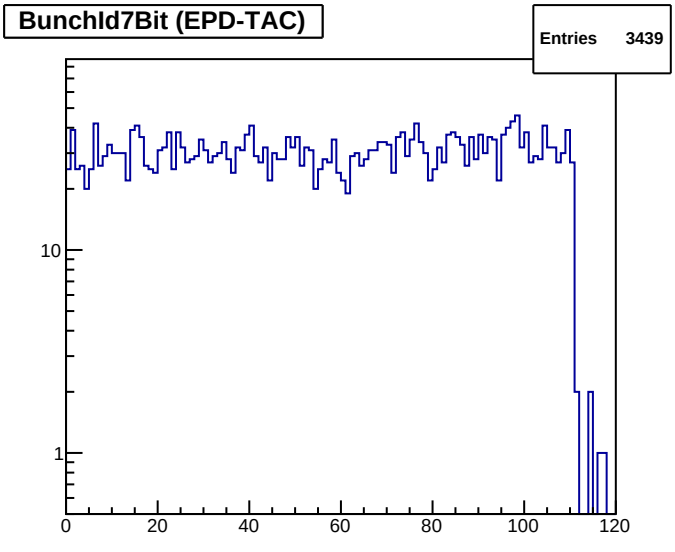
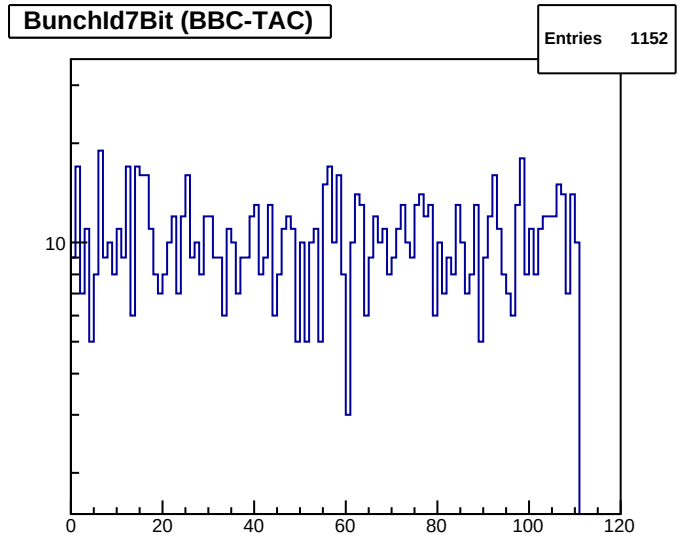
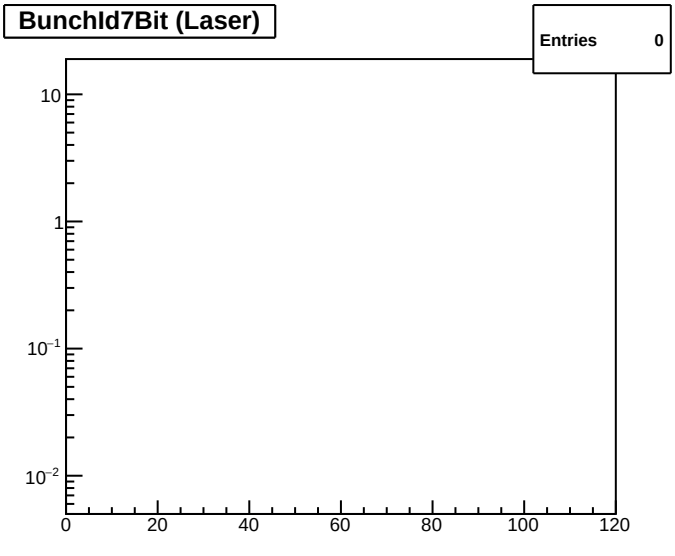
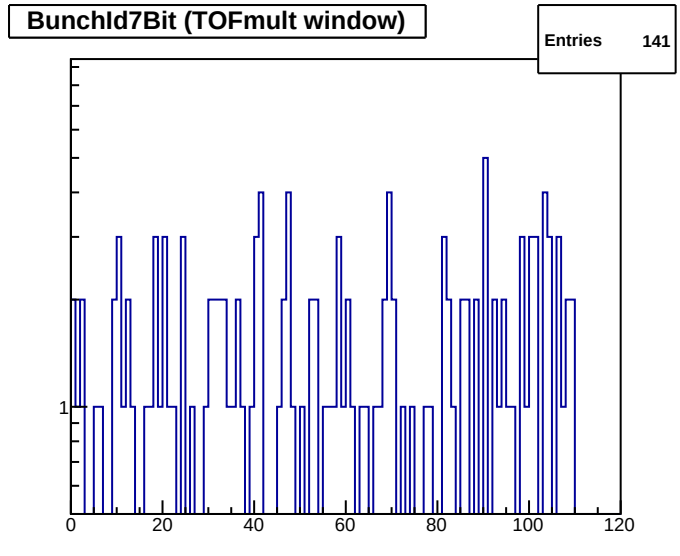

BunchId7Bit (VPD-TAC)
Entries 1834
10
1
0 20 40 60 80 100 120

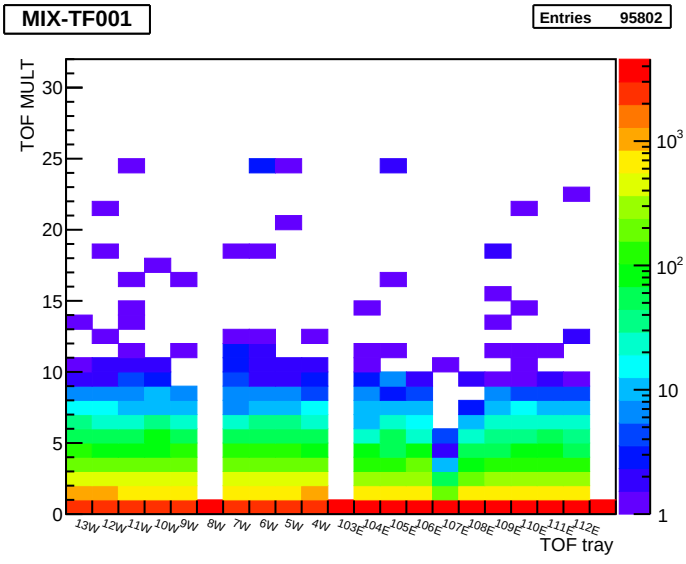

BunchId7Bit (ZDC-TAC)
Entries 9
1
10^{-1}
10^{-2}
10^{-3}
0 20 40 60 80 100 120

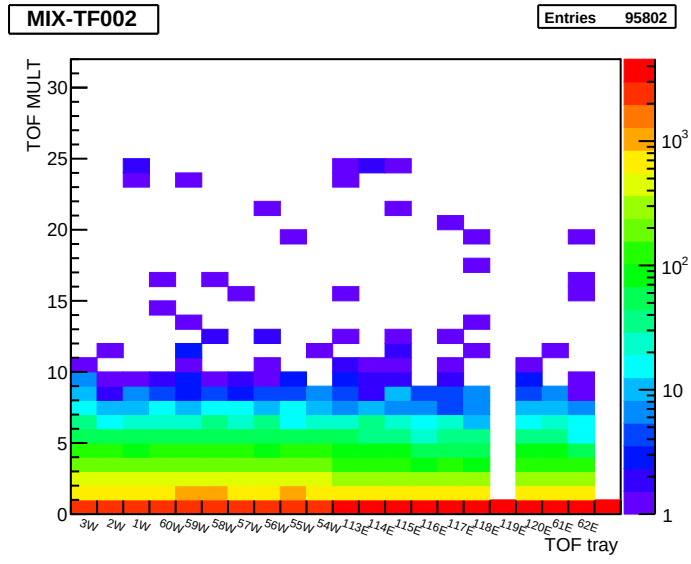

BunchId7Bit (EPD-TAC)
Entries 3439
10
1
0 20 40 60 80 100 120

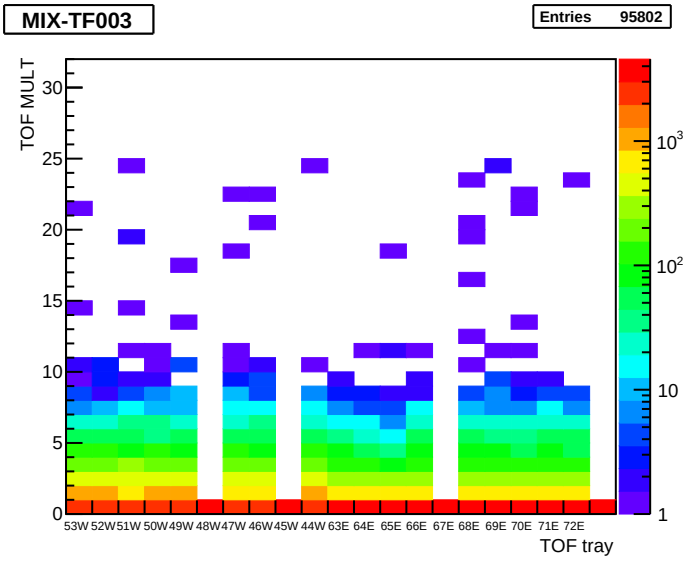

BunchId7Bit (BBC-TAC)
Entries 1152
10
0 20 40 60 80 100 120

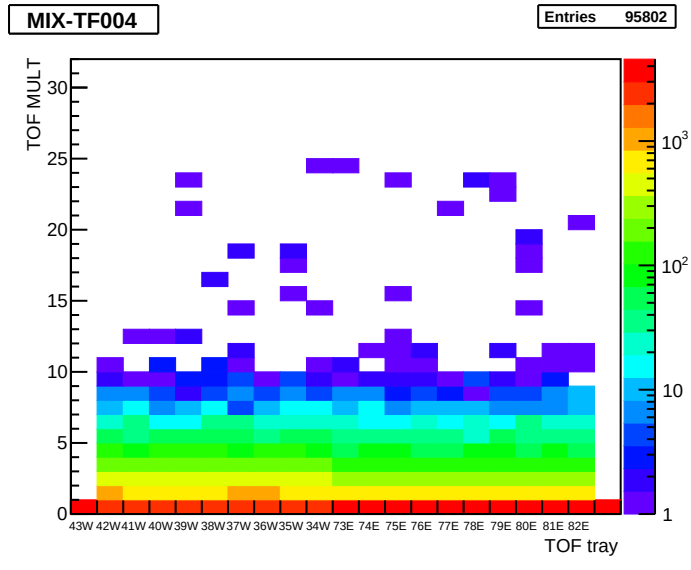

BunchId7Bit (Laser)
Entries 0
10
1
10^{-1}
10^{-2}
0 20 40 60 80 100 120

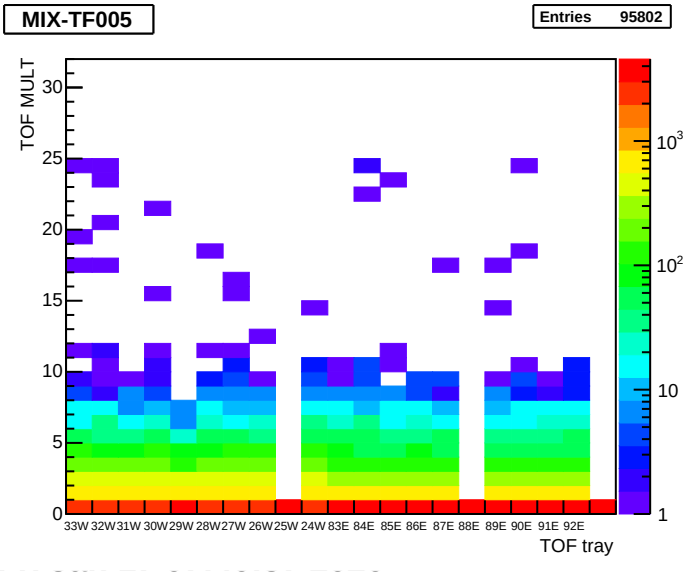

BunchId7Bit (TOFmult window)
Entries 141
1
0 20 40 60 80 100 120

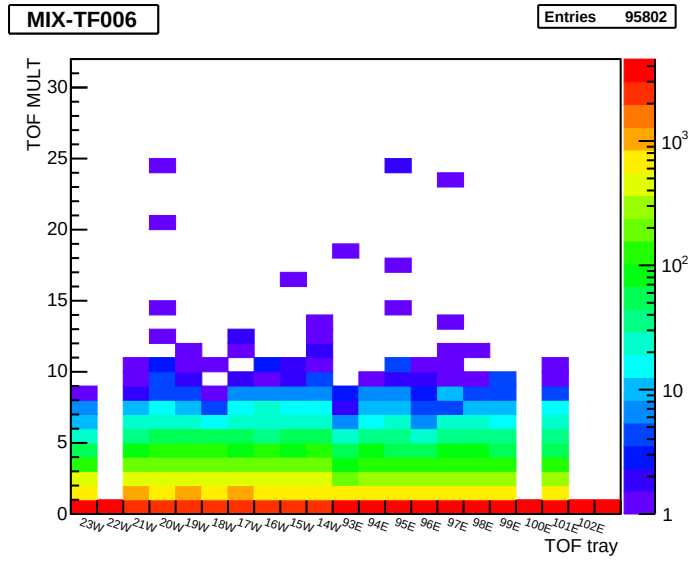

MIX-TF001
Entries 95802
TOF MULT
TOF tray

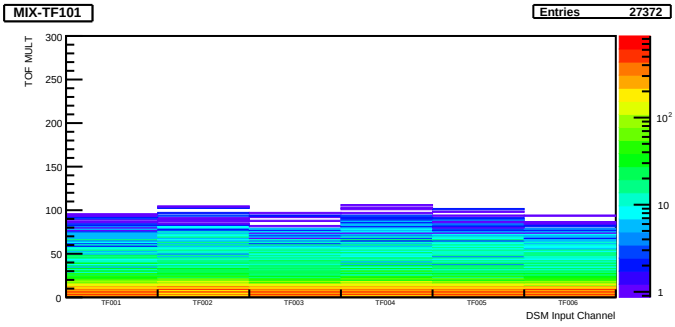

MIX-TF002
Entries 95802
TOF MULT
TOF tray

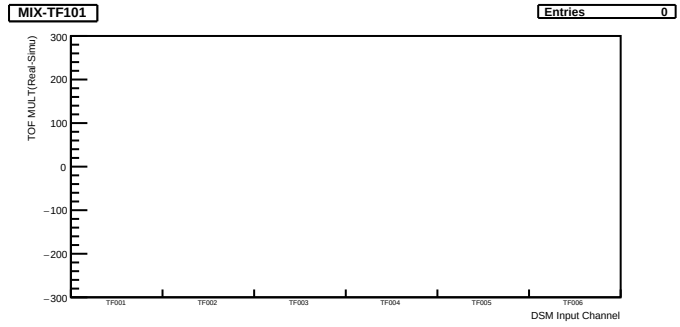

MIX-TF003
Entries 95802
TOF MULT
TOF tray

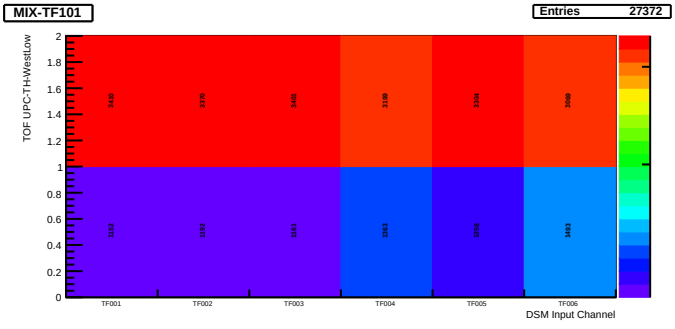

MIX-TF004
Entries 95802
TOF MULT
TOF tray

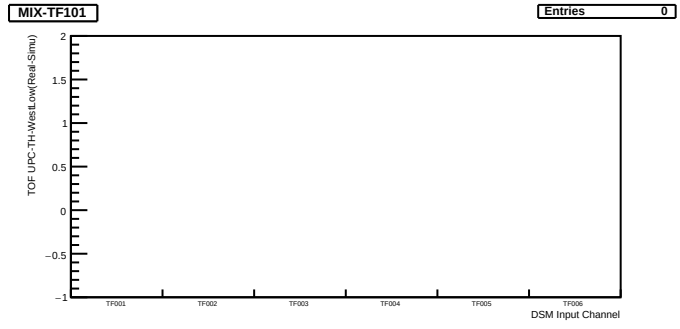

MIX-TF005
Entries 95802
TOF MULT
TOF tray

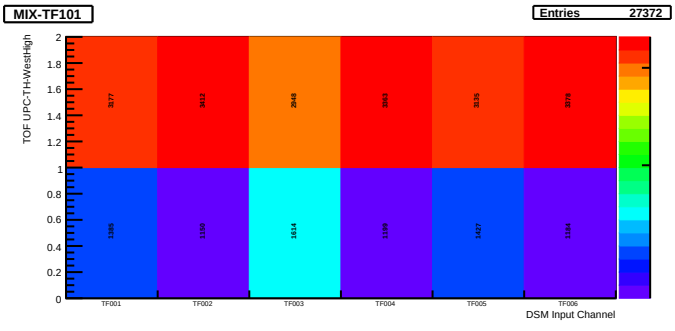

MIX-TF006
Entries 95802
TOF MULT
TOF tray

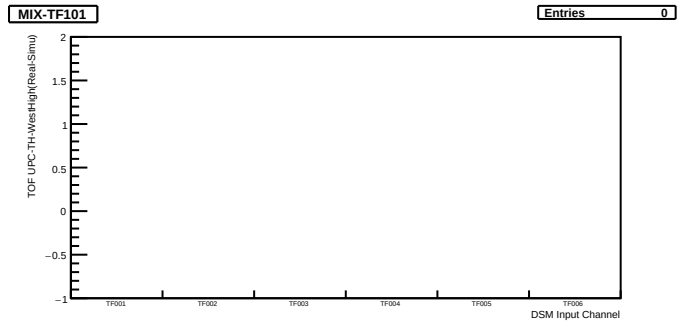

MIX-TF101
Entries 27372
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

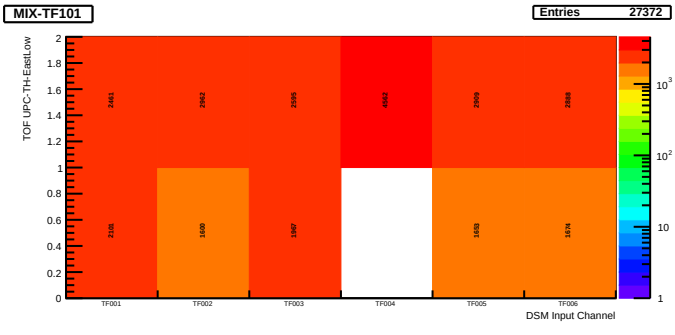

MIX-TF101
Entries 0
TOF MULT(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

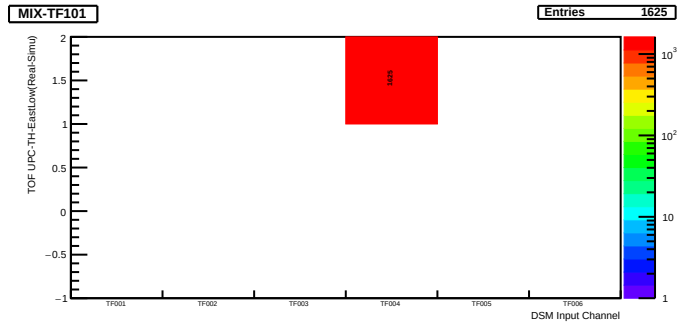

MIX-TF101
Entries 27372
TOF UPC-TH-WestLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

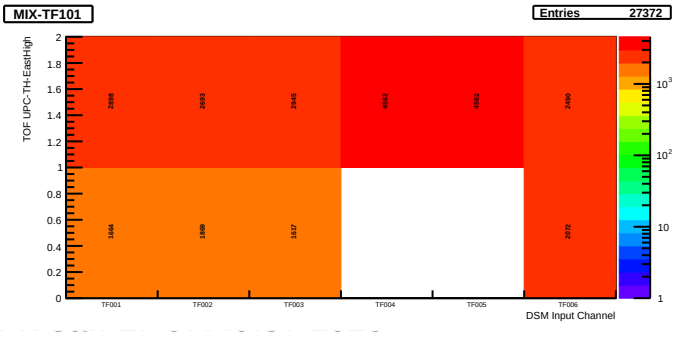

MIX-TF101
Entries 0
TOF UPC-TH-WestLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

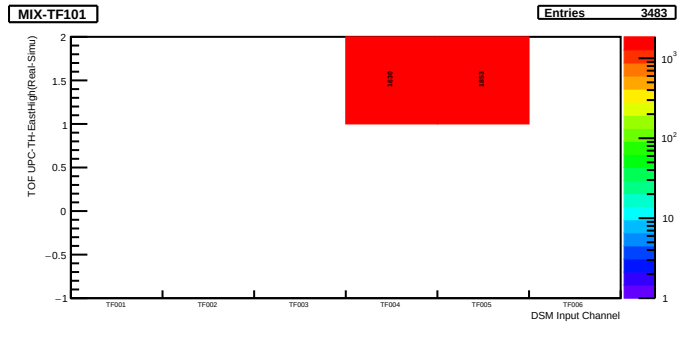

MIX-TF101
Entries 27372
TOF UPC-TH-WestHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

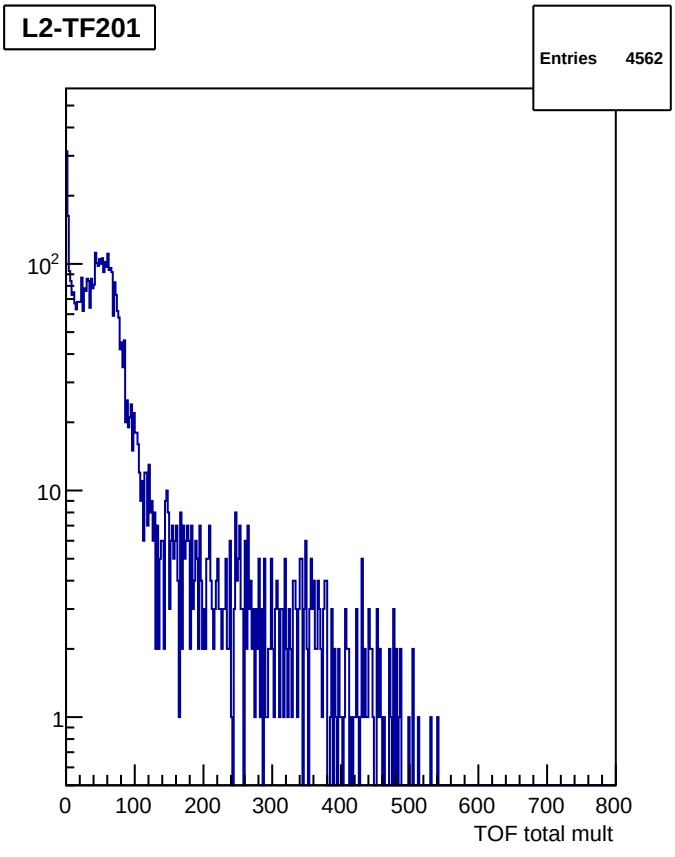

MIX-TF101
Entries 0
TOF UPC-TH-WestHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

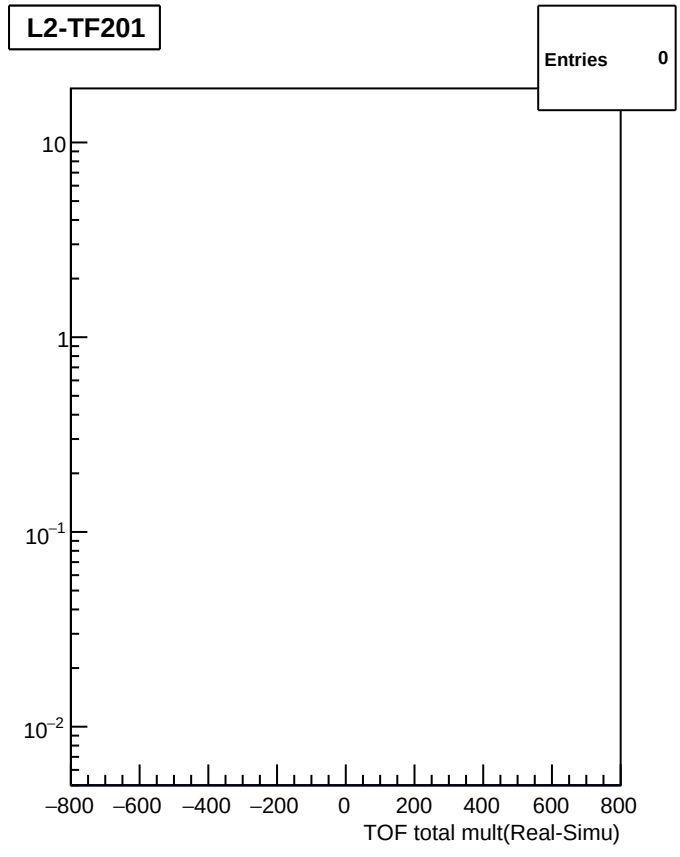

MIX-TF101
Entries 27372
TOF UPC-TH-EastLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

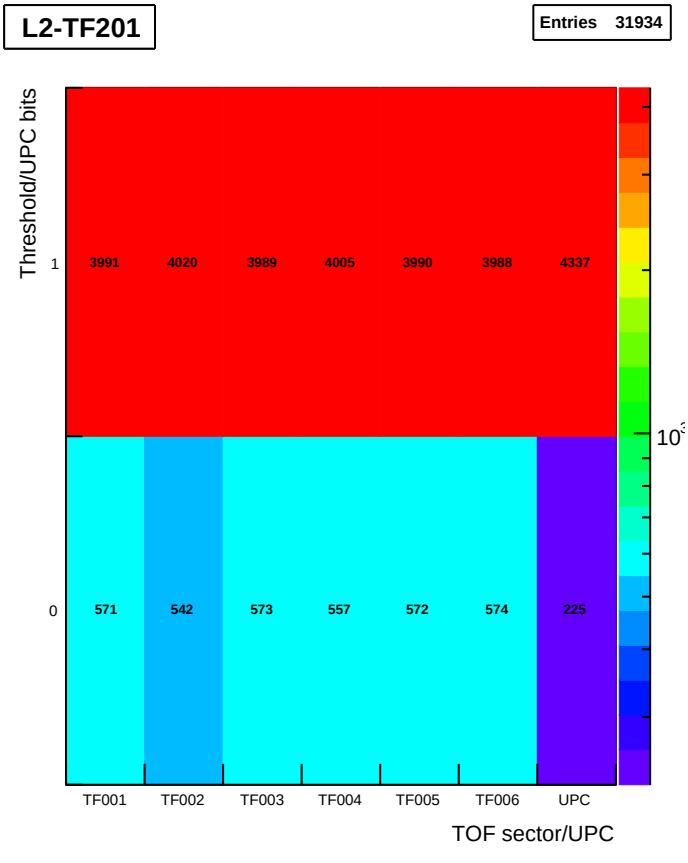

MIX-TF101
Entries 1625
TOF UPC-TH-EastLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

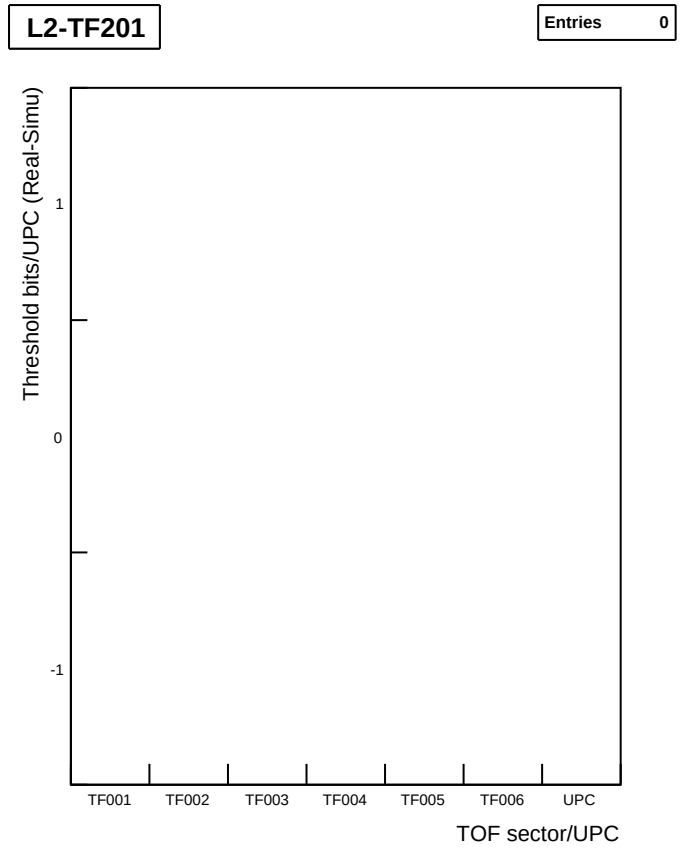

MIX-TF101
Entries 27372
TOF UPC-TH-EastHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

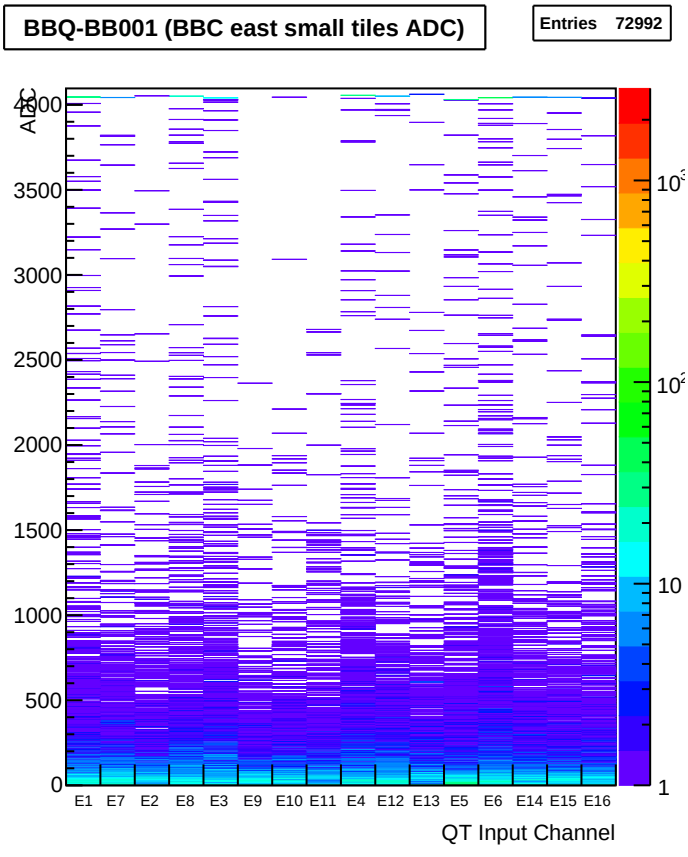

MIX-TF101
Entries 3483
TOF UPC-TH-EastHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

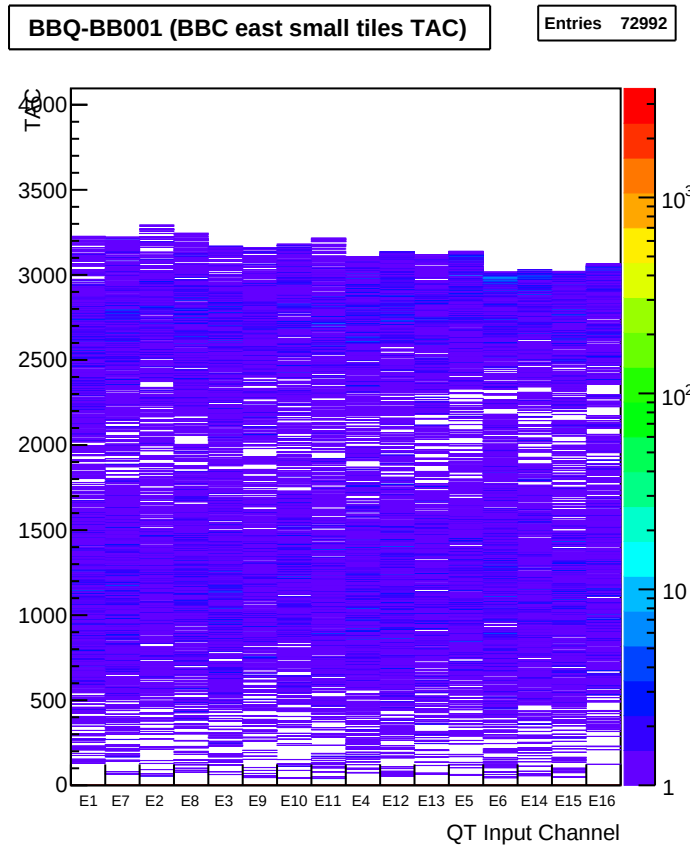

L2-TF201
Entries 4562
10^2
10
1
0 100 200 300 400 500 600 700 800
TOF total mult

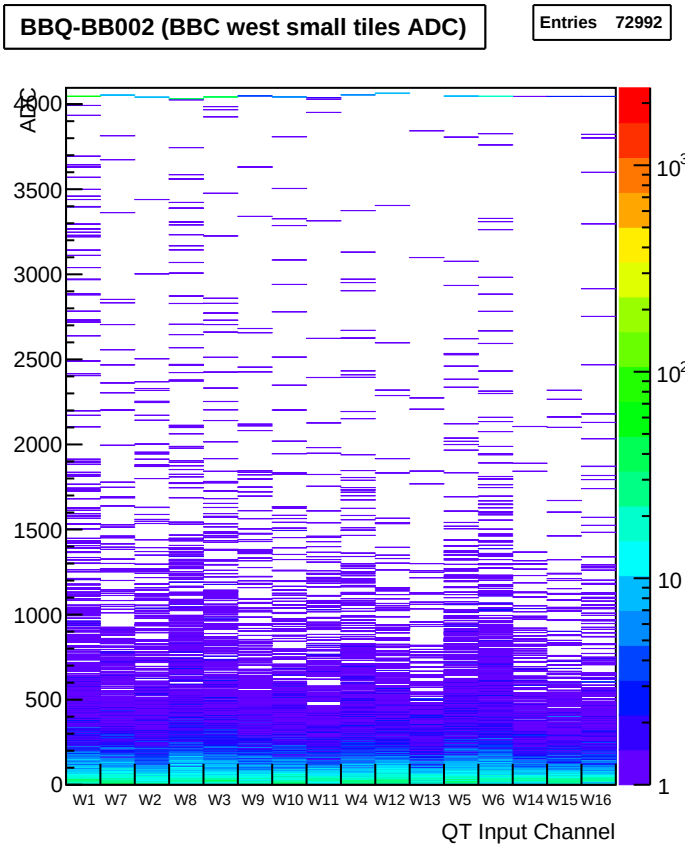

L2-TF201
Entries 0
10
1
10^{-1}
10^{-2}
−800 −600 −400 −200 0 200 400 600 800
TOF total mult(Real-Simu)

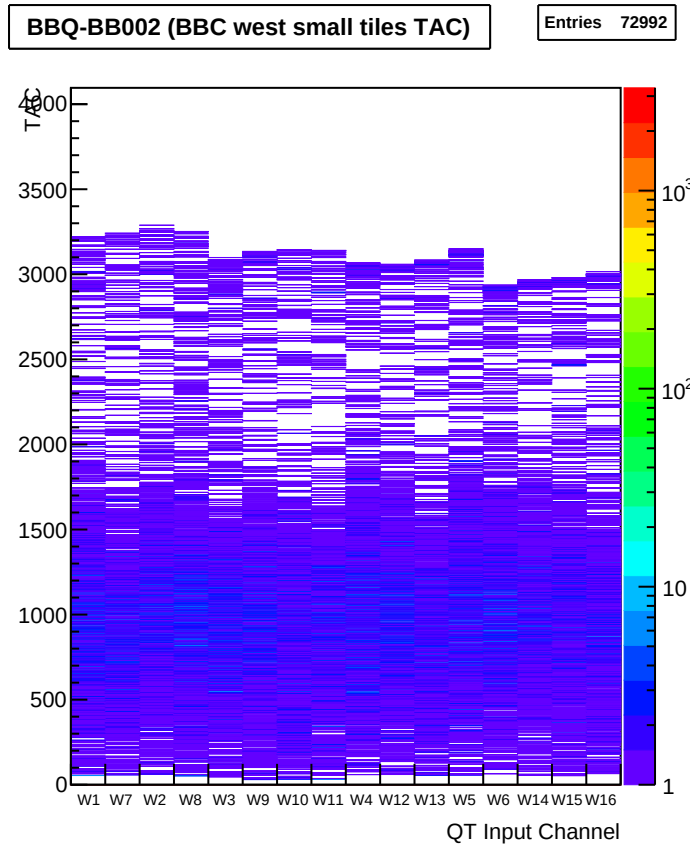

L2-TF201
Entries 31934
Threshold/UPC bits
1
0
3991 4020 3989 4005 3990 3988 4337
571 542 573 557 572 574 225
TF001 TF002 TF003 TF004 TF005 TF006 UPC
TOF sector/UPC

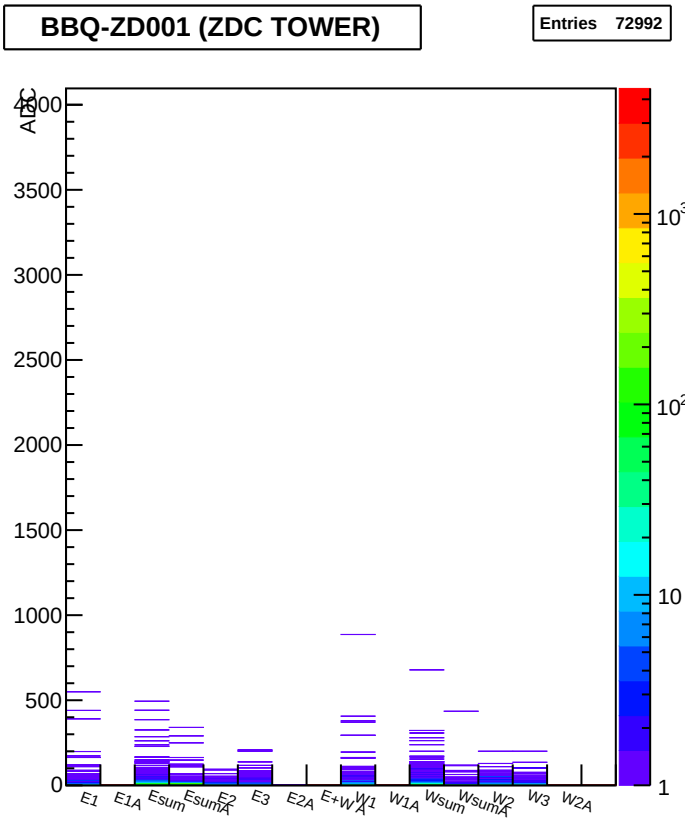

L2-TF201
Entries 0
Threshold bits/UPC (Real-Simu)
1
0
-1
TF001 TF002 TF003 TF004 TF005 TF006 UPC
TOF sector/UPC

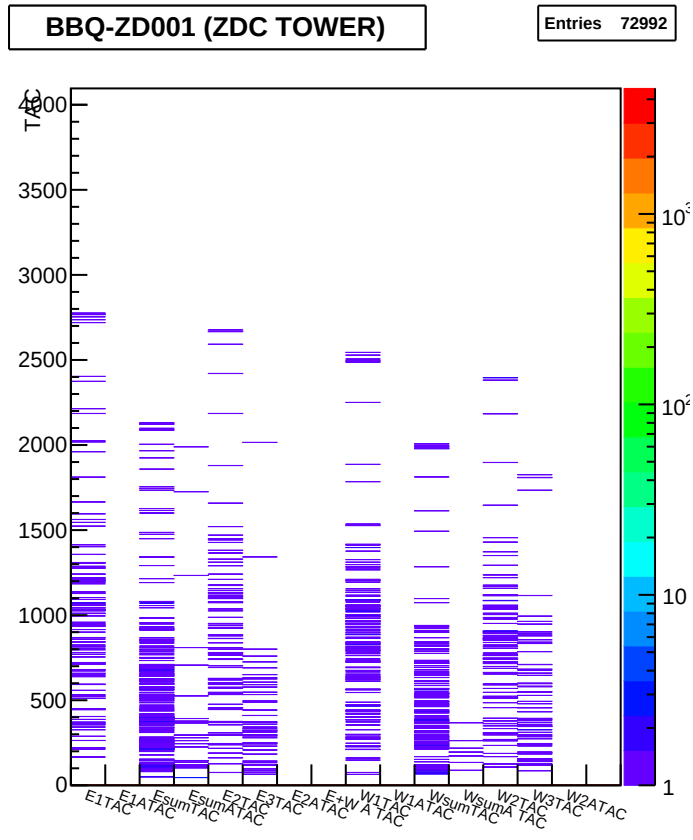

BBQ-BB001 (BBC east small tiles ADC)
Entries 72992
ADC
QT Input Channel
E1 E7 E2 E8 E3 E9 E10 E11 E4 E12 E13 E5 E6 E14 E15 E16

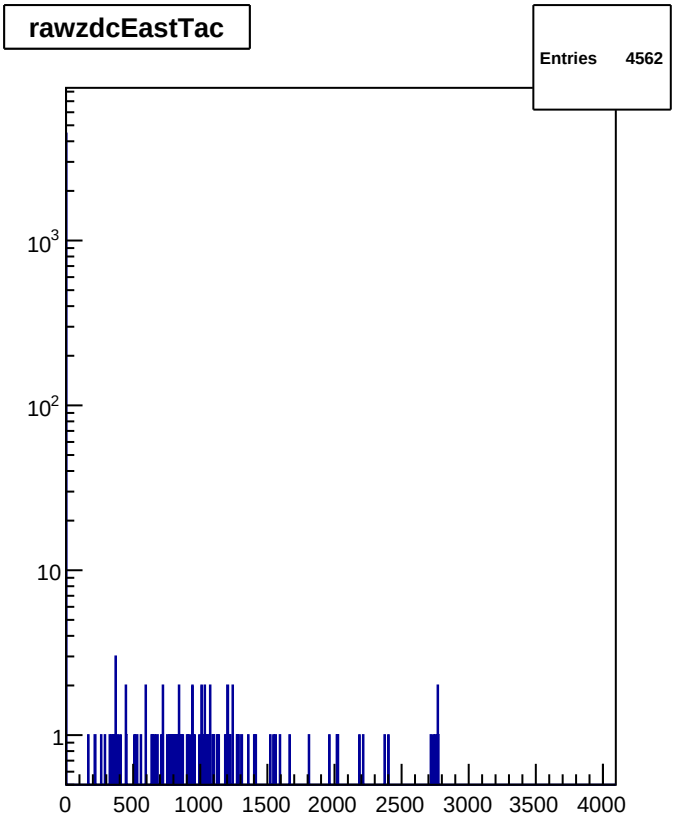

BBQ-BB001 (BBC east small tiles TAC)
Entries 72992
TAC
QT Input Channel
E1 E7 E2 E8 E3 E9 E10 E11 E4 E12 E13 E5 E6 E14 E15 E16

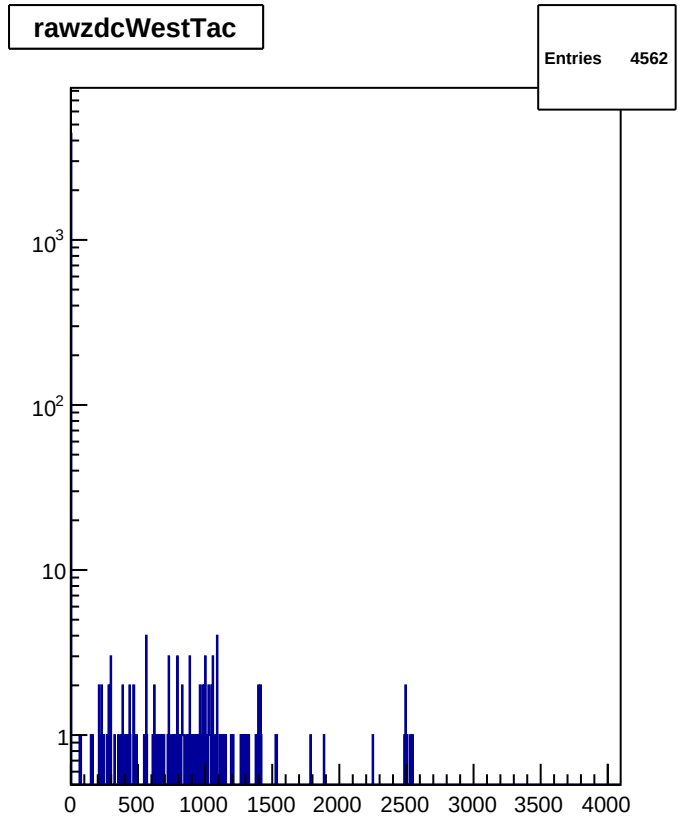

BBQ-BB002 (BBC west small tiles ADC)
Entries 72992
ADC
QT Input Channel
W1 W7 W2 W8 W3 W9 W10 W11 W4 W12 W13 W5 W6 W14 W15 W16

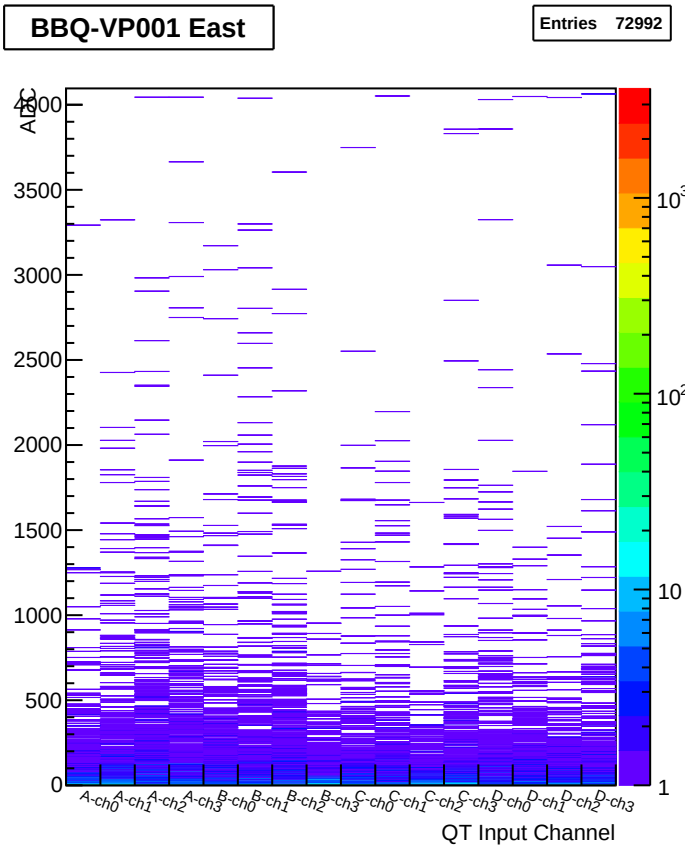

BBQ-BB002 (BBC west small tiles TAC)
Entries 72992
TAC
QT Input Channel
W1 W7 W2 W8 W3 W9 W10 W11 W4 W12 W13 W5 W6 W14 W15 W16

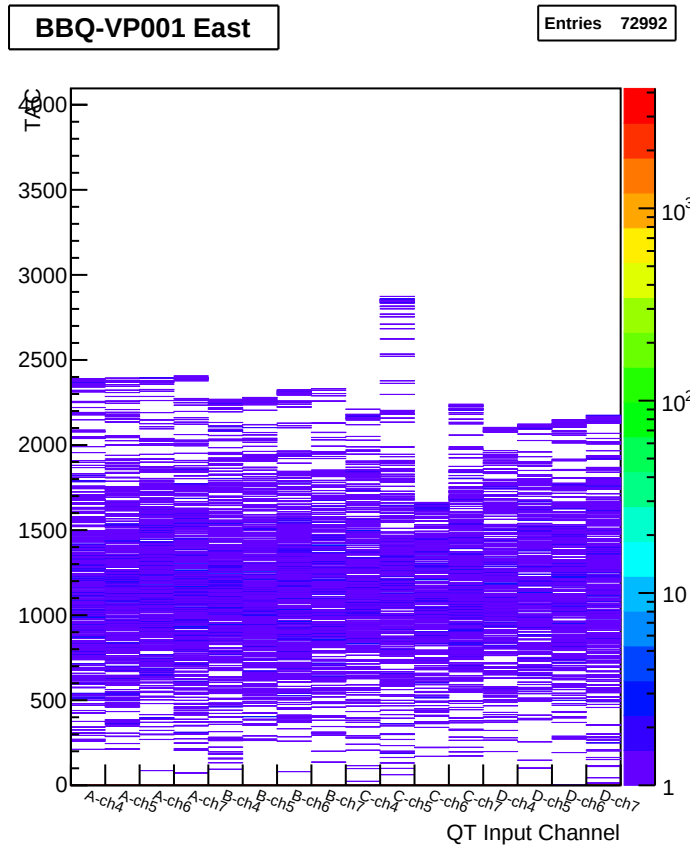

BBQ-ZD001 (ZDC TOWER)
Entries 72992
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
E1
E1A
Esum
EsumA
E2
E3
E2A
E+W
W1
W1A
Wsum
WsumA
W2
W3
W2A
10
1

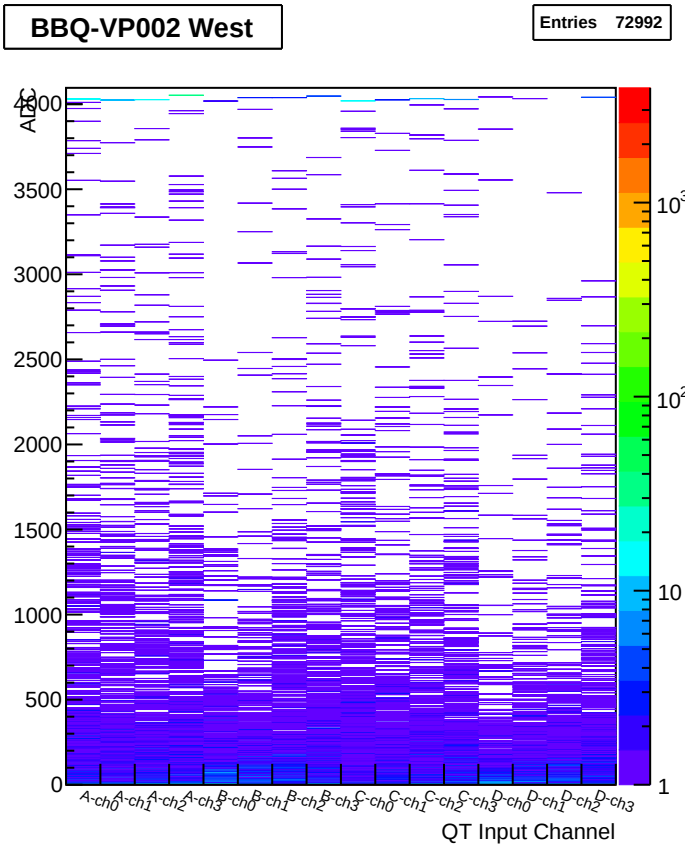

BBQ-ZD001 (ZDC TOWER)
Entries 72992
TAC
4000
3500
3000
2500
2000
1500
1000
500
0
E1TAC
E1ATAC
EsumTAC
EsumATAC
E2TAC
E3TAC
E2ATAC
W1TAC
W1ATAC
WsumTAC
WsumATAC
W2TAC
W3TAC
W2ATAC
10
1

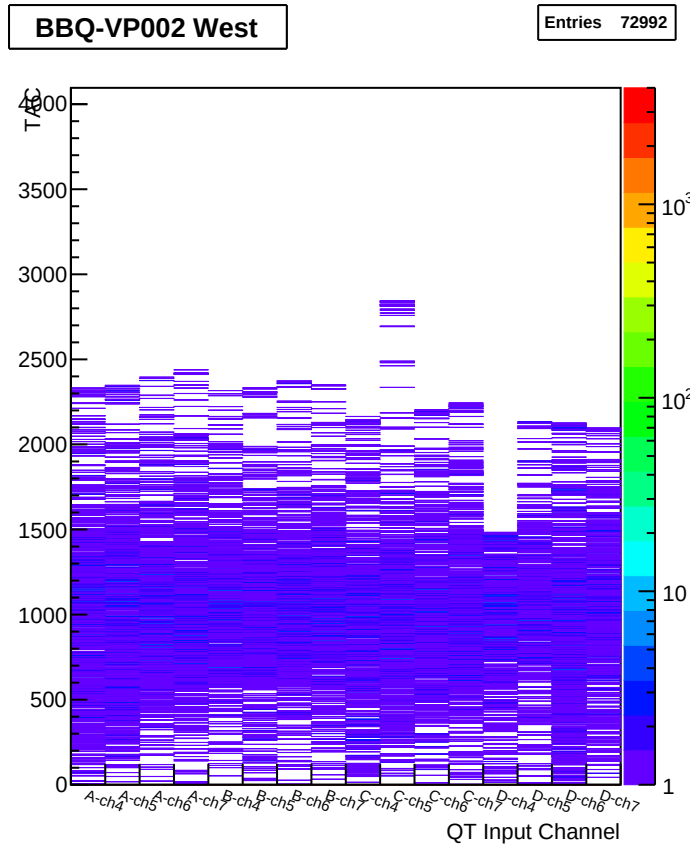

rawzdcEastTac
Entries 4562
10^3
10^2
10
1
0
500
1000
1500
2000
2500
3000
3500
4000

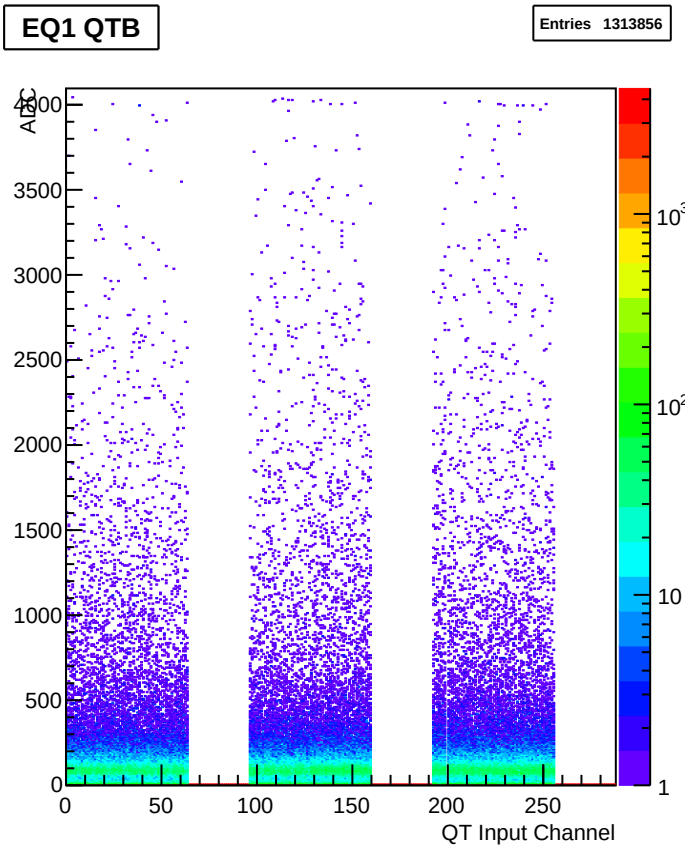

rawzdcWestTac
Entries 4562
10^3
10^2
10
1
0
500
1000
1500
2000
2500
3000
3500
4000

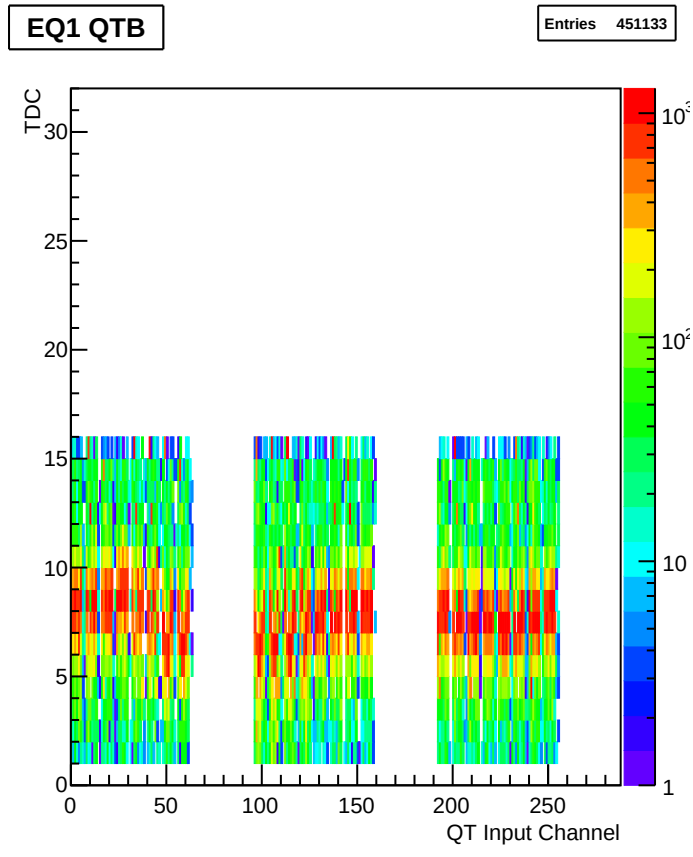

BBQ-VP001 East
Entries 72992
ADC
QT Input Channel

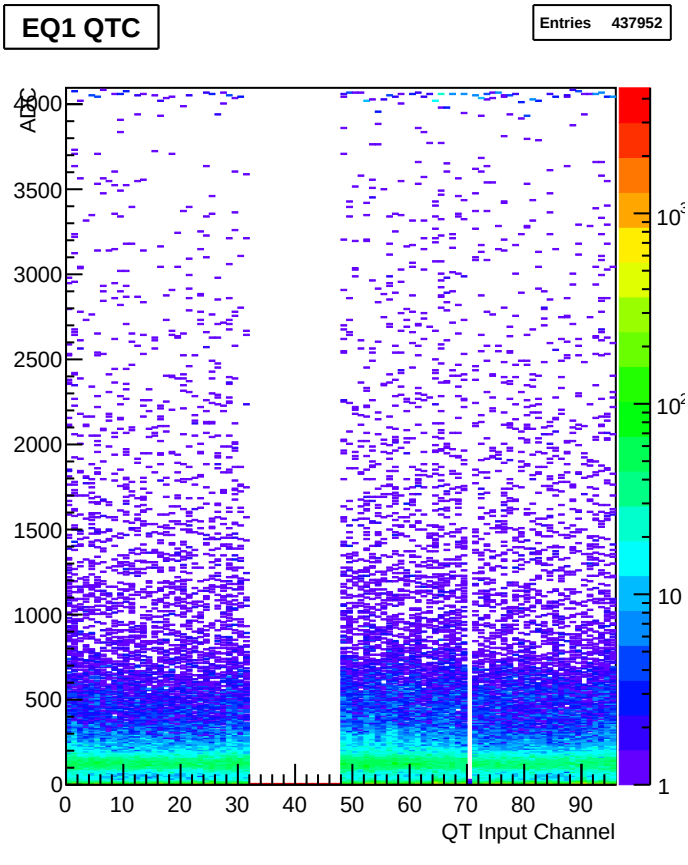

BBQ-VP001 East
Entries 72992
TAC
QT Input Channel

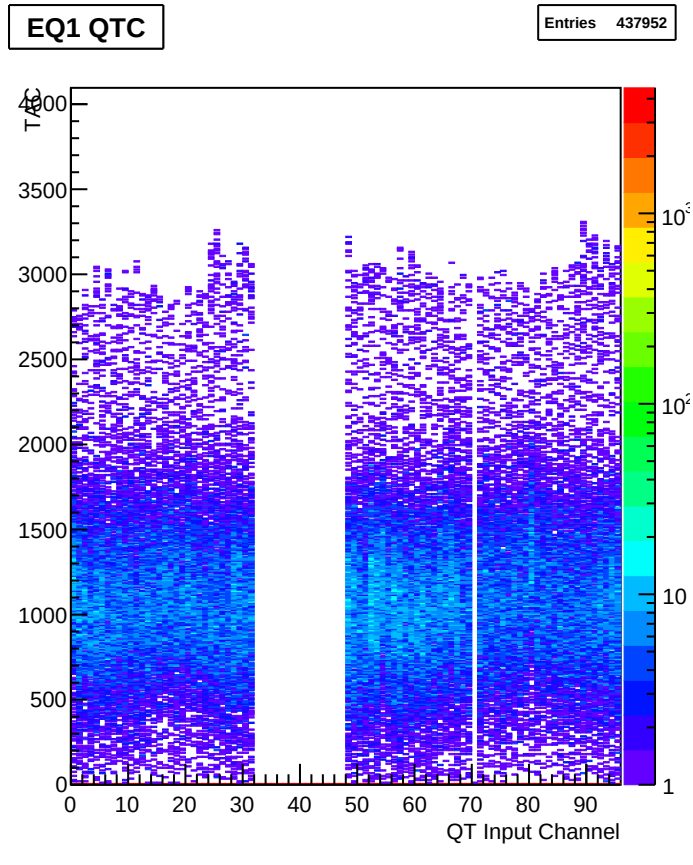

BBQ-VP002 West
Entries 72992
ADC
QT Input Channel

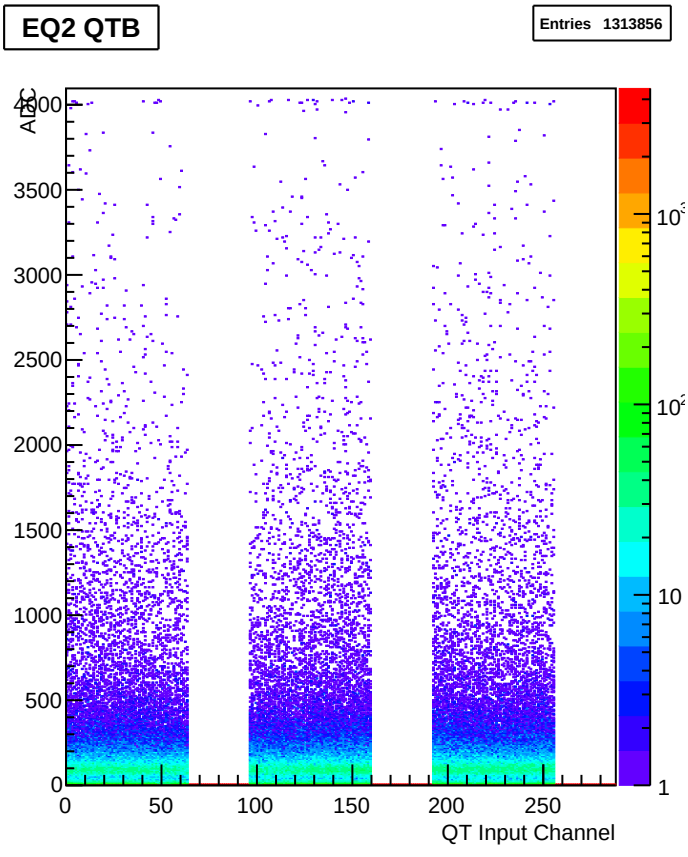

BBQ-VP002 West
Entries 72992
TAC
QT Input Channel

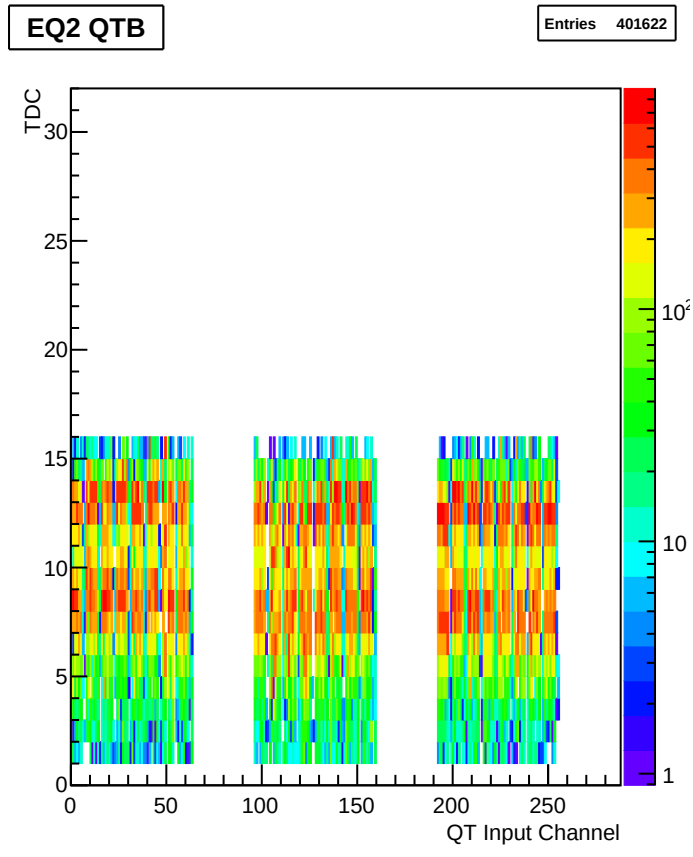

EQ1 QTB
Entries 1313856
ADC
QT Input Channel

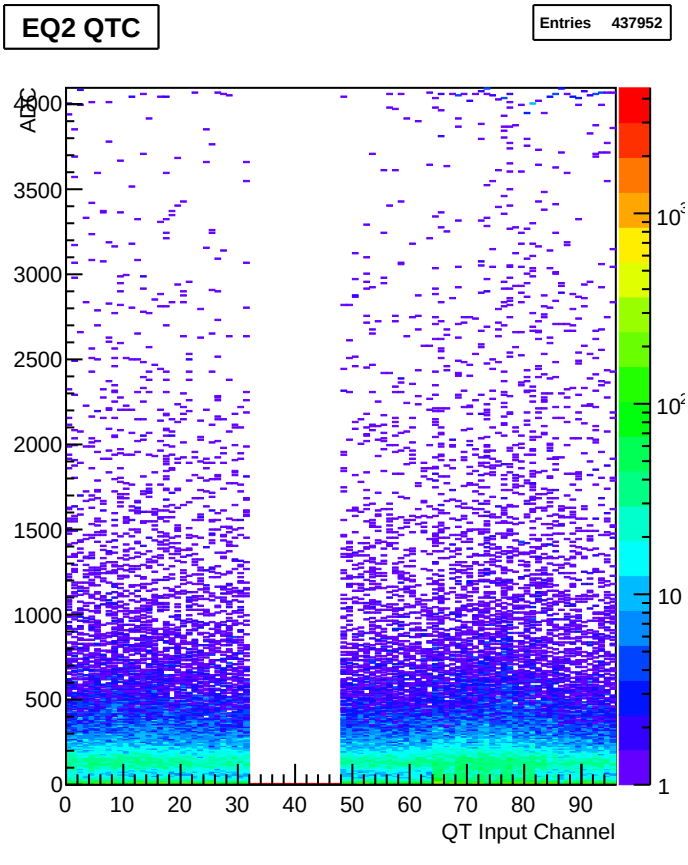

EQ1 QTB
Entries 451133
TDC
QT Input Channel

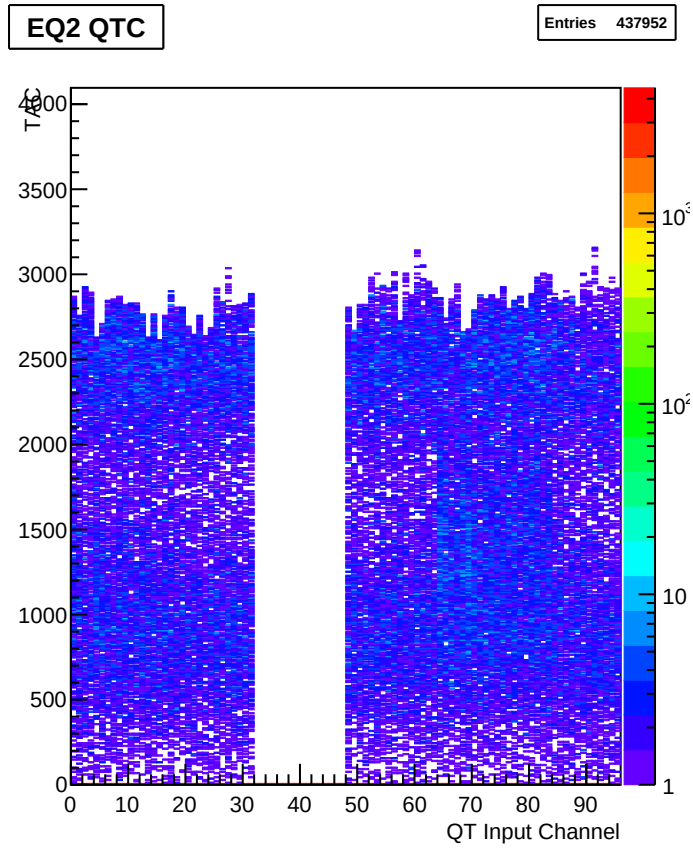

EQ1 QTC
Entries 437952
ADC
QT Input Channel

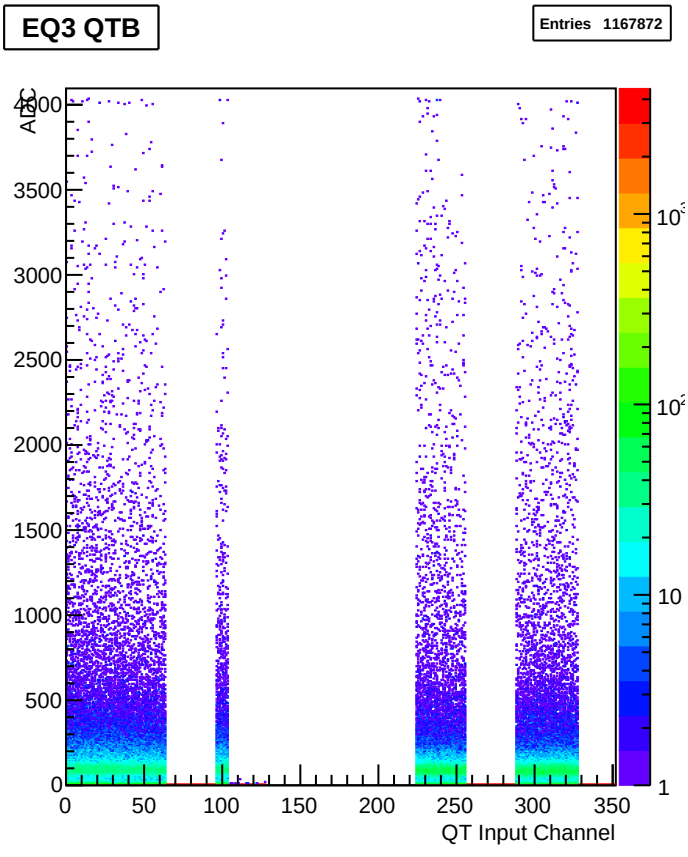

EQ1 QTC
Entries 437952
TADC
QT Input Channel

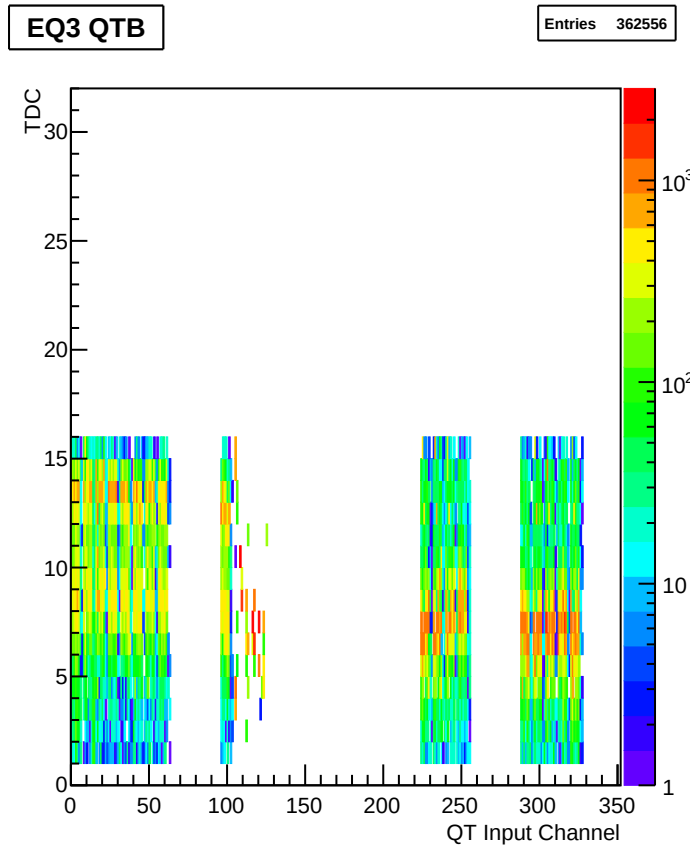

EQ2 QTB
Entries 1313856
ADC
QT Input Channel

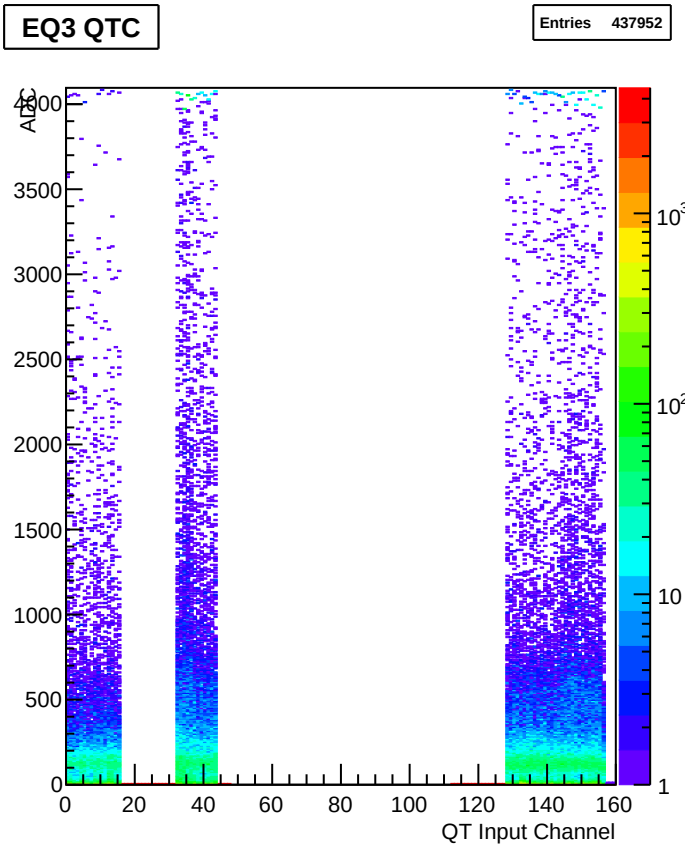

EQ2 QTB
Entries 401622
TDC
QT Input Channel

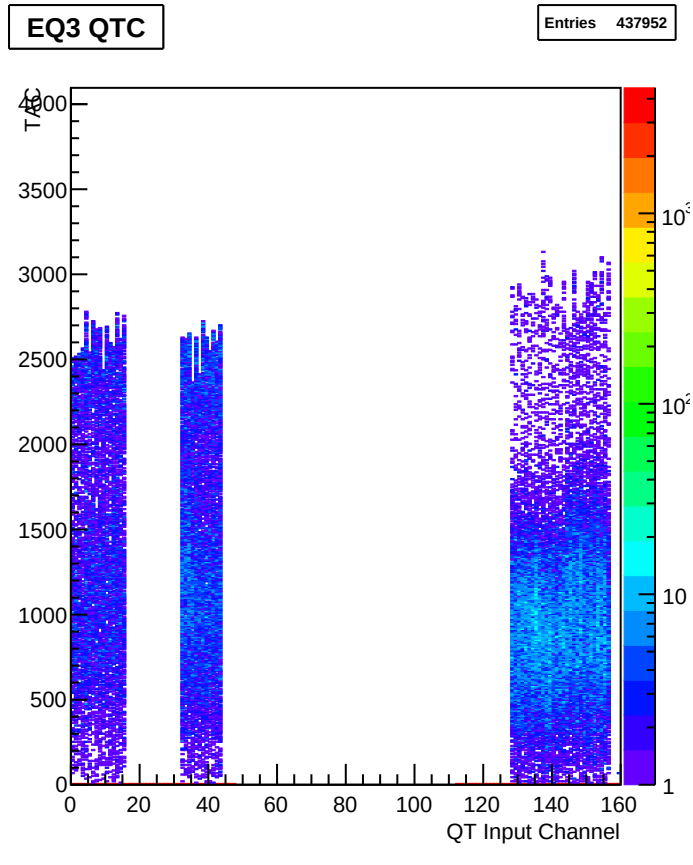

EQ2 QTC
Entries 437952
ADC
QT Input Channel

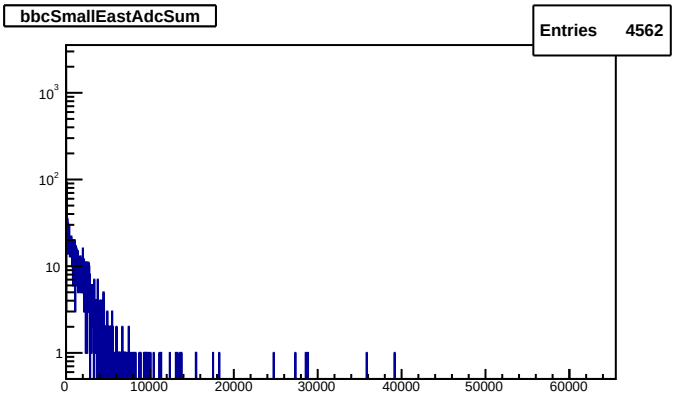

EQ2 QTC
Entries 437952
TADC
QT Input Channel

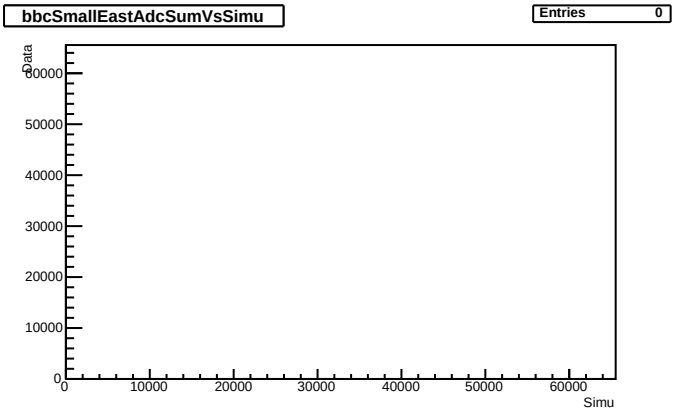

EQ3 QTB
Entries 1167872
ADC
QT Input Channel

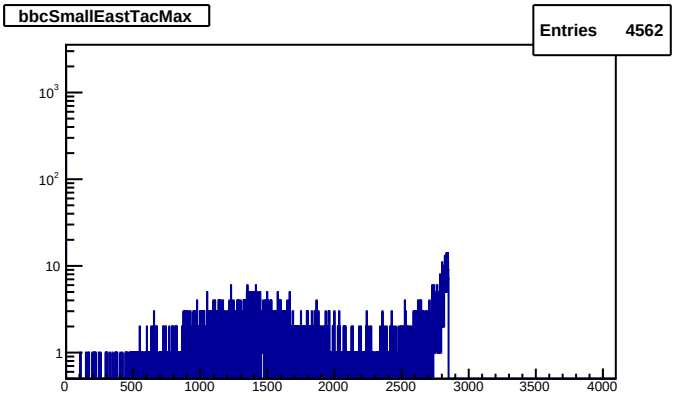

EQ3 QTB
Entries 362556
TDC
QT Input Channel

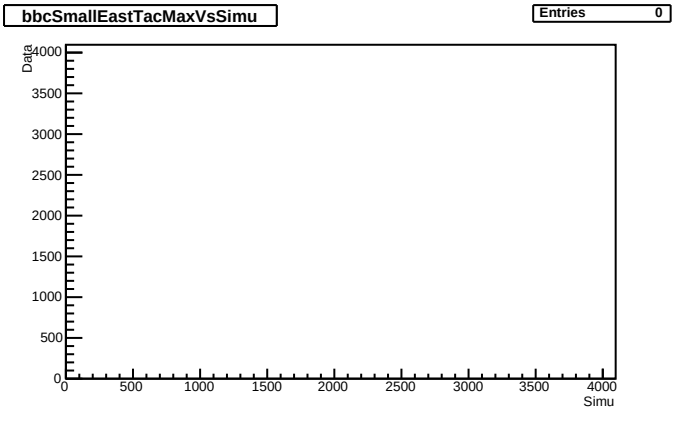

EQ3 QTC
Entries 437952
ADC
QT Input Channel


EQ3 QTC
Entries 437952
TADC
QT Input Channel


bbcSmallEastAdcSum
Entries 4562


bbcSmallEastAdcSumVsSimu
Entries 0
Data
Simu


bbcSmallEastTacMax
Entries 4562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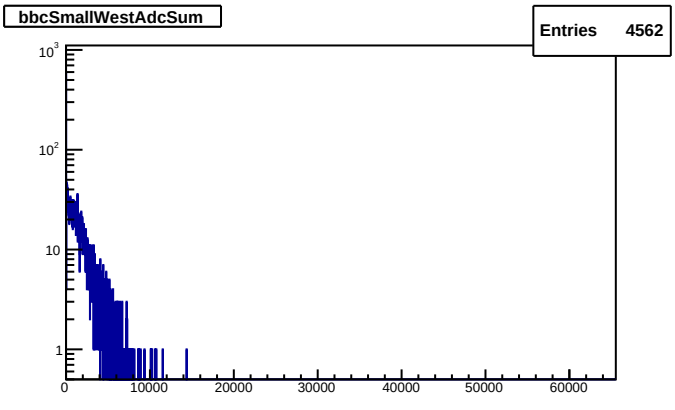
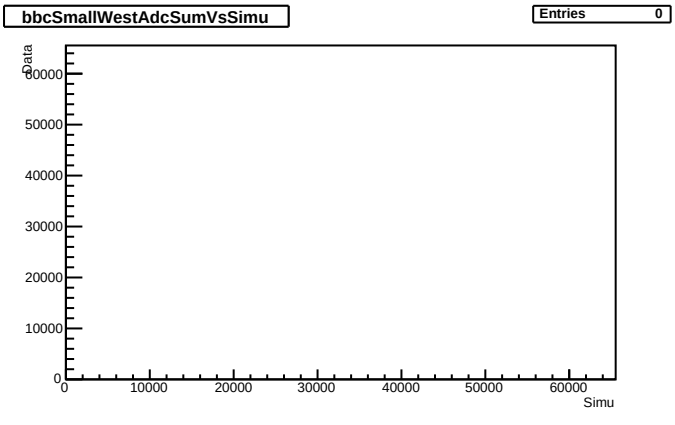
Entries 0
Data
Simu


bbcSmallWestAdcSum
Entries 4562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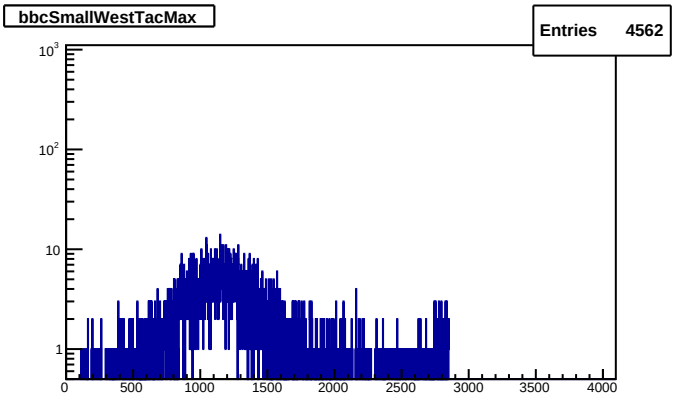
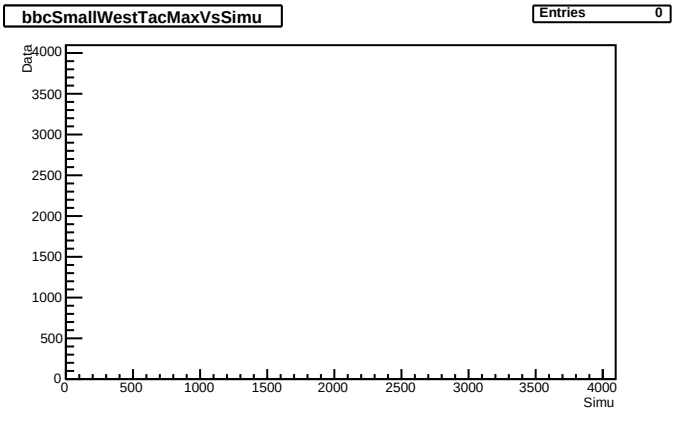
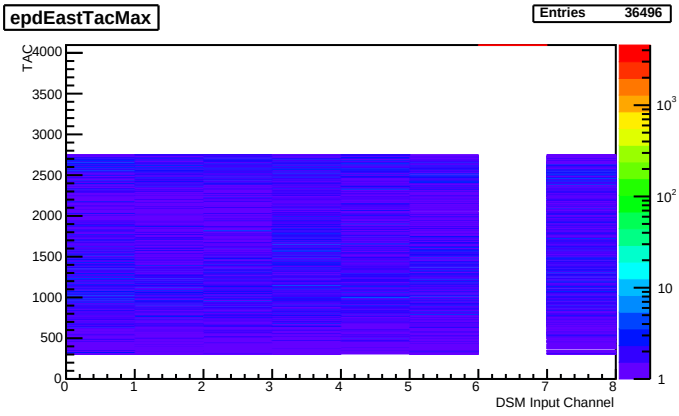
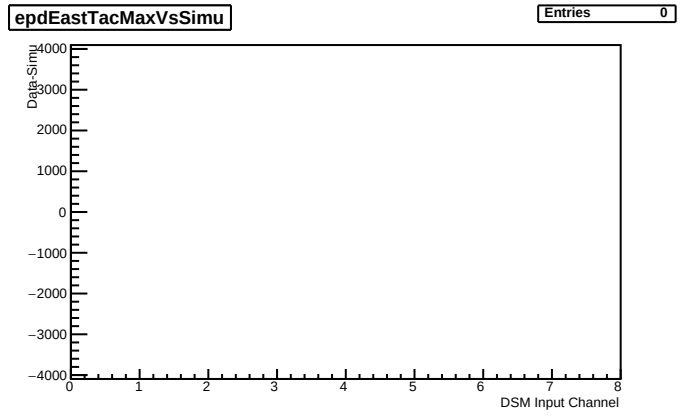
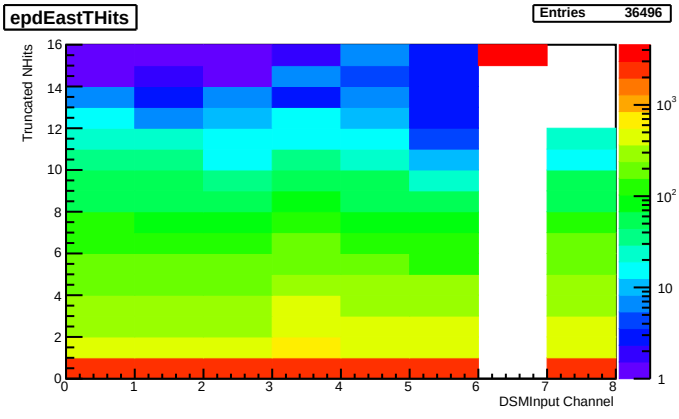
Entries 0
Data
Simu


bbcSmallWestTacMax
Entries 4562


bbcSmallWestTacMaxVsSimu
Entries 0
Data
Simu


epdEastTacMax
Entries 36496
TAC
DSM Input Channel


epdEastTacMaxVsSimu
Entries 0
Data-Simu
DSM Input Channel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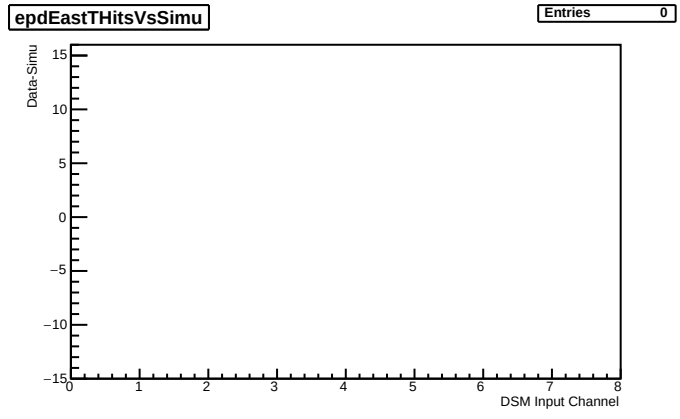
Entries 36496
Truncated NHits
DSMInput Channel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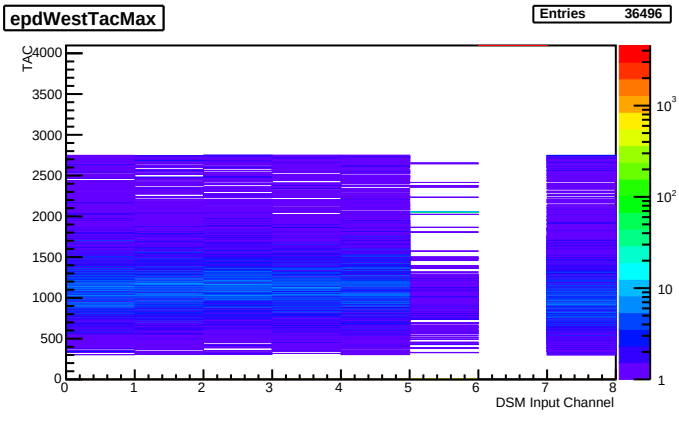
Data-Simu
DSM Input Channel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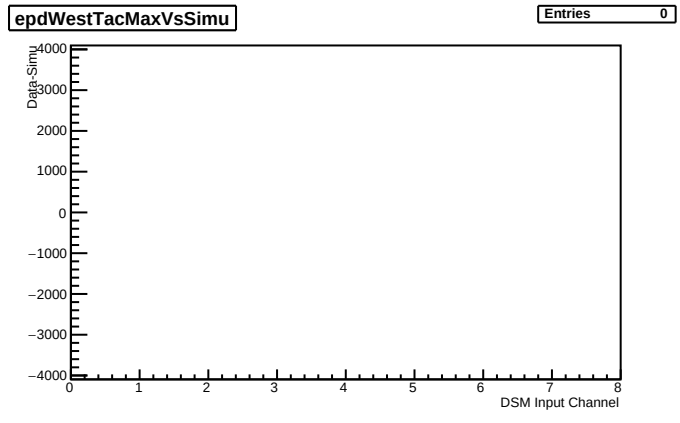
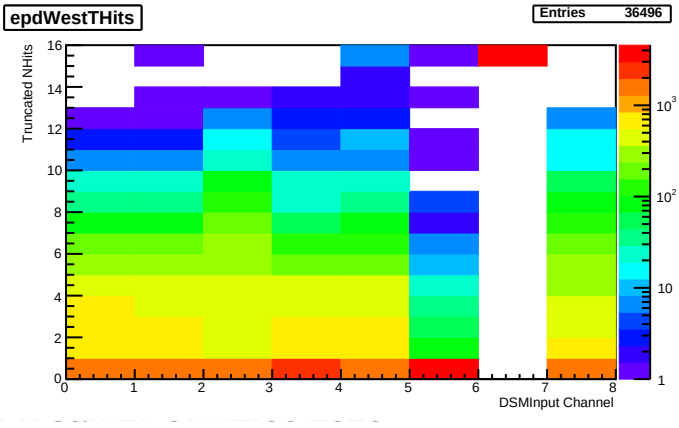
Entries 36496
TAC
DSM Input Channel


epdWestTacMaxVsSimu
Entries 0
Data-Simu
DSM Input Channel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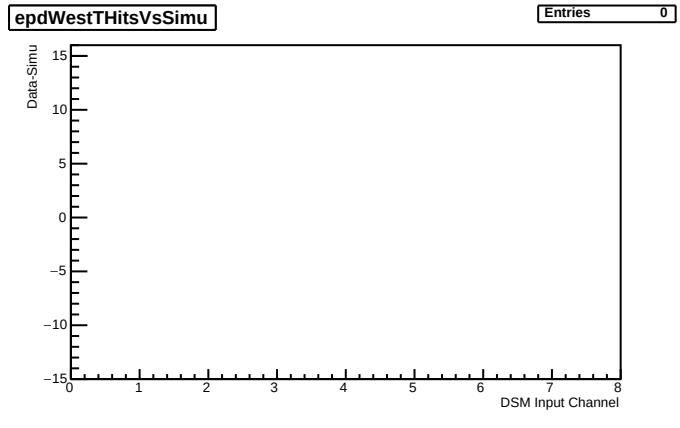
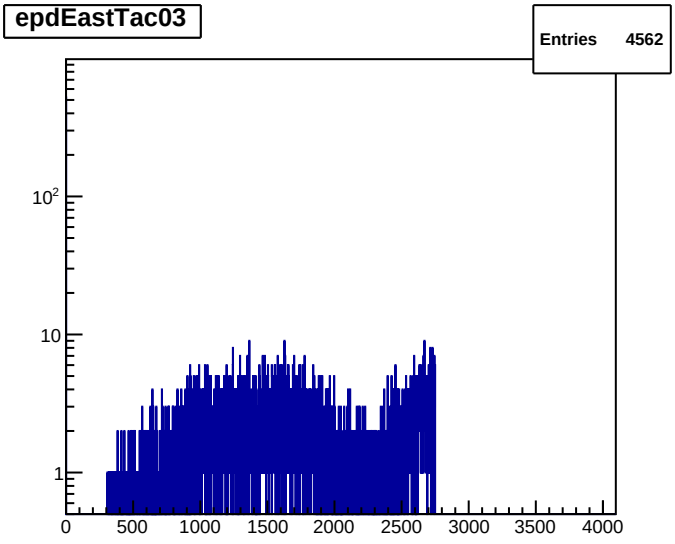
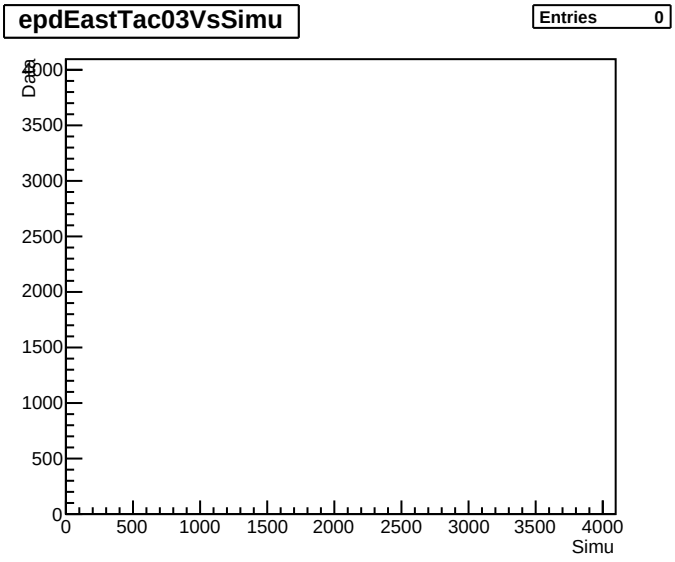
Entries 36496
Truncated NHits
DSMInput Channel


epdWestTHitsVsSimu
Entries 0
Data-Simu
DSM Input Channel


epdEastTac03
Entries 4562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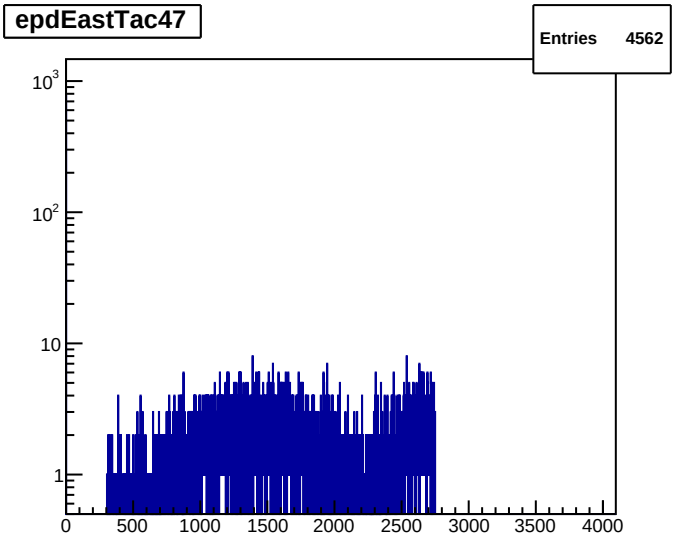
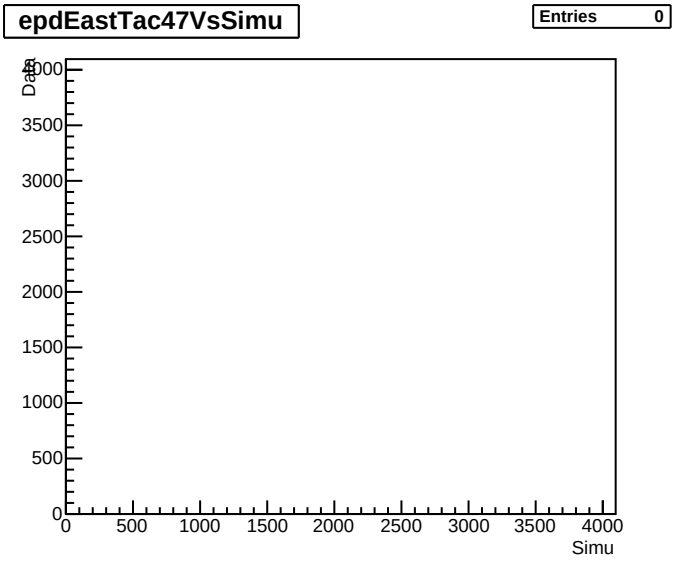
Data
Simu


epdEastTac47
Entries 4562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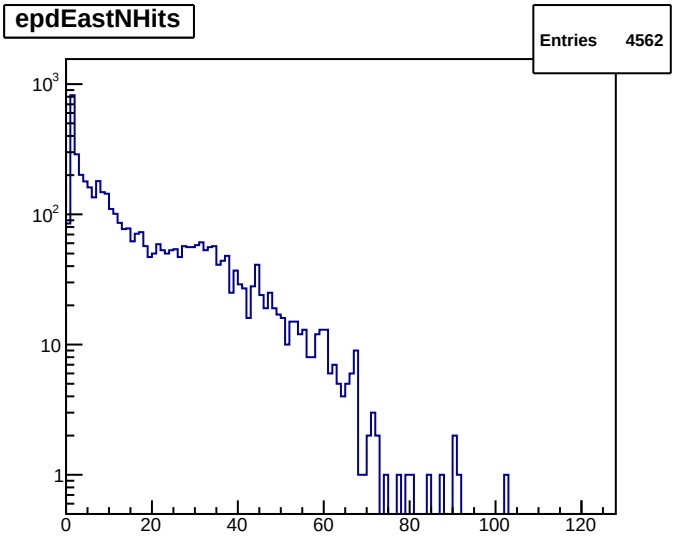
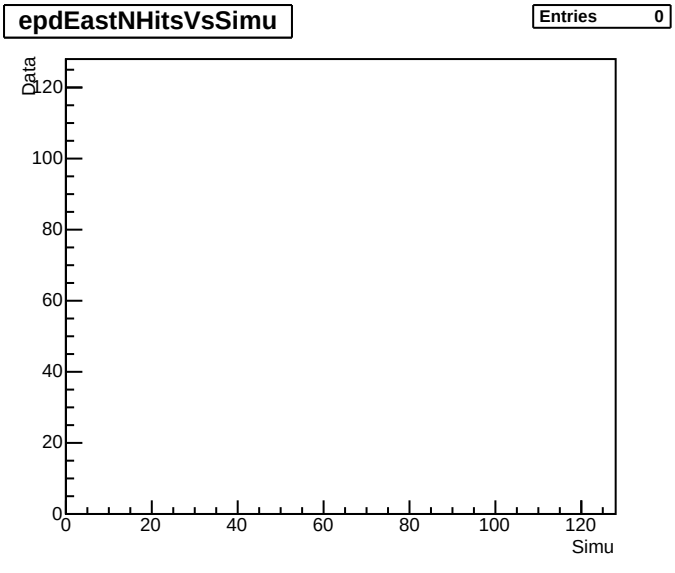
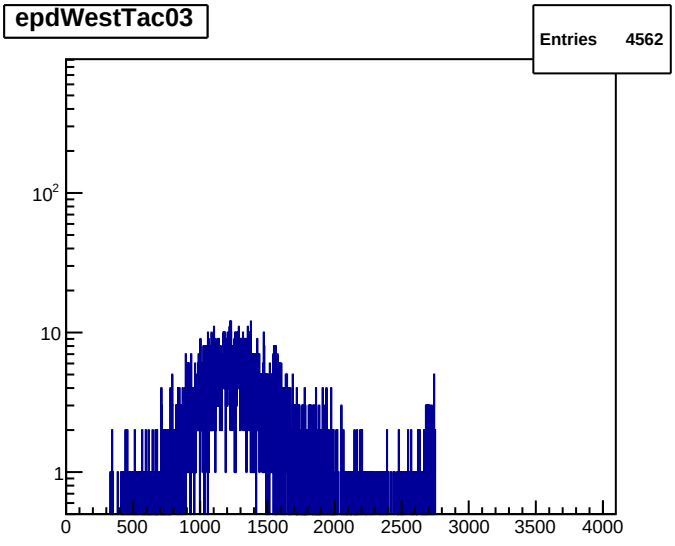
Entries 0
Data
Simu


epdEastNHits
Entries 4562


epdEastNHitsVsSimu
Entries 0
Data
Simu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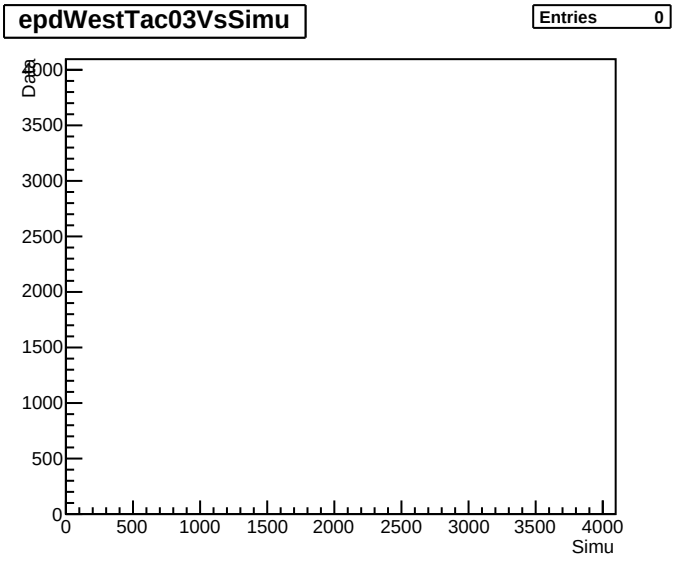
Entries 4562
10^2
10
1
0 500 1000 1500 2000 2500 3000 3500 4000


epdWestTac03VsSimu
Entries 0
Data
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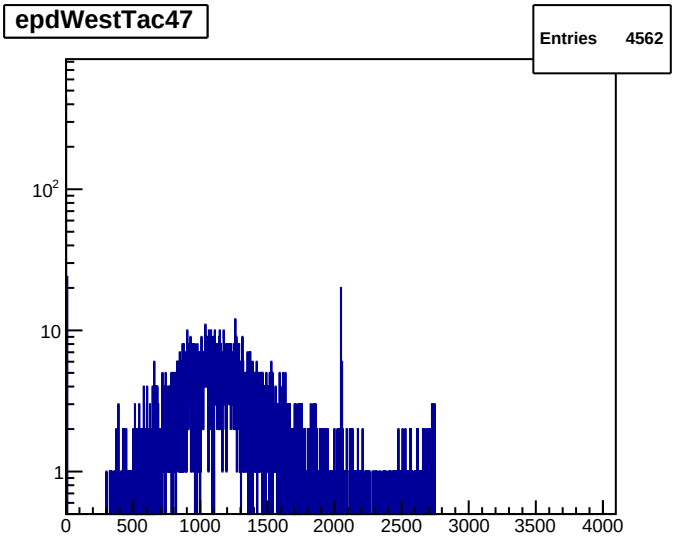
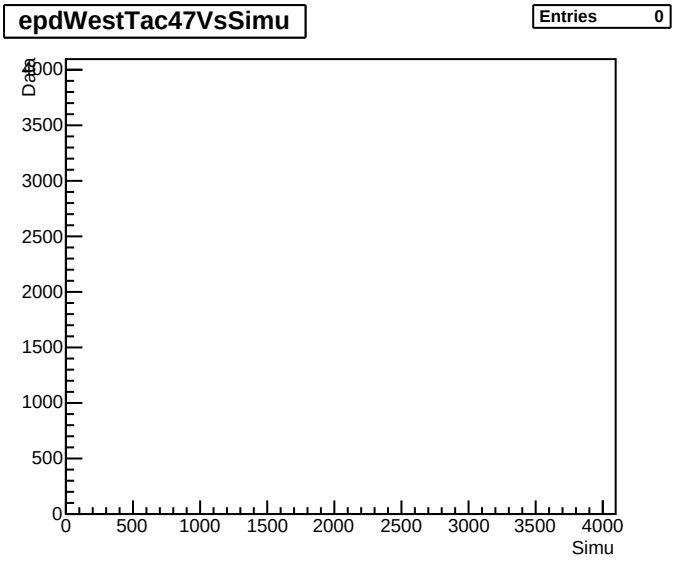
0 500 1000 1500 2000 2500 3000 3500 4000
Simu


epdWestTac47
Entries 4562
10^2
10
1
0 500 1000 1500 2000 2500 3000 3500 4000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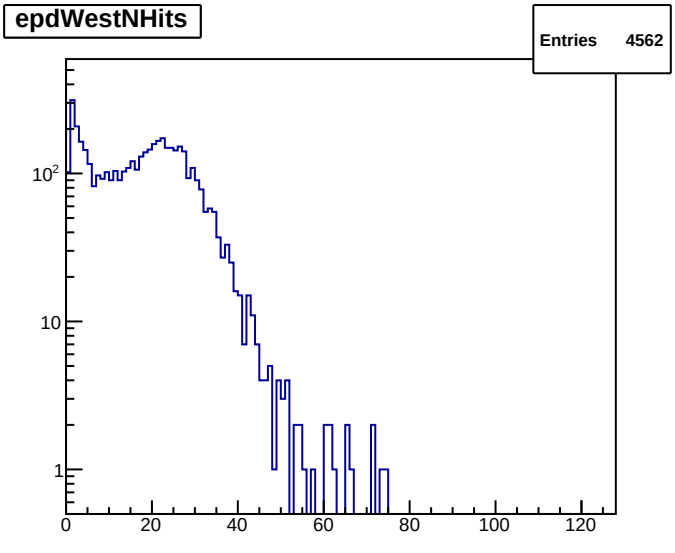
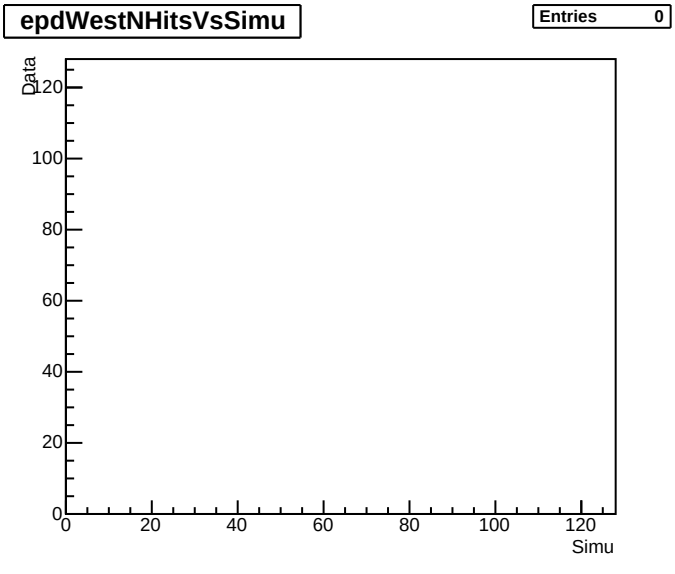
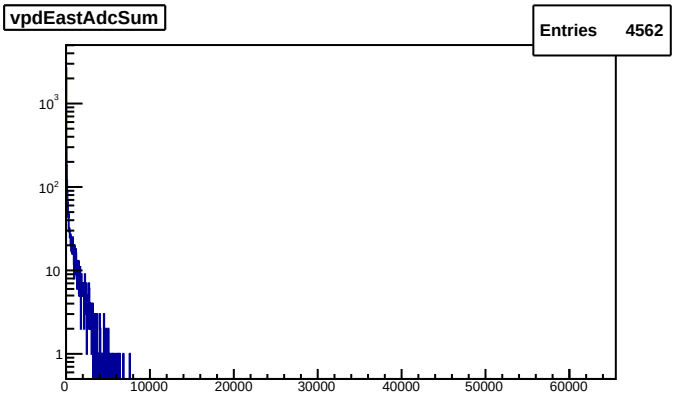
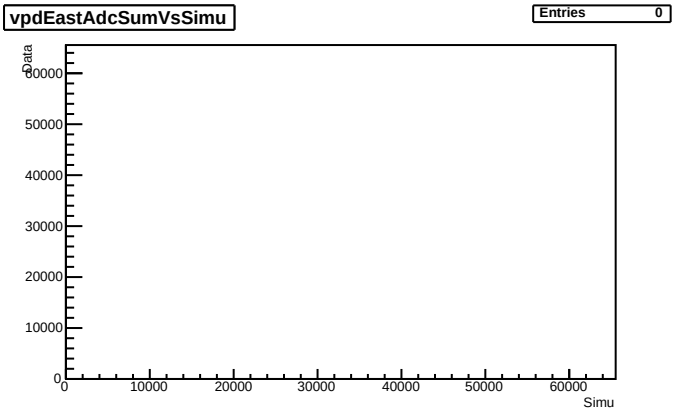
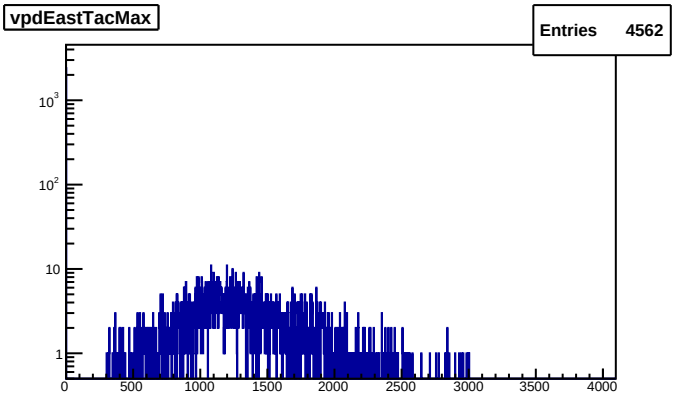
Entries 0
Data
4000 3500 3000 2500 2000 1500 1000 500 0
0 500 1000 1500 2000 2500 3000 3500 4000
Simu

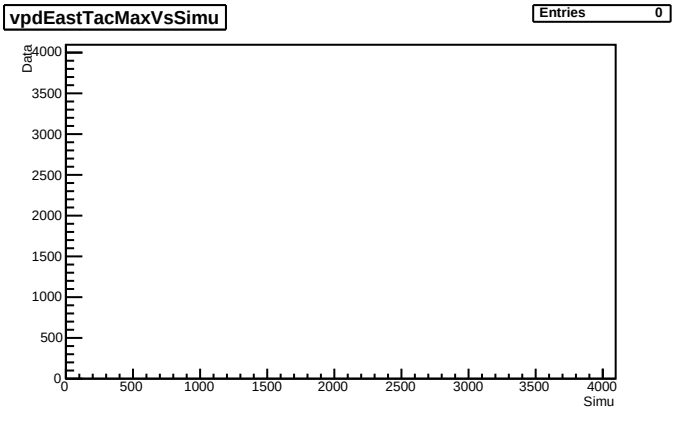

epdWestNHits
Entries 4562
10^2
10
1
0 20 40 60 80 100 120

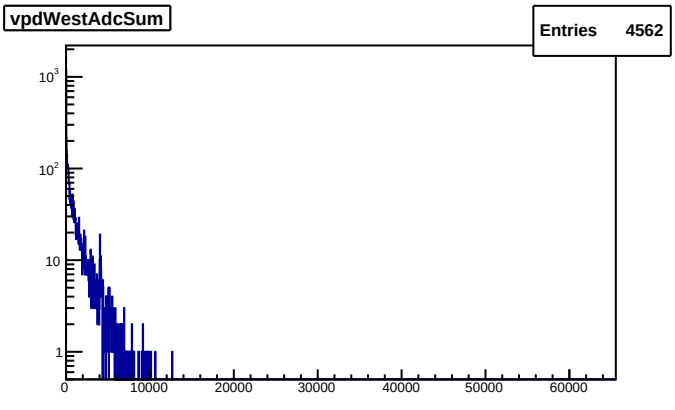

epdWestNHitsVsSimu
Entries 0
Data
120 100 80 60 40 20 0
0 20 40 60 80 100 120
Simu

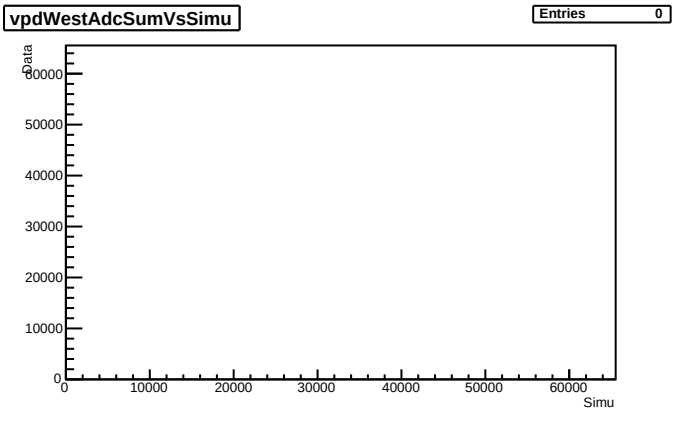

vpdEastAdcSum
Entries 4562

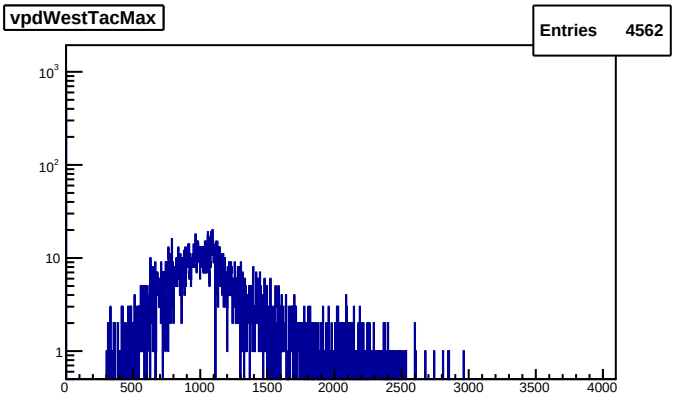

vpdEastAdcSumVsSimu
Entries 0
Data
Simu

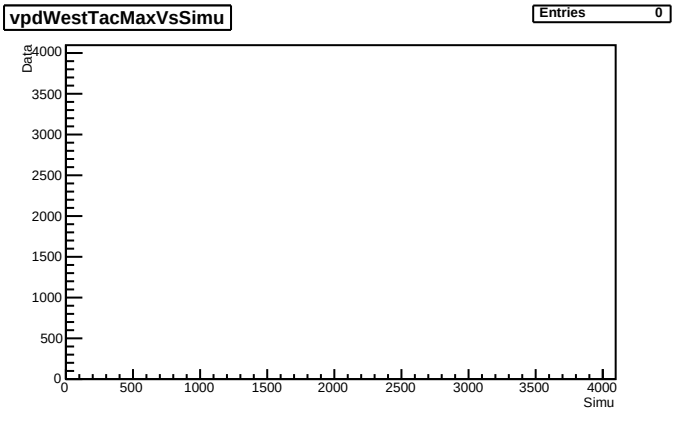

vpdEastTacMax
Entries 4562

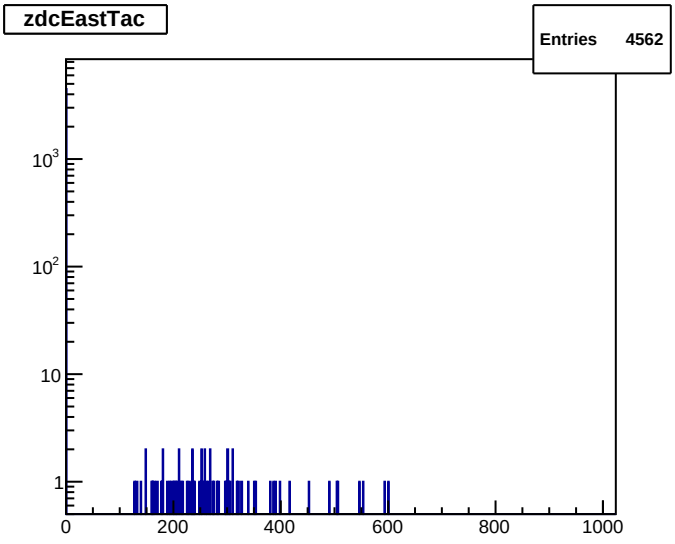

vpdEastTacMaxVsSimu
Entries 0
Data
Simu

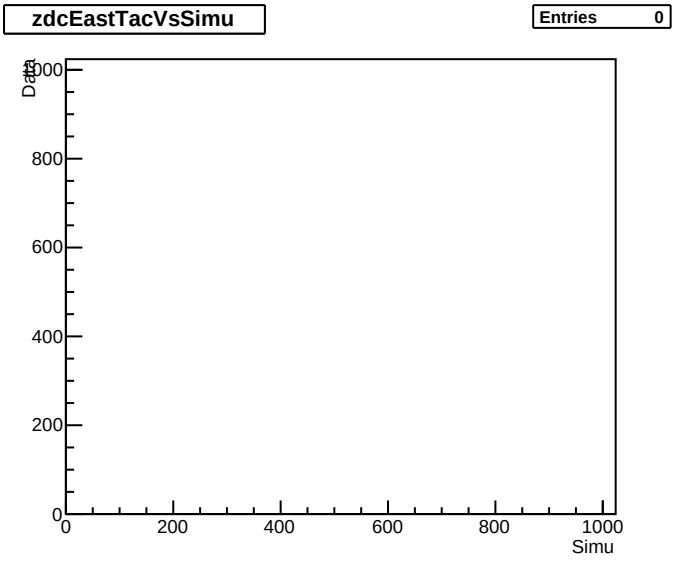

vpdWestAdcSum
Entries 4562

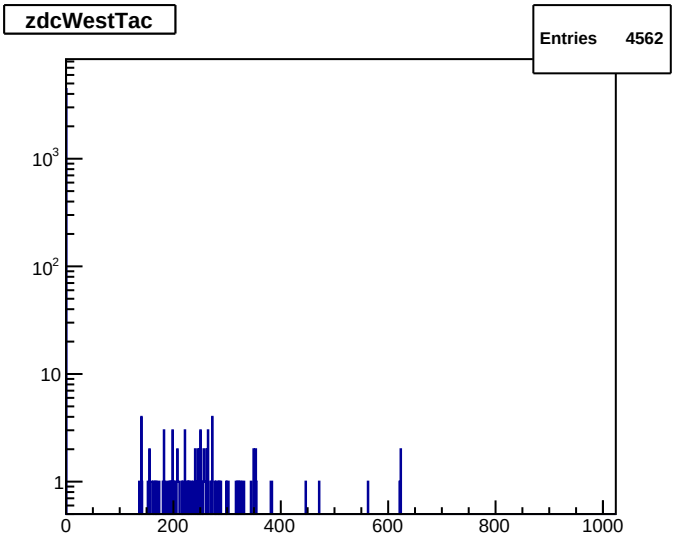

vpdWestAdcSumVsSimu
Entries 0
Data
Simu

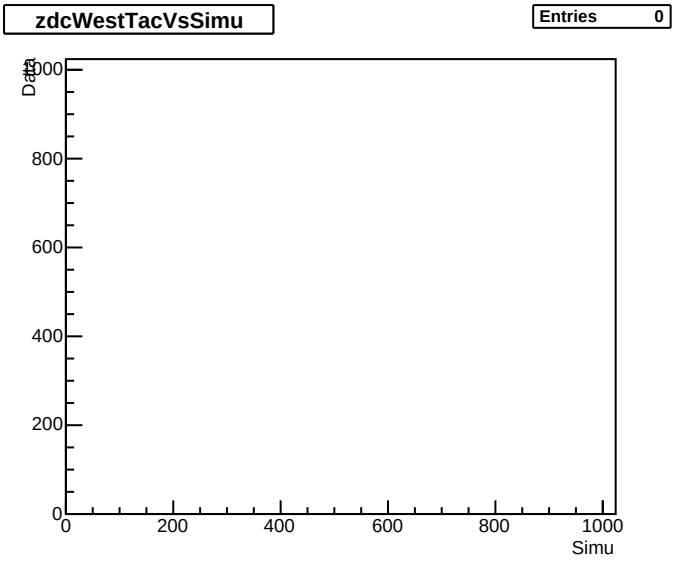

vpdWestTacMax
Entries 4562

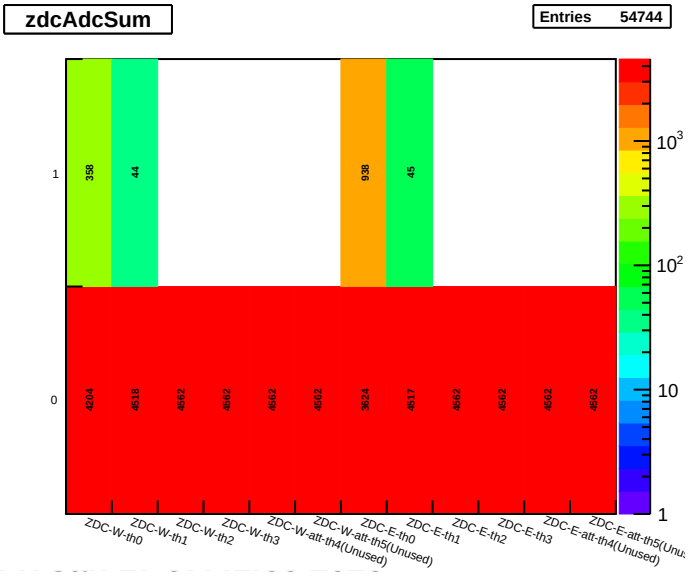

vpdWestTacMaxVsSimu
Entries 0
Data
Simu

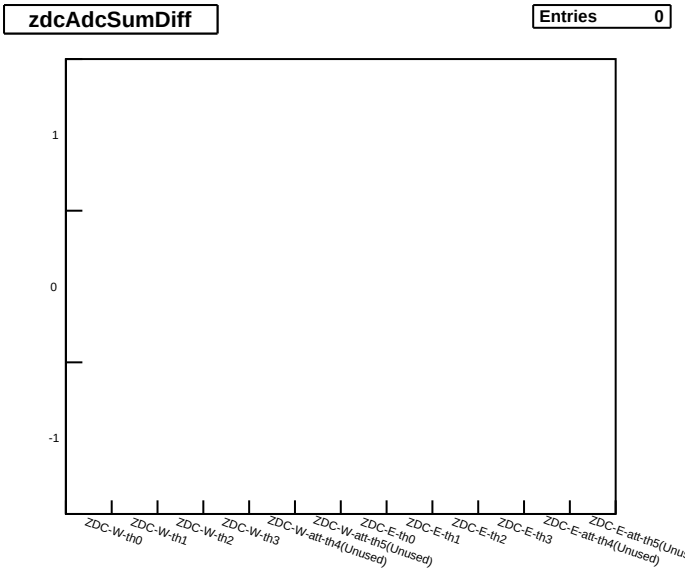

zdcEastTac
Entries 4562
10^3
10^2
10
1
0
200
400
600
800
1000

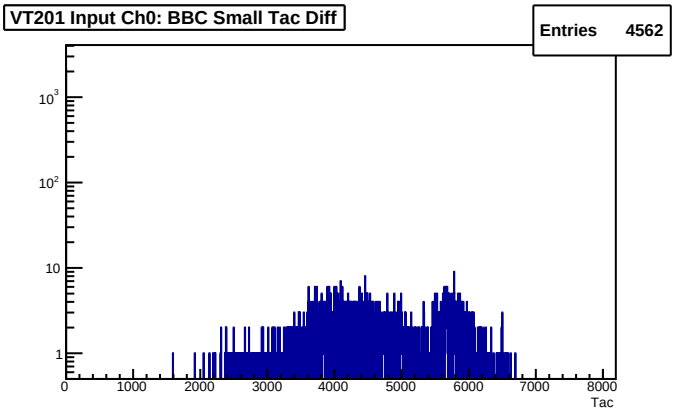

zdcEastTacVsSimu
Entries 0
Data
1000
800
600
400
200
0
0
200
400
600
800
1000
Simu

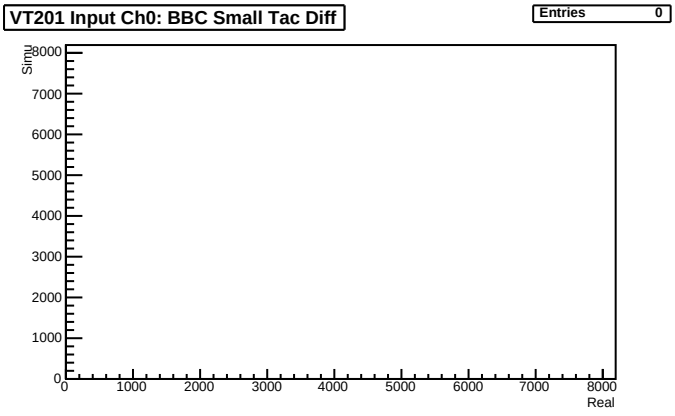

zdcWestTac
Entries 4562
10^3
10^2
10
1
0
200
400
600
800
1000

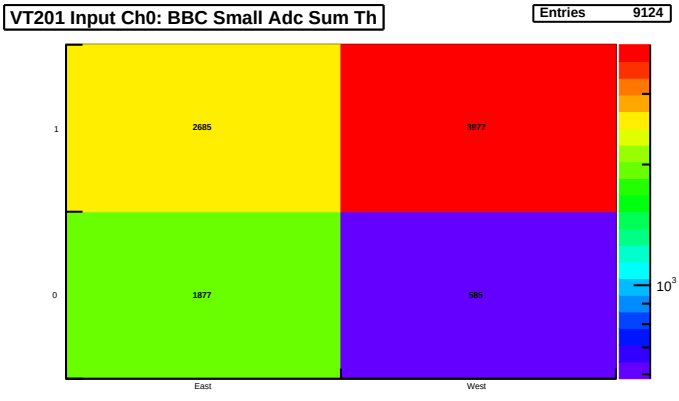

zdcWestTacVsSimu
Entries 0
Data
1000
800
600
400
200
0
0
200
400
600
800
1000
Simu

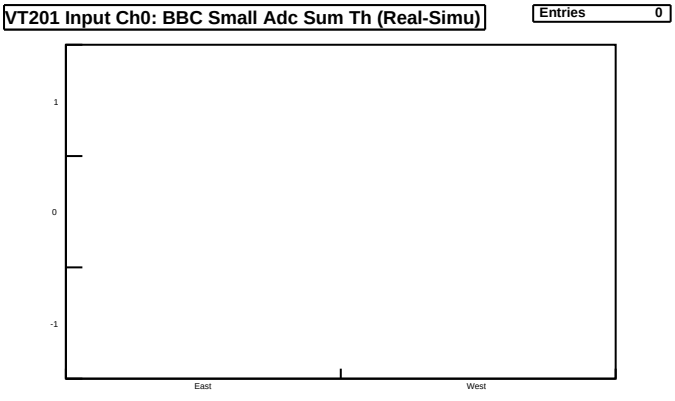

zdcAdcSum
Entries 54744
1
0
358
44
938
45
4204
4518
4562
4562
4562
4562
3624
4517
4562
4562
4562
4562
10^3
10^2
10
1
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)
ZDC-E-att-th5(Unu

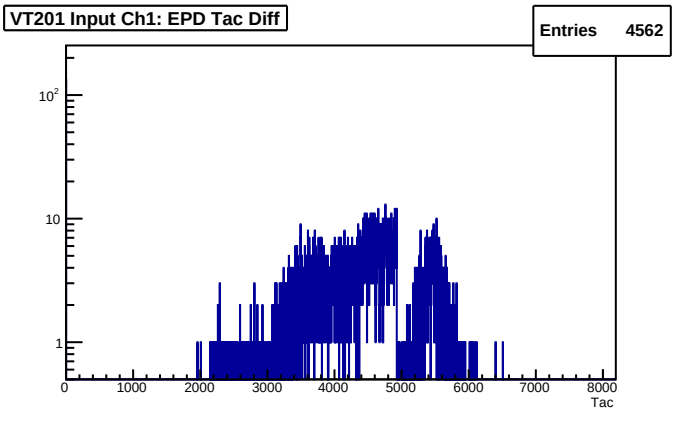

zdcAdcSumDiff
Entries 0
1
0
-1
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)
ZDC-E-att-th5(Unu

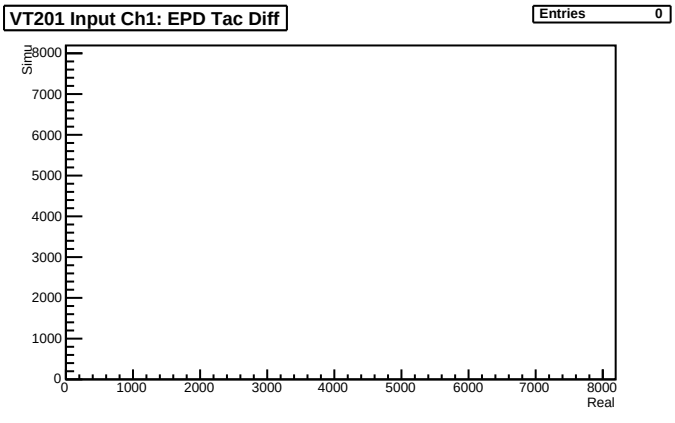

VT201 Input Ch0: BBC Small Tac Diff
Entries 4562
Tac

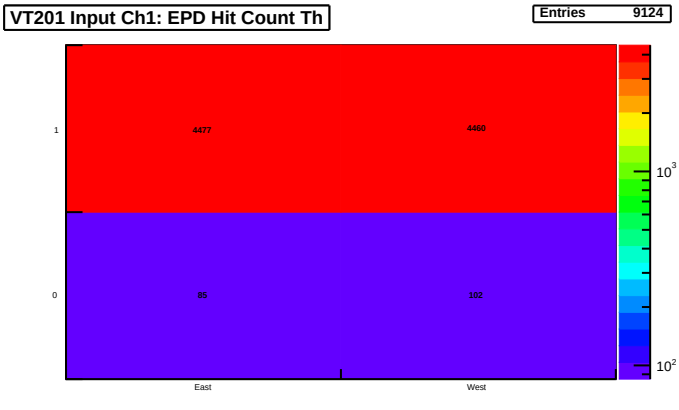

VT201 Input Ch0: BBC Small Tac Diff
Entries 0
Simu
Real

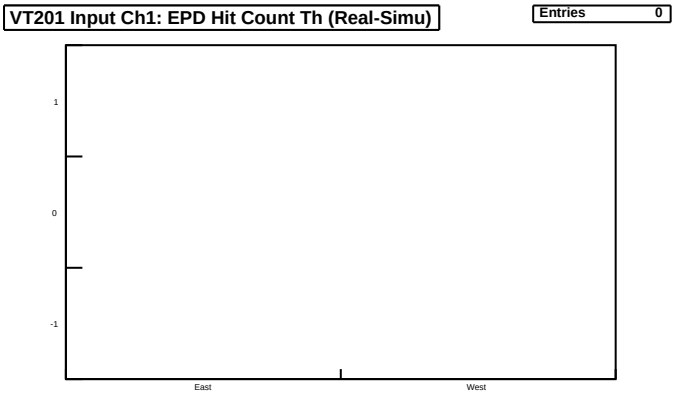

VT201 Input Ch0: BBC Small Adc Sum Th
Entries 9124
2685
3977
1877
585
East
West

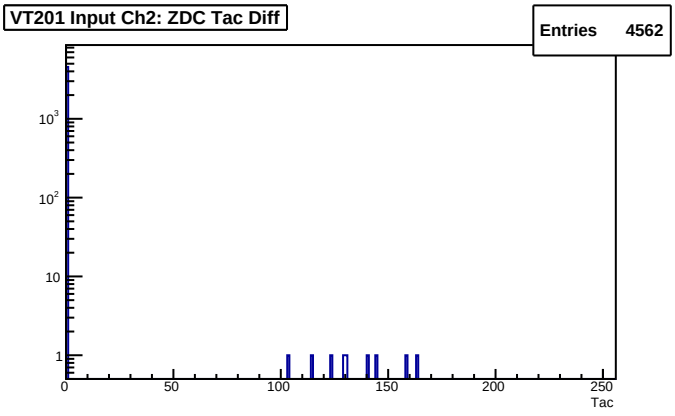

VT201 Input Ch0: BBC Small Adc Sum Th (Real-Simu)
Entries 0
East
West

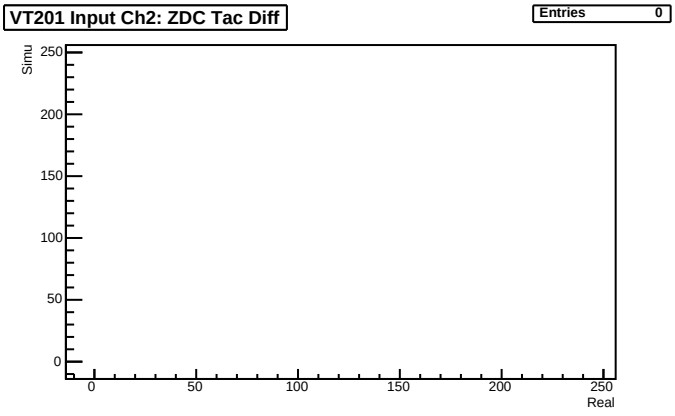

VT201 Input Ch1: EPD Tac Diff
Entries 4562
Tac

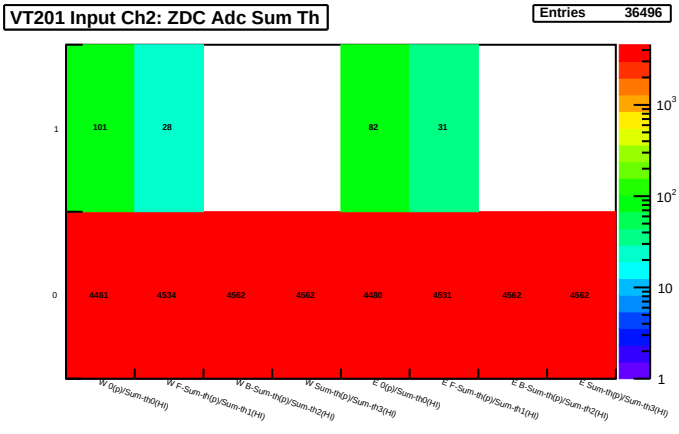

VT201 Input Ch1: EPD Tac Diff
Entries 0
Simu
Real

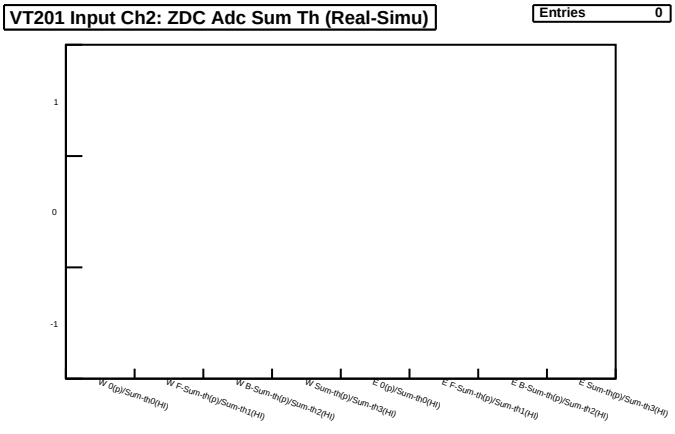

VT201 Input Ch1: EPD Hit Count Th
Entries 9124
4477
4460
85
102
East
West

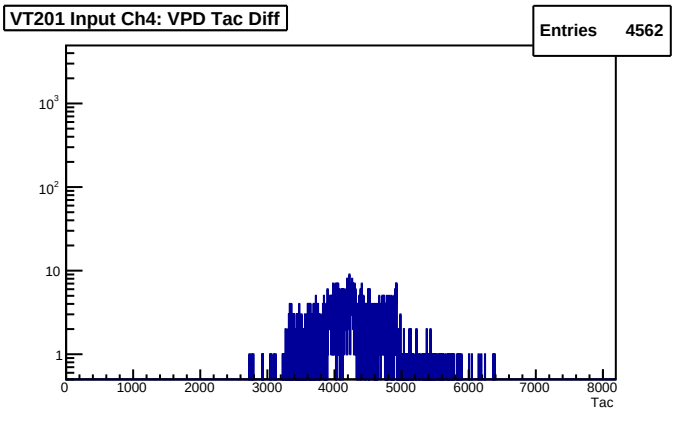

VT201 Input Ch1: EPD Hit Count Th (Real-Simu)
Entries 0
East
West

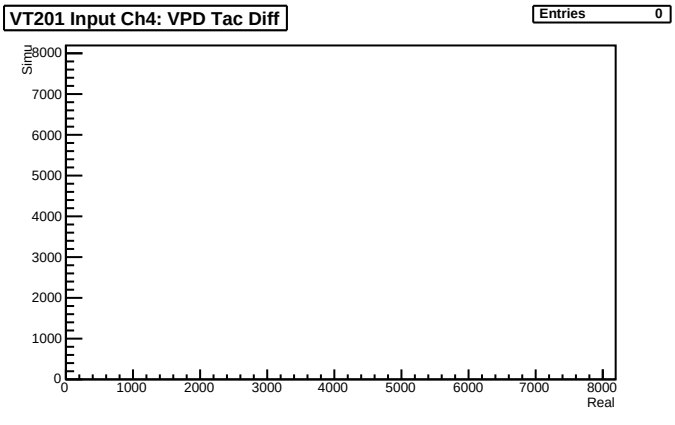

VT201 Input Ch2: ZDC Tac Diff
Entries 4562
Tac

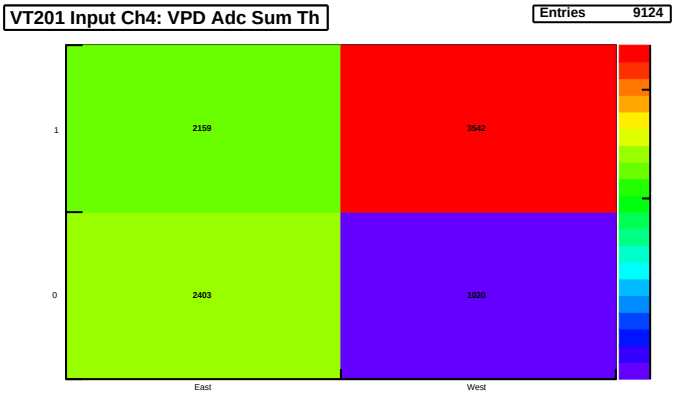

VT201 Input Ch2: ZDC Tac Diff
Entries 0
Simu
Real

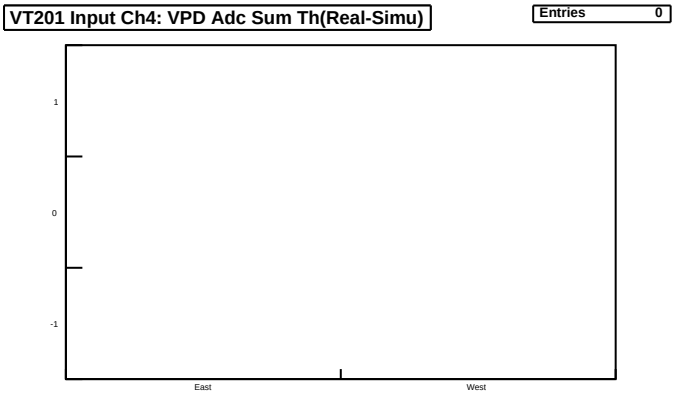

VT201 Input Ch2: ZDC Adc Sum Th
Entries 36496
101
28
82
31
4461
4534
4562
4562
4480
4531
4562
4562
W 0(p)/Sum-th0(H)
W F-Sum-th(p)/Sum-th1(H)
W B-Sum-th(p)/Sum-th2(H)
W Sum-th(p)/Sum-th3(H)
E 0(p)/Sum-th0(H)
E F-Sum-th(p)/Sum-th1(H)
E B-Sum-th(p)/Sum-th2(H)
E Sum-th(p)/Sum-th3(H)

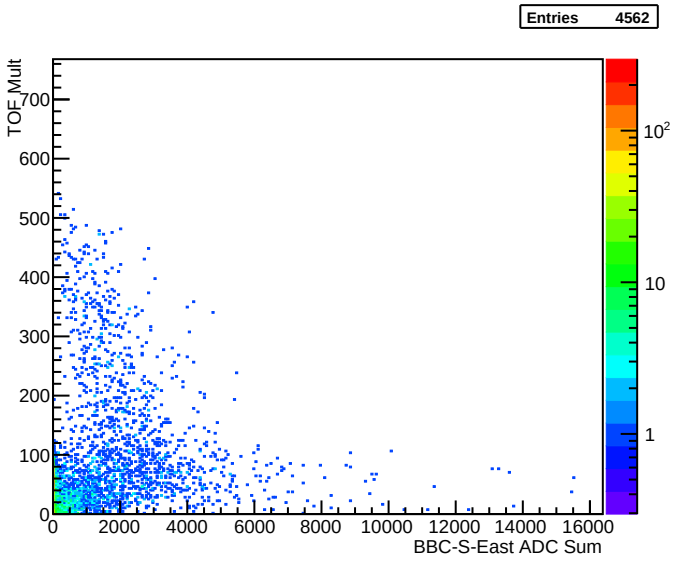

VT201 Input Ch2: ZDC Adc Sum Th (Real-Simu)
Entries 0
W 0(p)/Sum-th0(H)
W F-Sum-th(p)/Sum-th1(H)
W B-Sum-th(p)/Sum-th2(H)
W Sum-th(p)/Sum-th3(H)
E 0(p)/Sum-th0(H)
E F-Sum-th(p)/Sum-th1(H)
E B-Sum-th(p)/Sum-th2(H)
E Sum-th(p)/Sum-th3(H)

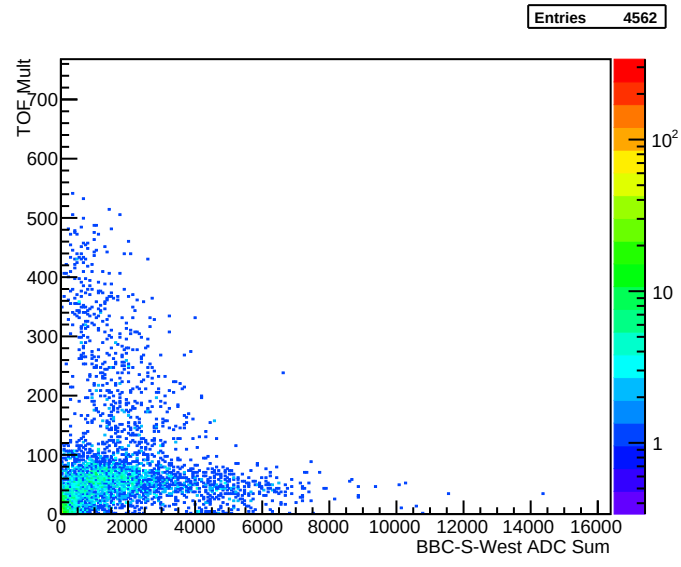

VT201 Input Ch4: VPD Tac Diff
Entries 4562
Tac

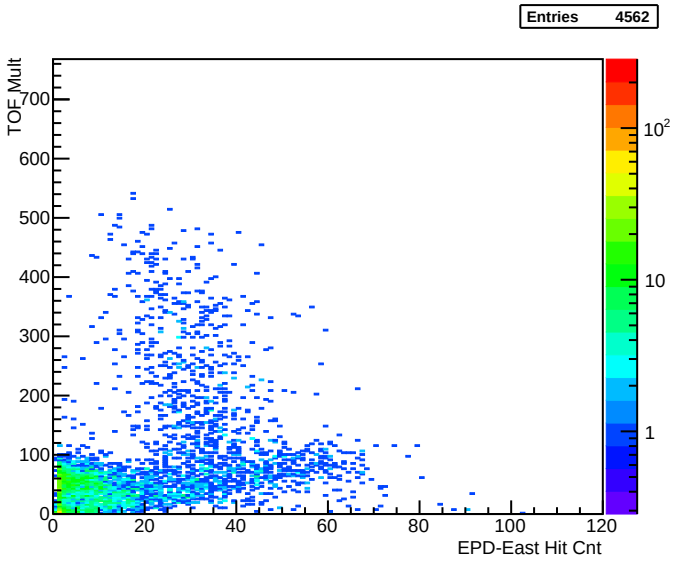

VT201 Input Ch4: VPD Tac Diff
Entries 0
Simu
Real

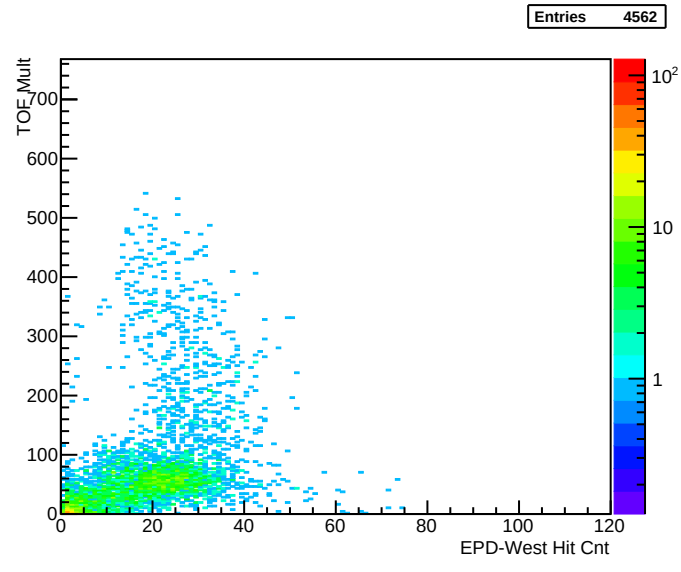

VT201 Input Ch4: VPD Adc Sum Th
Entries 9124
2159
3542
2403
1020
East
West

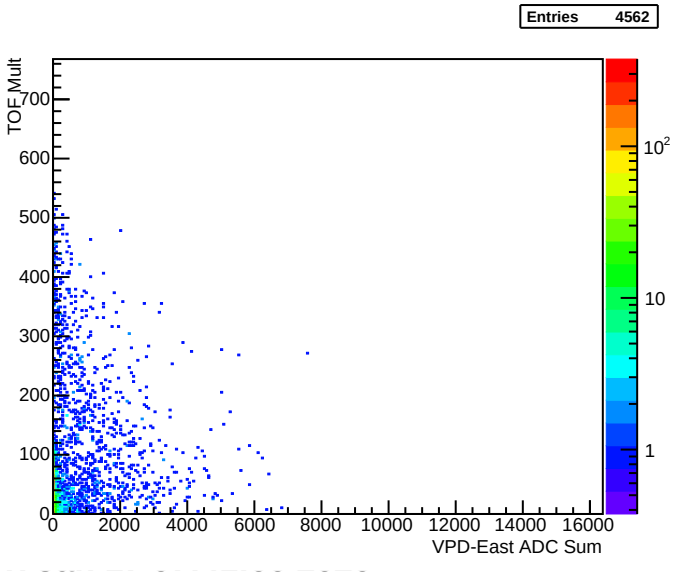

VT201 Input Ch4: VPD Adc Sum Th(Real-Simu)
Entries 0
East
West

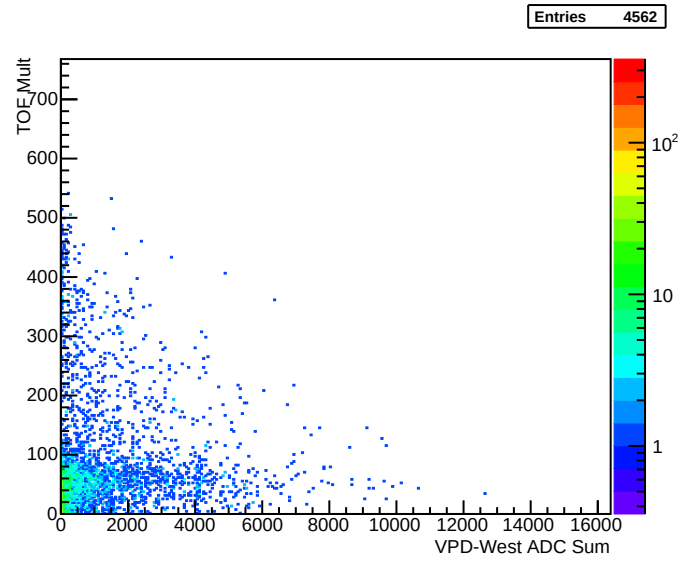

Entries 4562
TOF_Mult
BBC-S-East ADC Sum

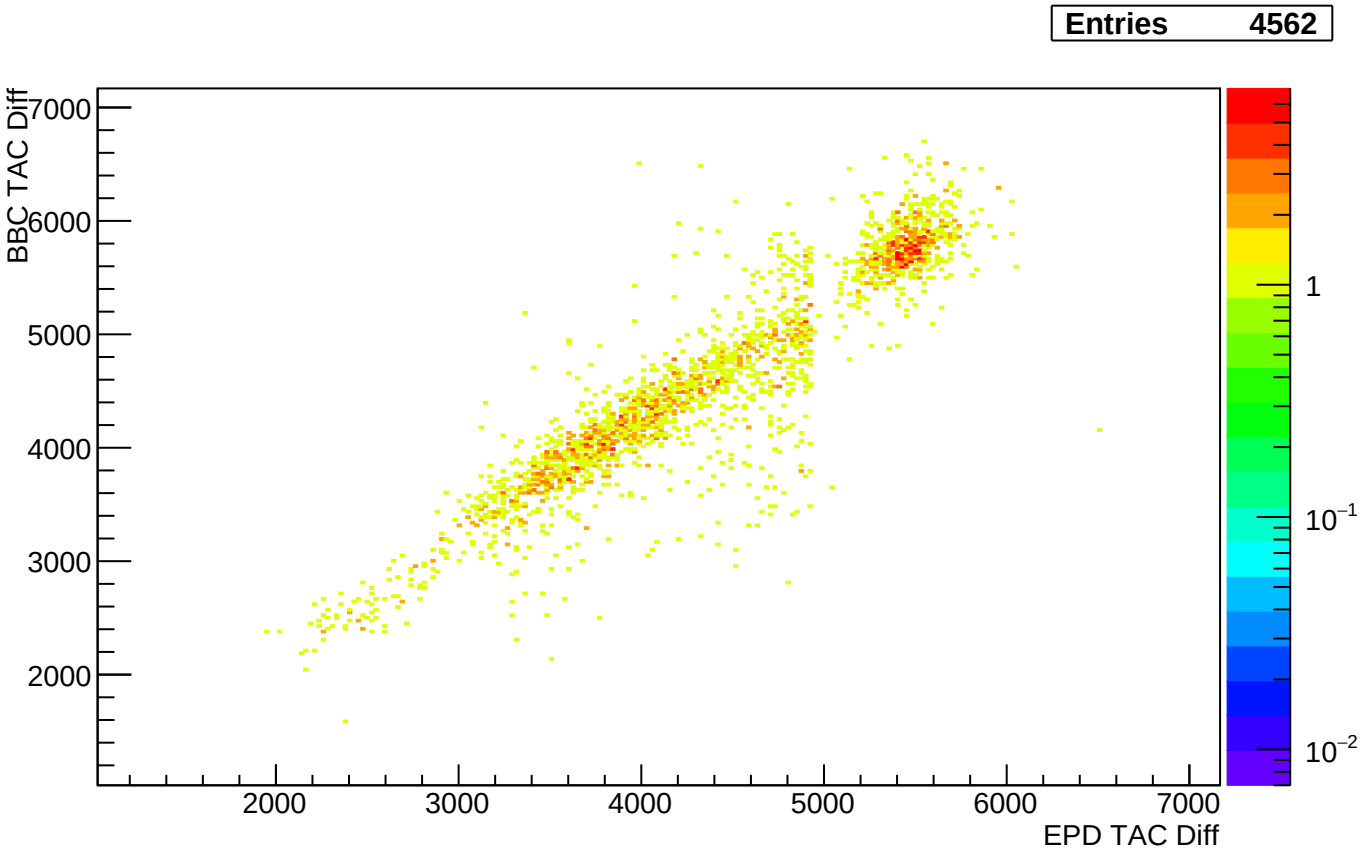

Entries 4562
TOF_Mult
BBC-S-West ADC Sum

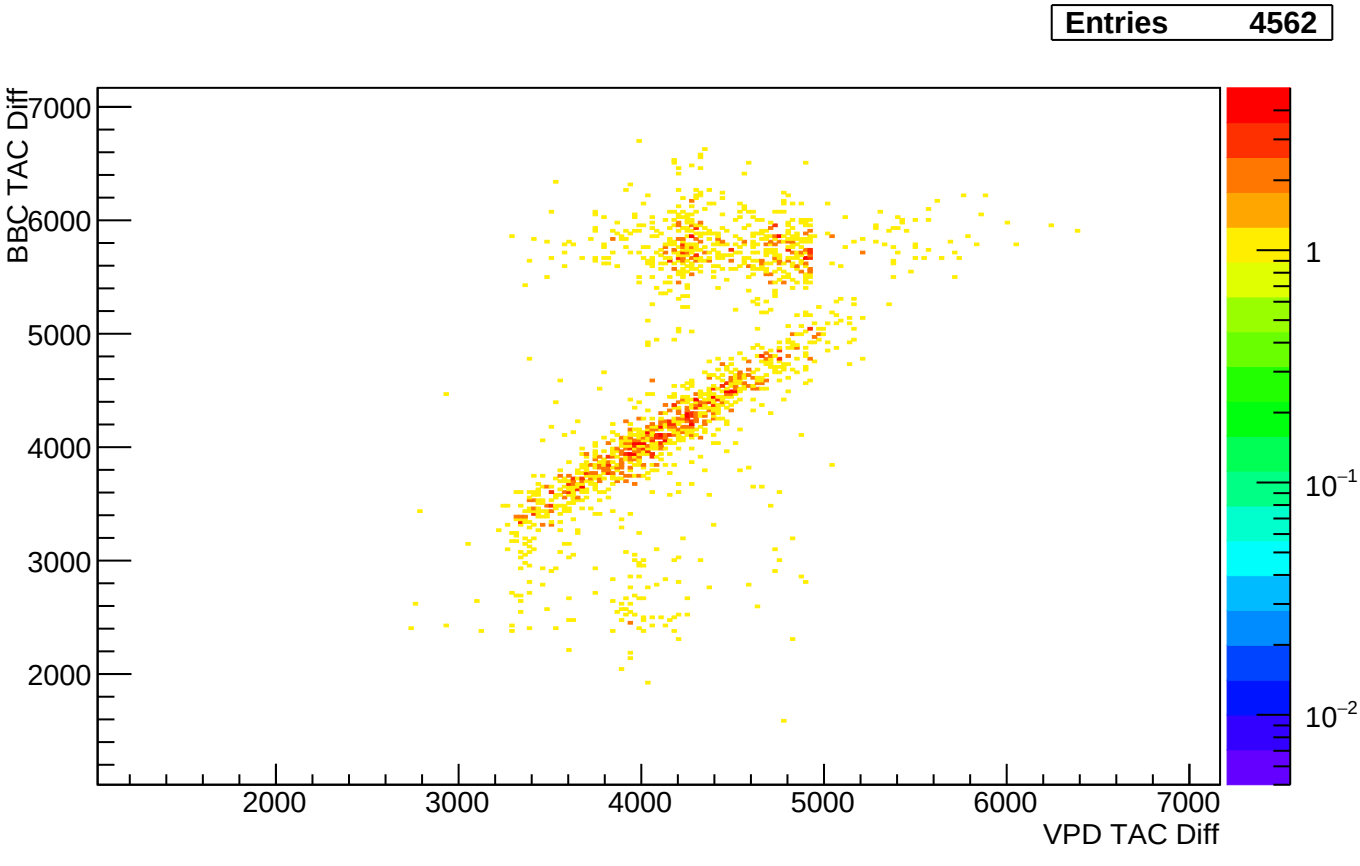

Entries 4562
TOF_Mult
EPD-East Hit Cnt

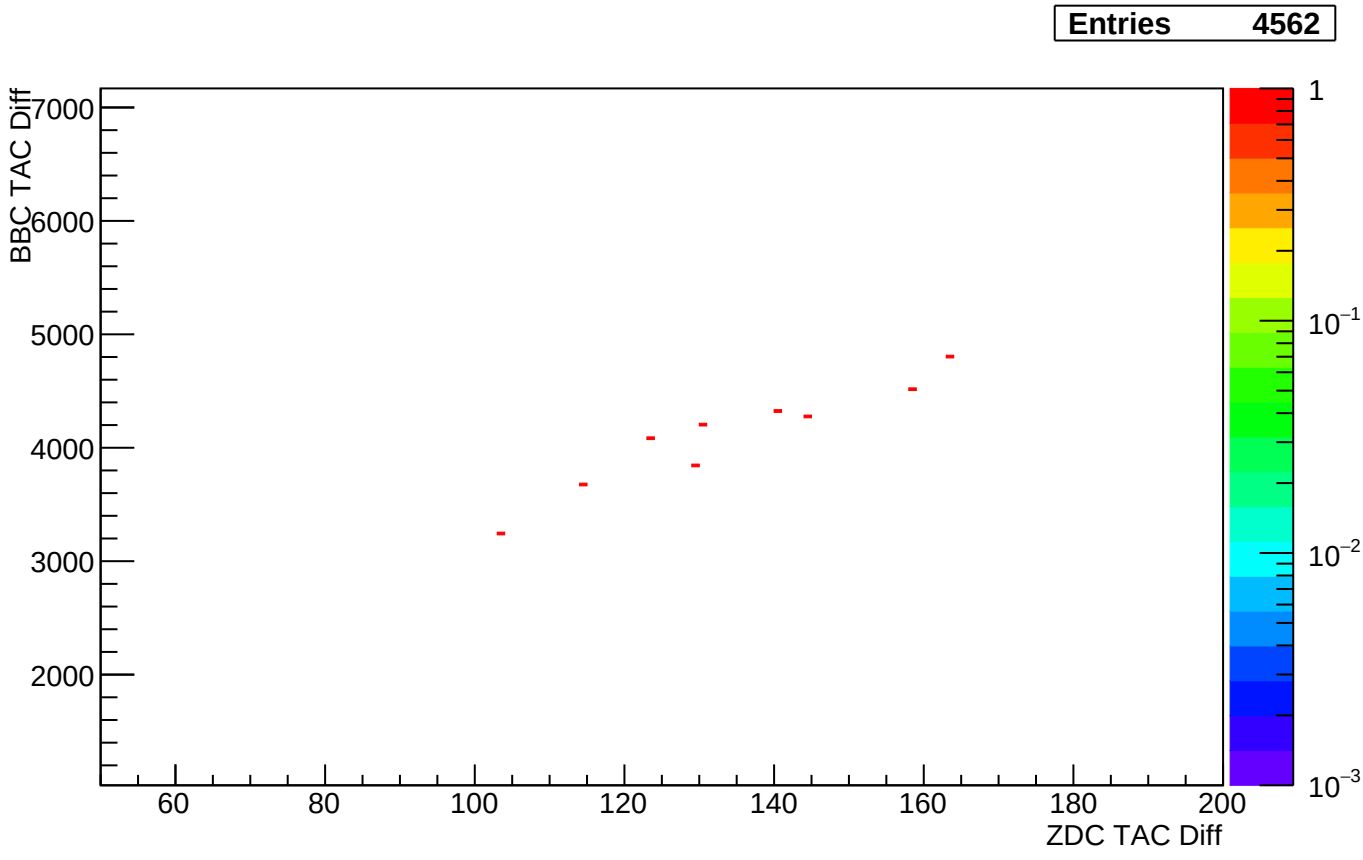

Entries 4562
TOF_Mult
EPD-West Hit Cnt

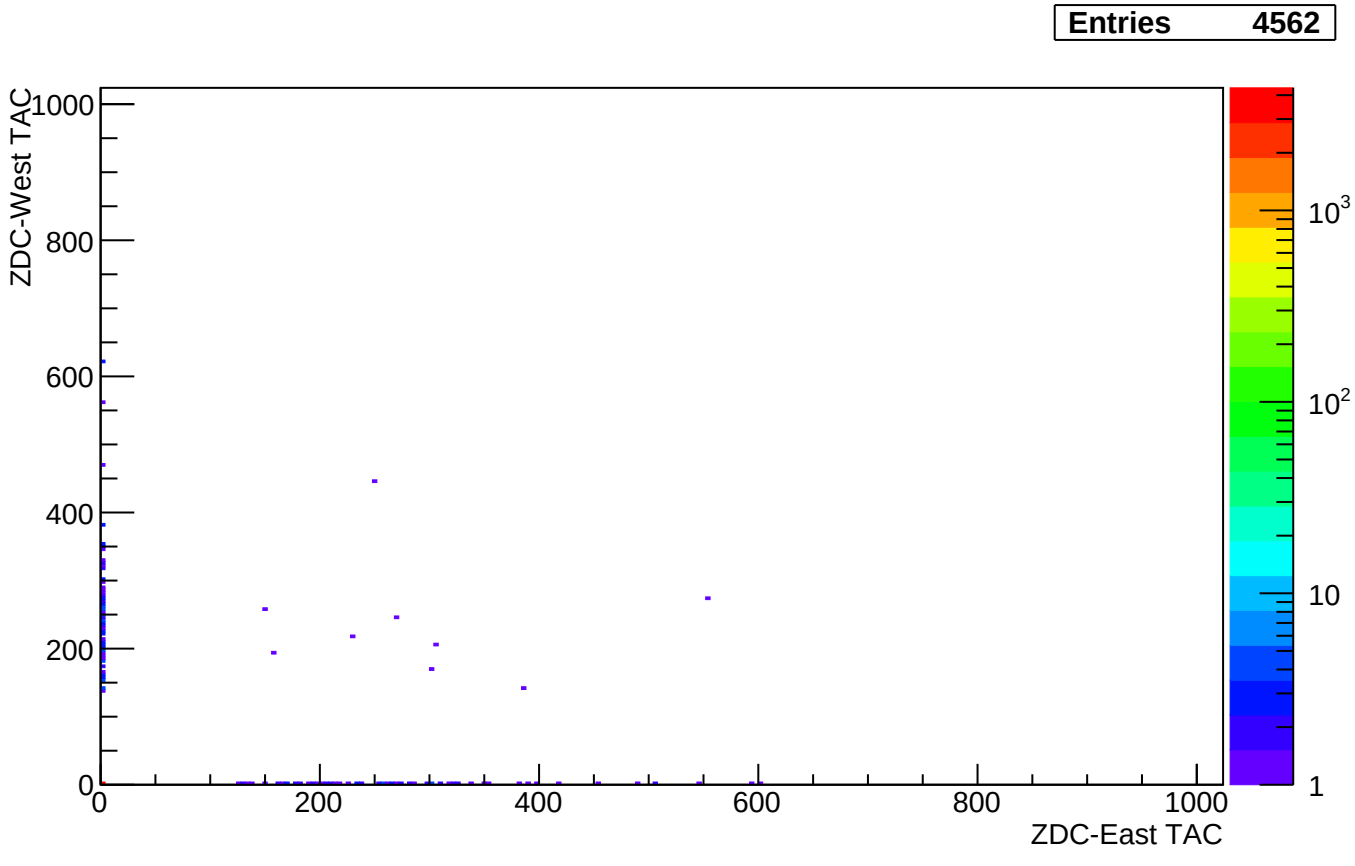

Entries 4562
TOF_Mult
VPD-East ADC Sum

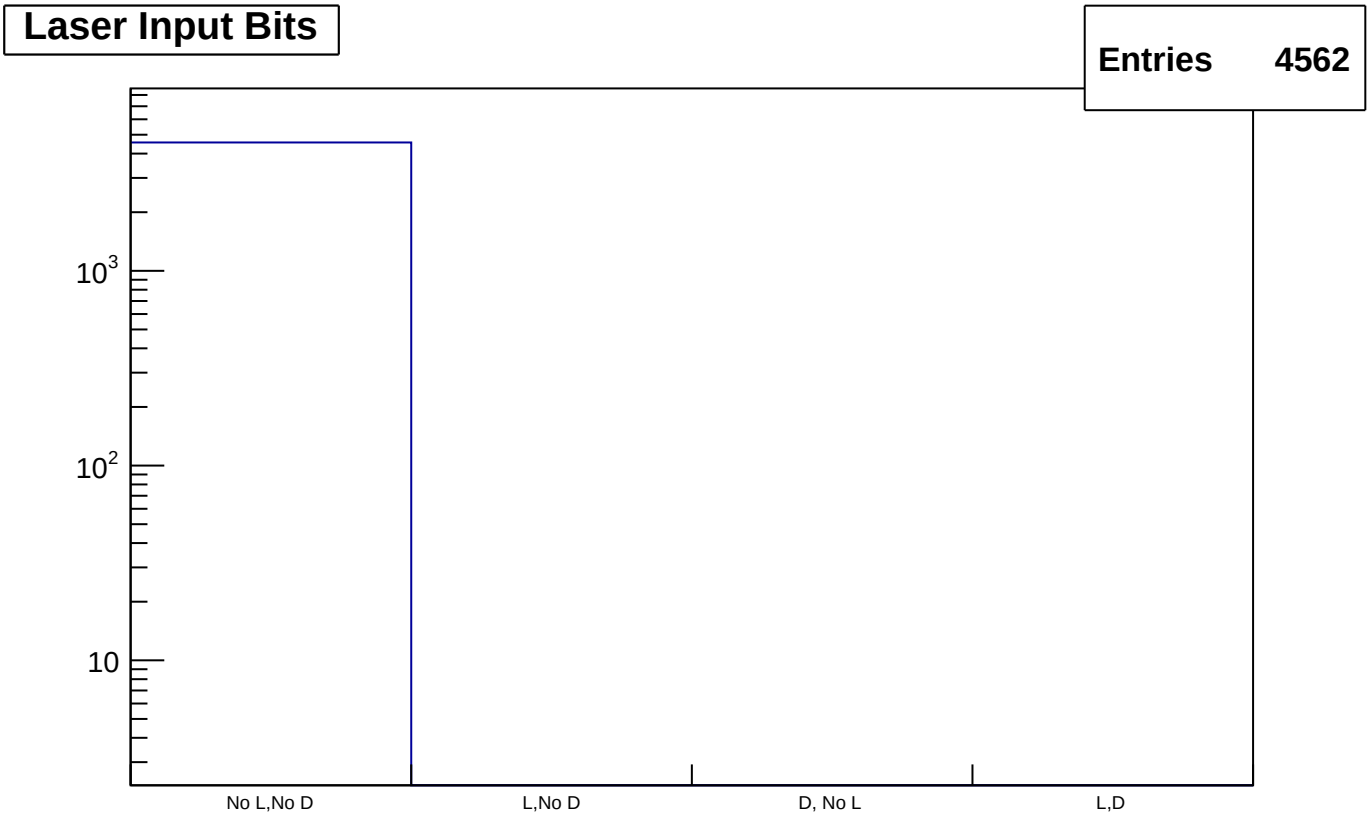

Entries 4562
TOF_Mult
VPD-West ADC Sum

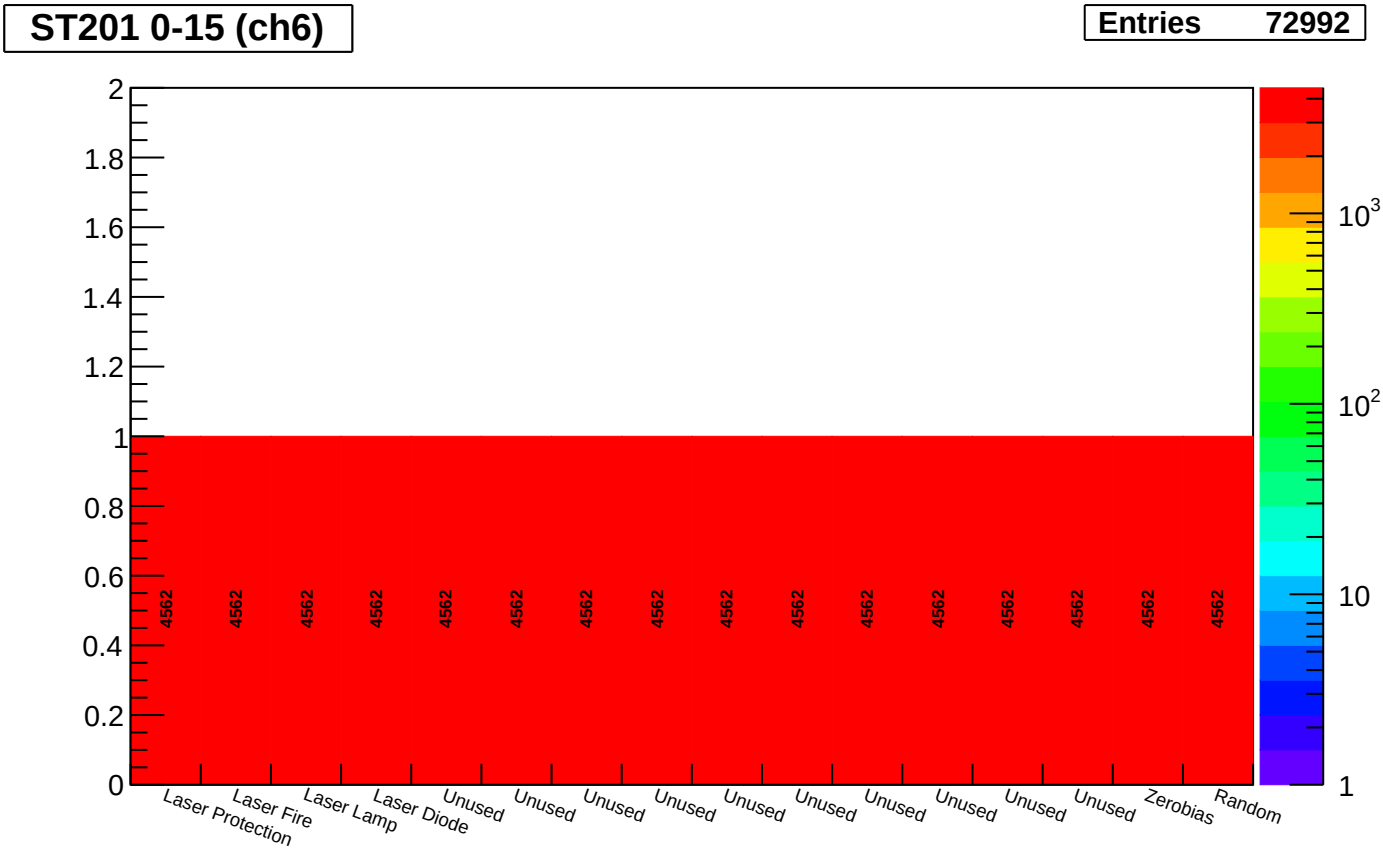

Entries 4562
BBC TAC Diff
7000
6000
5000
4000
3000
2000
2000
3000
4000
5000
6000
7000
EPD TAC Diff
1
10^{-1}
10^{-2}

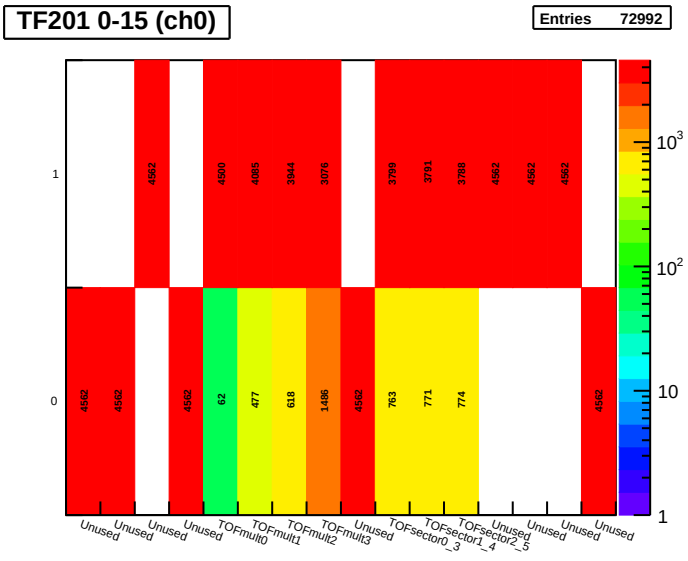

Entries 4562
BBC TAC Diff
7000
6000
5000
4000
3000
2000
2000
3000
4000
5000
6000
7000
VPD TAC Diff
1
10^{-1}
10^{-2}

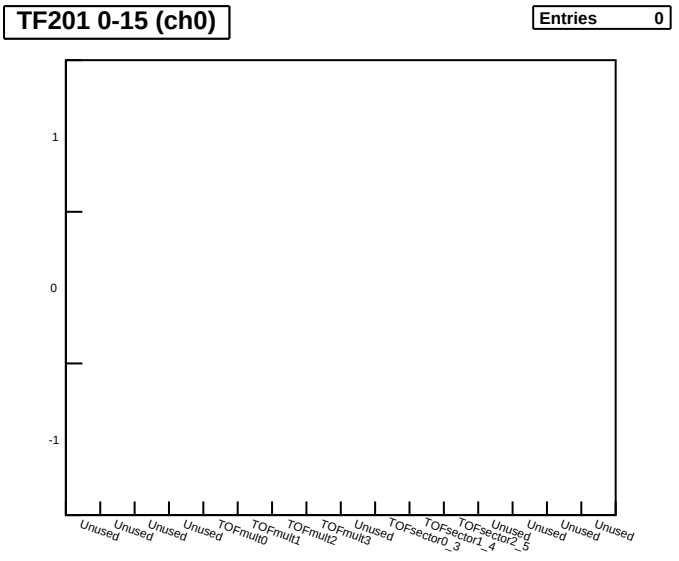

Entries 4562
BBC TAC Diff
7000
6000
5000
4000
3000
2000
60
80
100
120
140
160
180
200
ZDC TAC Diff
1
10^{-1}
10^{-2}
10^{-3}

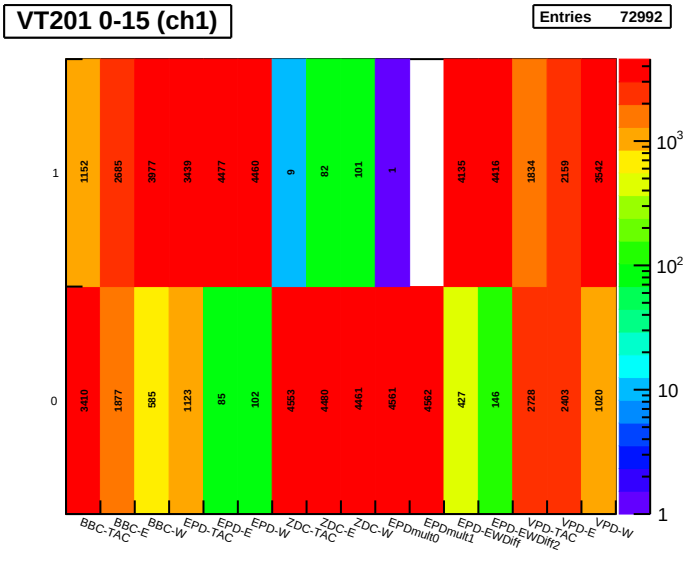

Entries 4562
ZDC-West TAC
1000
800
600
400
200
0
0
200
400
600
800
1000
ZDC-East TAC
10^3
10^2
10
1

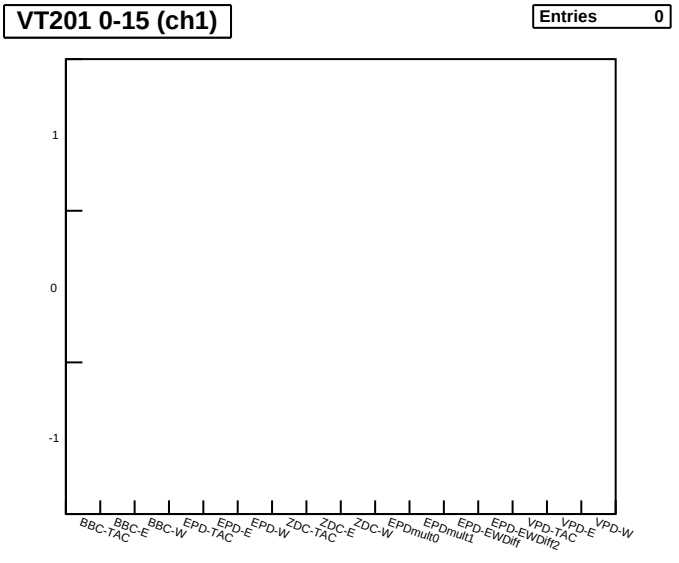

Laser Input Bits
Entries 4562
10^3
10^2
10
No L,No D
L,No D
D, No L
L,D

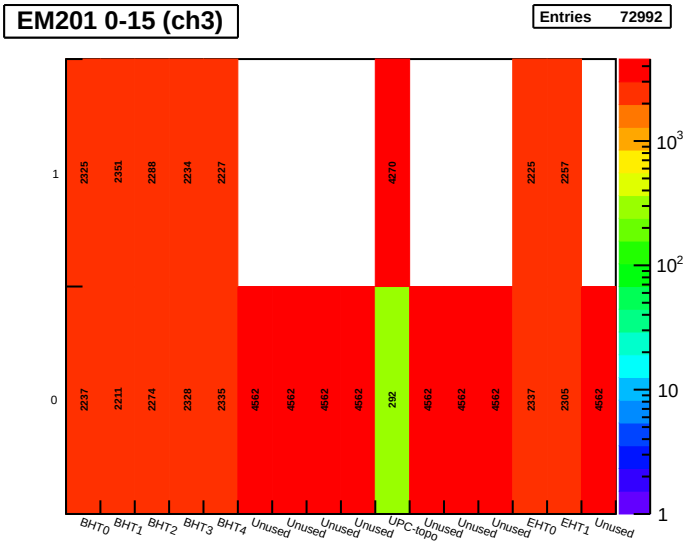

ST201 0-15 (ch6)
Entries 72992
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
4562
Laser Protection
Laser Fire
Laser Lamp
Laser Diode
Unused
Zerobias
Random
10^3
10^2
10
1

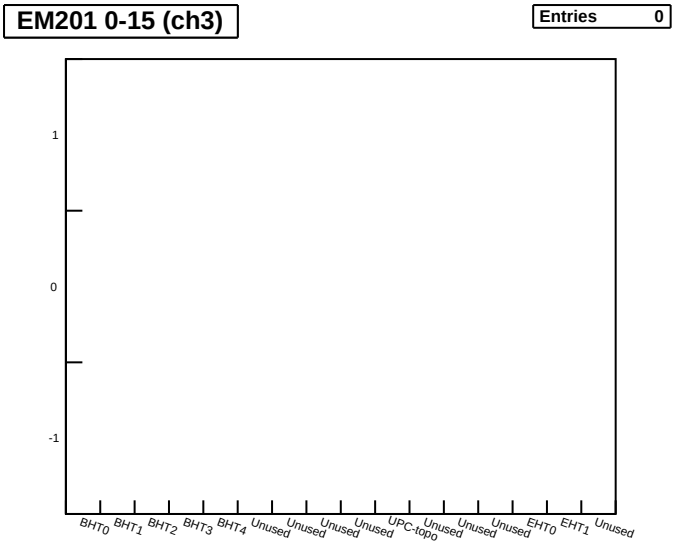

TF201 0-15 (ch0)
Entries 72992
1
0
4562 4562 4562 4562 4562 4562 4562 4562 4562 4562 4562 4562 4562 4562 4562 4562
4562 4562 4562 62 477 618 1486 4562 763 771 774 4562
Unused Unused Unused Unused TOFmult0 TOFmult1 TOFmult2 TOFmult3 Unused TOFsector0_3 TOFsector1_4 TOFsector2_5 Unused Unused Unused Unused
10^3
10^2
10
1


TF201 0-15 (ch0)
Entries 0
1
0
-1
Unused Unused Unused Unused TOFmult0 TOFmult1 TOFmult2 TOFmult3 Unused TOFsector0_3 TOFsector1_4 TOFsector2_5 Unused Unused Unused


VT201 0-15 (ch1)
Entries 72992
1
0
1152 2685 3977 3439 4477 4460 9 82 101 1 4135 4416 1834 2159 3542
3410 1877 585 1123 85 102 4553 4480 4461 4561 4562 427 146 2728 2403 1020
BBC-TAC BBC-E BBC-W EPD-TAC EPD-E EPD-W ZDC-TAC ZDC-E ZDC-W EPDmult0 EPDmult1 EPD-EWDiff EPD-EWDiff2 VPD-TAC VPD-E VPD-W
10^3
10^2
10
1


VT201 0-15 (ch1)
Entries 0
1
0
-1
BBC-TAC BBC-E BBC-W EPD-TAC EPD-E EPD-W ZDC-TAC ZDC-E ZDC-W EPDmult0 EPDmult1 EPD-EWDiff EPD-EWDiff2 VPD-TAC VPD-E VPD-W


EM201 0-15 (ch3)
Entries 72992
1
0
2325 2351 2288 2234 2227 4270 2225 2257
2237 2211 2274 2328 2335 4562 4562 4562 4562 292 4562 4562 4562 2337 2305 4562
BHT0 BHT1 BHT2 BHT3 BHT4 Unused Unused Unused Unused UPC-topo Unused Unused Unused EHT0 EHT1 Unused
10^3
10^2
10
1


EM201 0-15 (ch3)
Entries 0
1
0
-1
BHT0 BHT1 BHT2 BHT3 BHT4 Unused Unused Unused Unused UPC-topo Unused Unused Unused EHT0 EHT1 Unused