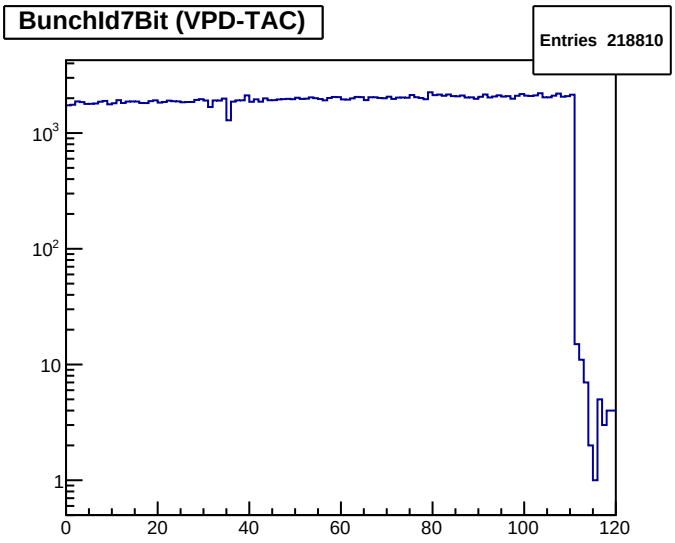
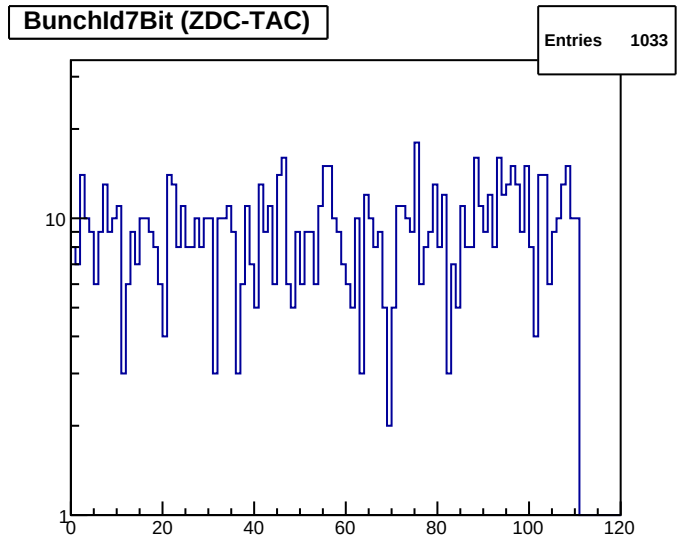
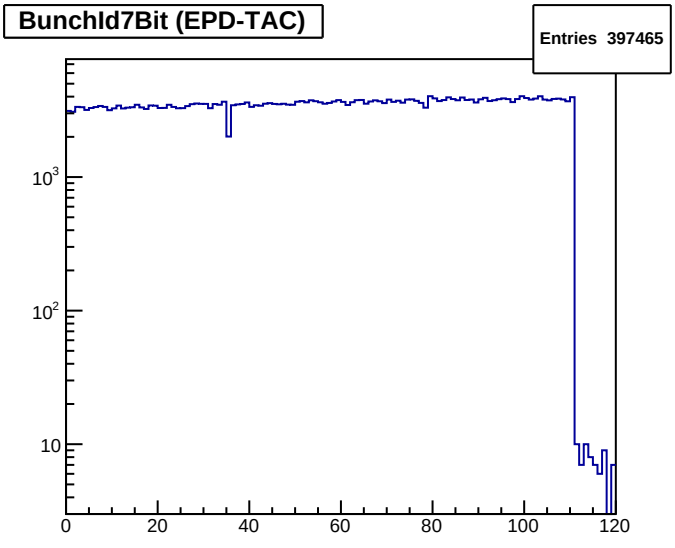
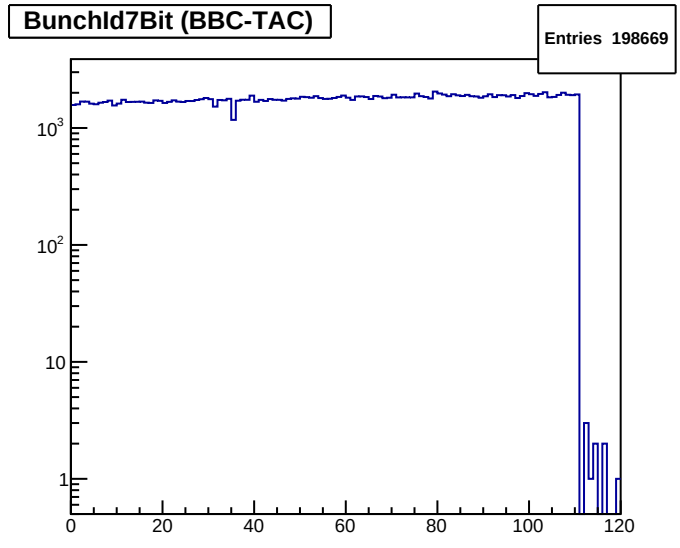
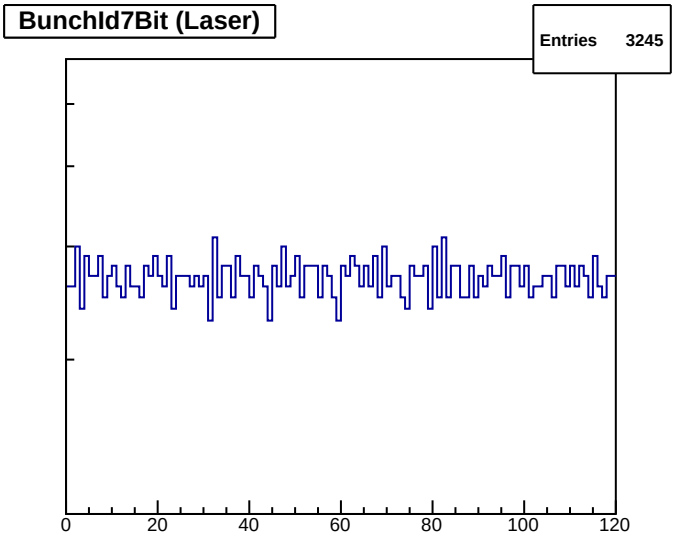
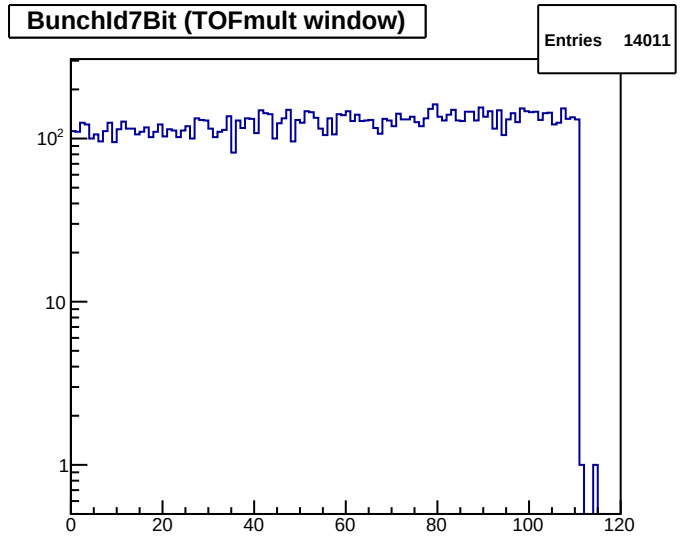

BunchId7Bit (VPD-TAC)
Entries 218810
10^3
10^2
10
1
0
20
40
60
80
100
120

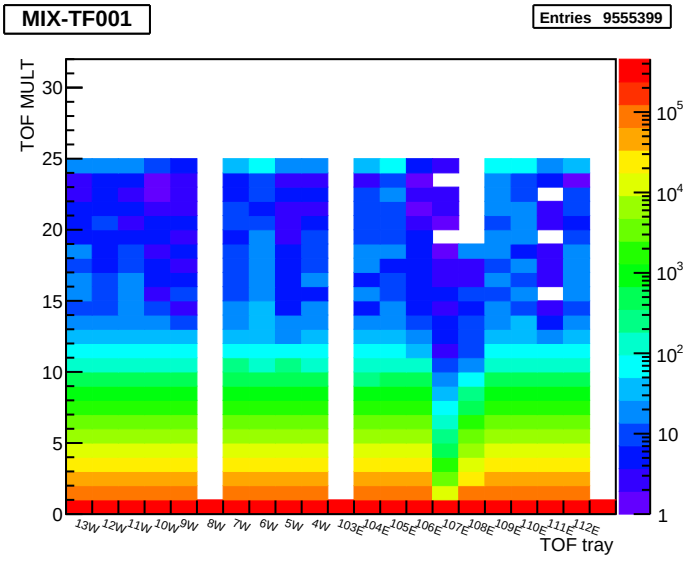

BunchId7Bit (ZDC-TAC)
Entries 1033
10
1
0
20
40
60
80
100
120

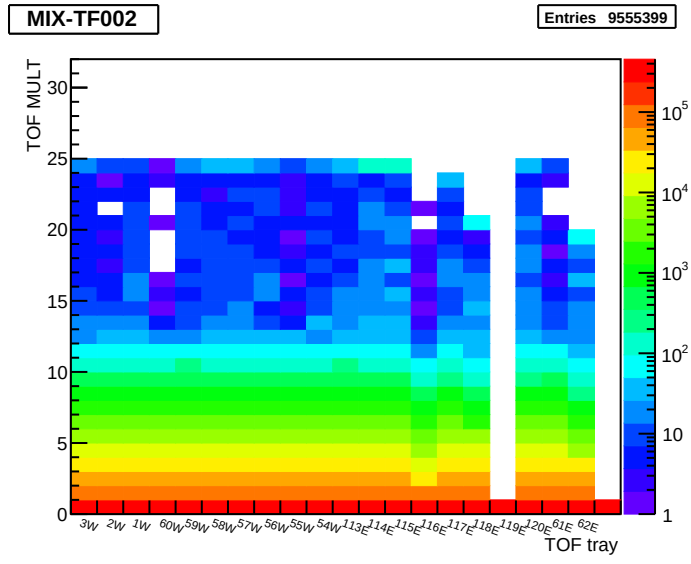

BunchId7Bit (EPD-TAC)
Entries 397465
10^3
10^2
10
0
20
40
60
80
100
120

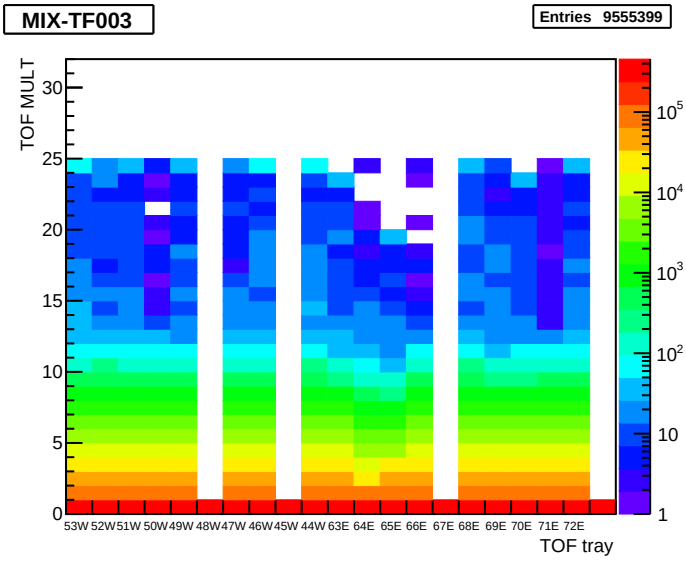

BunchId7Bit (BBC-TAC)
Entries 198669
10^3
10^2
10
1
0
20
40
60
80
100
120

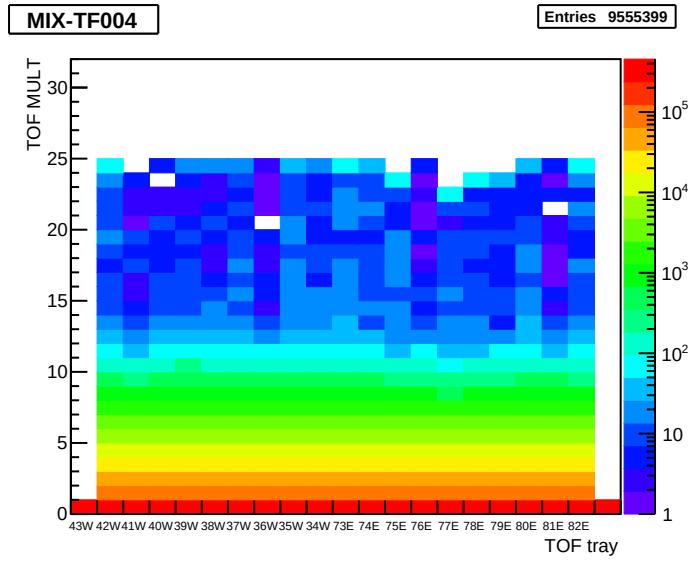

BunchId7Bit (Laser)
Entries 3245
0
20
40
60
80
100
120

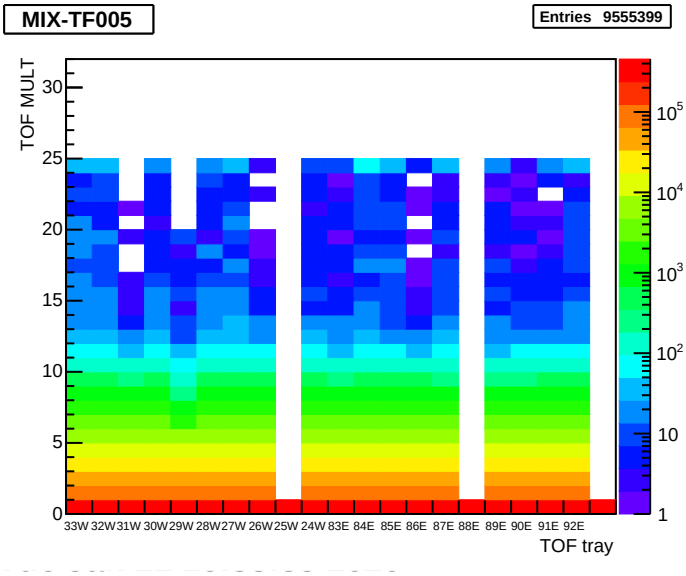

BunchId7Bit (TOFmult window)
Entries 14011
10^2
10
1
0
20
40
60
80
100
120

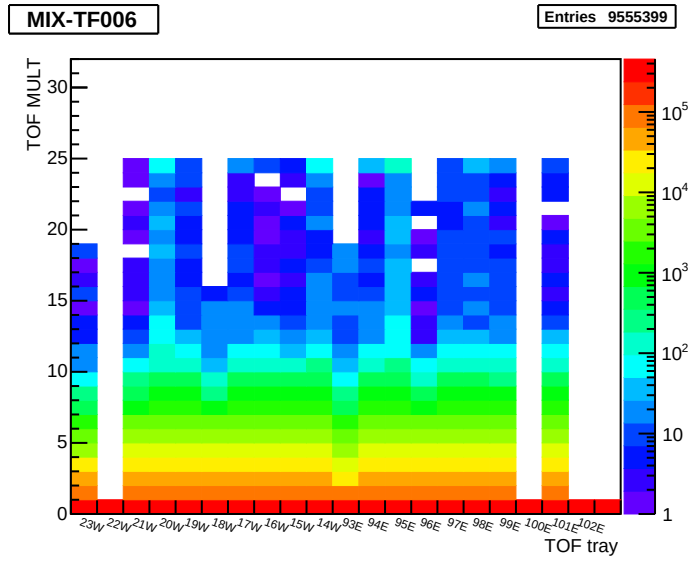

MIX-TF001
Entries 9555399
TOF MULT
TOF tray

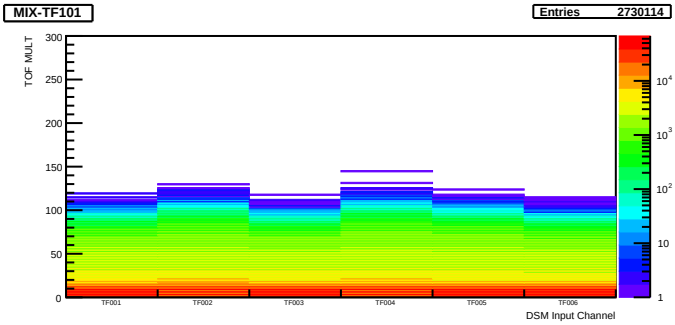

MIX-TF002
Entries 9555399
TOF MULT
TOF tray

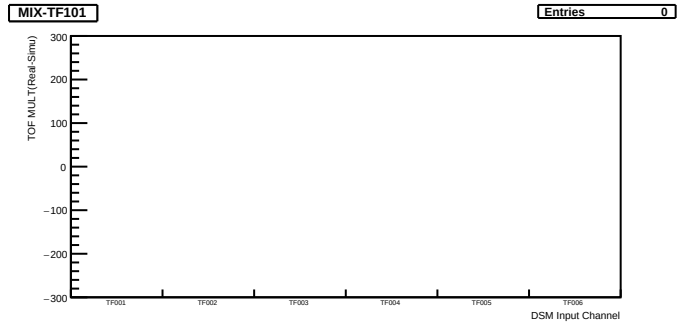

MIX-TF003
Entries 9555399
TOF MULT
TOF tray

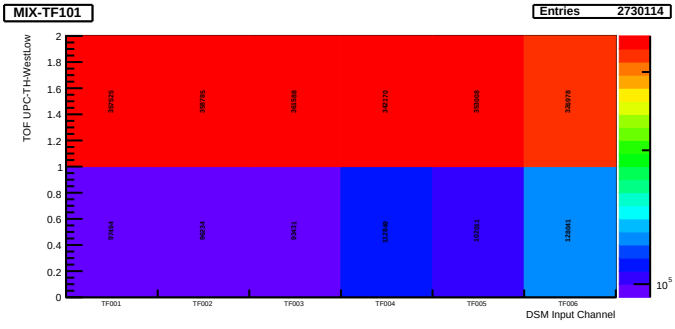

MIX-TF004
Entries 9555399
TOF MULT
TOF tray

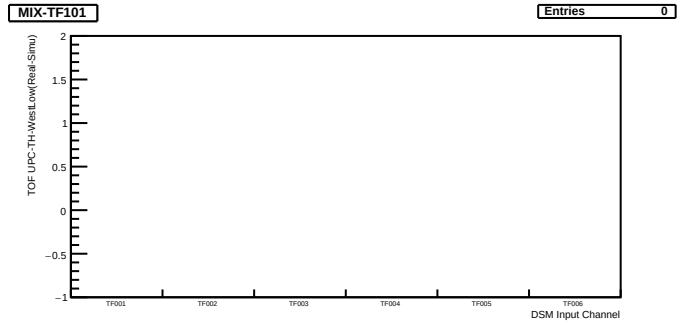

MIX-TF005
Entries 9555399
TOF MULT
TOF tray

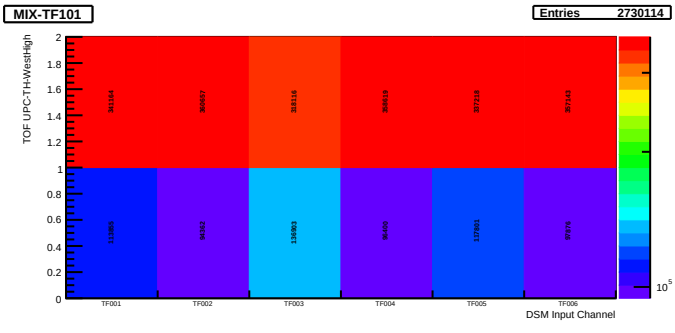

MIX-TF006
Entries 9555399
TOF MULT
TOF tray

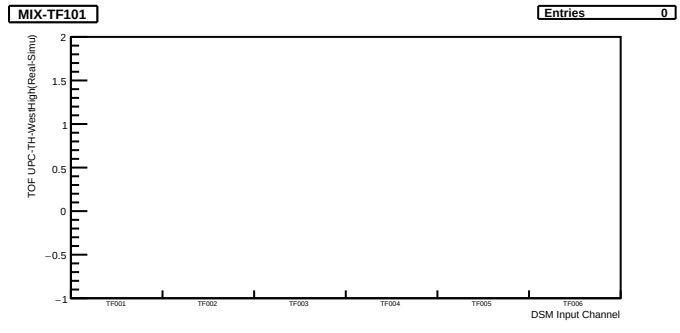

MIX-TF101
Entries 2730114
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

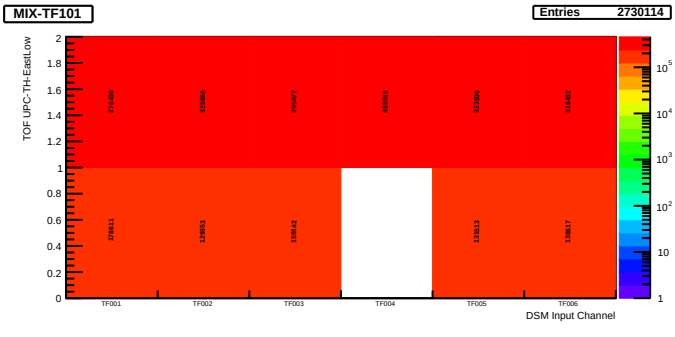

MIX-TF101
Entries 0
TOF MULT(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

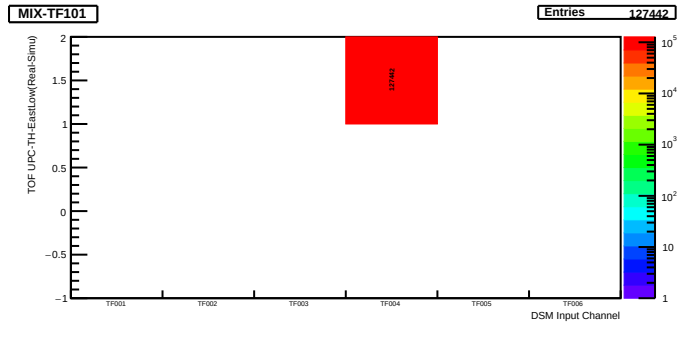

MIX-TF101
Entries 2730114
TOF UPC-TH-WestLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

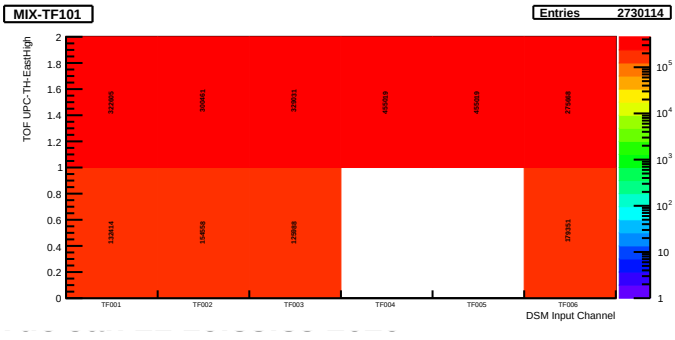

MIX-TF101
Entries 0
TOF UPC-TH-WestLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

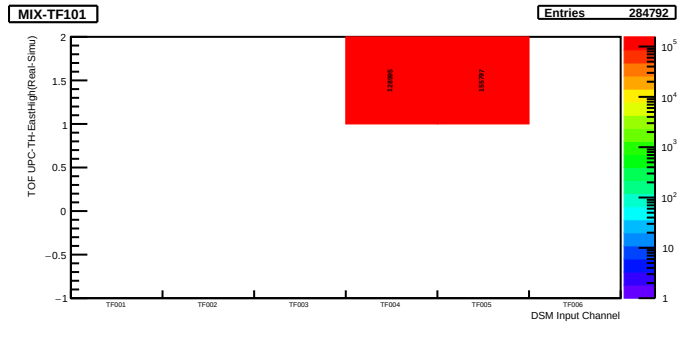

MIX-TF101
Entries 2730114
TOF UPC-TH-WestHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

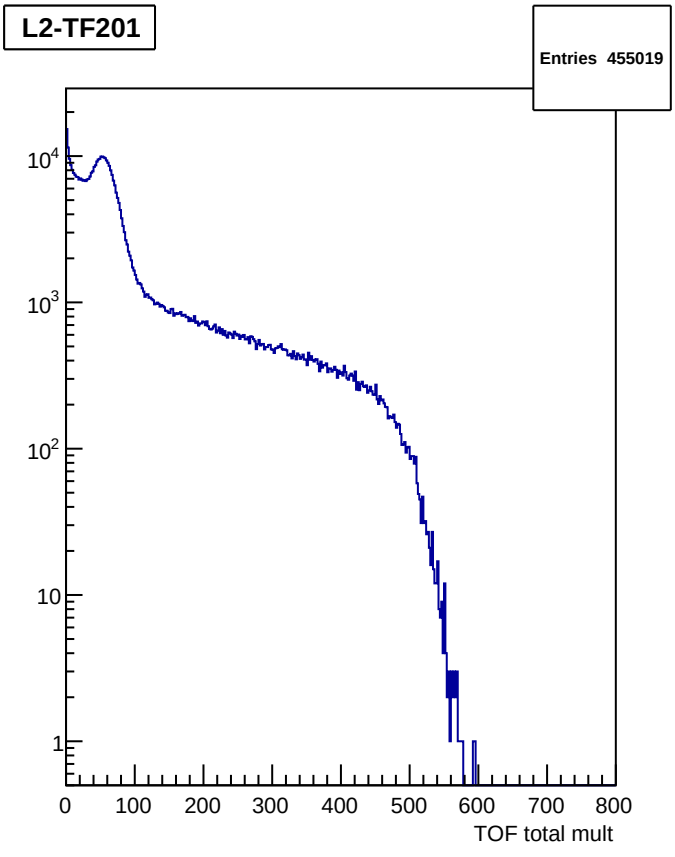

MIX-TF101
Entries 0
TOF UPC-TH-WestHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

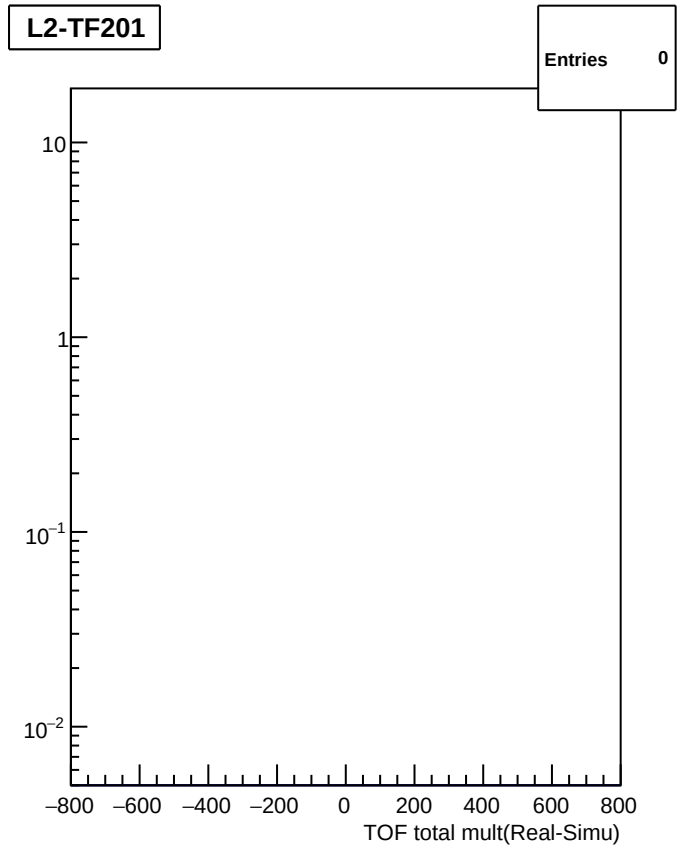

MIX-TF101
Entries 2730114
TOF UPC-TH-EastLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

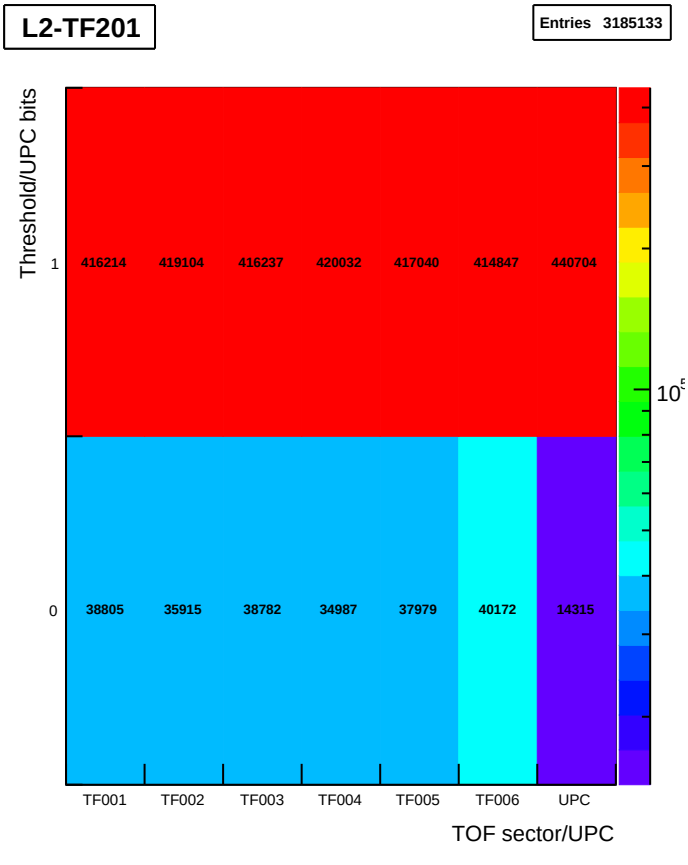

MIX-TF101
Entries 127442
TOF UPC-TH-EastLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

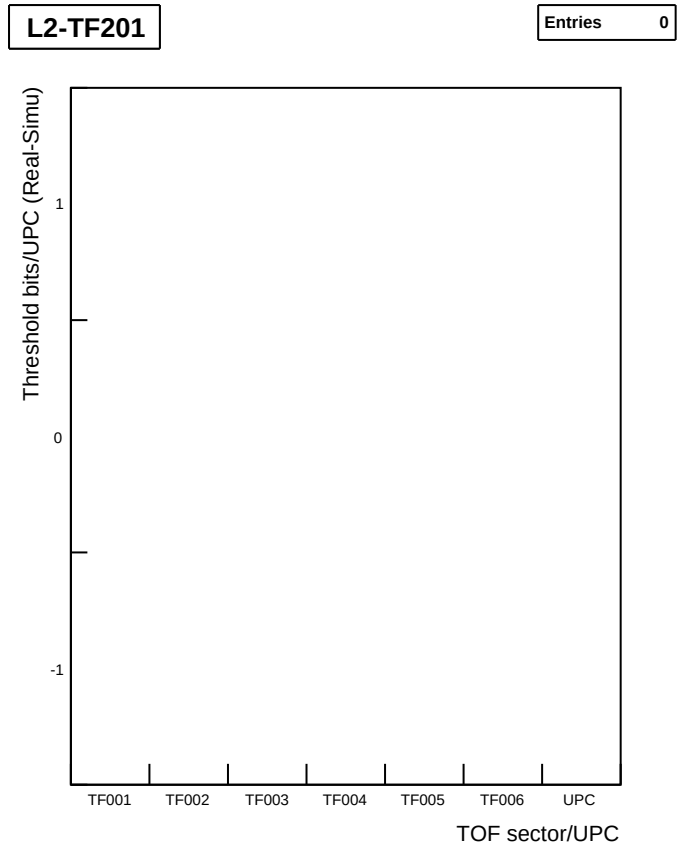

MIX-TF101
Entries 2730114
TOF UPC-TH-EastHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

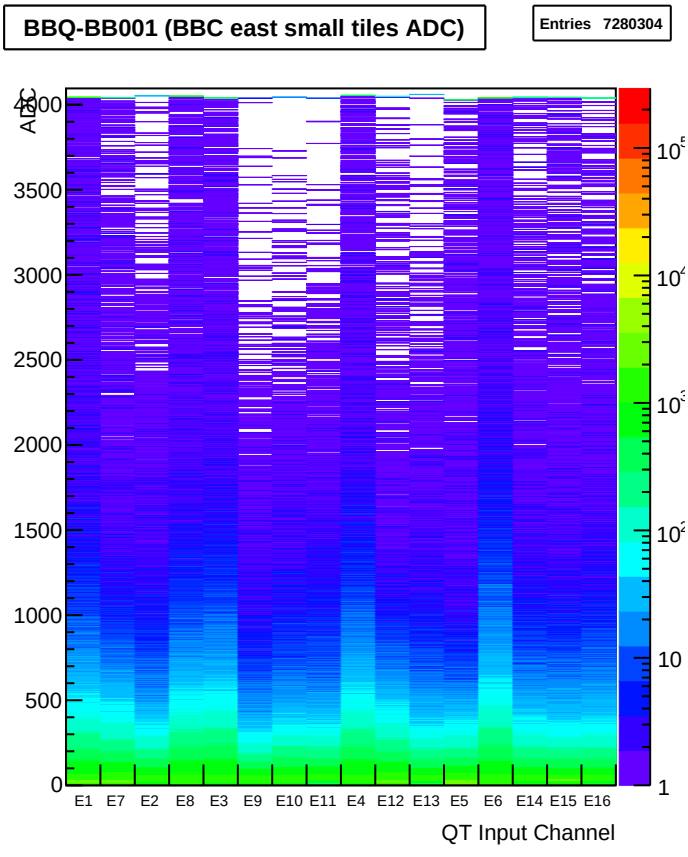

MIX-TF101
Entries 284792
TOF UPC-TH-EastHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

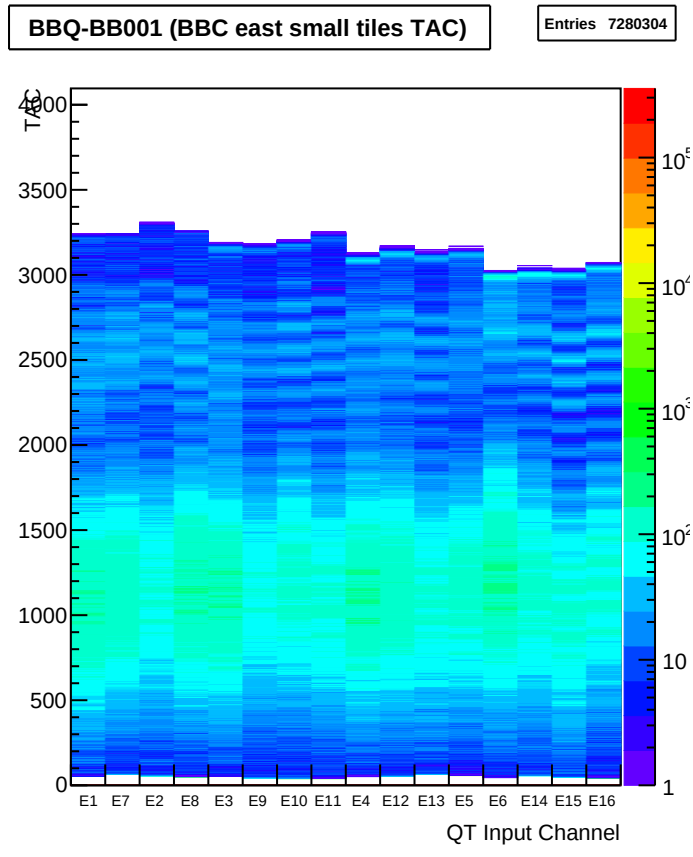

L2-TF201
Entries 455019
TOF total mult

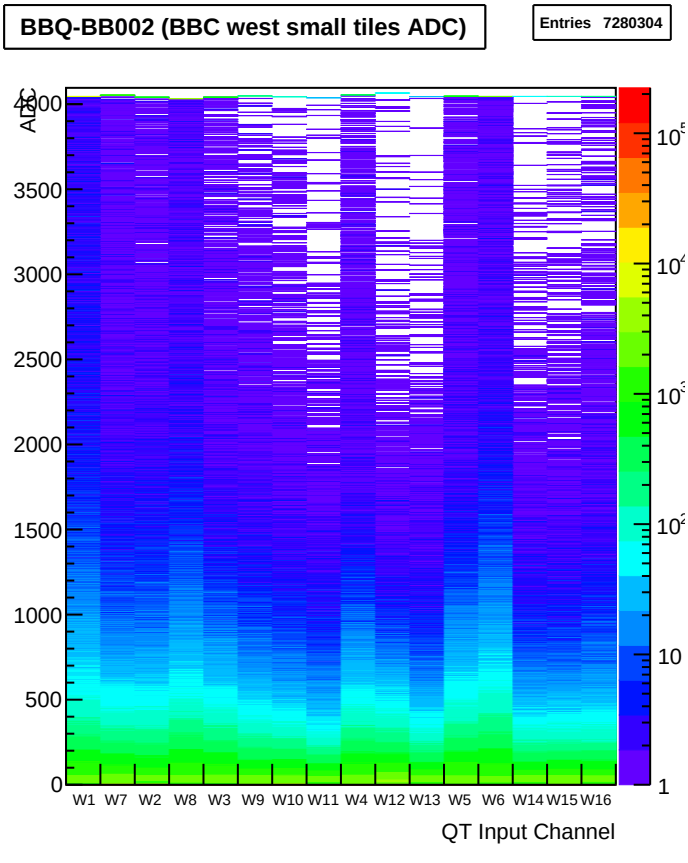

L2-TF201
Entries 0
TOF total mult(Real-Simu)

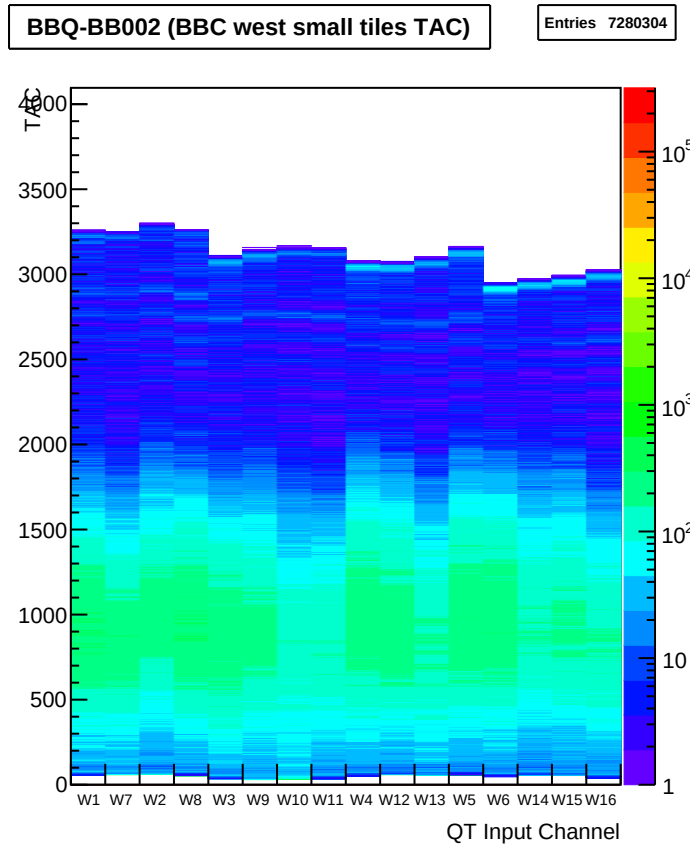

L2-TF201
Entries 3185133
Threshold/UPC bits
416214
419104
416237
420032
417040
414847
440704
38805
35915
38782
34987
37979
40172
14315
TF001
TF002
TF003
TF004
TF005
TF006
UPC
TOF sector/UPC

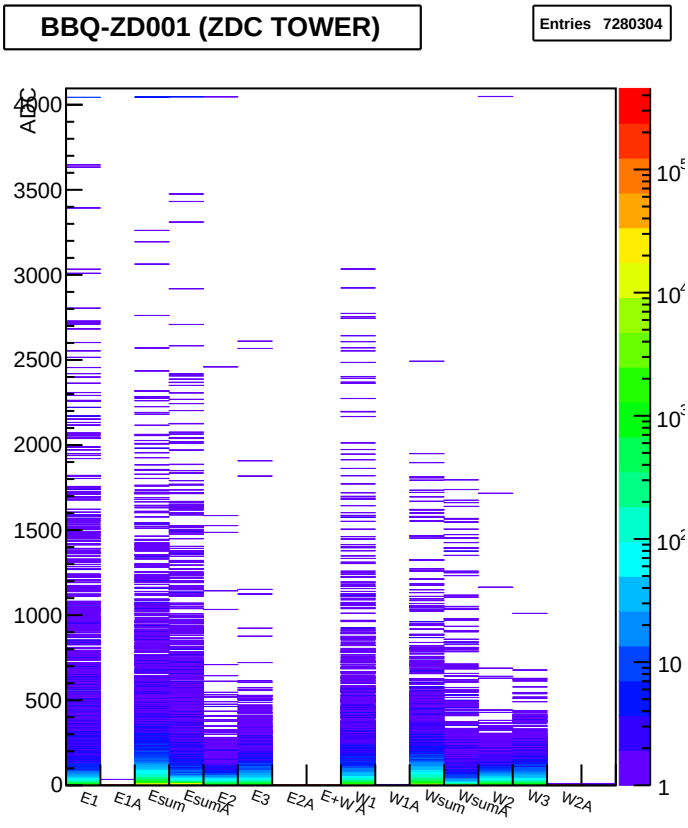

L2-TF201
Entries 0
Threshold bits/UPC (Real-Simu)
TF001
TF002
TF003
TF004
TF005
TF006
UPC
TOF sector/UPC

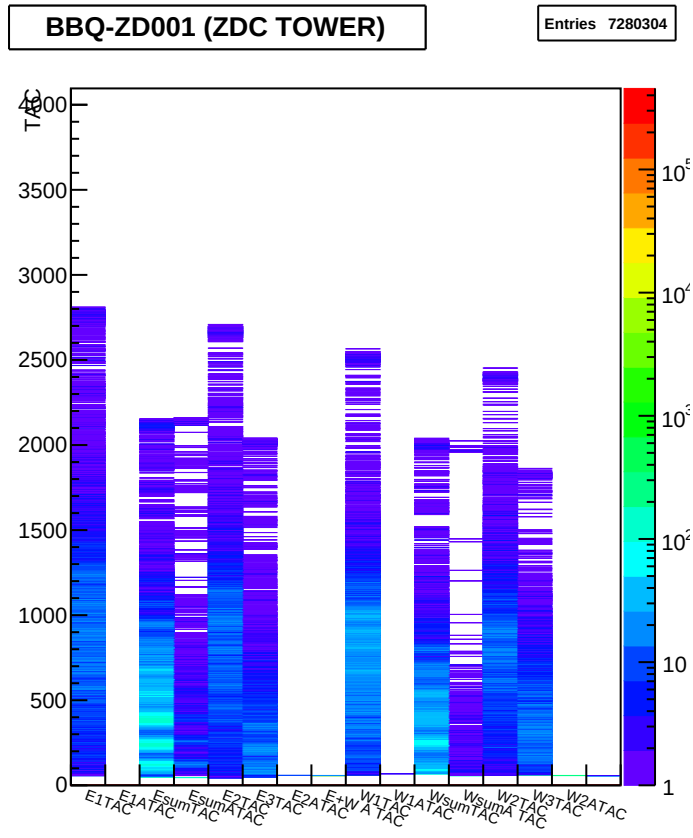

BBQ-BB001 (BBC east small tiles ADC)
Entries 7280304
ADC
QT Input Channel
E1 E7 E2 E8 E3 E9 E10 E11 E4 E12 E13 E5 E6 E14 E15 E16

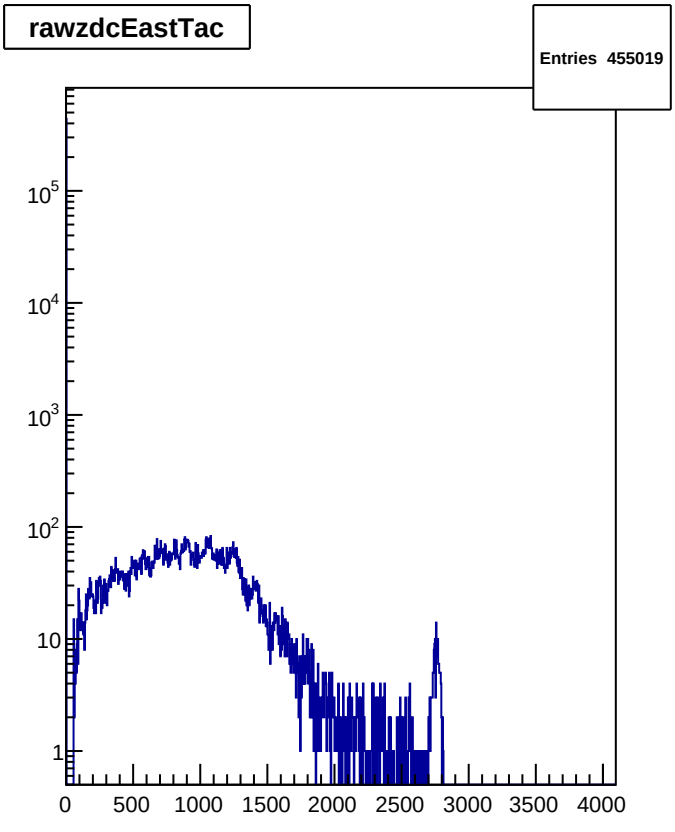

BBQ-BB001 (BBC east small tiles TAC)
Entries 7280304
TAC
QT Input Channel
E1 E7 E2 E8 E3 E9 E10 E11 E4 E12 E13 E5 E6 E14 E15 E16

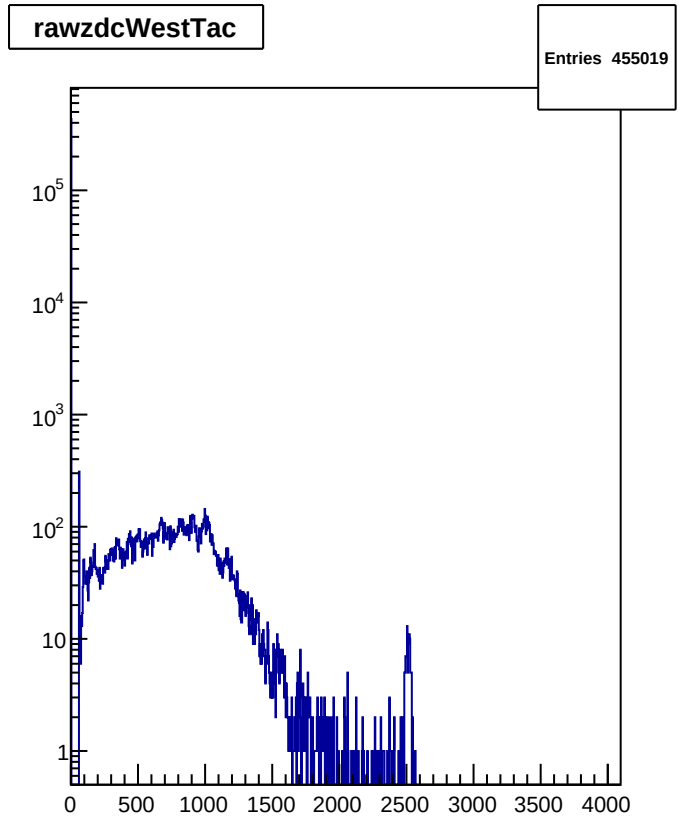

BBQ-BB002 (BBC west small tiles ADC)
Entries 7280304
ADC
QT Input Channel
W1 W7 W2 W8 W3 W9 W10 W11 W4 W12 W13 W5 W6 W14 W15 W16

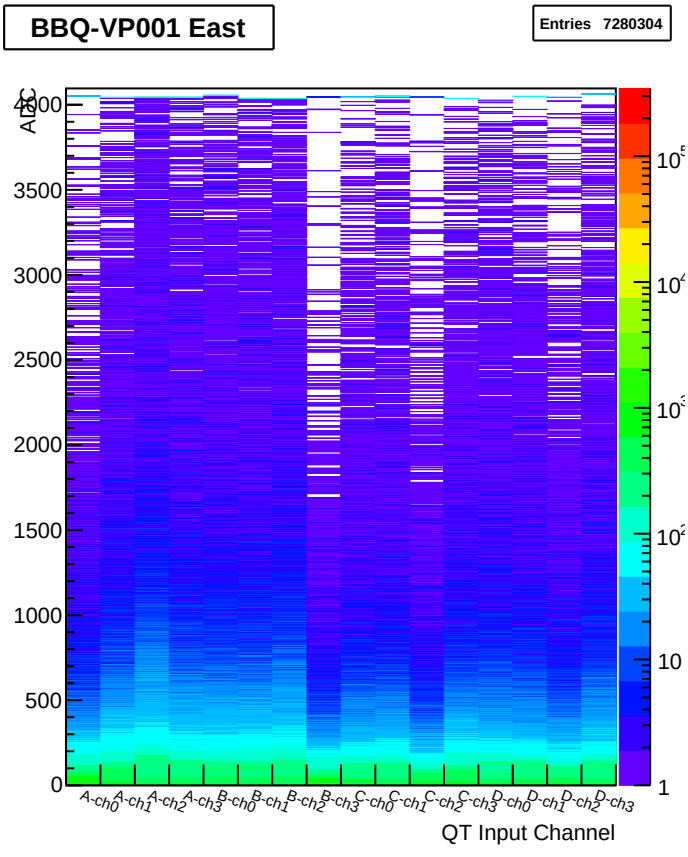

BBQ-BB002 (BBC west small tiles TAC)
Entries 7280304
TAC
QT Input Channel
W1 W7 W2 W8 W3 W9 W10 W11 W4 W12 W13 W5 W6 W14 W15 W16

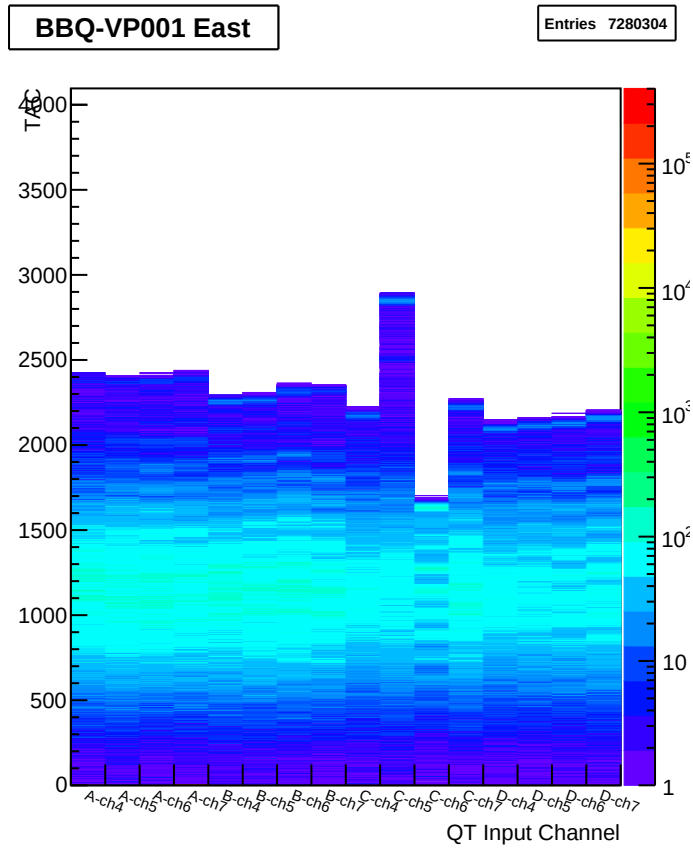

BBQ-ZD001 (ZDC TOWER)
Entries 7280304
ADC
E1
E1A
Esum
EsumA
E2
E3
E2A
E+W A
W1
W1A
Wsum
WsumA
W2
W3
W2A

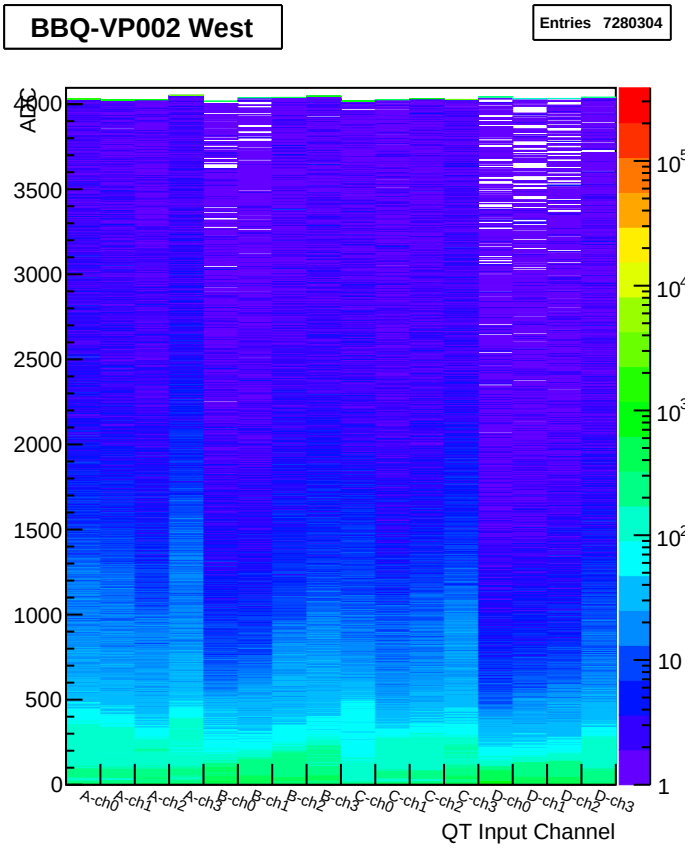

BBQ-ZD001 (ZDC TOWER)
Entries 7280304
TAC
E1TAC
E1ATAC
EsumTAC
EsumATAC
E2TAC
E3TAC
E2ATAC
E+W A TAC
W1TAC
W1ATAC
WsumTAC
WsumA TAC
W2TAC
W3TAC
W2ATAC

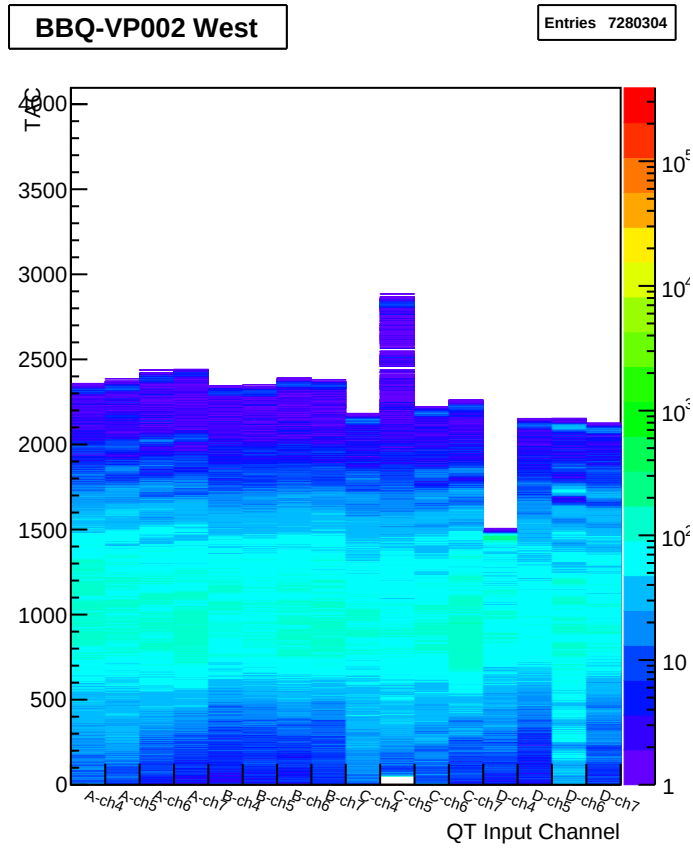

rawzdcEastTac
Entries 455019

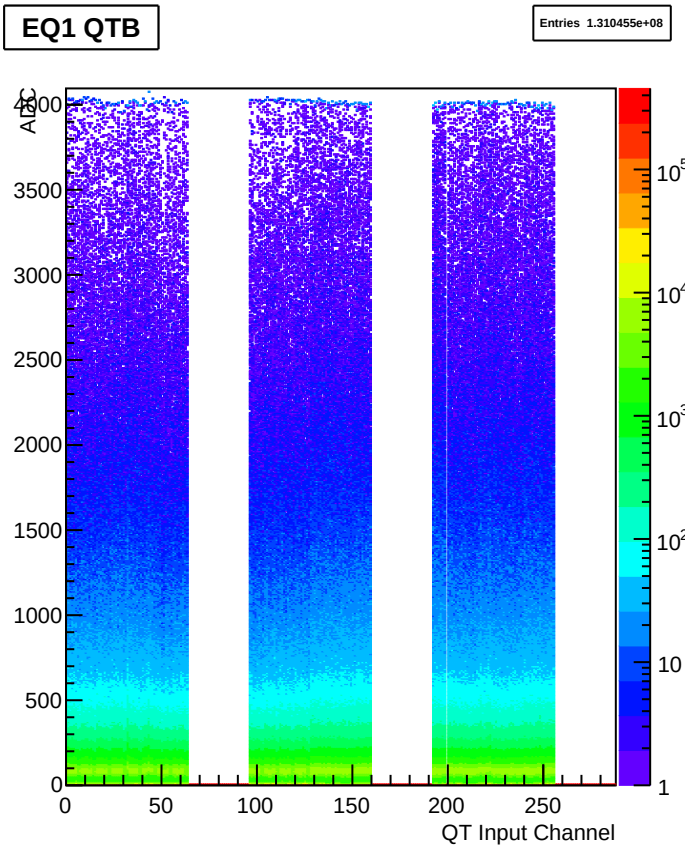

rawzdcWestTac
Entries 455019

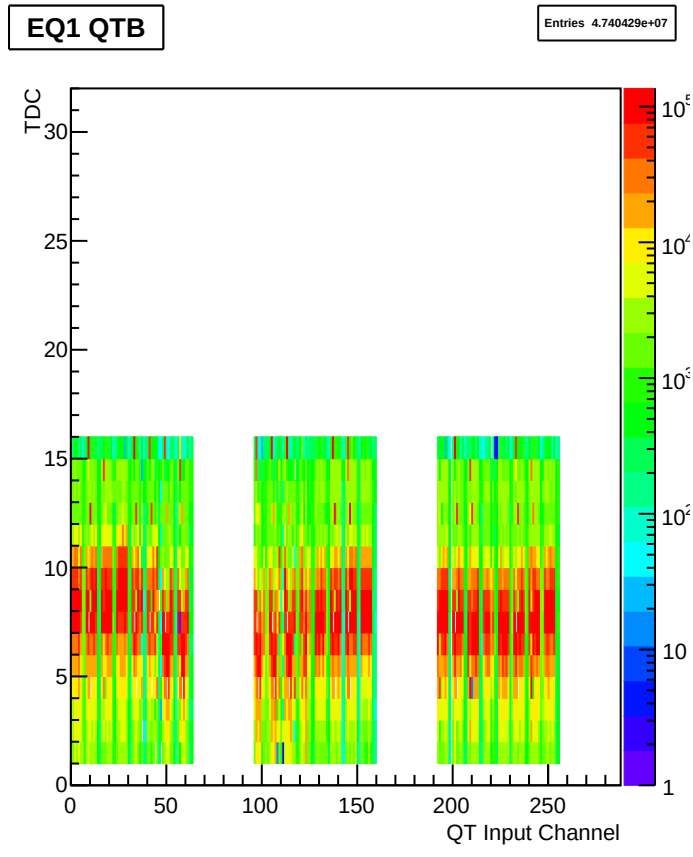

BBQ-VP001 East
Entries 7280304
ADC
QT Input Channel

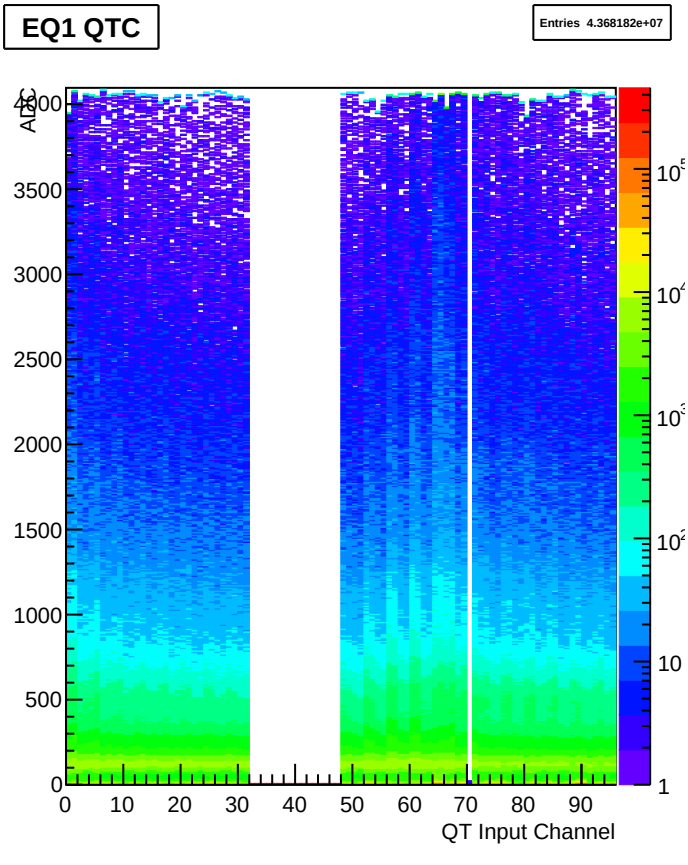

BBQ-VP001 East
Entries 7280304
ADC
QT Input Channel

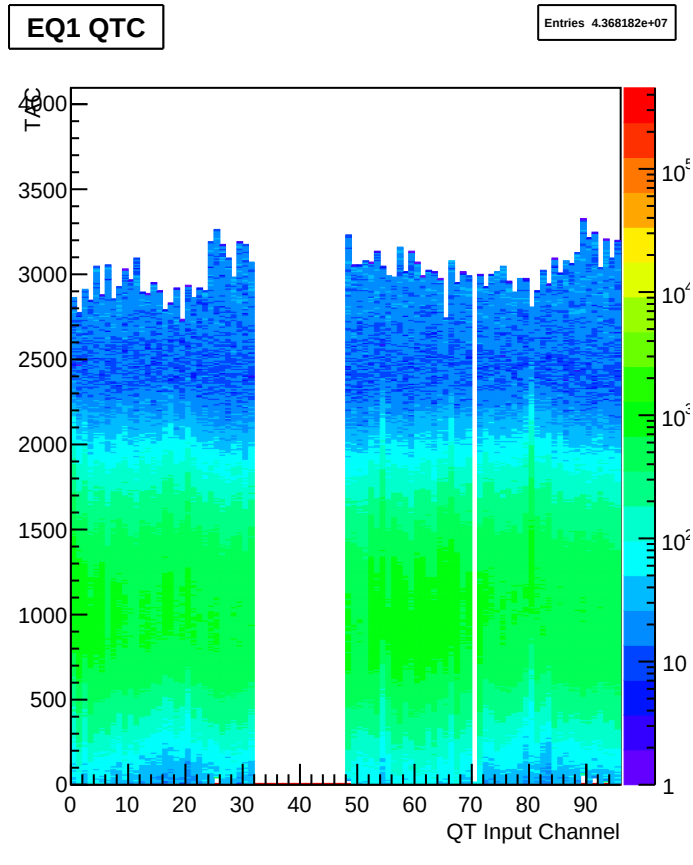

BBQ-VP002 West
Entries 7280304
ADC
QT Input Channel

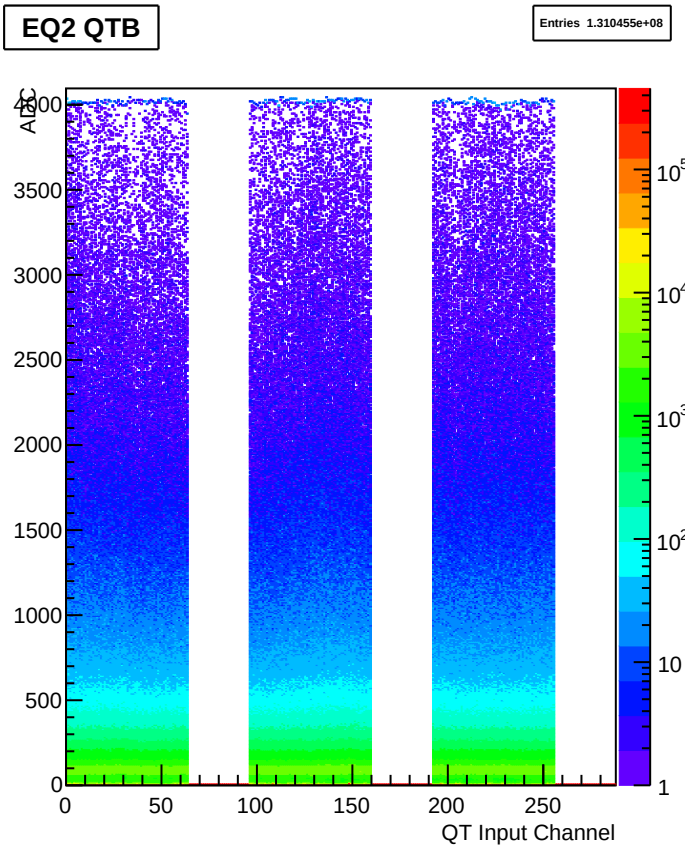

BBQ-VP002 West
Entries 7280304
ADC
QT Input Channel

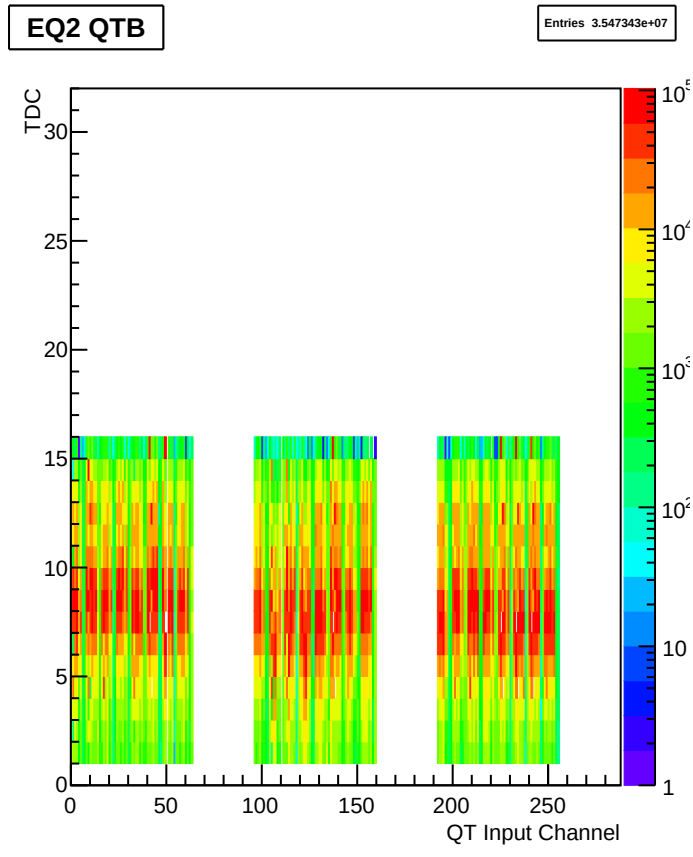

EQ1 QTB
Entries 1.310455e+08
ADC
QT Input Channel

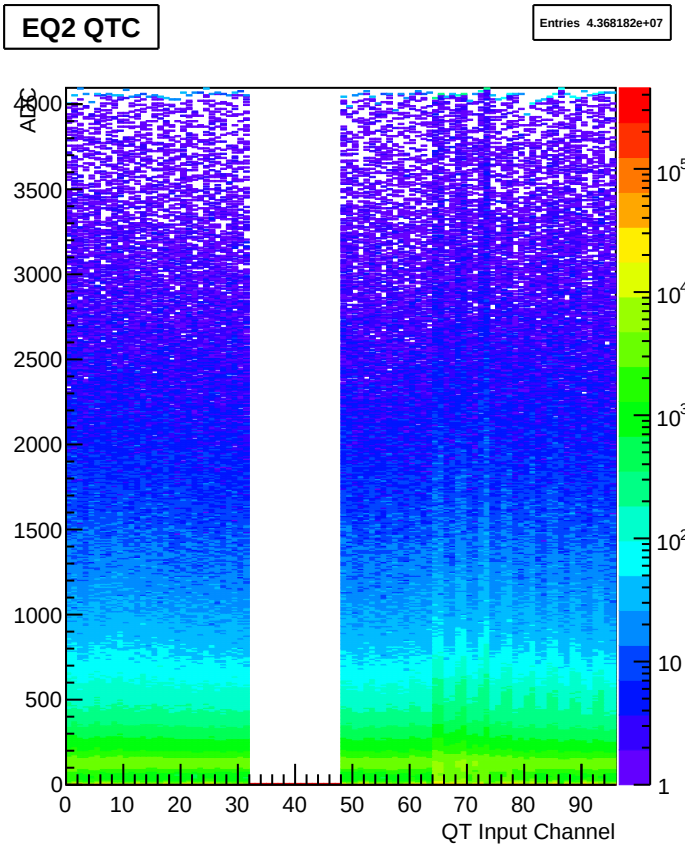

EQ1 QTB
Entries 4.740429e+07
TDC
QT Input Channel

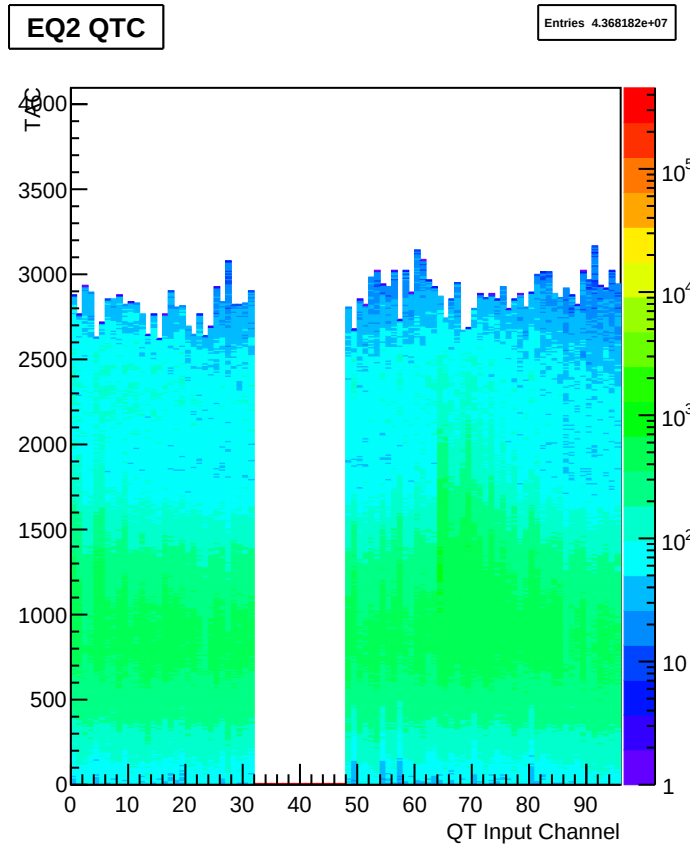

EQ1 QTC
Entries 4.368182e+07
ADC
QT Input Channel

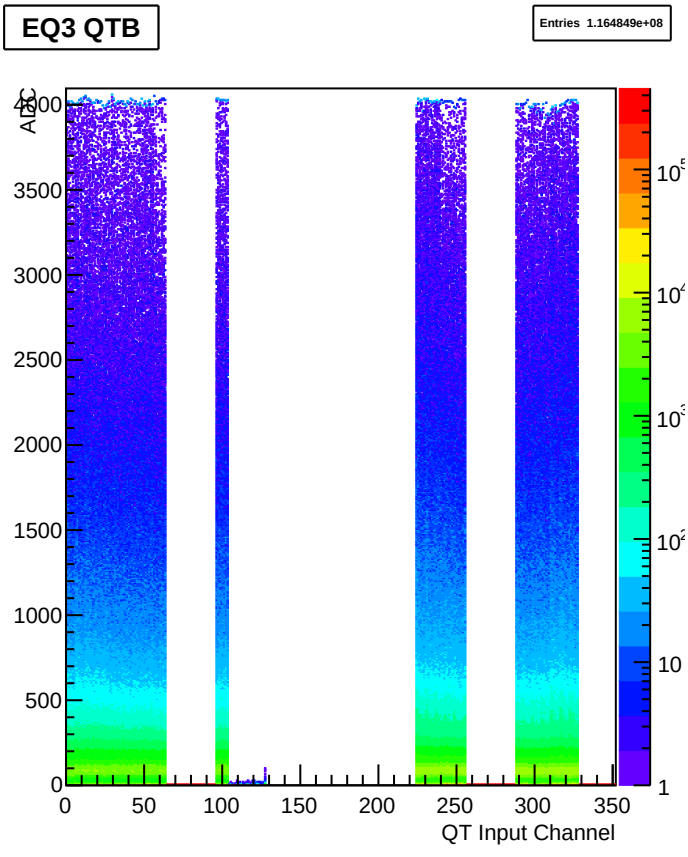

EQ1 QTC
Entries 4.368182e+07
TADC
QT Input Channel

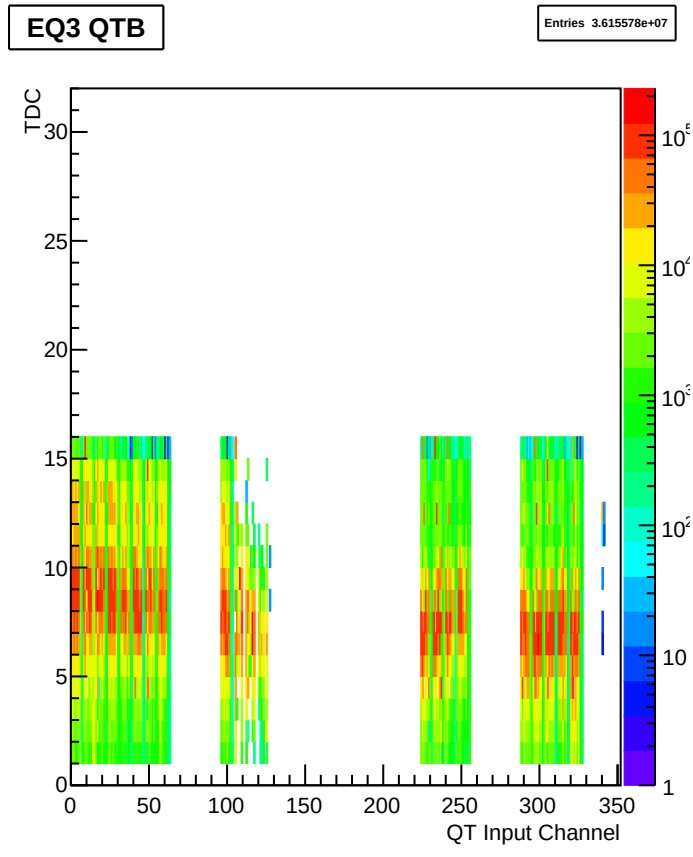

EQ2 QTB
Entries 1.310455e+08
ADC
QT Input Channel

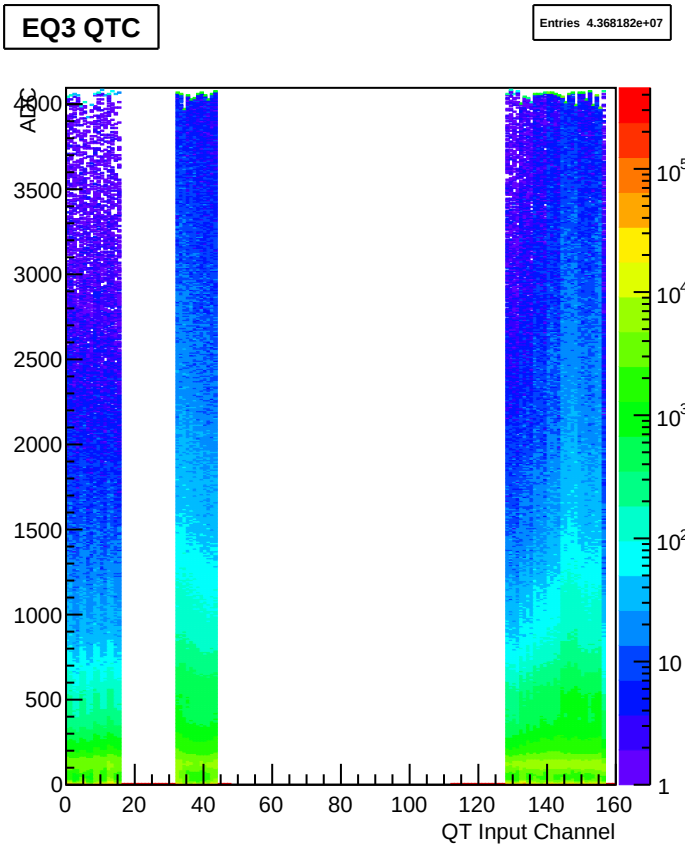

EQ2 QTB
Entries 3.547343e+07
TDC
QT Input Channel


EQ2 QTC
Entries 4.368182e+07
ADC
QT Input Channel


EQ2 QTC
Entries 4.368182e+07
TADC
QT Input Channel


EQ3 QTB
Entries 1.164849e+08
ADC
QT Input Channel


EQ3 QTB
Entries 3.615578e+07
TDC
QT Input Channel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Entries 4.368182e+07
ADC
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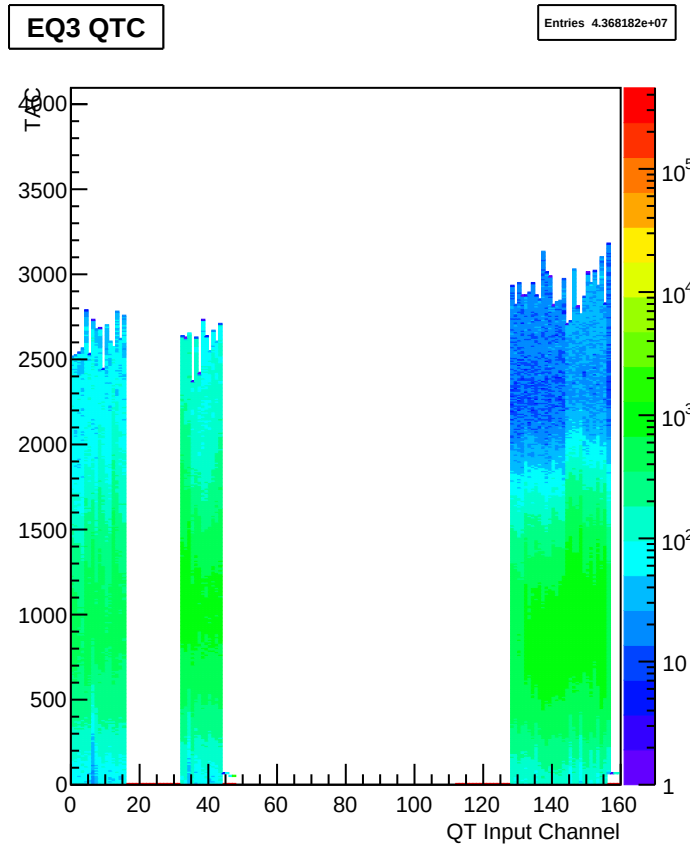
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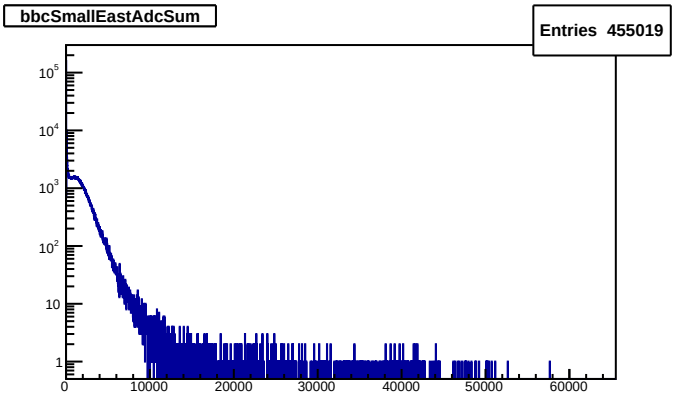
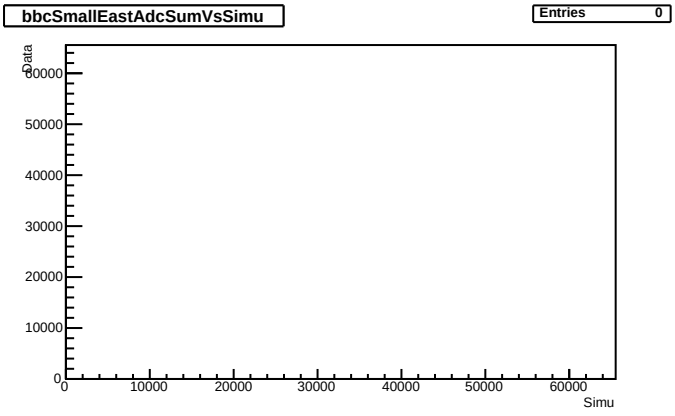
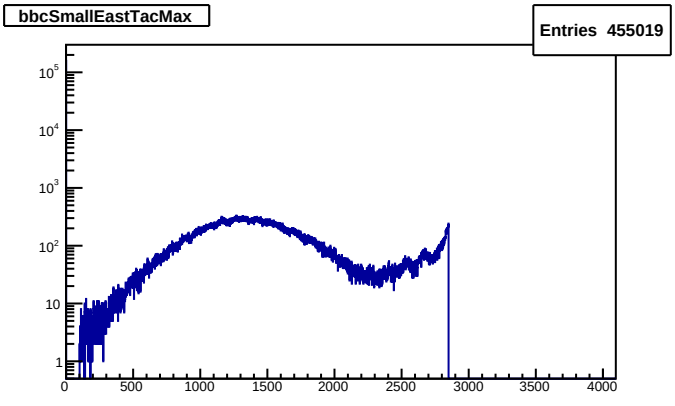
Entries 4.368182e+07
TADC
QT Input Channel


bbcSmallEastAdcSum
Entries 455019


bbcSmallEastAdcSumVsSimu
Entries 0
Data
Simu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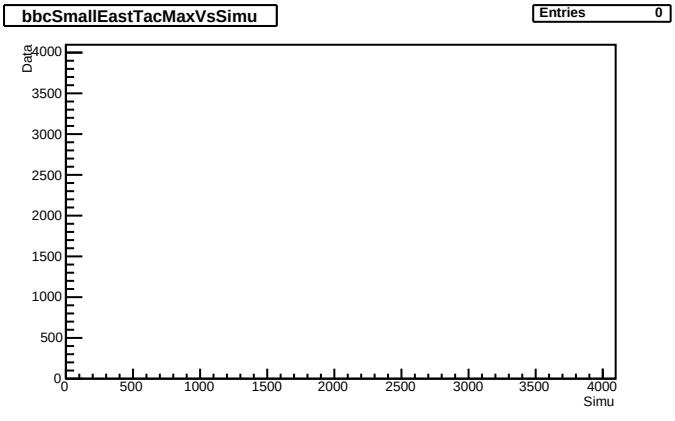
Entries 455019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Entries 0
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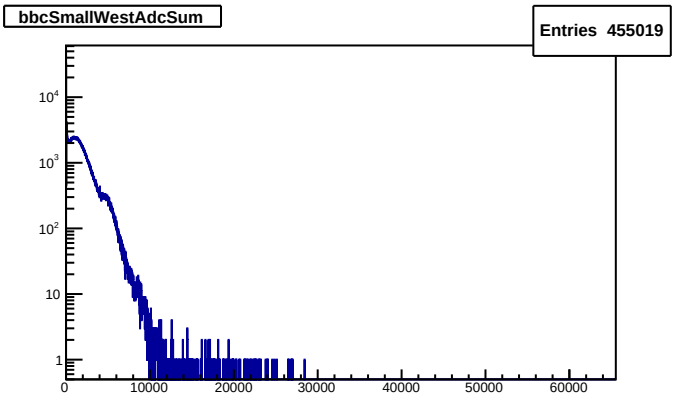
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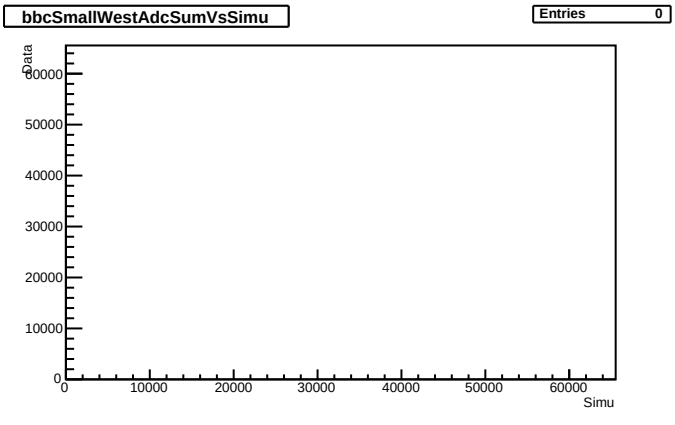
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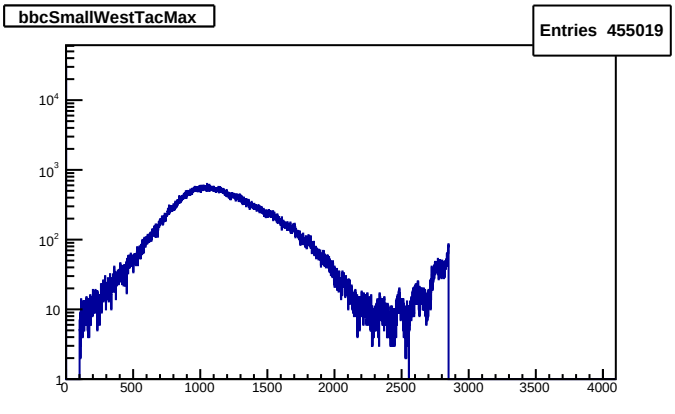
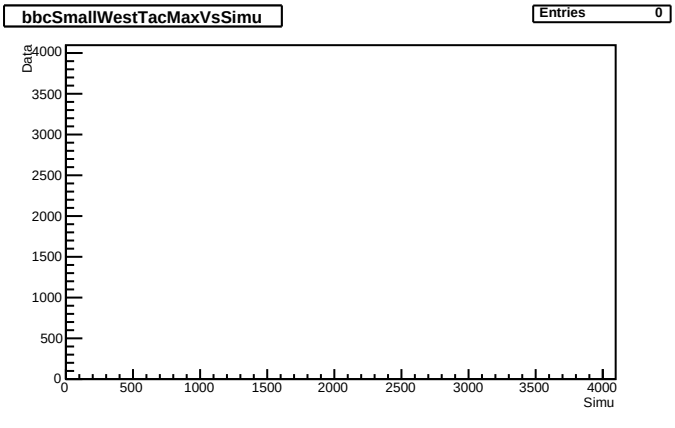
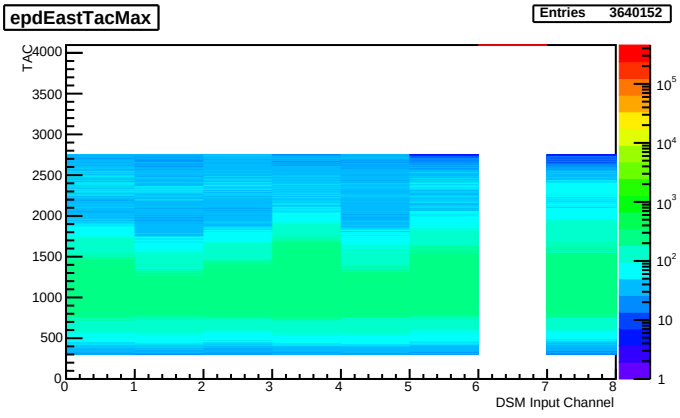
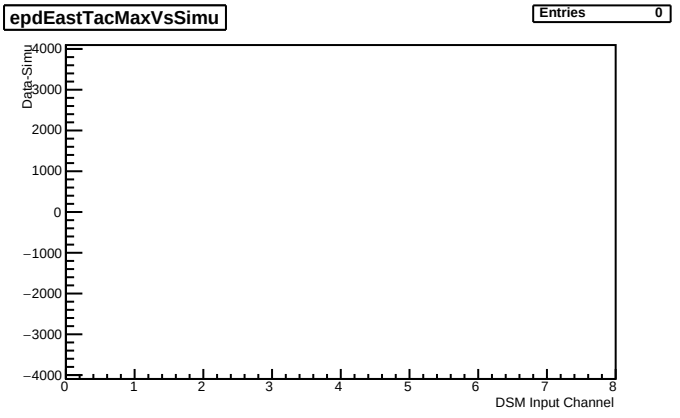
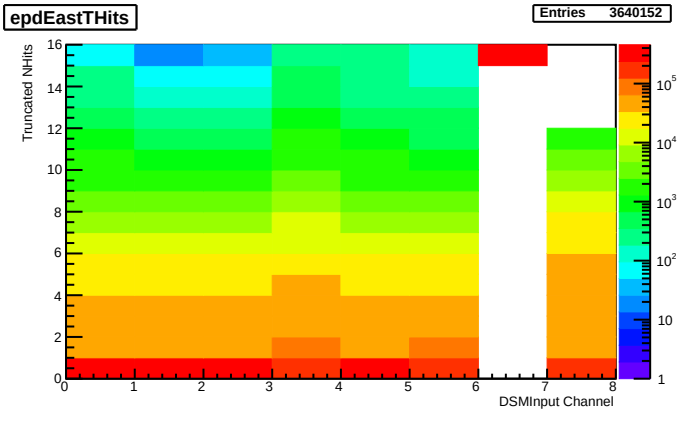
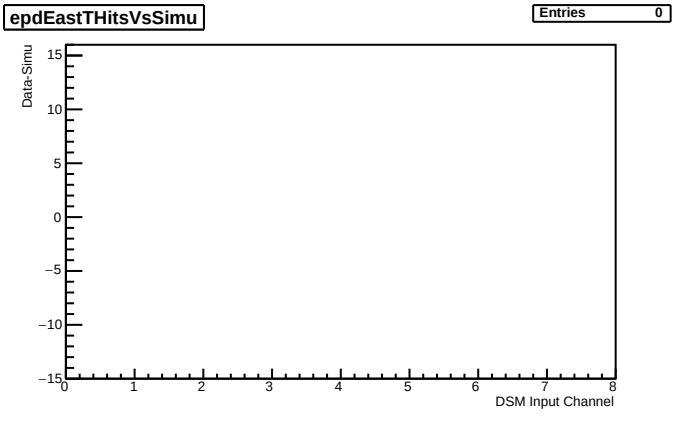

bbcSmallWestTacMax
Entries 455019


bbcSmallWestTacMaxVsSimu
Entries 0
Data
Simu


epdEastTacMax
Entries 3640152
TAC
DSM Input Channel


epdEastTacMaxVsSimu
Entries 0
Data-Simu
DSM Input Channel


epdEastTHits
Entries 3640152
Truncated NHits
DSMInput Channel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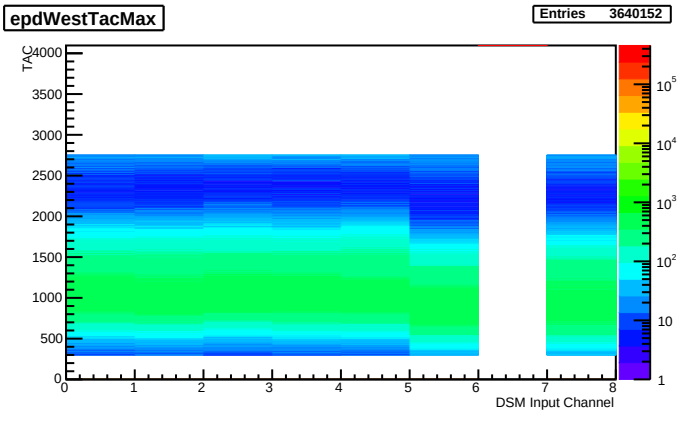
Data-Simu
DSM Input Channel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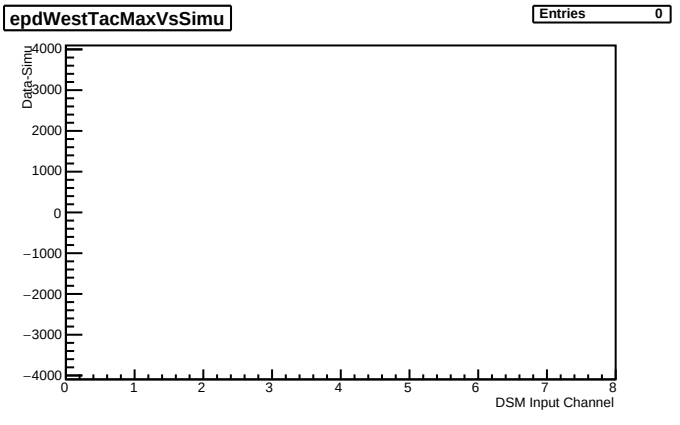
Entries 3640152
TAC
DSM Input Channel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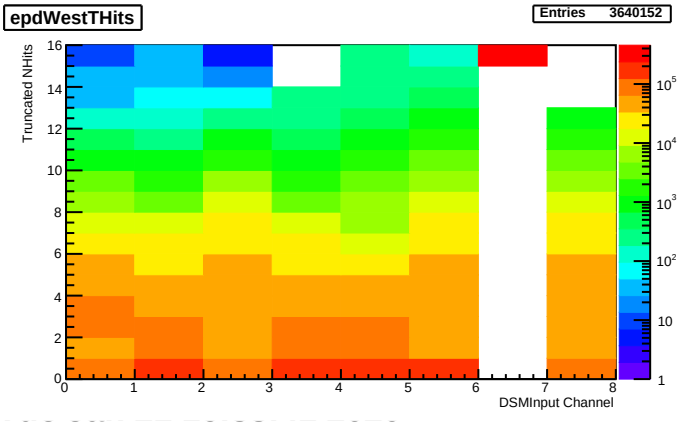
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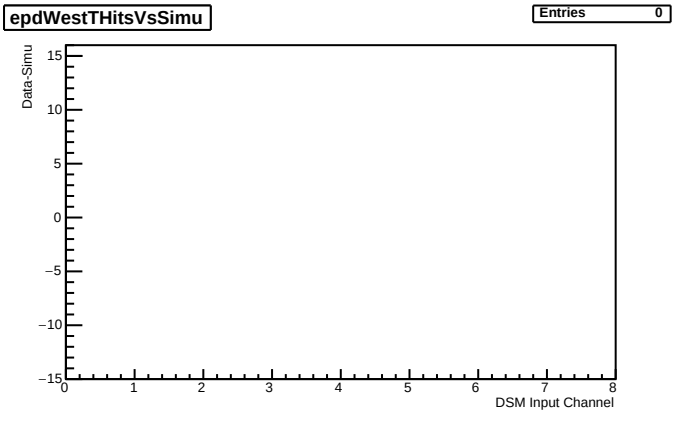
Entries 3640152
Truncated NHits
DSMInput Channel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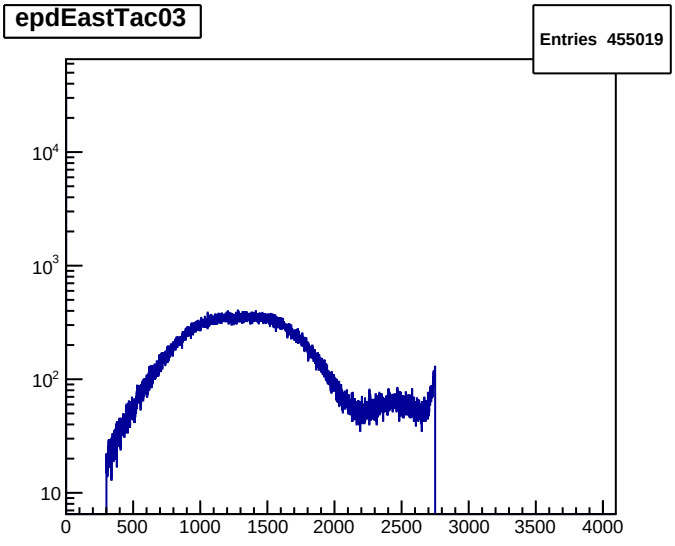
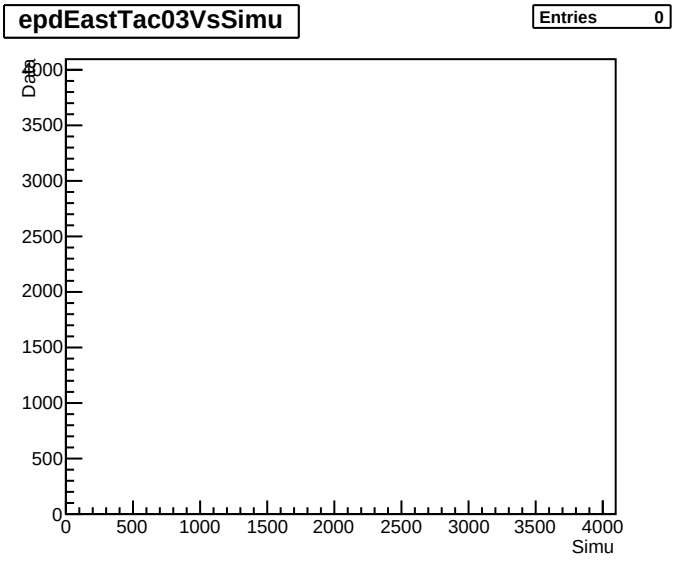
DSM Input Channel


epdEastTac03
Entries 455019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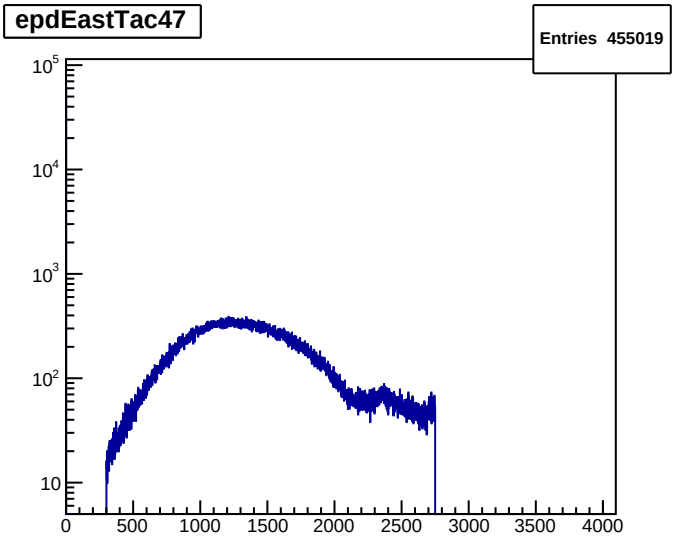
Entries 0
Data
Simu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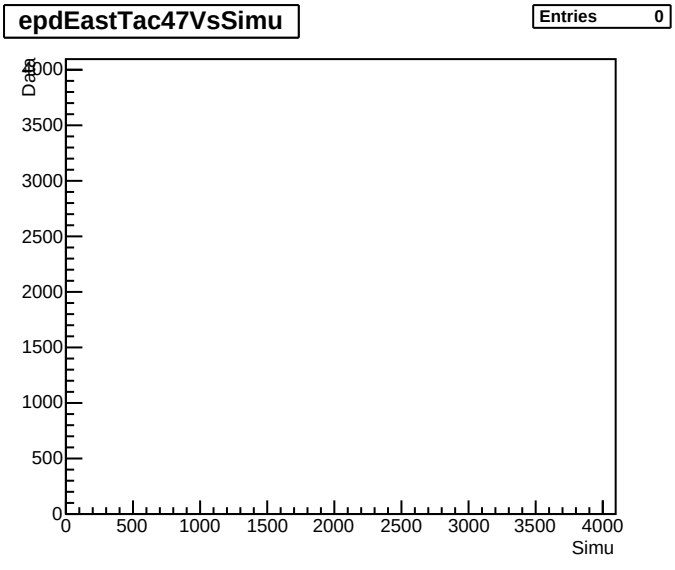
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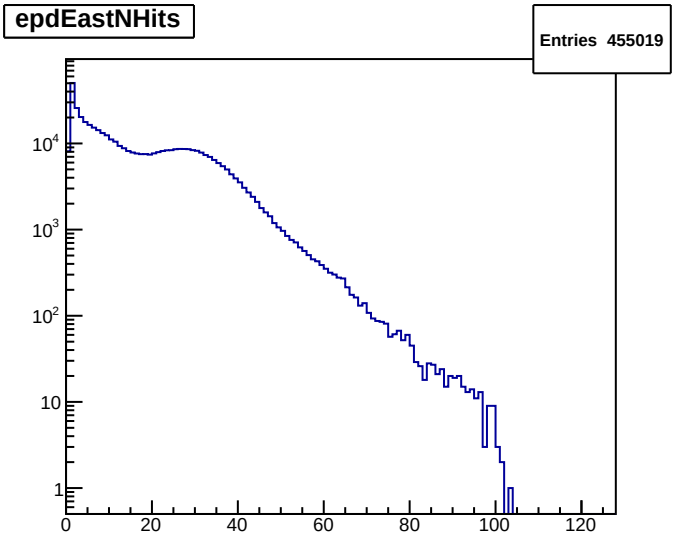
Entries 0
Data
Simu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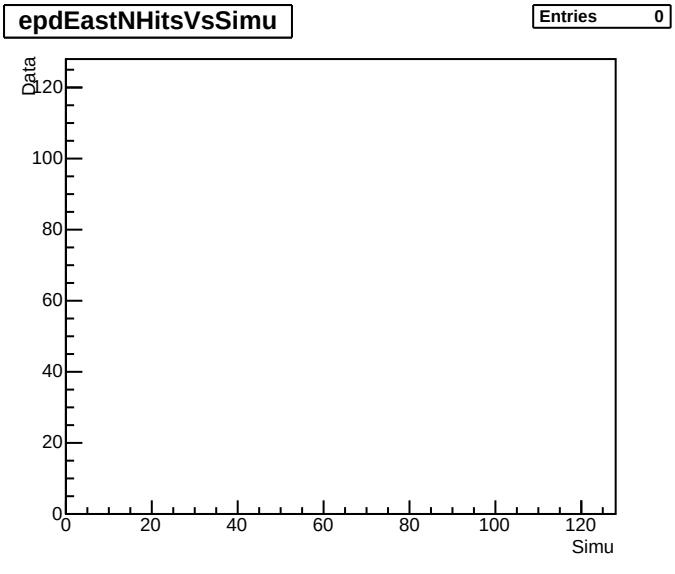
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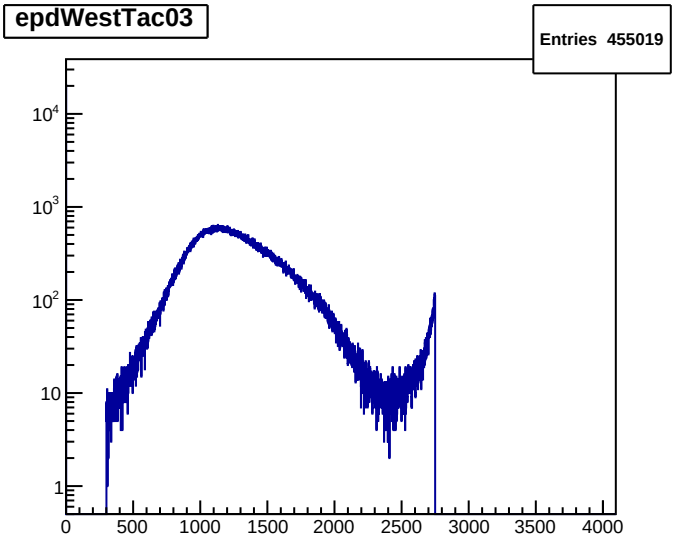
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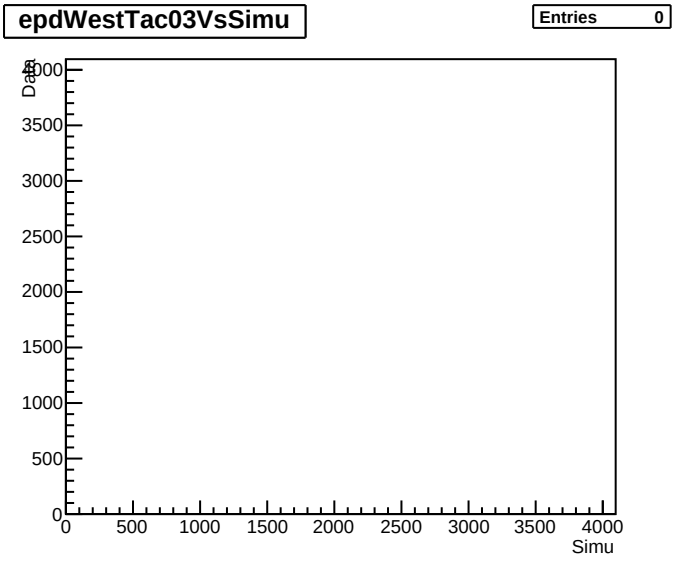
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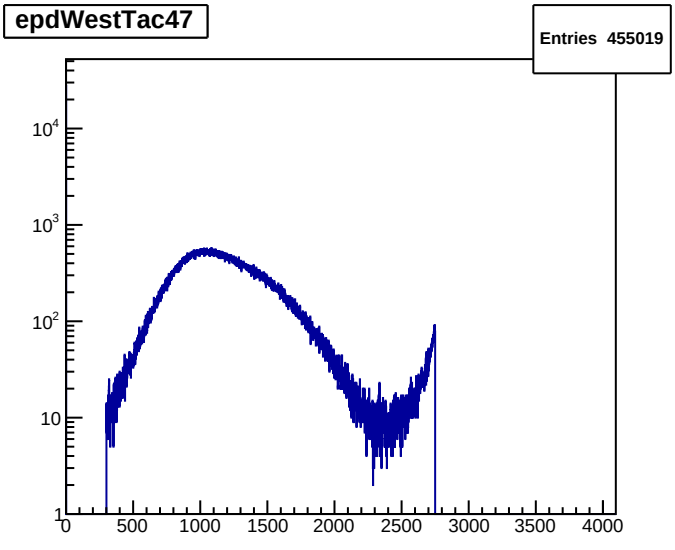
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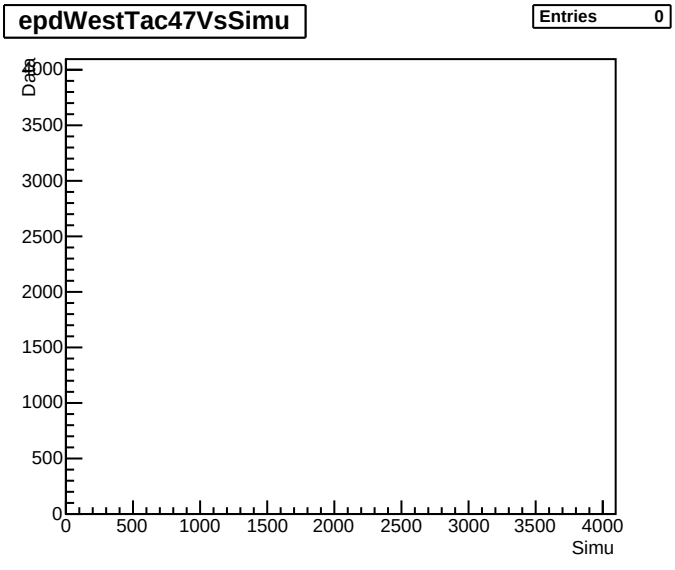
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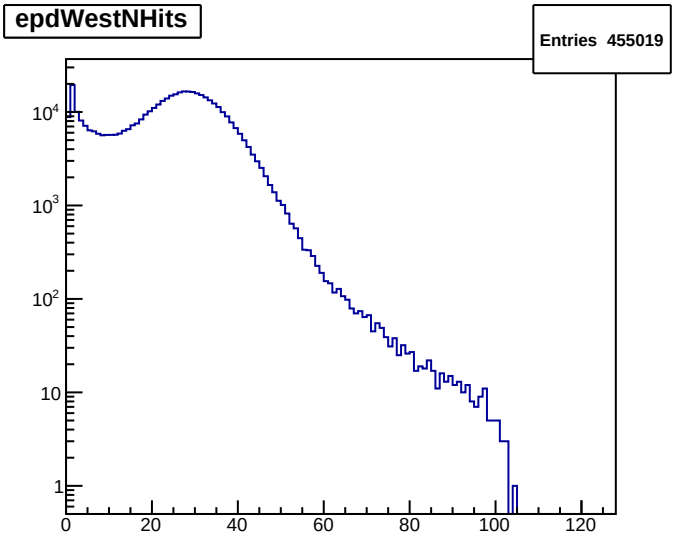
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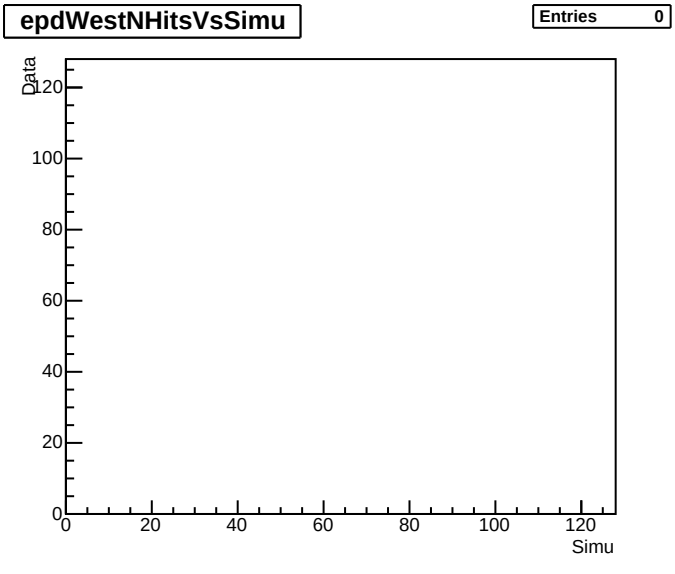
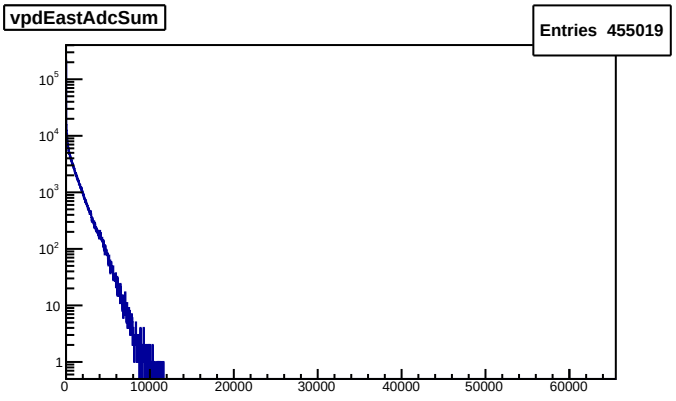
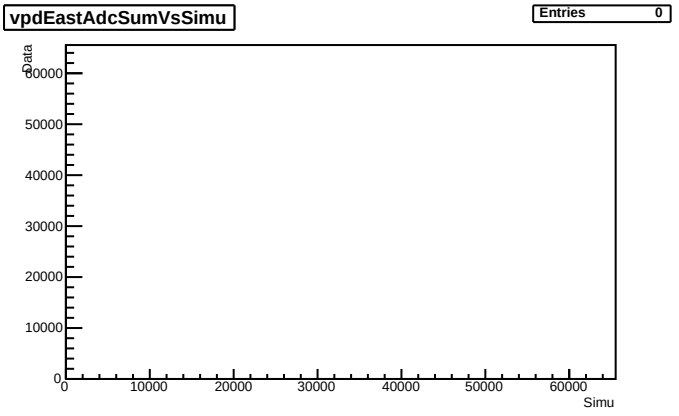
Entries 455019


epdWestNHitsVsSimu
Entries 0
Data
Simu


vpdEastAdcSum
Entries 455019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Entries 0
Data
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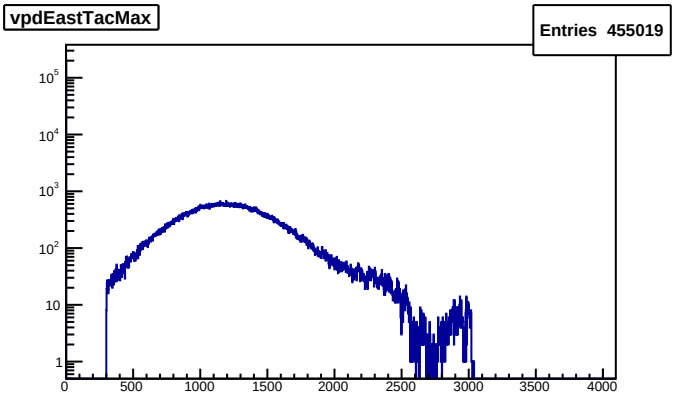
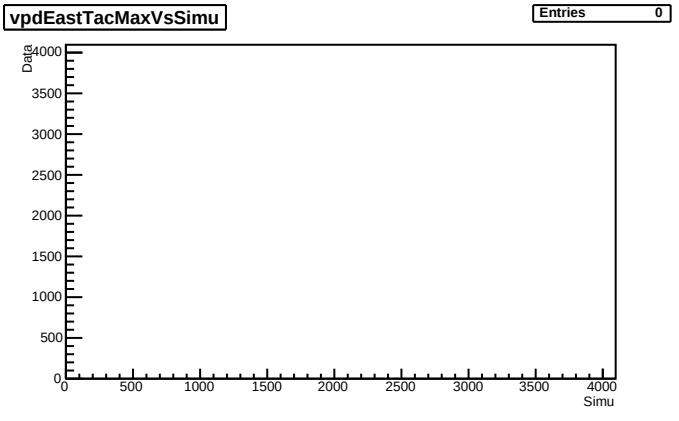

vpdEastTacMax
Entries 455019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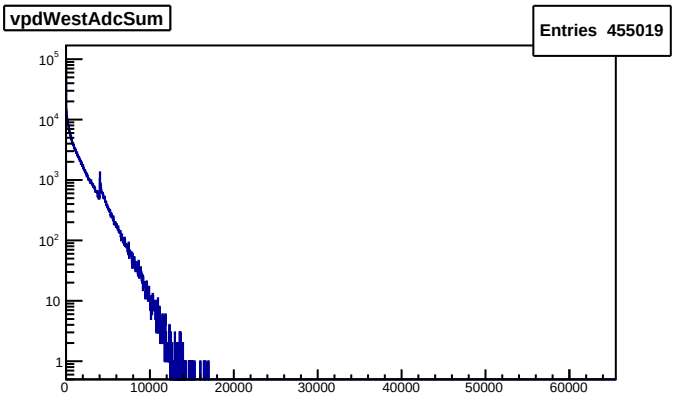
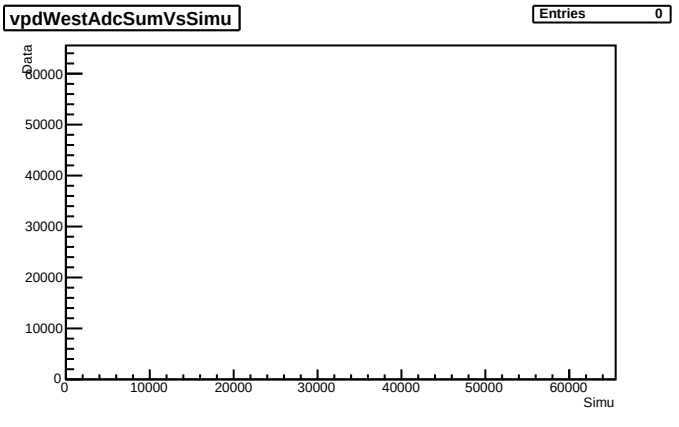
Data
Simu


vpdWestAdcSum
Entries 455019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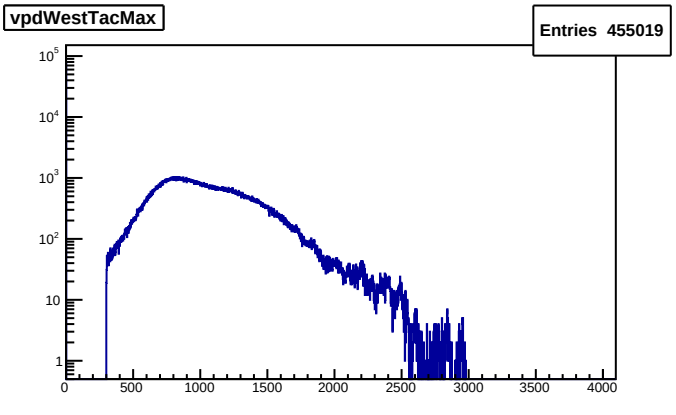
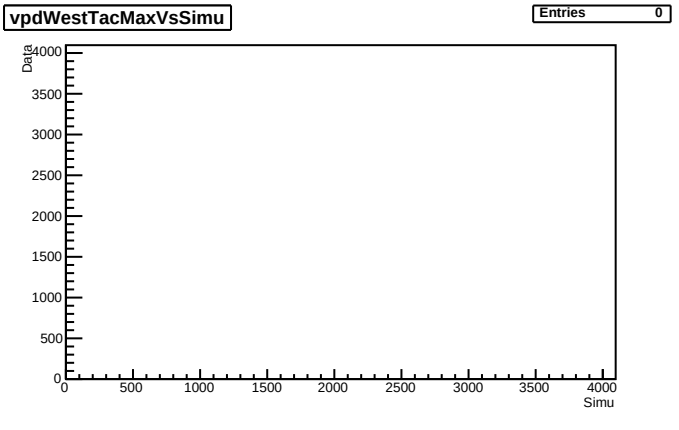
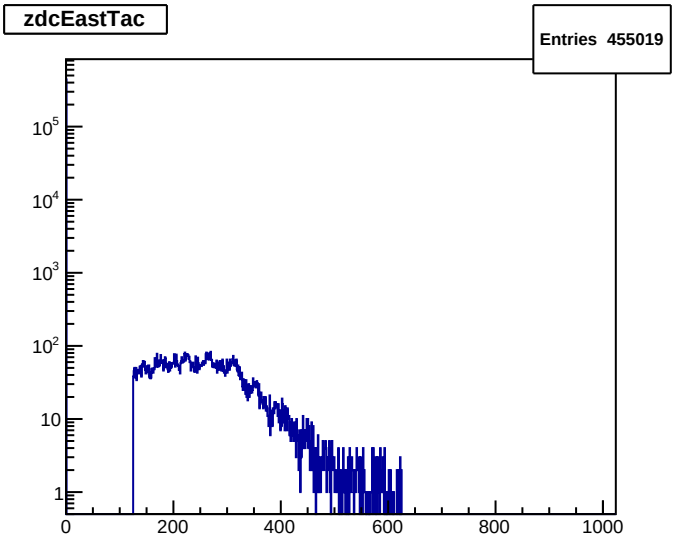
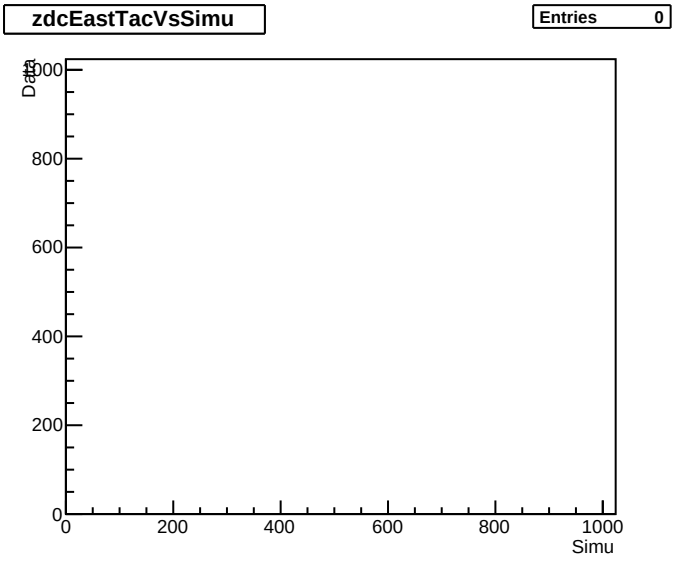
Entries 0
Data
Simu


vpdWestTacMax
Entries 455019


vpdWestTacMaxVsSimu
Entries 0
Data
Simu


zdcEastTac
Entries 455019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Entries 0
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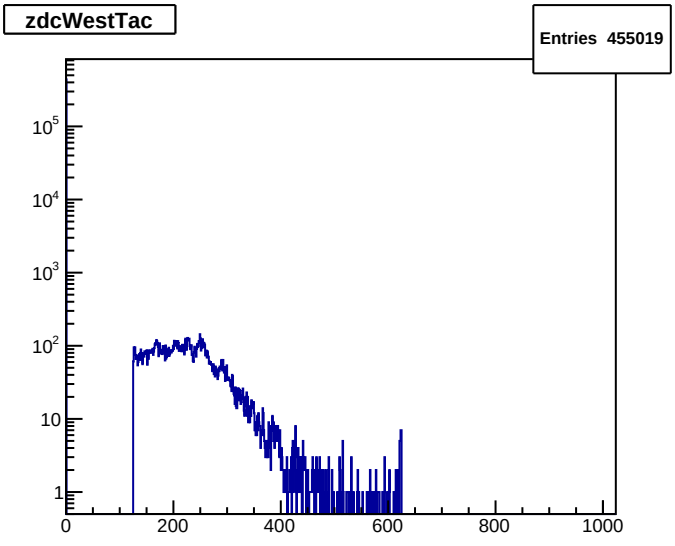
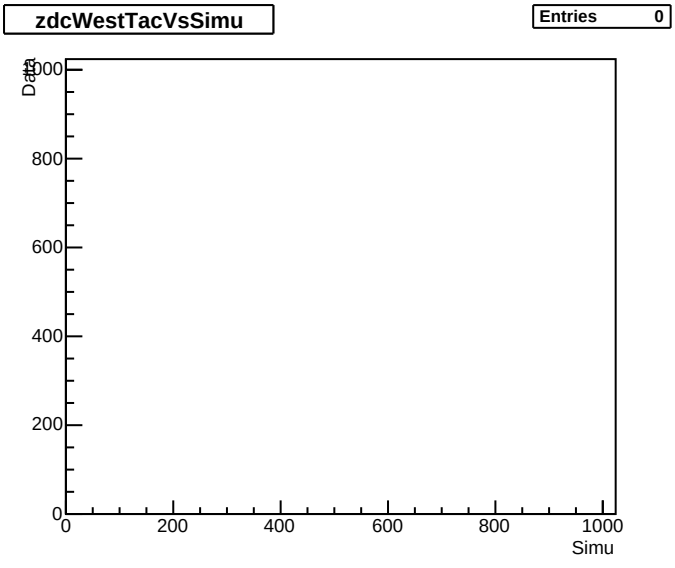
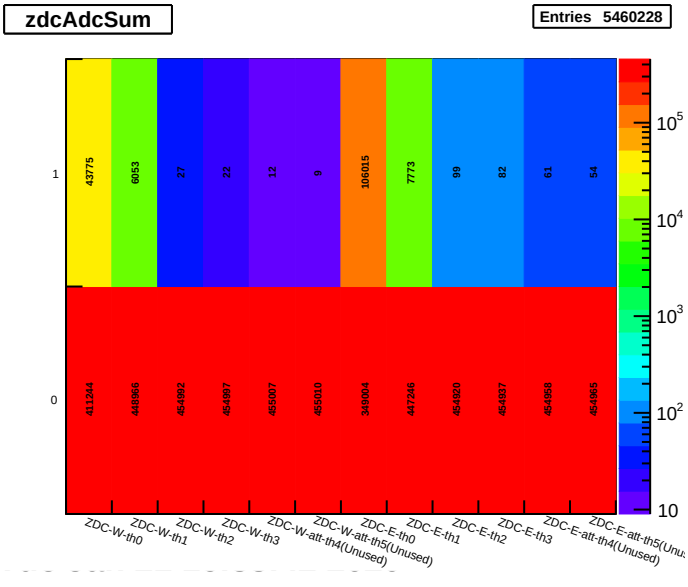
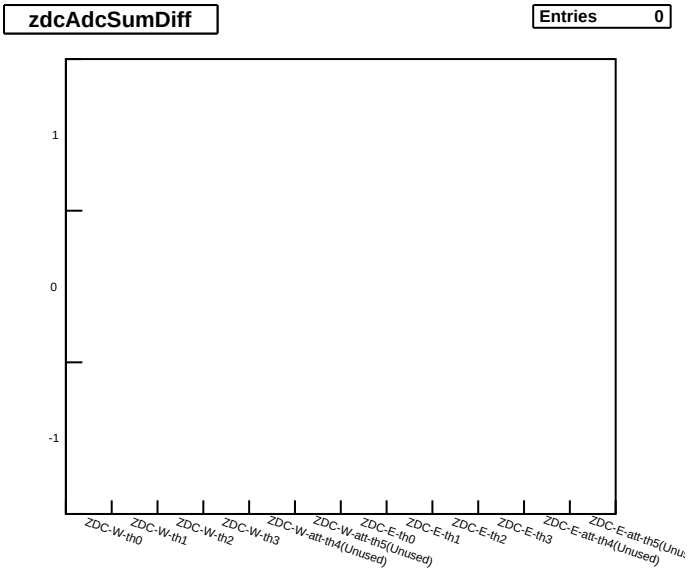

zdcWestTac
Entries 455019


zdcWestTacVsSimu
Entries 0
Data
Simu


zdcAdcSum
Entries 5460228
43775
6053
27
22
12
9
106015
7773
99
82
61
54
411244
448966
454992
454997
455007
455010
349004
447246
454920
454937
454958
454965
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)


zdcAdcSumDiff
Entries 0
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
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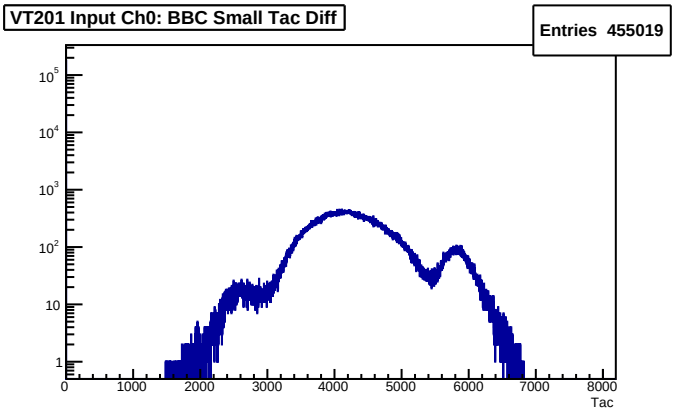
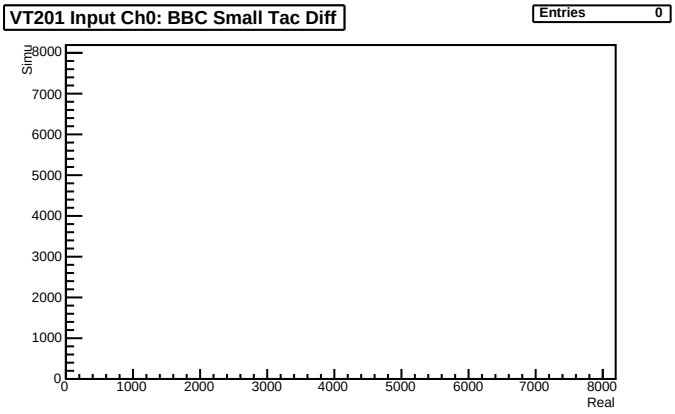
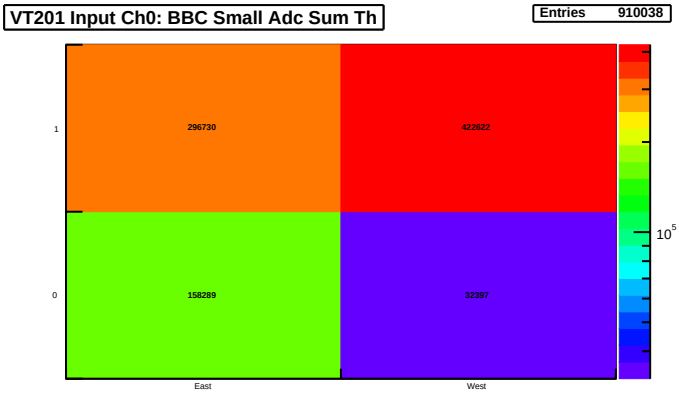
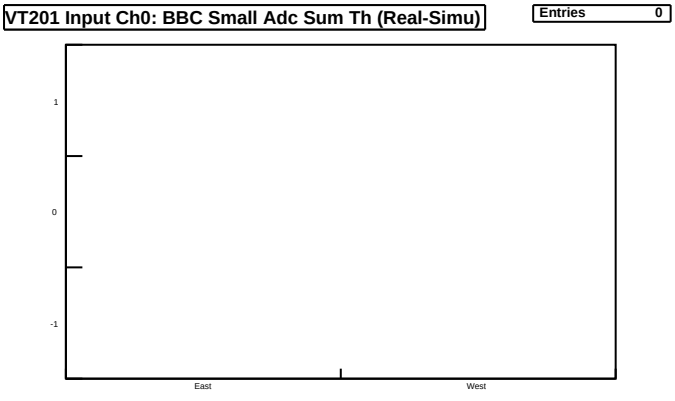
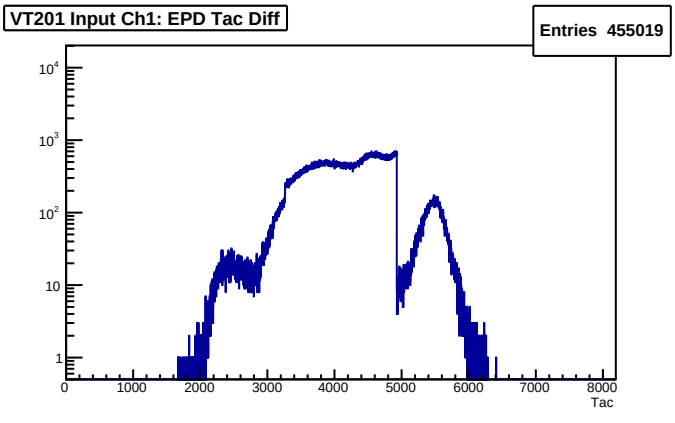
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)

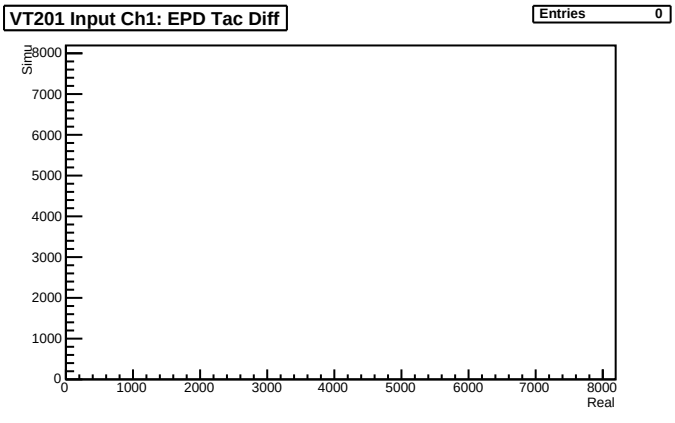

VT201 Input Ch0: BBC Small Tac Diff
Entries 455019
Tac

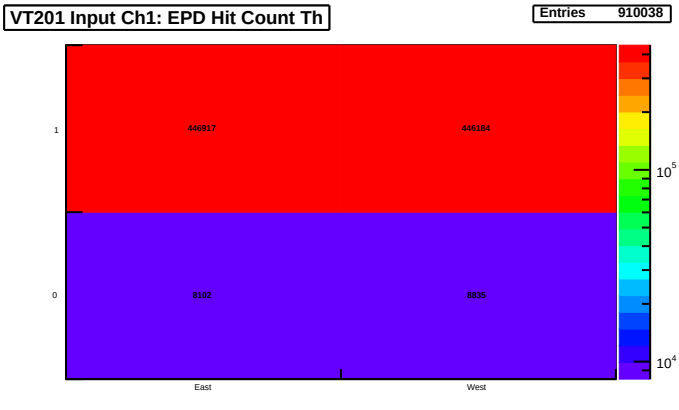

VT201 Input Ch0: BBC Small Tac Diff
Entries 0
Simu
Real

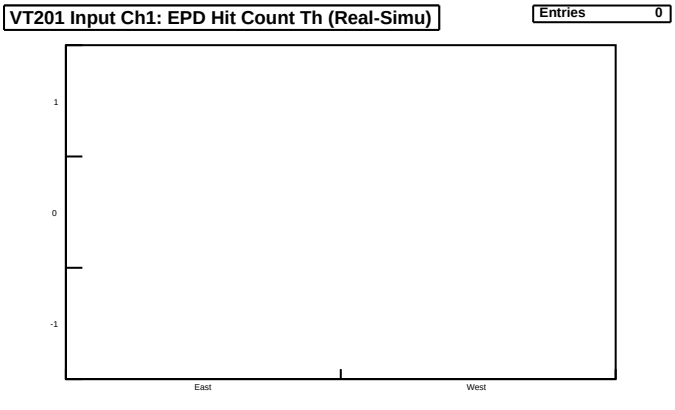

VT201 Input Ch0: BBC Small Adc Sum Th
Entries 910038
296730
422622
158289
32397
East
West

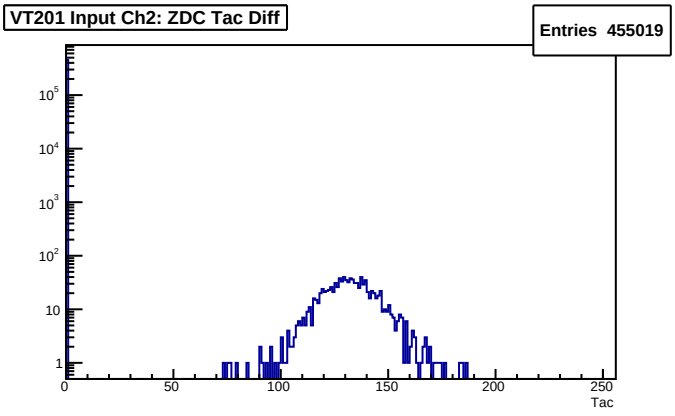

VT201 Input Ch0: BBC Small Adc Sum Th (Real-Simu)
Entries 0
East
West

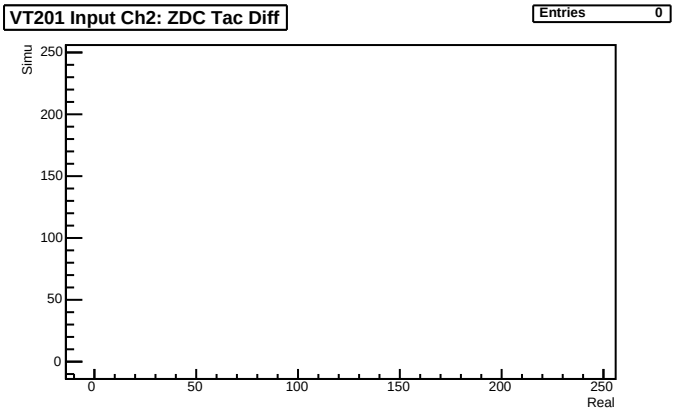

VT201 Input Ch1: EPD Tac Diff
Entries 455019
Tac

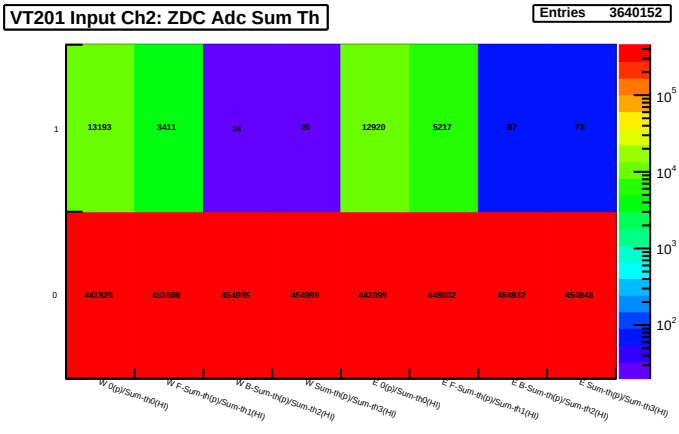

VT201 Input Ch1: EPD Tac Diff
Entries 0
Simu
Real

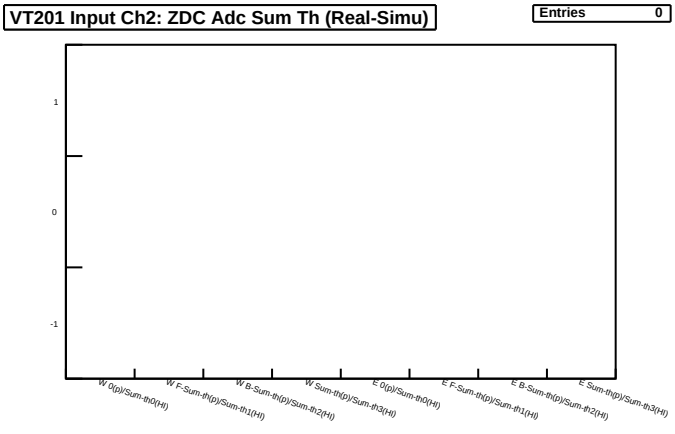

VT201 Input Ch1: EPD Hit Count Th
Entries 910038
446917
446184
8102
8835
East
West

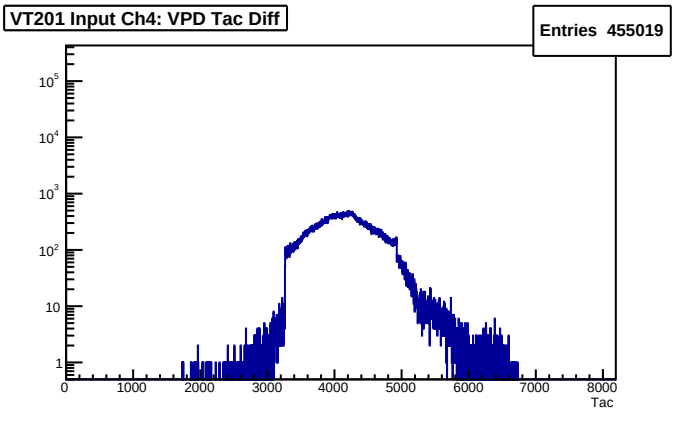

VT201 Input Ch1: EPD Hit Count Th (Real-Simu)
Entries 0
East
West

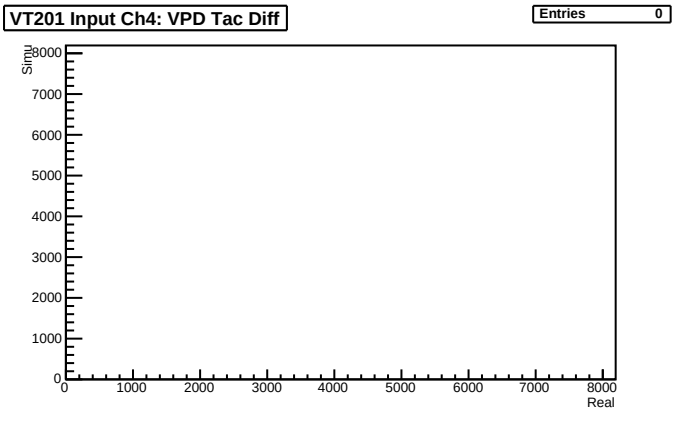

VT201 Input Ch2: ZDC Tac Diff
Entries 455019
Tac

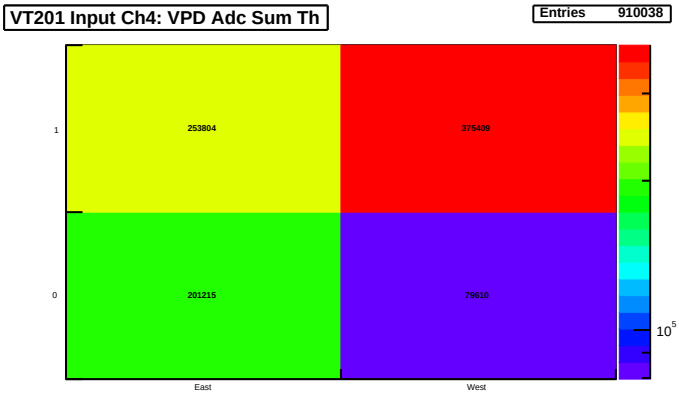

VT201 Input Ch2: ZDC Tac Diff
Entries 0
Simu
Real

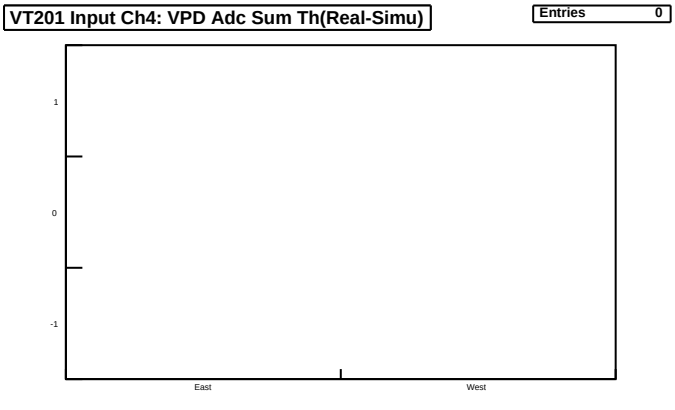

VT201 Input Ch2: ZDC Adc Sum Th
Entries 3640152
13193
3411
24
20
12920
5217
87
73
441826
451608
454995
454999
442099
449802
454932
454946
W 0(p)/Sum-th0(Hl)
W F-Sum-th(p)/Sum-th1(Hl)
W B-Sum-th(p)/Sum-th2(Hl)
W Sum-th(p)/Sum-th3(Hl)
E 0(p)/Sum-th0(Hl)
E F-Sum-th(p)/Sum-th1(Hl)
E B-Sum-th(p)/Sum-th2(Hl)
E Sum-th(p)/Sum-th3(Hl)

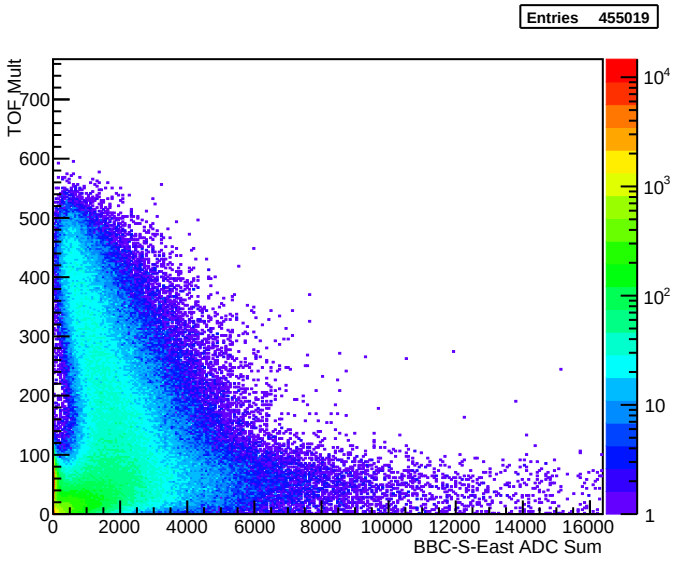

VT201 Input Ch2: ZDC Adc Sum Th (Real-Simu)
Entries 0
W 0(p)/Sum-th0(Hl)
W F-Sum-th(p)/Sum-th1(Hl)
W B-Sum-th(p)/Sum-th2(Hl)
W Sum-th(p)/Sum-th3(Hl)
E 0(p)/Sum-th0(Hl)
E F-Sum-th(p)/Sum-th1(Hl)
E B-Sum-th(p)/Sum-th2(Hl)
E Sum-th(p)/Sum-th3(Hl)

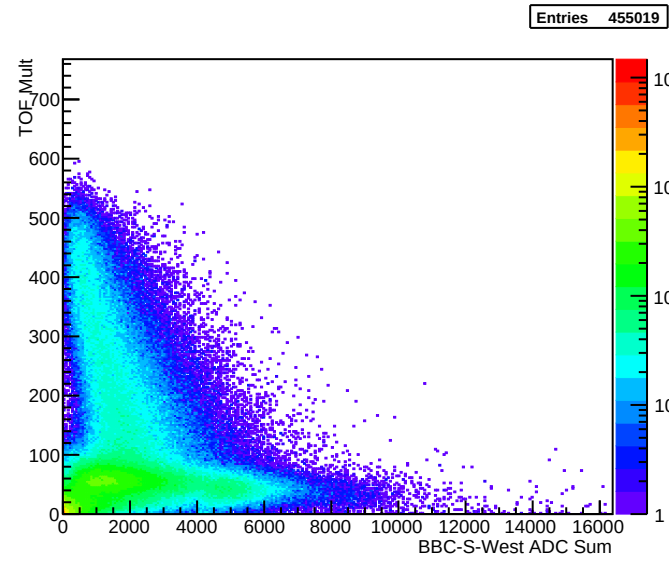

VT201 Input Ch4: VPD Tac Diff
Entries 455019
Tac

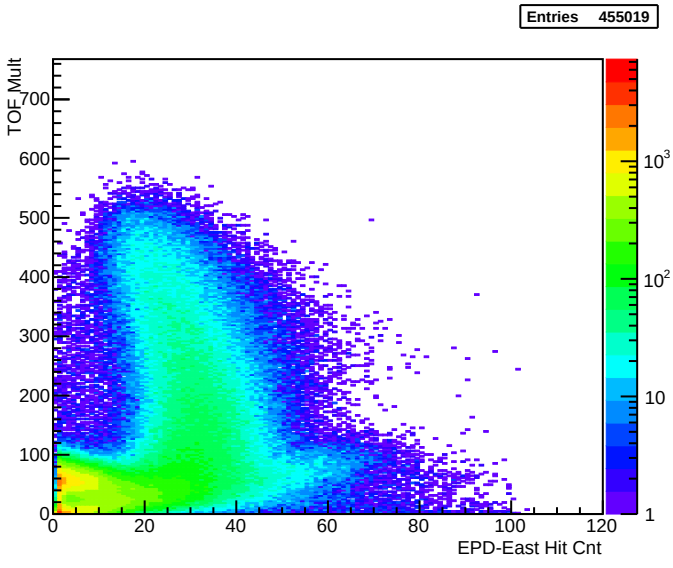

VT201 Input Ch4: VPD Tac Diff
Entries 0
Simu
Real

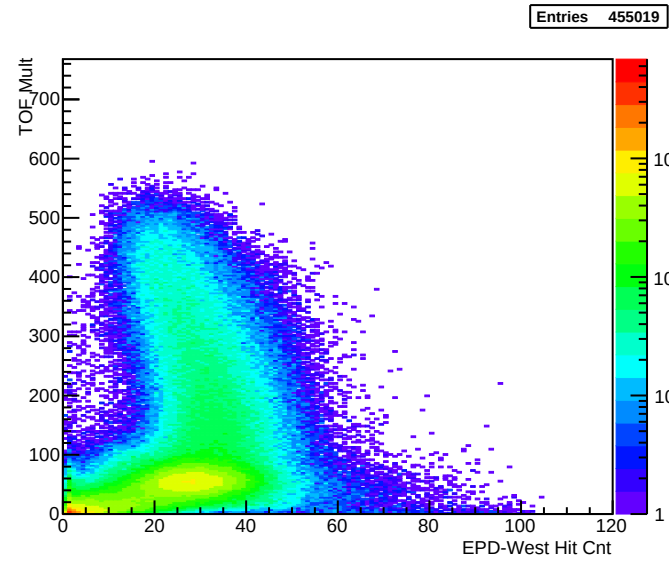

VT201 Input Ch4: VPD Adc Sum Th
Entries 910038
253804
375409
201215
79610
East
West

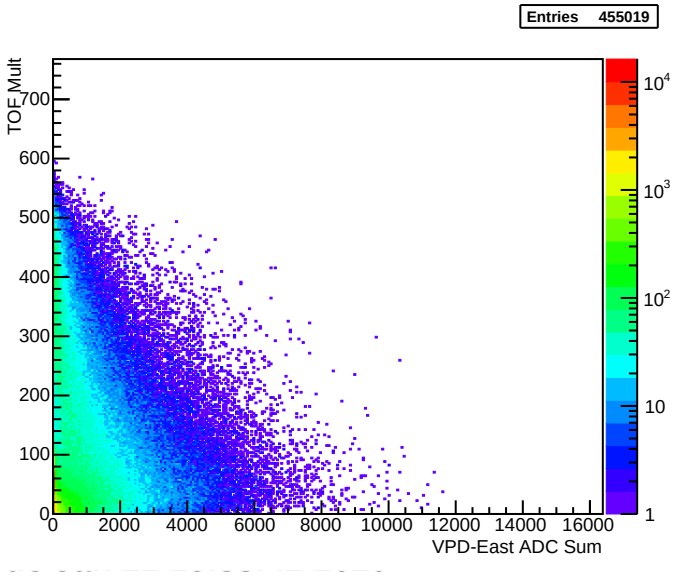

VT201 Input Ch4: VPD Adc Sum Th(Real-Simu)
Entries 0
East
West

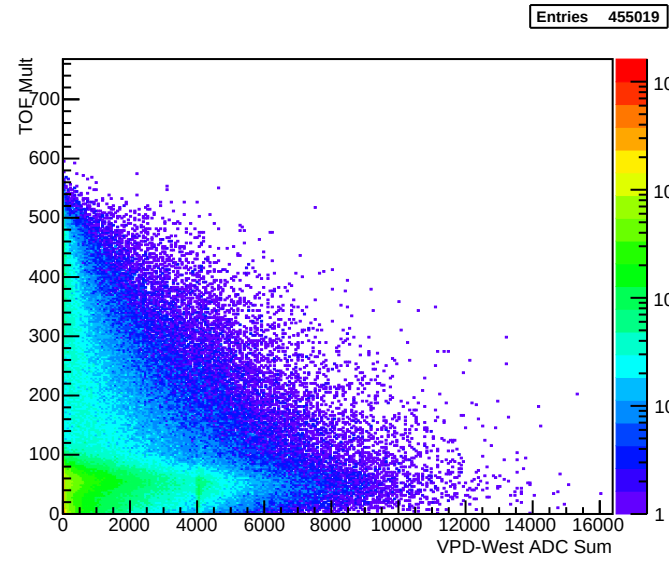

Entries 455019
TOF_Mult
BBC-S-East ADC Sum

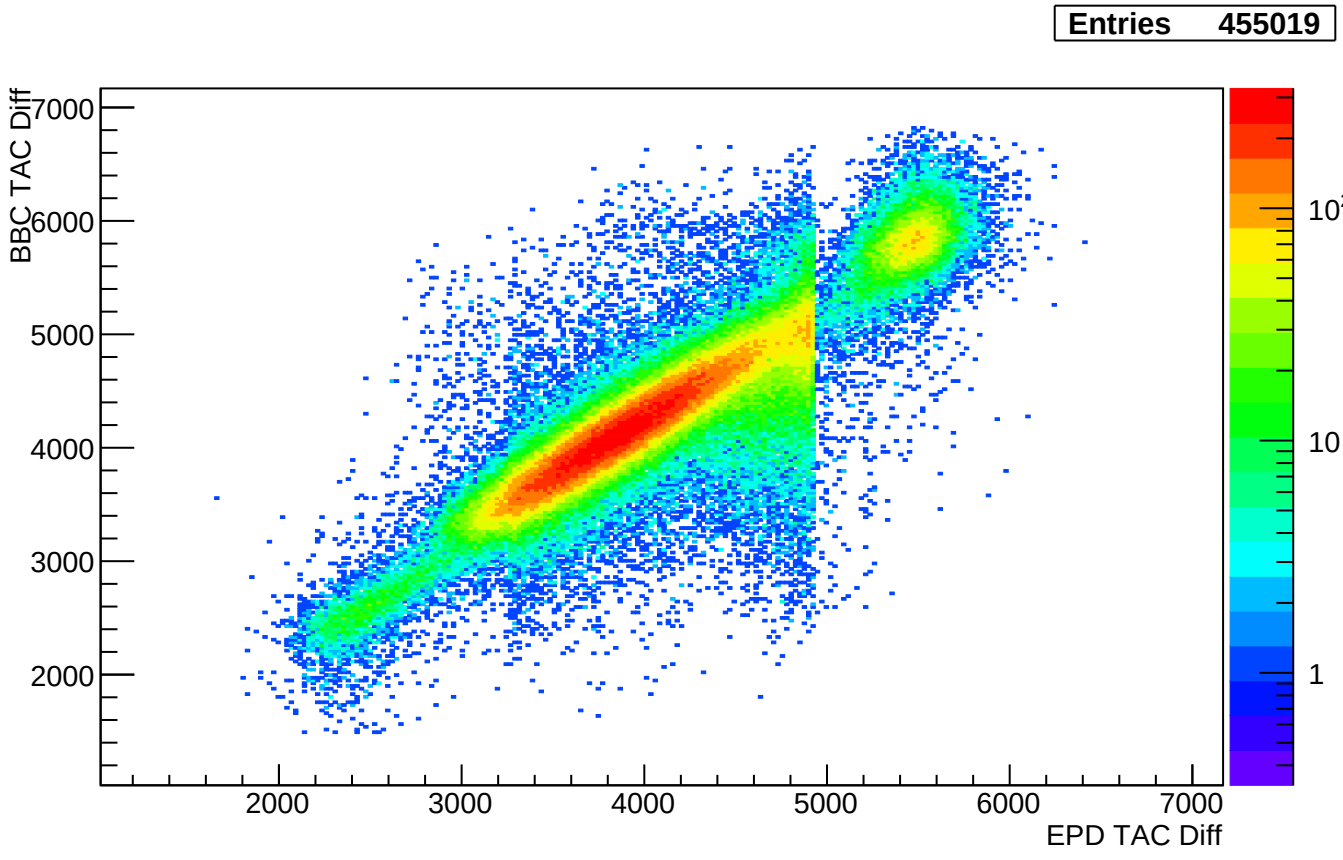

Entries 455019
TOF_Mult
BBC-S-West ADC Sum

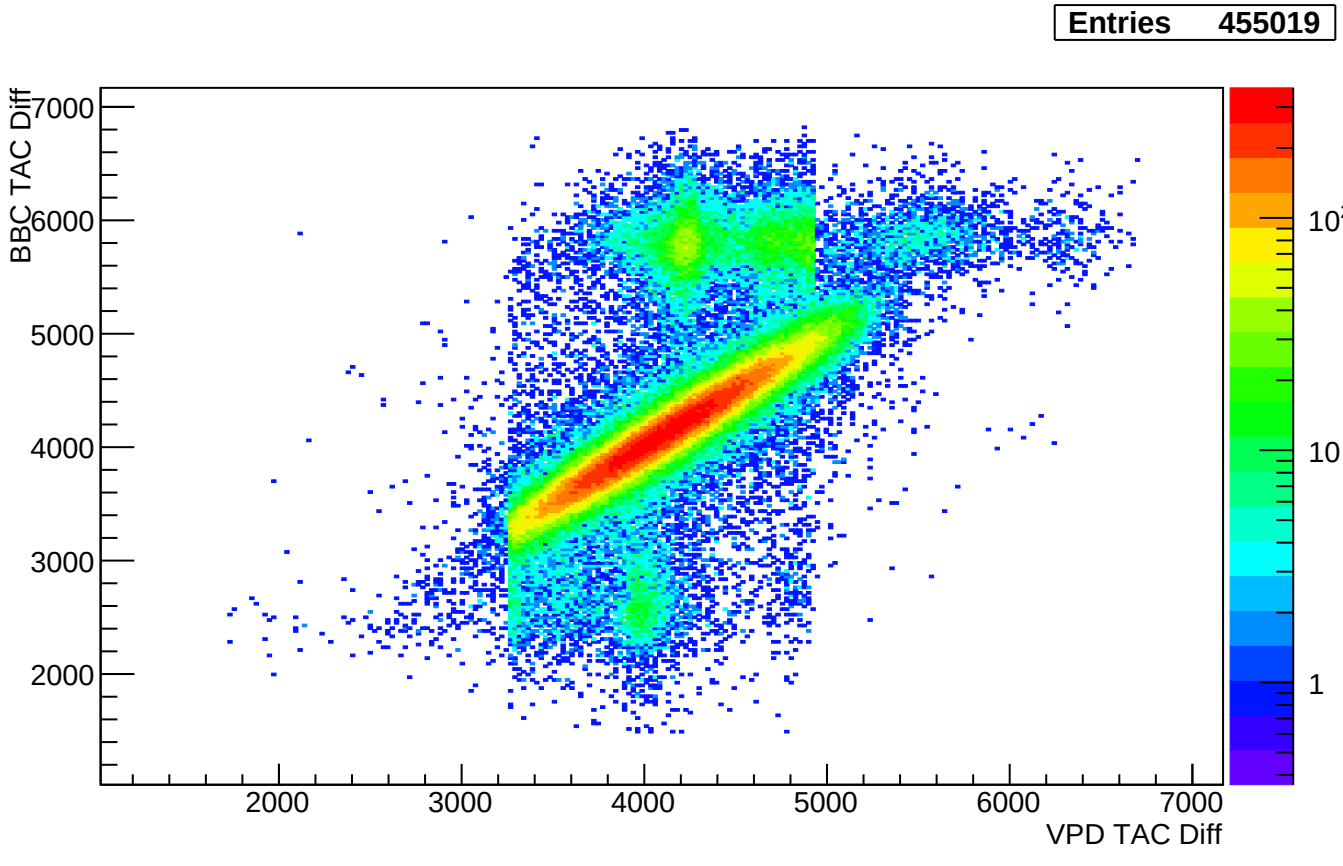

Entries 455019
TOF_Mult
EPD-East Hit Cnt

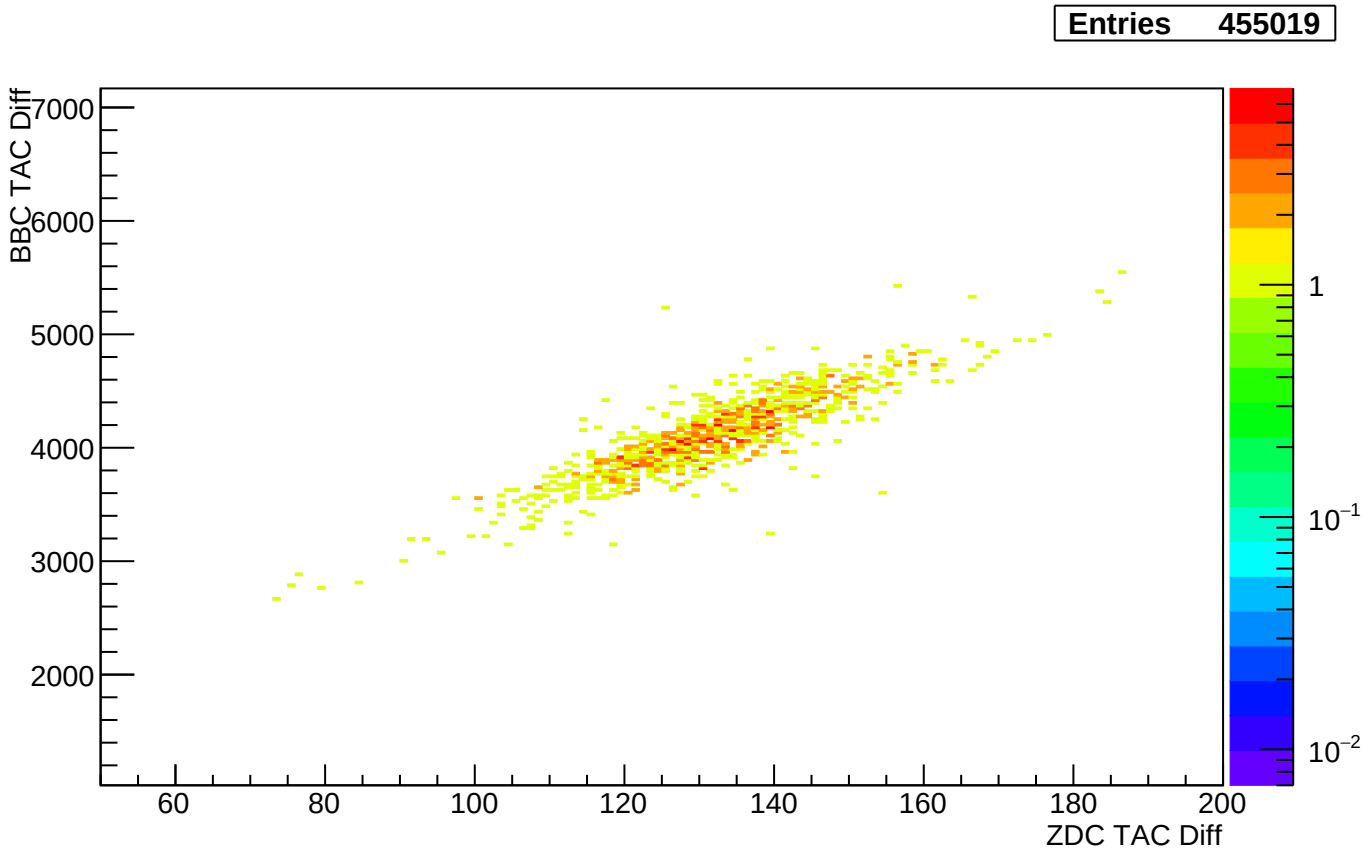

Entries 455019
TOF_Mult
EPD-West Hit Cnt

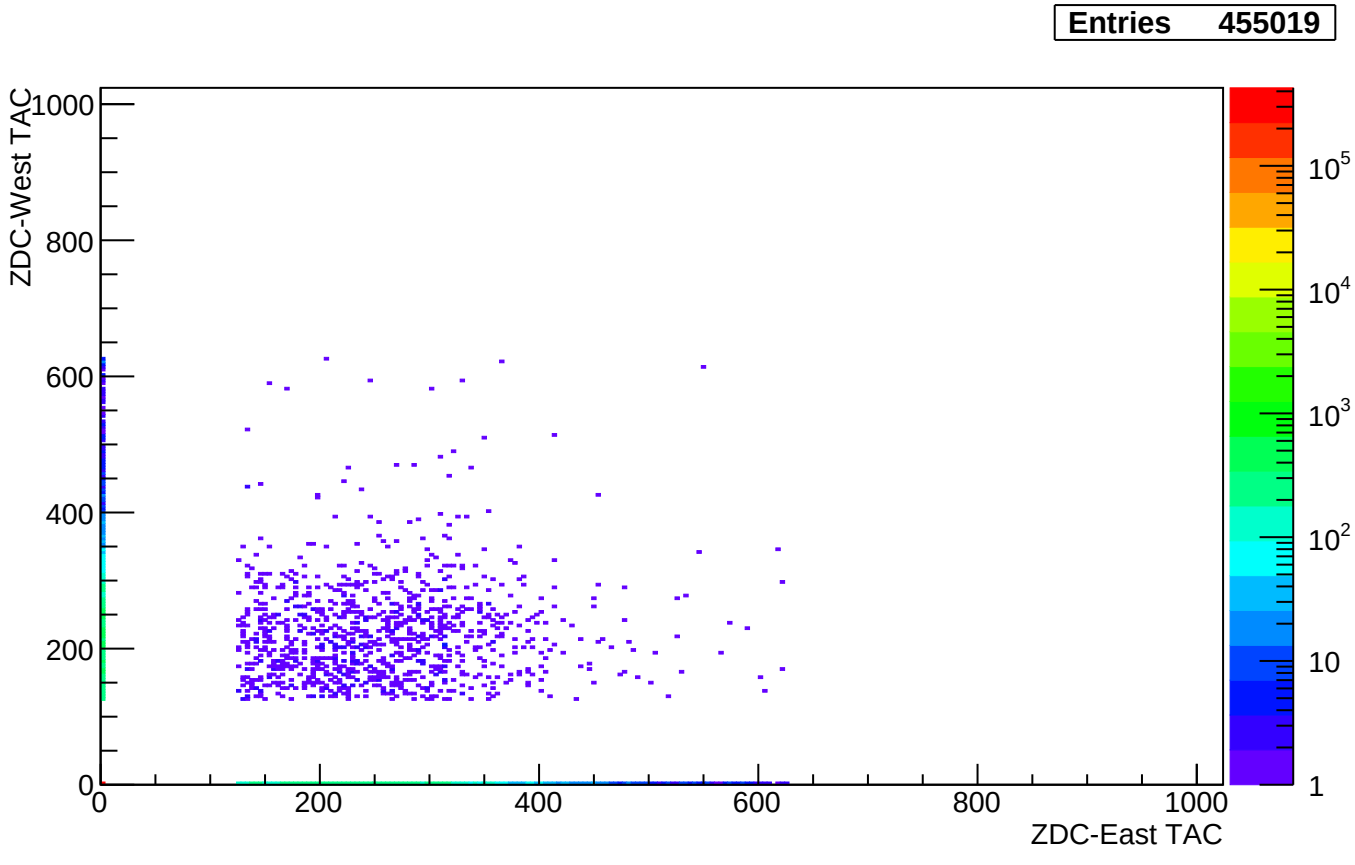

Entries 455019
TOF_Mult
VPD-East ADC Sum

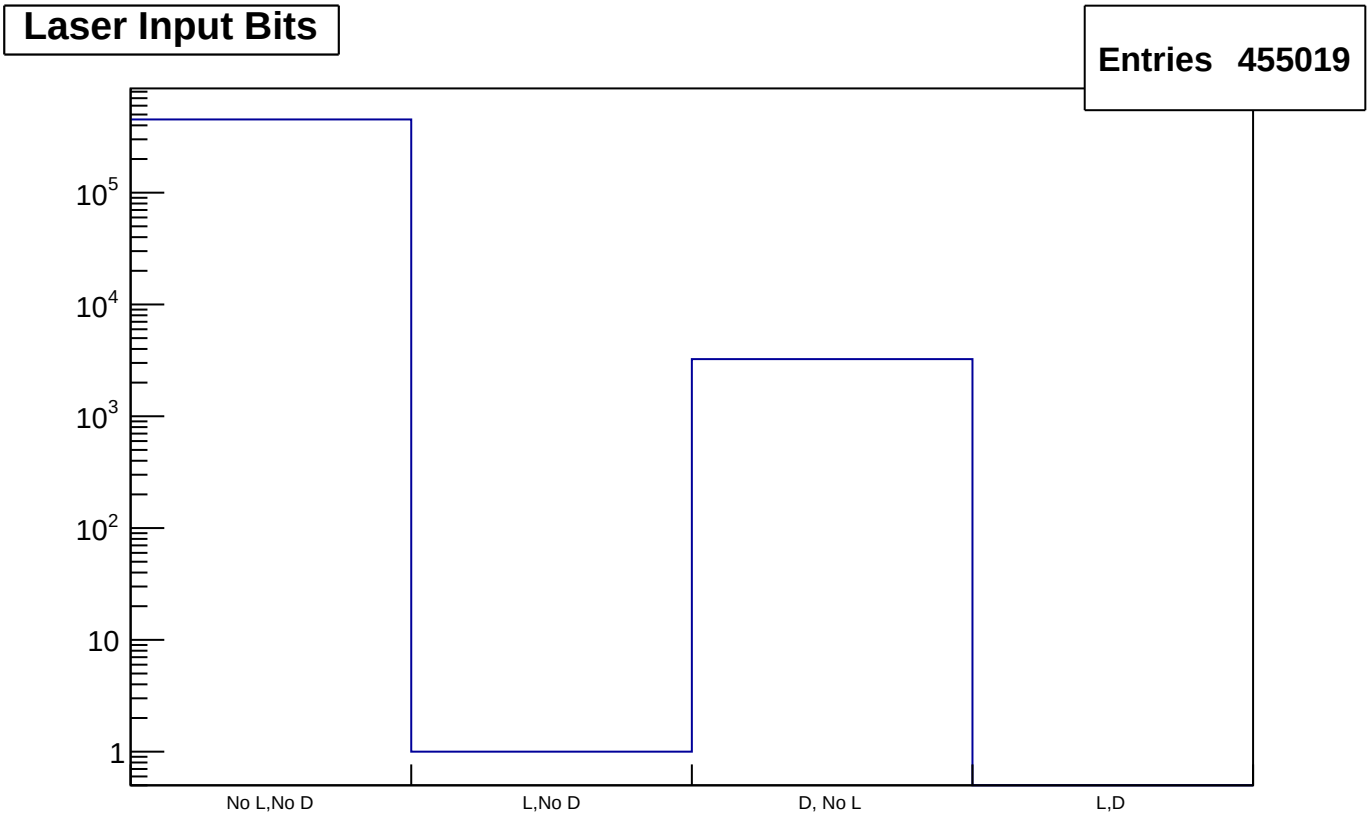

Entries 455019
TOF_Mult
VPD-West ADC Sum

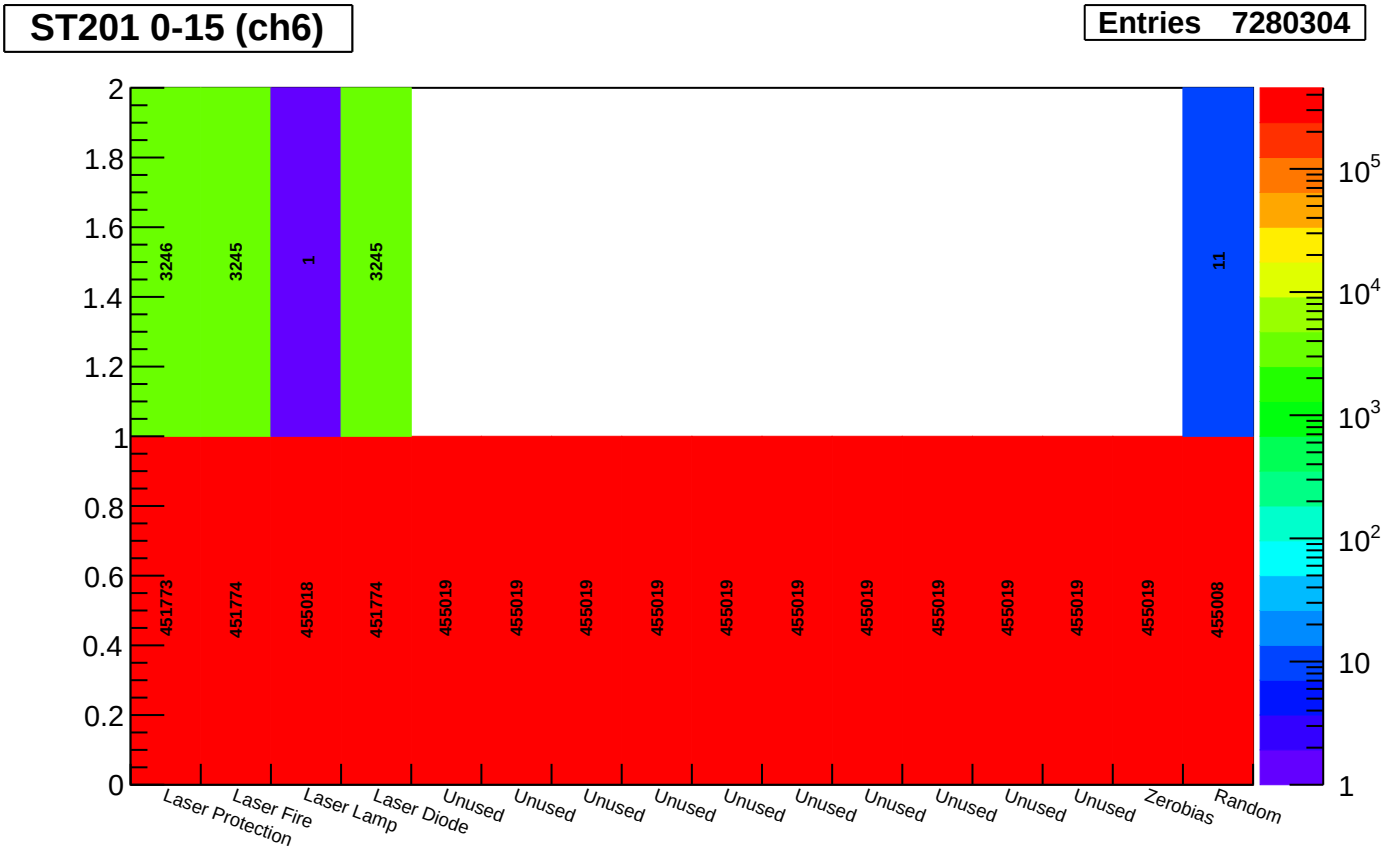

Entries 455019
BBC TAC Diff
7000
6000
5000
4000
3000
2000
2000
3000
4000
5000
6000
7000
EPD TAC Diff
10^2
10
1

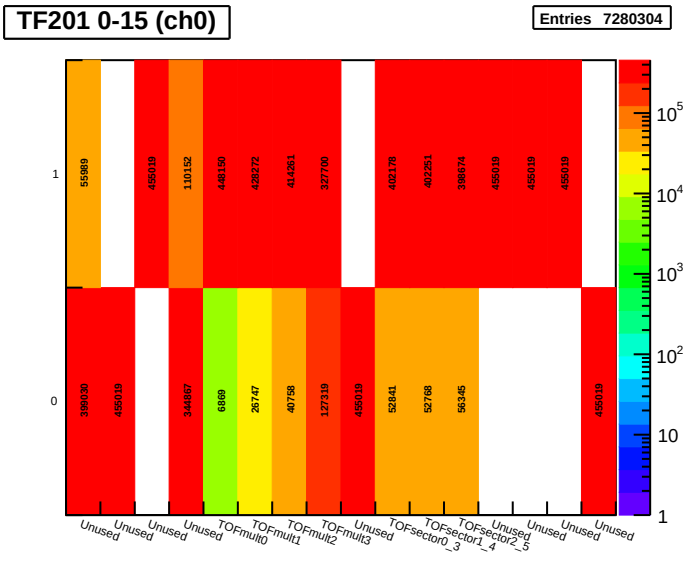

Entries 455019
BBC TAC Diff
7000
6000
5000
4000
3000
2000
2000
3000
4000
5000
6000
7000
VPD TAC Diff
10^2
10
1

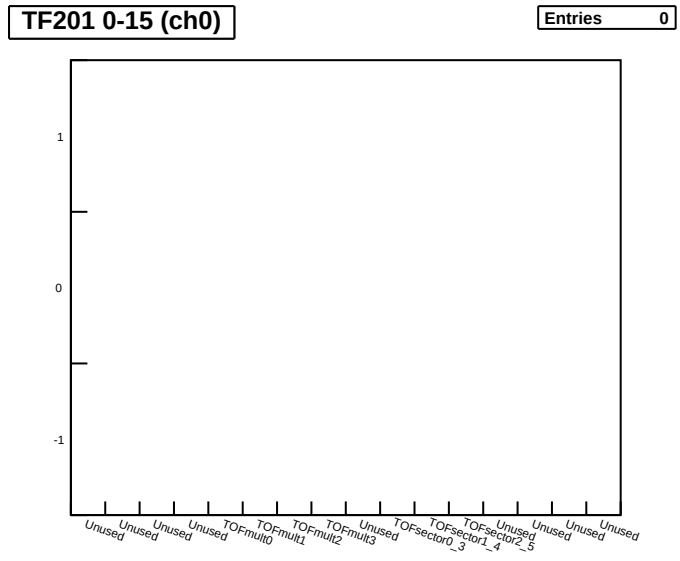

Entries 455019
BBC TAC Diff
7000
6000
5000
4000
3000
2000
60
80
100
120
140
160
180
200
ZDC TAC Diff
1
10^{-1}
10^{-2}

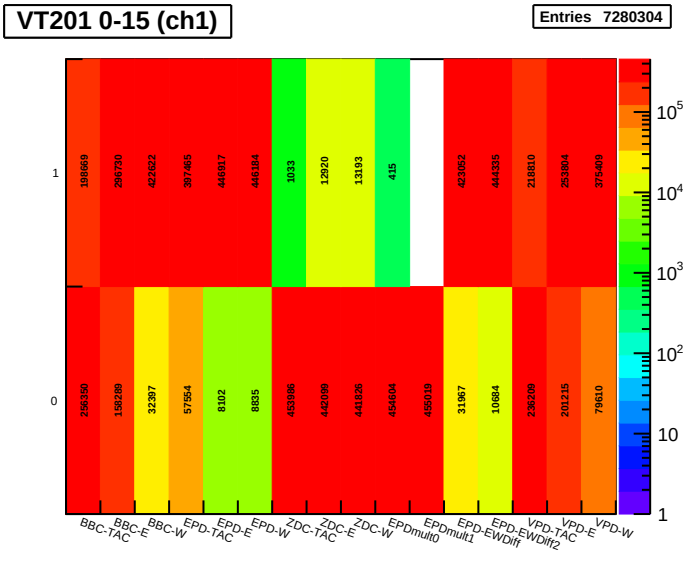

Entries 455019
ZDC-West TAC
1000
800
600
400
200
0
0
200
400
600
800
1000
ZDC-East TAC
10^5
10^4
10^3
10^2
10
1

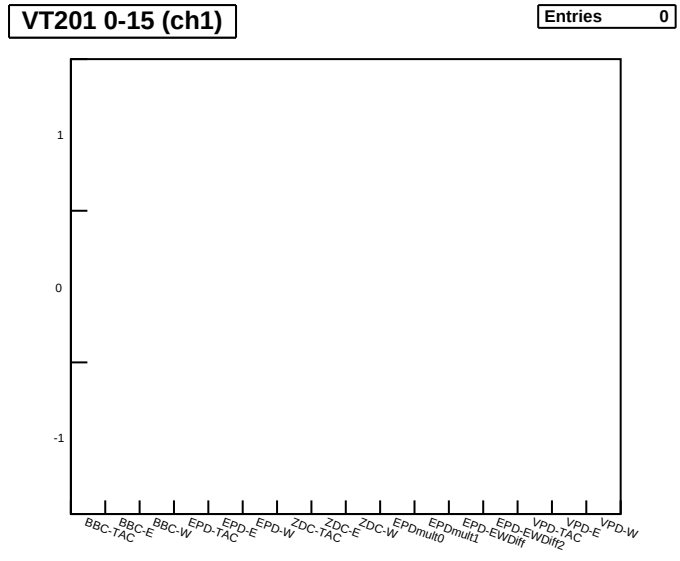

Laser Input Bits
Entries 455019
10^5
10^4
10^3
10^2
10
1
No L,No D
L,No D
D, No L
L,D

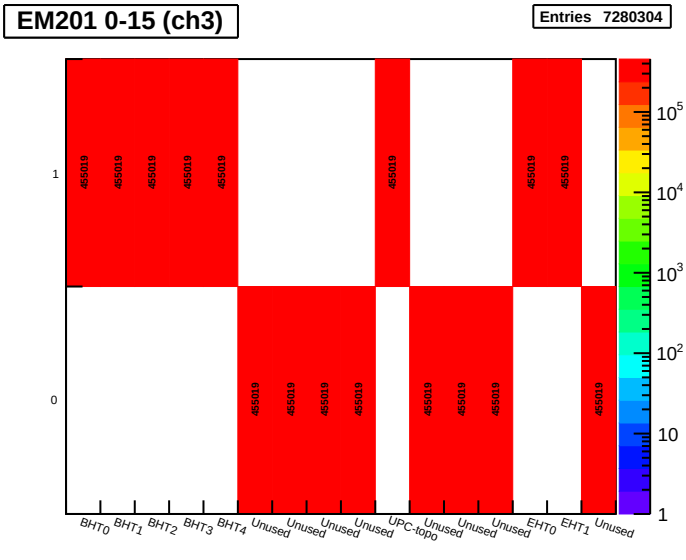

ST201 0-15 (ch6)
Entries 7280304
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
3246
3245
1
3245
11
451773
451774
455018
451774
455019
455019
455019
455019
455019
455019
455019
455019
455019
455019
455019
455008
Laser Protection
Laser Fire
Laser Lamp
Laser Diode
Unused
Unused
Unused
Unused
Unused
Unused
Unused
Unused
Unused
Unused
Zerobias
Random
10^5
10^4
10^3
10^2
10
1










EM201 0-15 (ch3)
Entries 7280304
1
0
455019
455019
455019