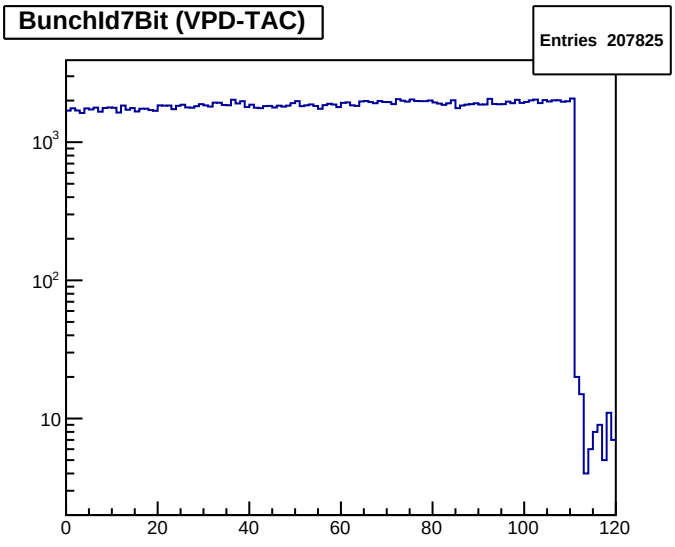
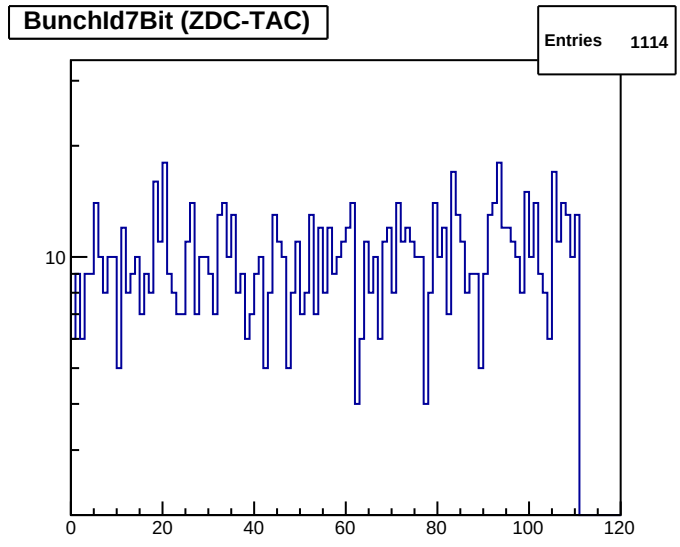
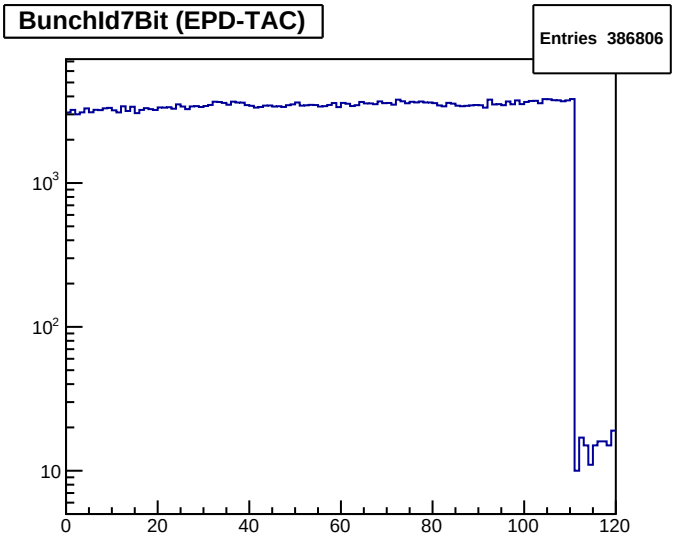
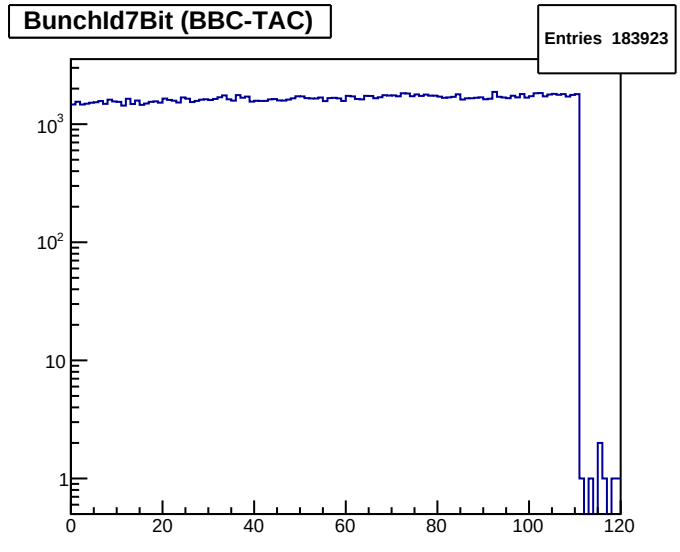
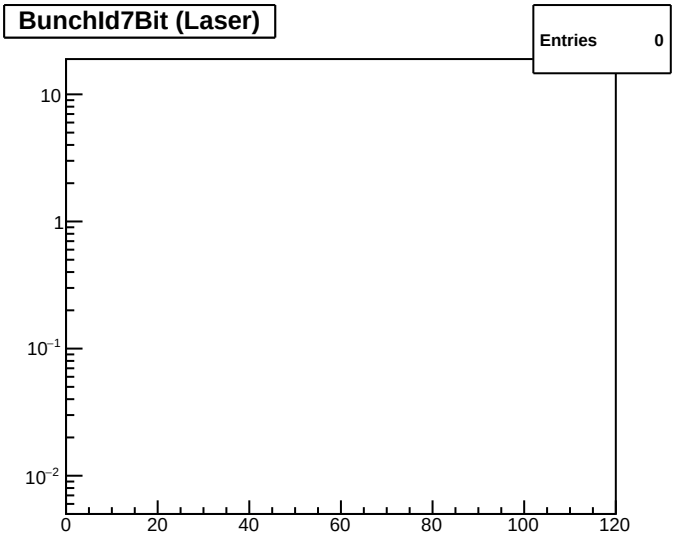
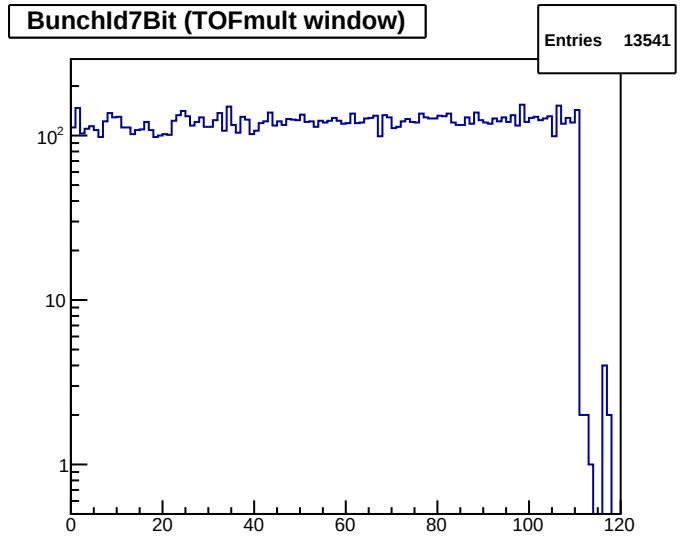

BunchId7Bit (VPD-TAC)
Entries 207825
10^3
10^2
10
0 20 40 60 80 100 120

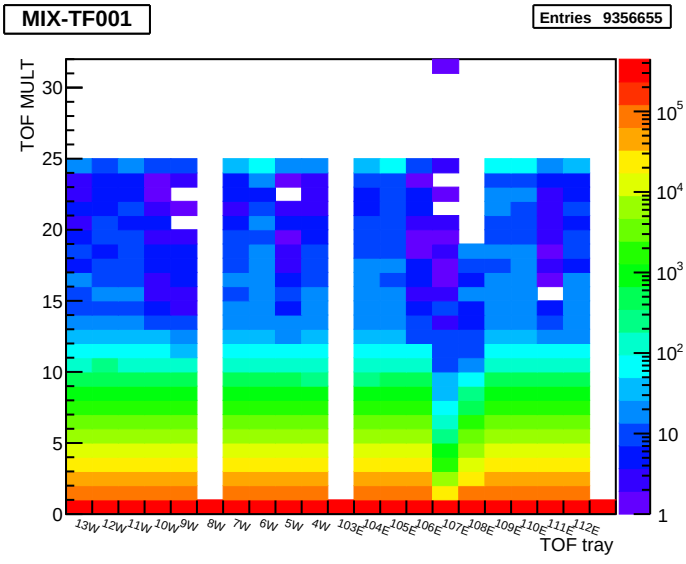

BunchId7Bit (ZDC-TAC)
Entries 1114
10
0 20 40 60 80 100 120

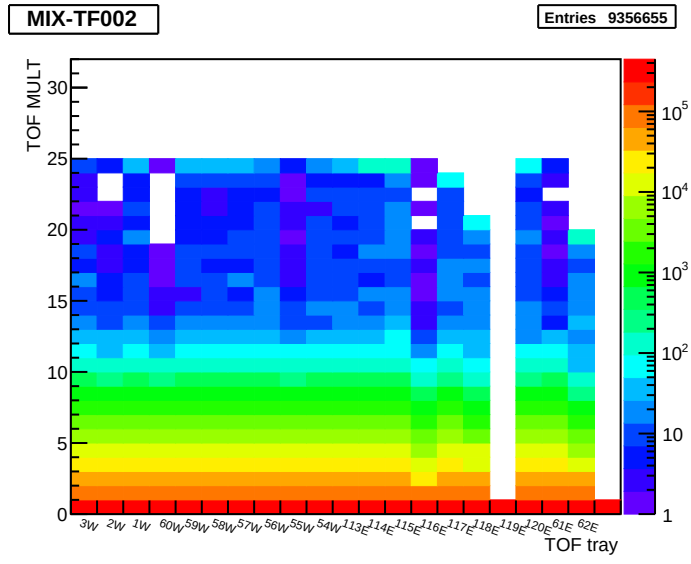

BunchId7Bit (EPD-TAC)
Entries 386806
10^3
10^2
10
0 20 40 60 80 100 120

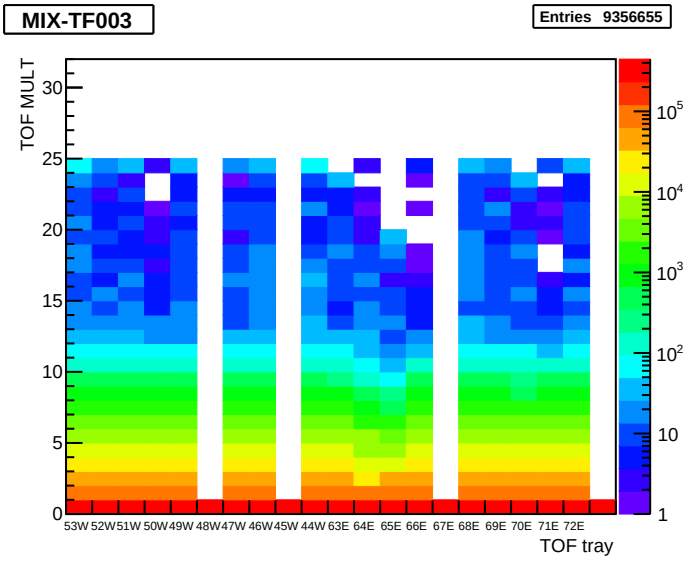

BunchId7Bit (BBC-TAC)
Entries 183923
10^3
10^2
10
1
0 20 40 60 80 100 120

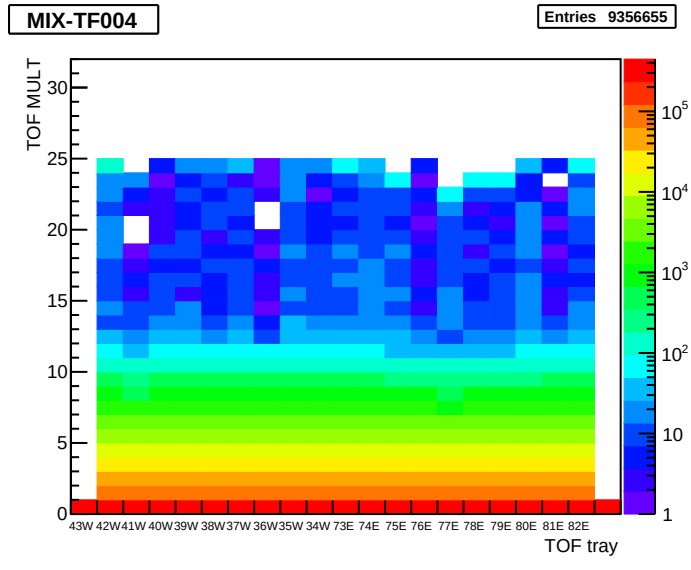

BunchId7Bit (Laser)
Entries 0
10
1
10^{-1}
10^{-2}
0 20 40 60 80 100 120

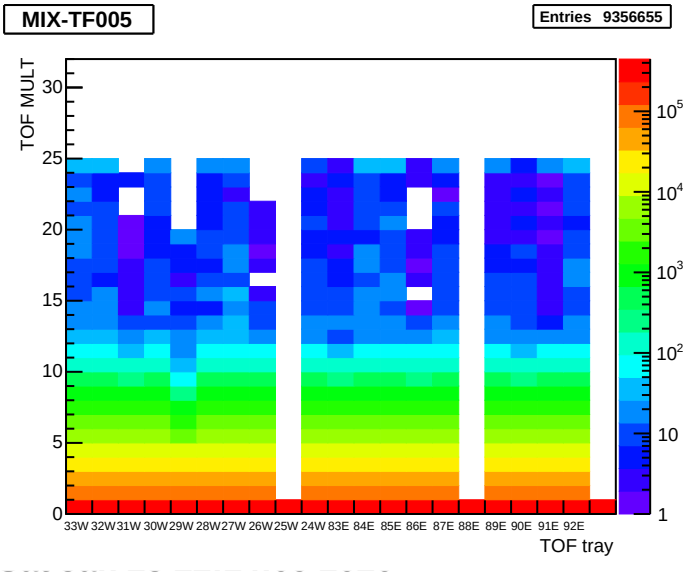

BunchId7Bit (TOFmult window)
Entries 13541
10^2
10
1
0 20 40 60 80 100 120

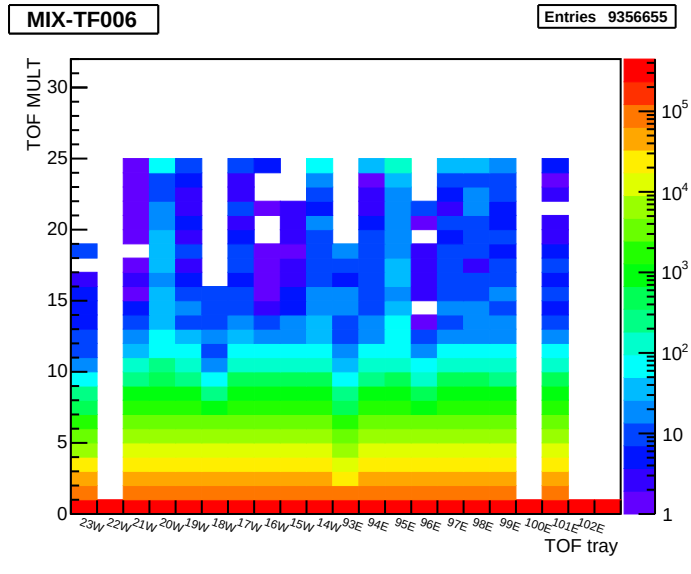

MIX-TF001
Entries 9356655
TOF MULT
TOF tray

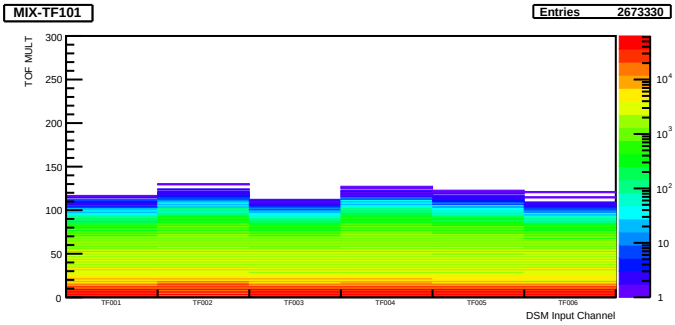

MIX-TF002
Entries 9356655
TOF MULT
TOF tray

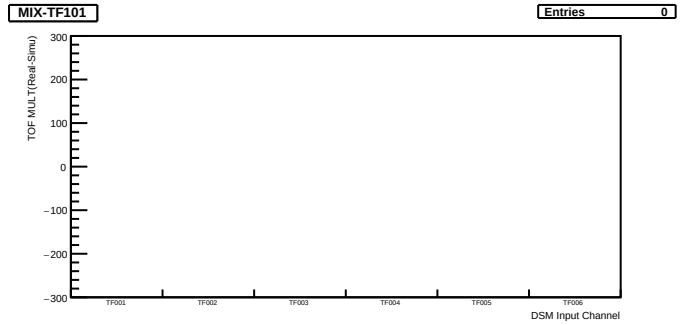

MIX-TF003
Entries 9356655
TOF MULT
TOF tray

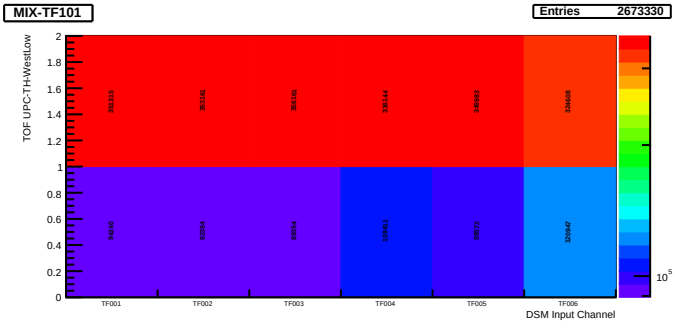

MIX-TF004
Entries 9356655
TOF MULT
TOF tray

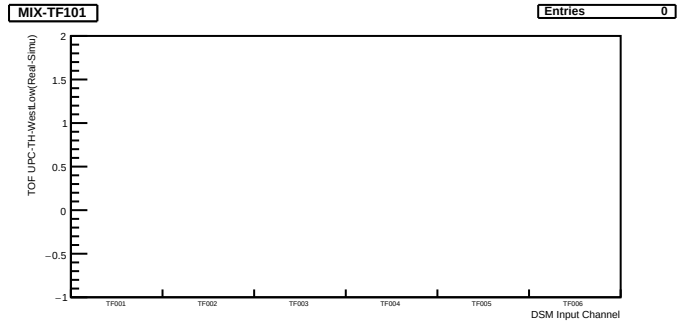

MIX-TF005
Entries 9356655
TOF MULT
TOF tray

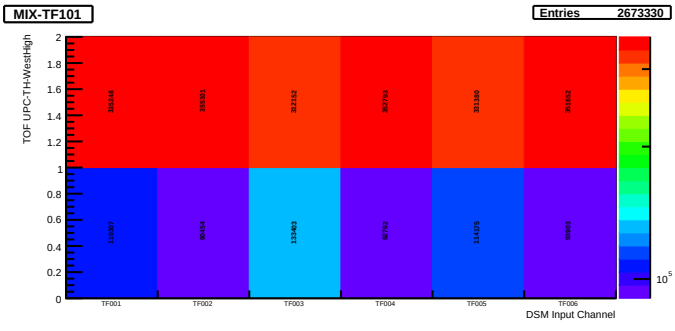

MIX-TF006
Entries 9356655
TOF MULT
TOF tray

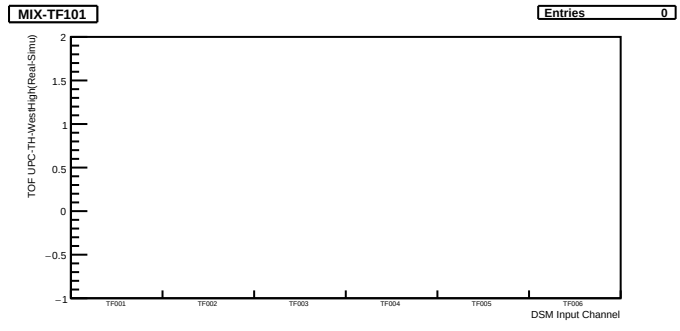

MIX-TF101
Entries 2673330
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

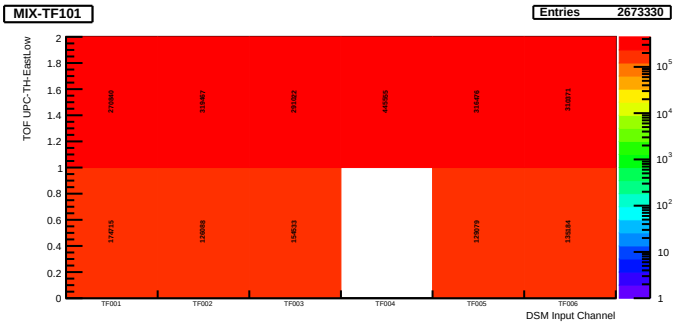

MIX-TF101
Entries 0
TOF MULT(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

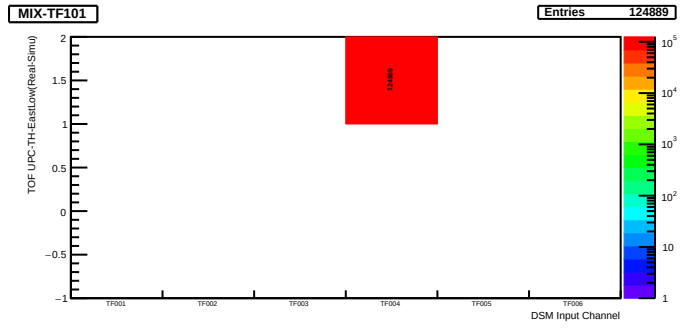

MIX-TF101
Entries 2673330
TOF UPC-TH-WestLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

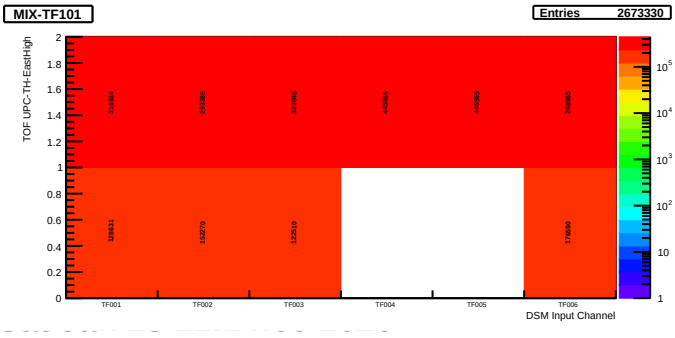

MIX-TF101
Entries 0
TOF UPC-TH-WestLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

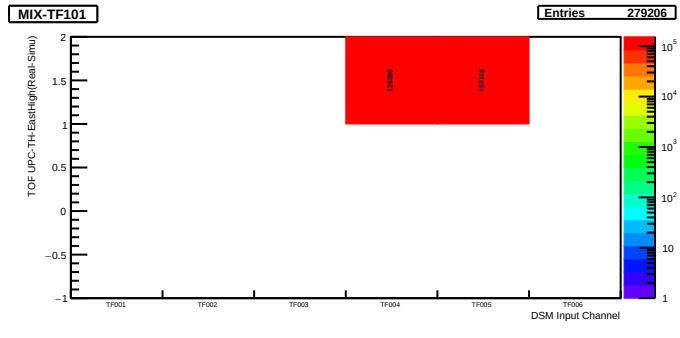

MIX-TF101
Entries 2673330
TOF UPC-TH-WestHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

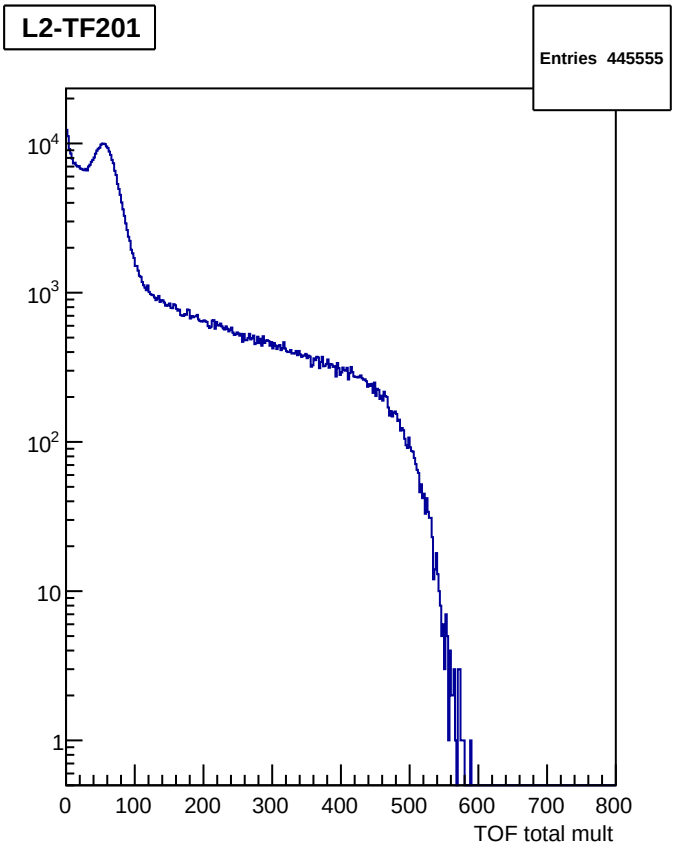

MIX-TF101
Entries 0
TOF UPC-TH-WestHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

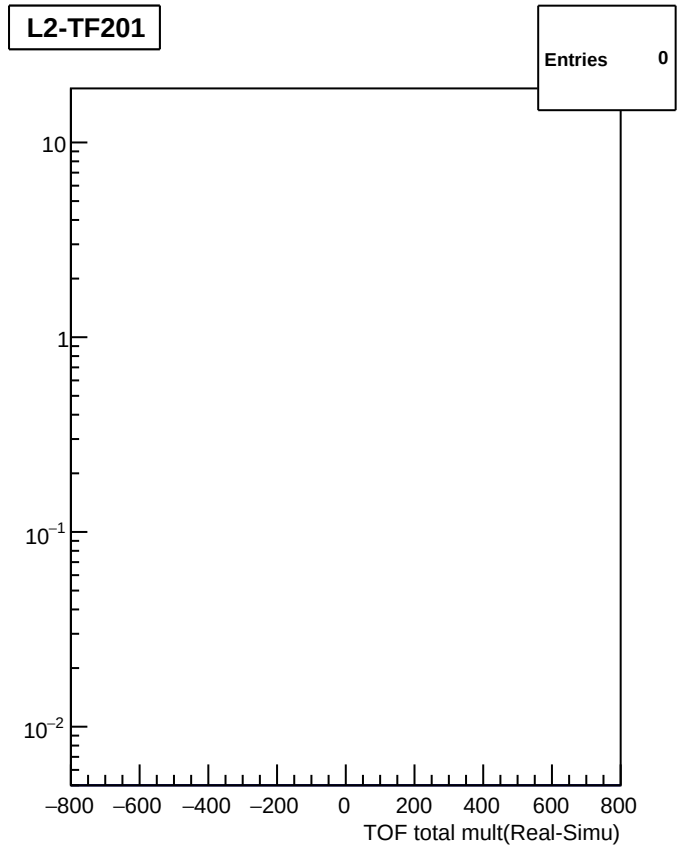

MIX-TF101
Entries 2673330
TOF UPC-TH-EastLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

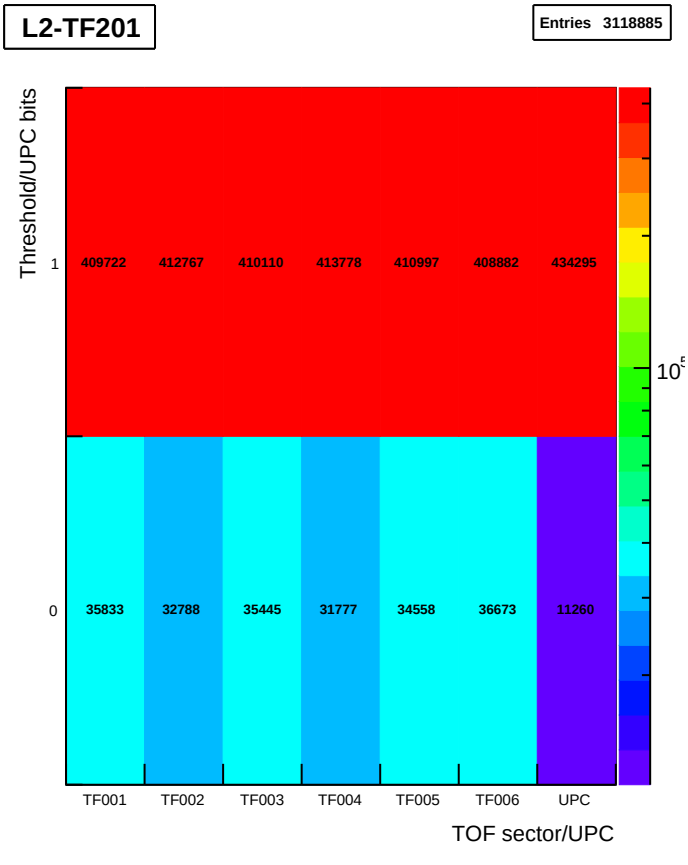

MIX-TF101
Entries 124889
TOF UPC-TH-EastLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

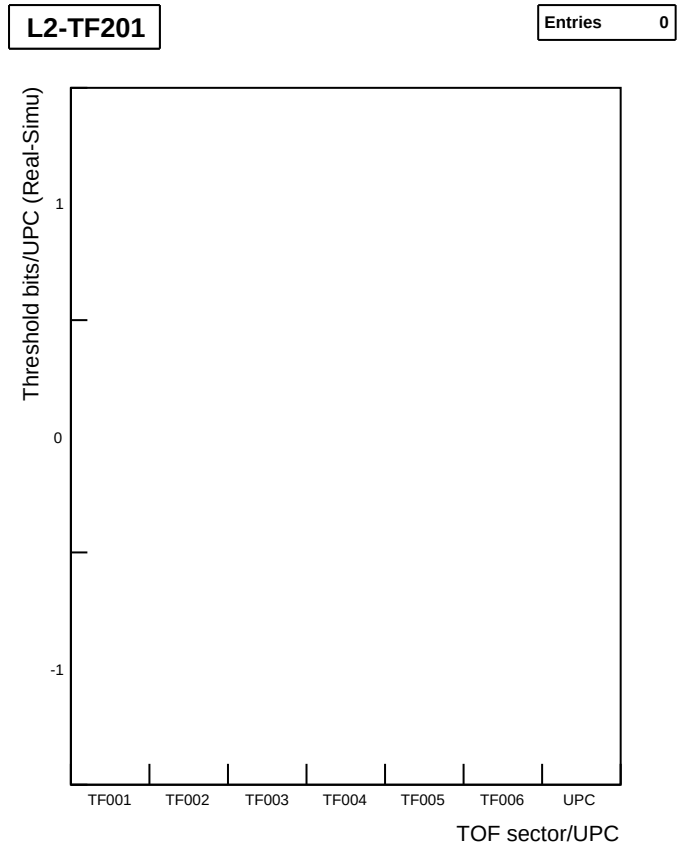

MIX-TF101
Entries 2673330
TOF UPC-TH-EastHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

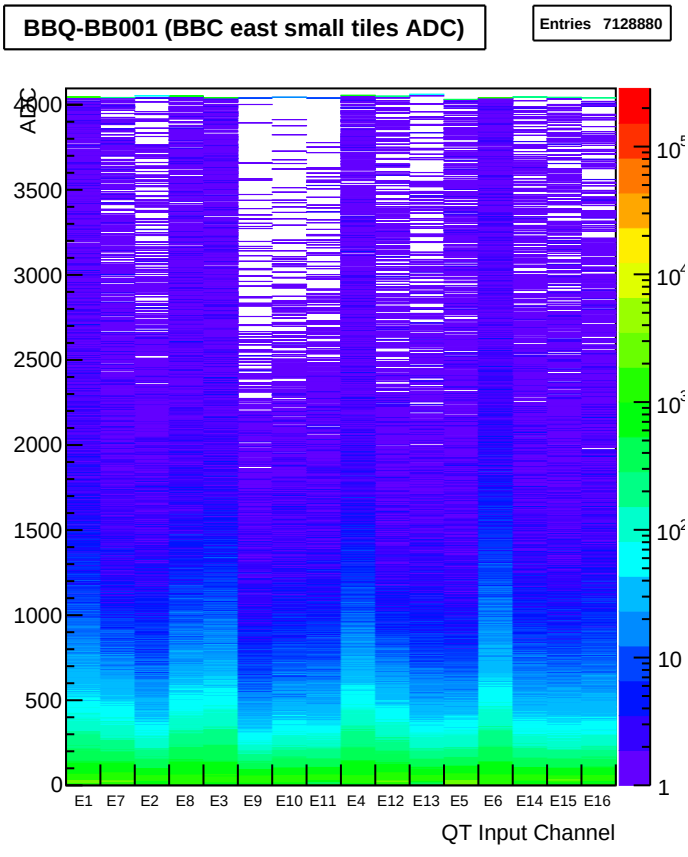

MIX-TF101
Entries 279206
TOF UPC-TH-EastHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

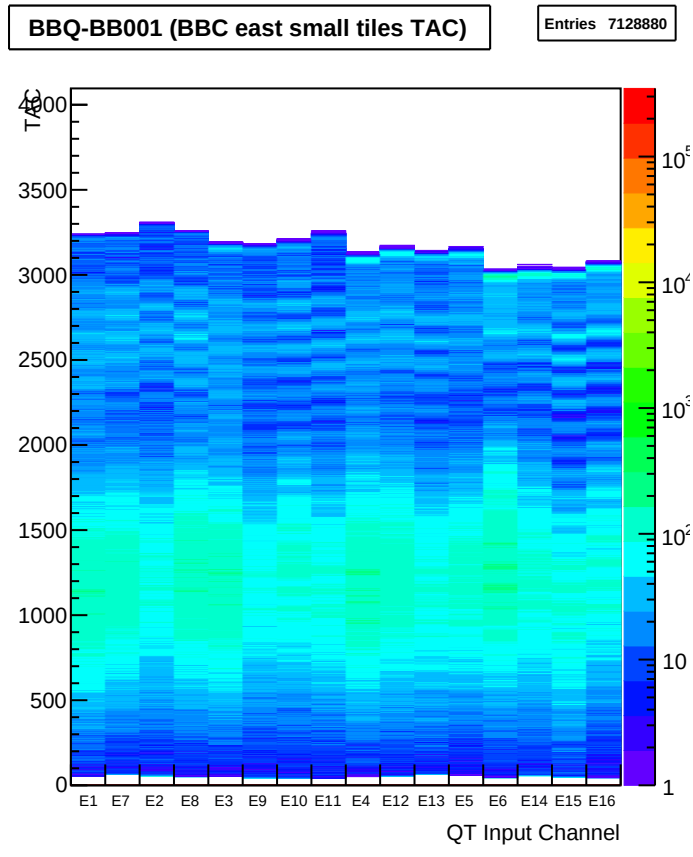

L2-TF201
Entries 445555
TOF total mult

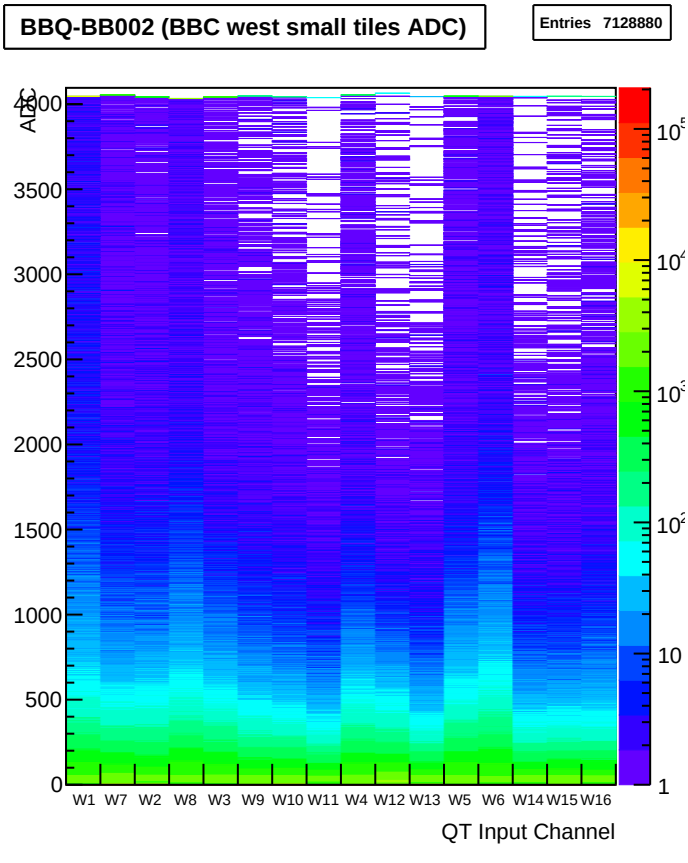

L2-TF201
Entries 0
TOF total mult(Real-Simu)

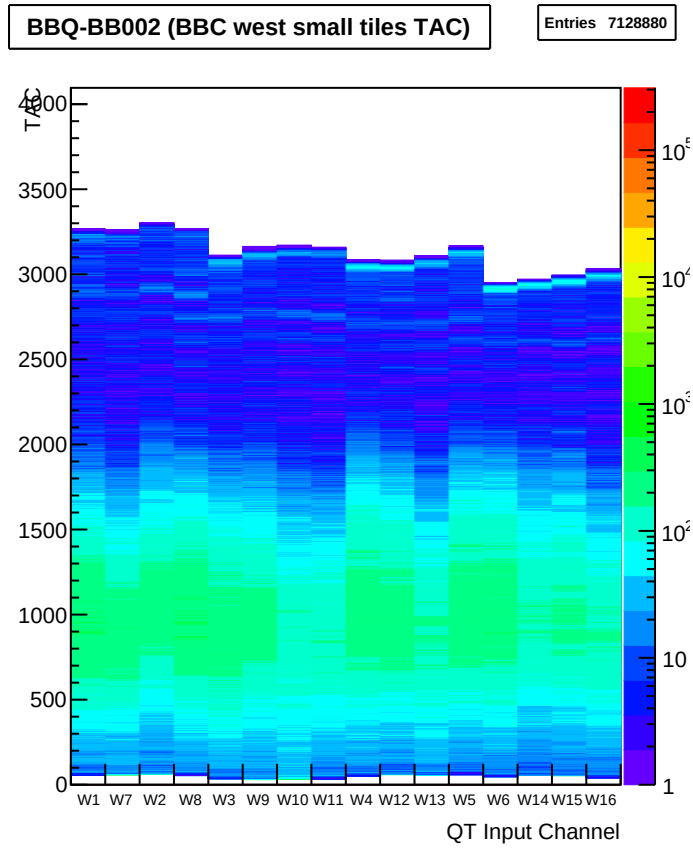

L2-TF201
Entries 3118885
Threshold/UPC bits
409722
412767
410110
413778
410997
408882
434295
35833
32788
35445
31777
34558
36673
11260
TF001
TF002
TF003
TF004
TF005
TF006
UPC
TOF sector/UPC

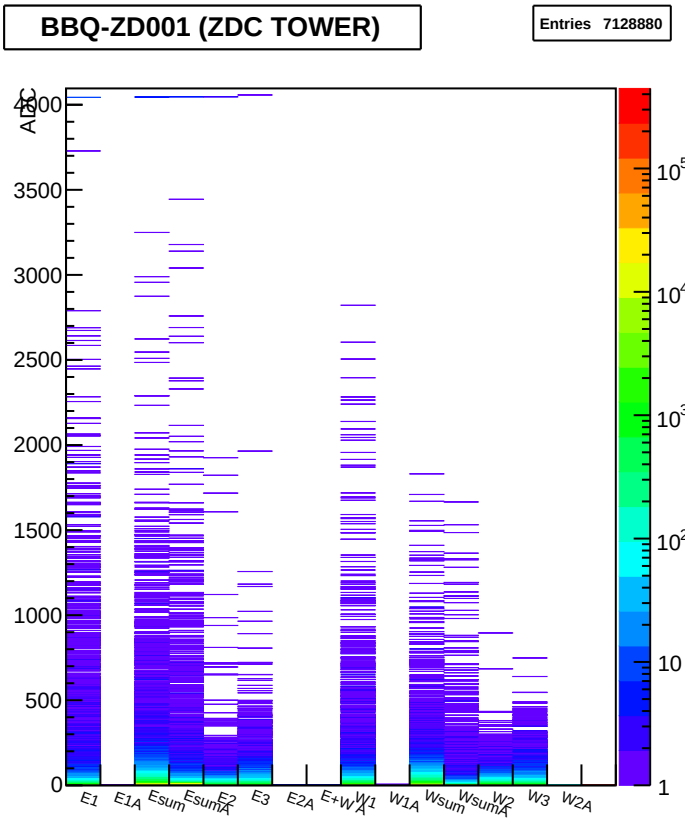

L2-TF201
Entries 0
Threshold bits/UPC (Real-Simu)
TF001
TF002
TF003
TF004
TF005
TF006
UPC
TOF sector/UPC

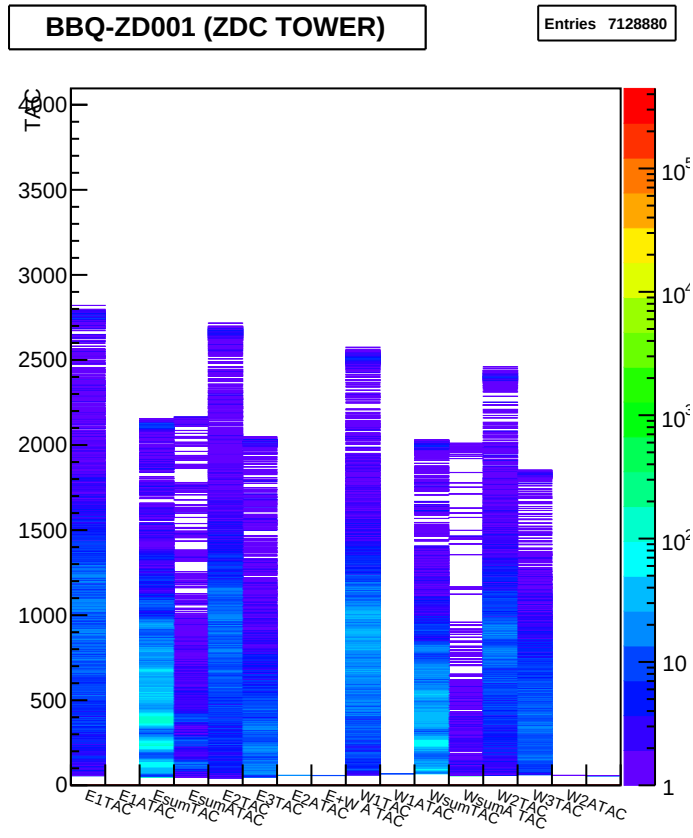

BBQ-BB001 (BBC east small tiles ADC)
Entries 7128880
ADC
QT Input Channel
E1 E7 E2 E8 E3 E9 E10 E11 E4 E12 E13 E5 E6 E14 E15 E16

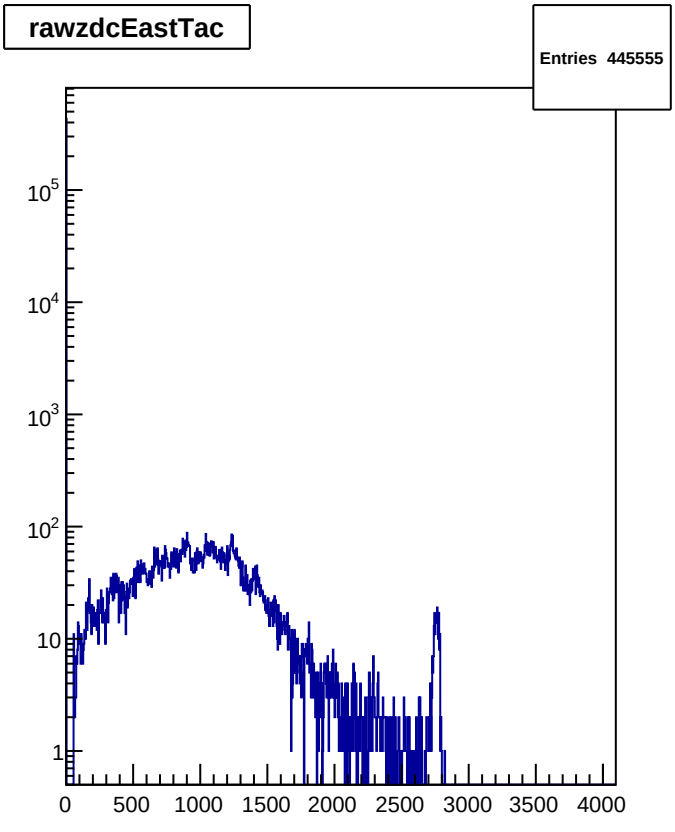

BBQ-BB001 (BBC east small tiles TAC)
Entries 7128880
TAC
QT Input Channel
E1 E7 E2 E8 E3 E9 E10 E11 E4 E12 E13 E5 E6 E14 E15 E16

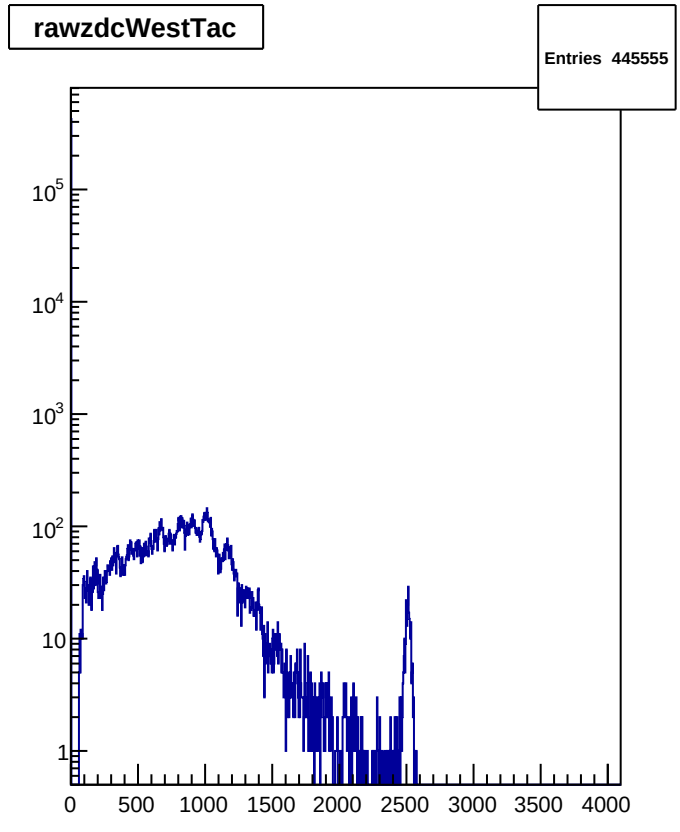

BBQ-BB002 (BBC west small tiles ADC)
Entries 7128880
ADC
QT Input Channel
W1 W7 W2 W8 W3 W9 W10 W11 W4 W12 W13 W5 W6 W14 W15 W16

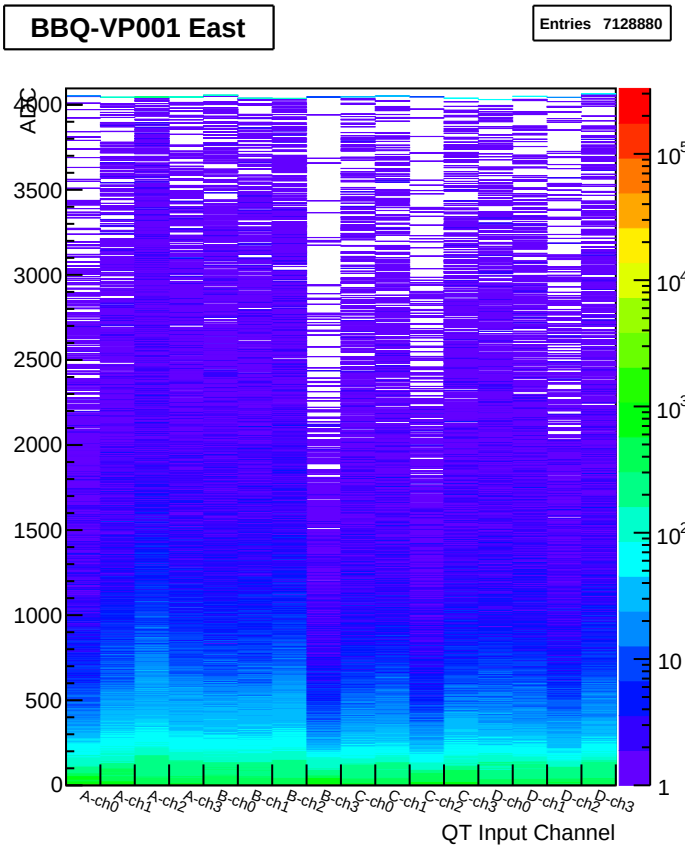

BBQ-BB002 (BBC west small tiles TAC)
Entries 7128880
TAC
QT Input Channel
W1 W7 W2 W8 W3 W9 W10 W11 W4 W12 W13 W5 W6 W14 W15 W16

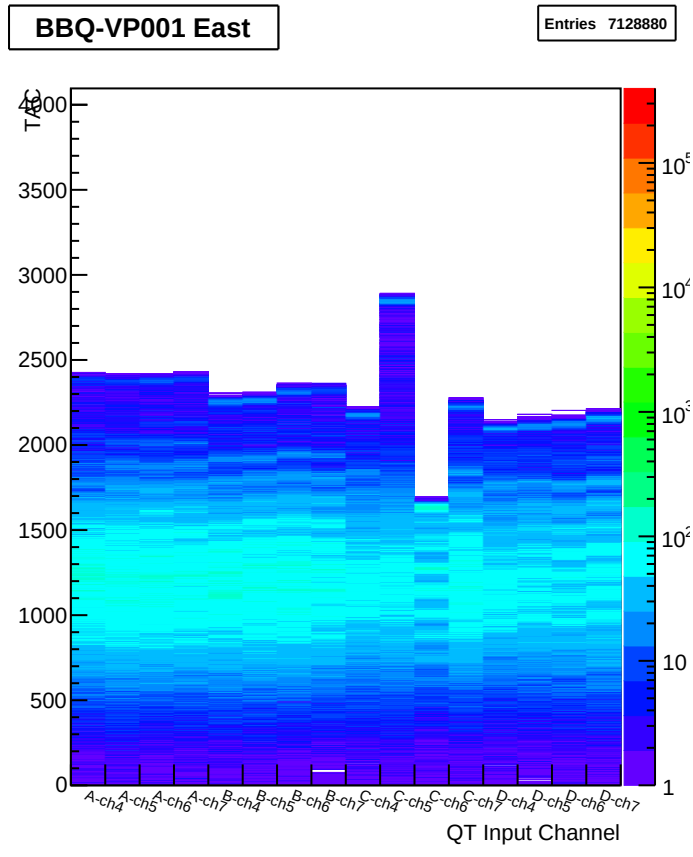

BBQ-ZD001 (ZDC TOWER)
Entries 7128880
ADC
E1
E1A
Esum
EsumA
E2
E3
E2A
E+W
W1
W1A
Wsum
WsumA
W2
W3
W2A

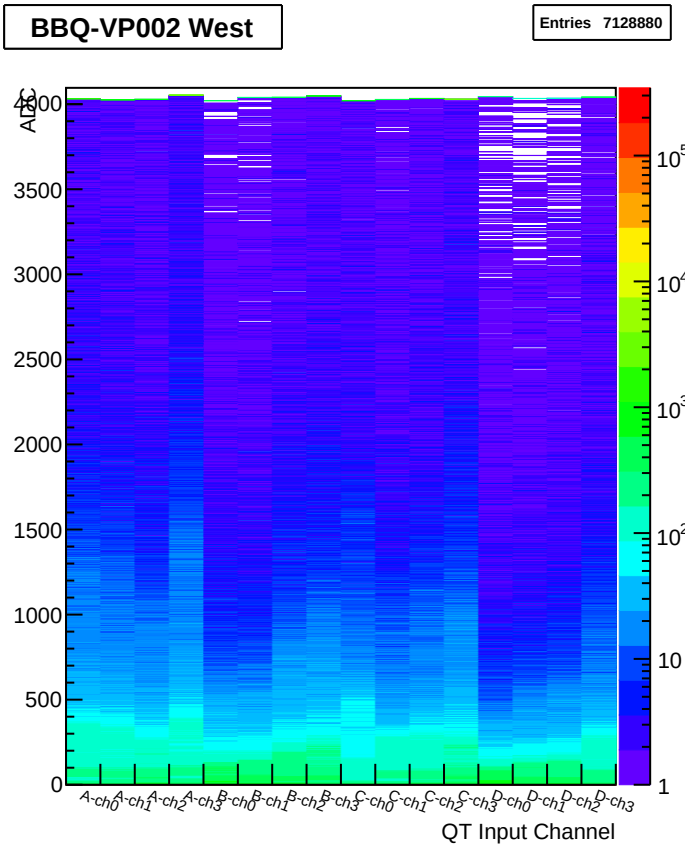

BBQ-ZD001 (ZDC TOWER)
Entries 7128880
TAC
E1TAC
E1ATAC
EsumTAC
EsumATAC
E2TAC
E3TAC
E2ATAC
E+W TAC
W1TAC
W1ATAC
WsumTAC
WsumATAC
W2TAC
W3TAC
W2ATAC

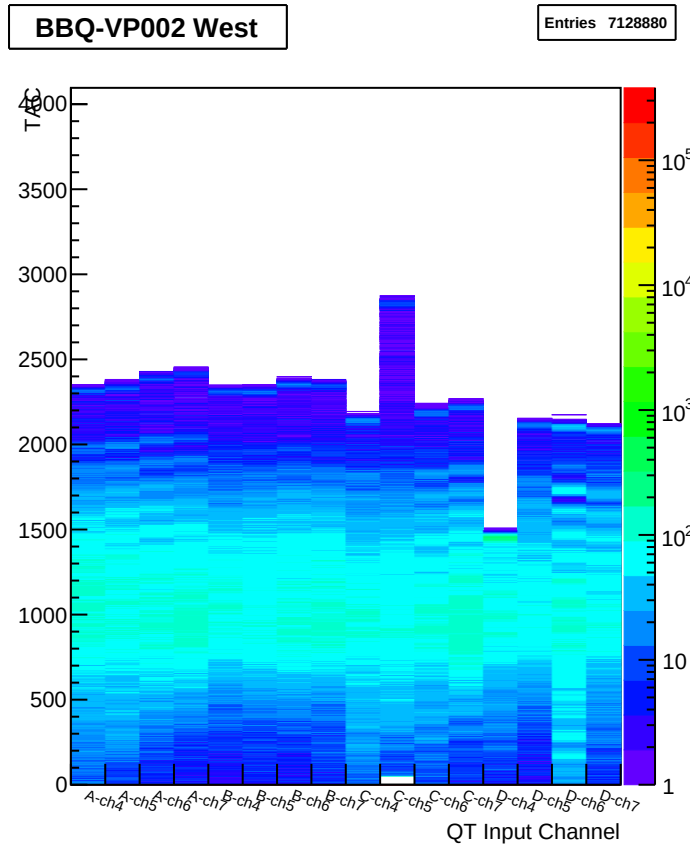

rawzdcEastTac
Entries 445555

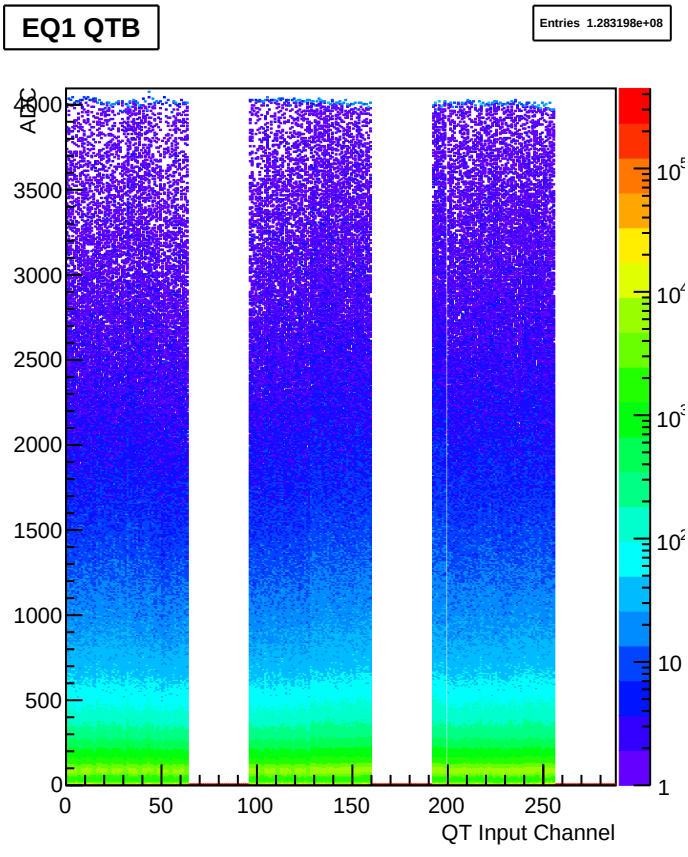

rawzdcWestTac
Entries 445555

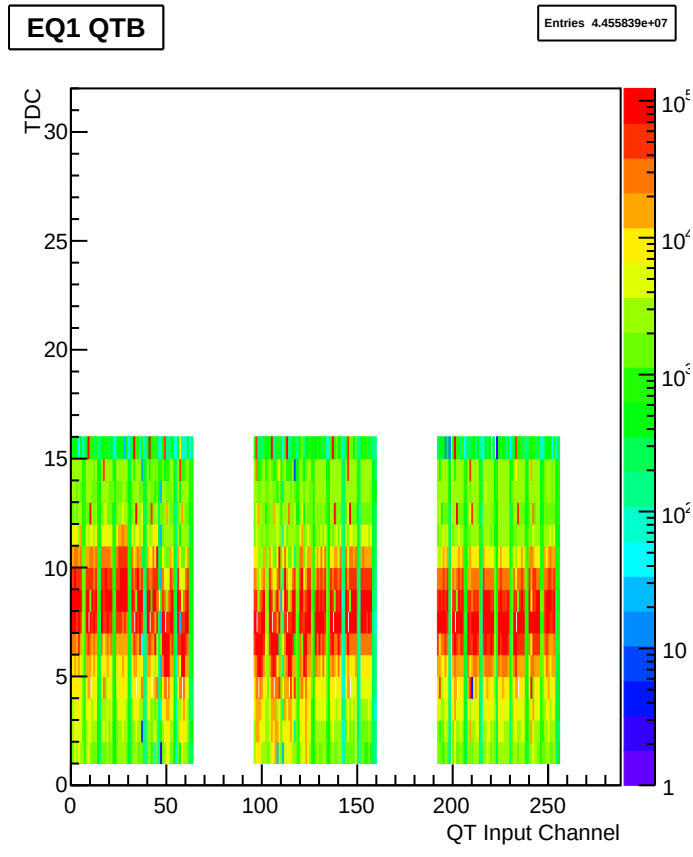

BBQ-VP001 East
Entries 7128880
ADC
QT Input Channel

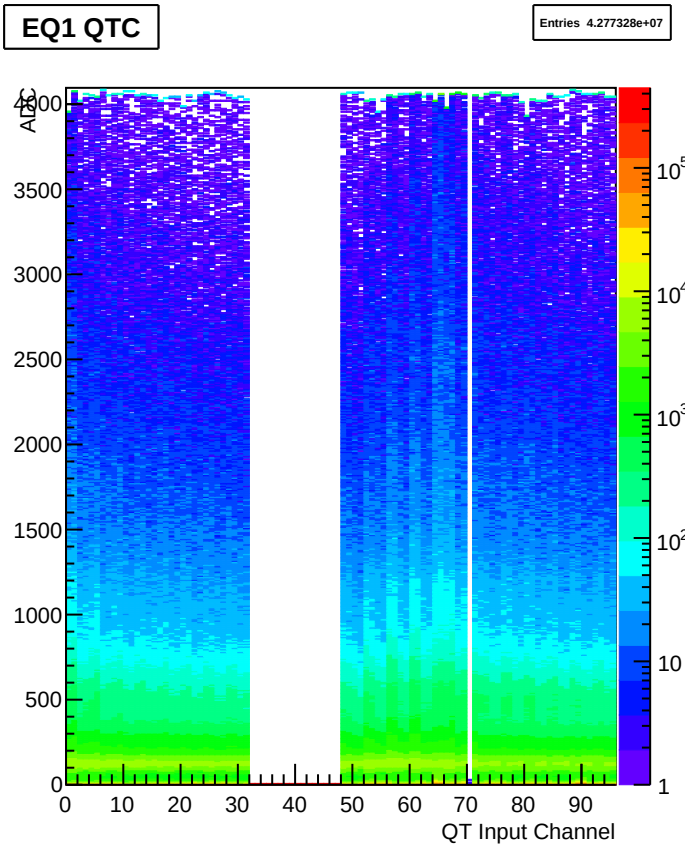

BBQ-VP001 East
Entries 7128880
ADC
QT Input Channel

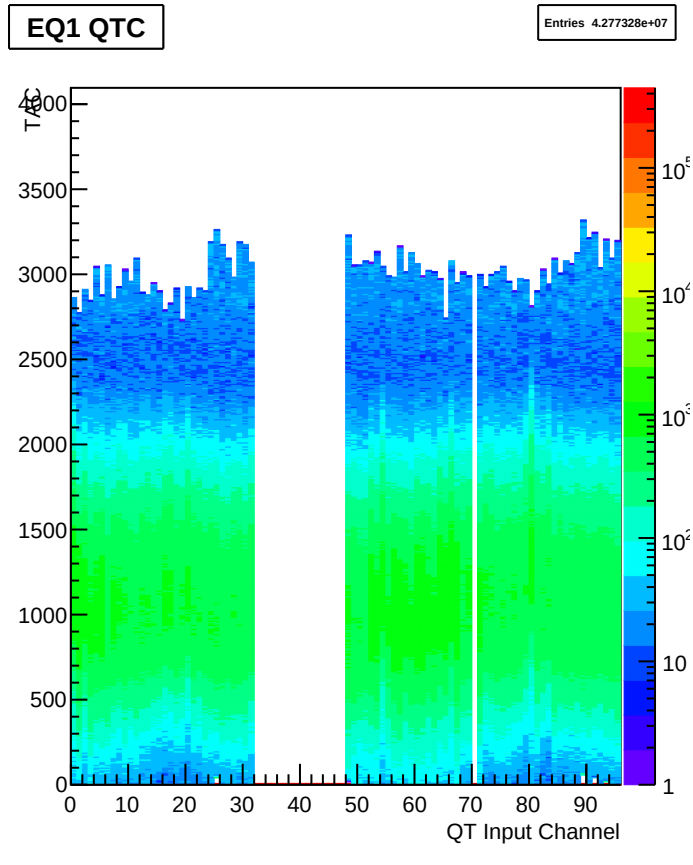

BBQ-VP002 West
Entries 7128880
ADC
QT Input Channel

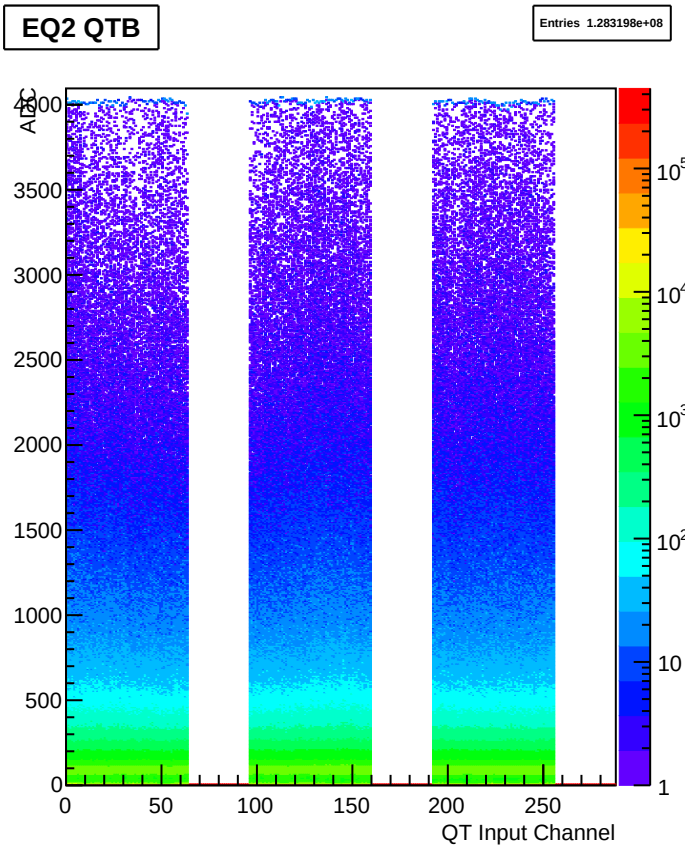

BBQ-VP002 West
Entries 7128880
ADC
QT Input Channel

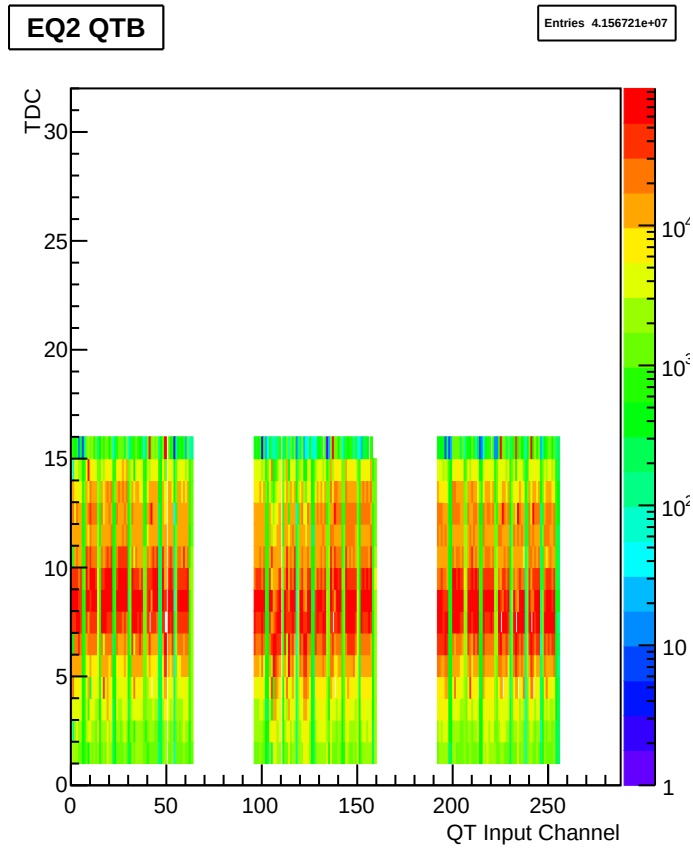

EQ1 QTB
Entries 1.283198e+08
ADC
QT Input Channel

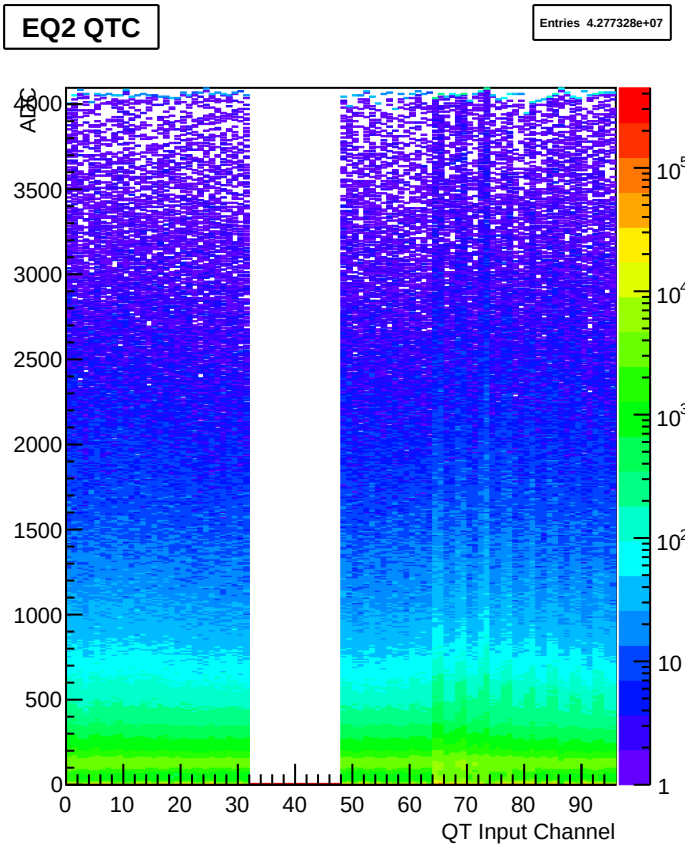

EQ1 QTB
Entries 4.455839e+07
TDC
QT Input Channel

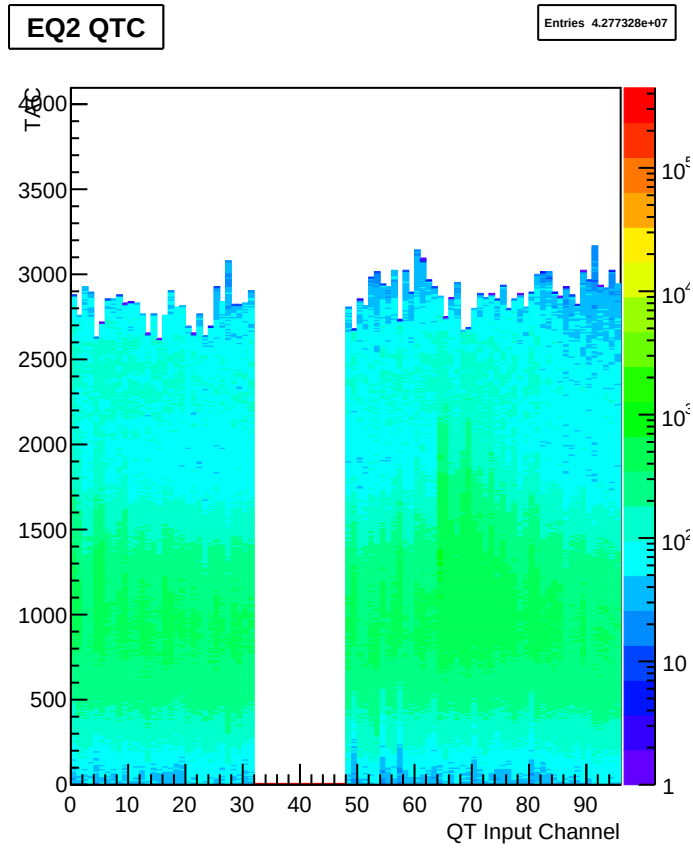

EQ1 QTC
Entries 4.277328e+07
ADC
QT Input Channel

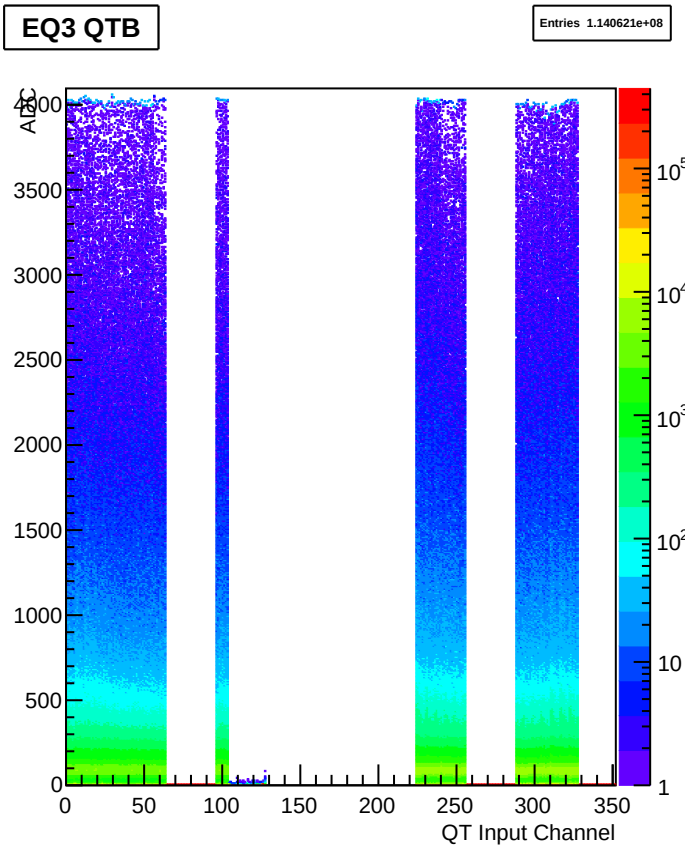

EQ1 QTC
Entries 4.277328e+07
TADC
QT Input Channel

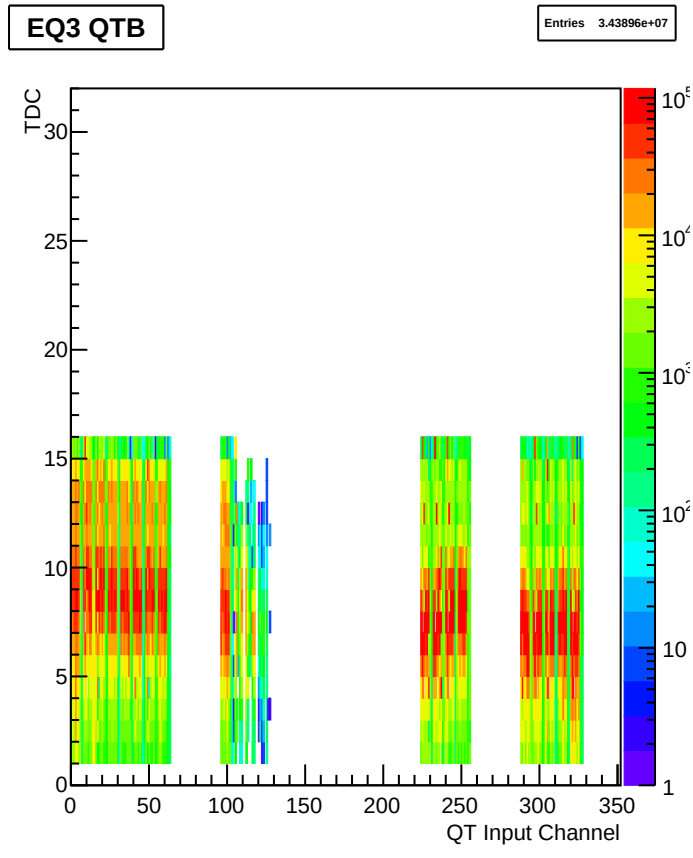

EQ2 QTB
Entries 1.283198e+08
ADC
QT Input Channel

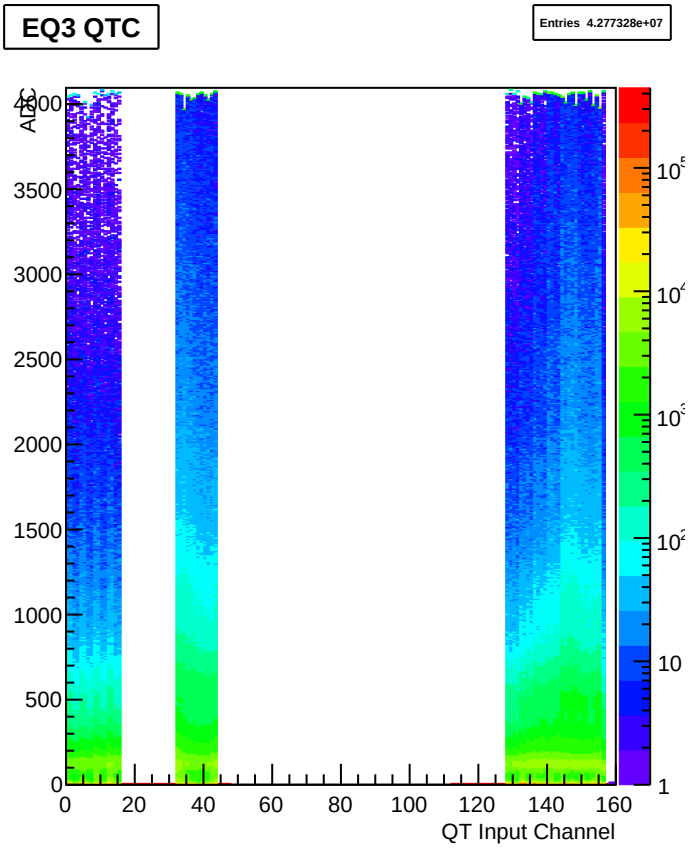

EQ2 QTB
Entries 4.156721e+07
TDC
QT Input Channel

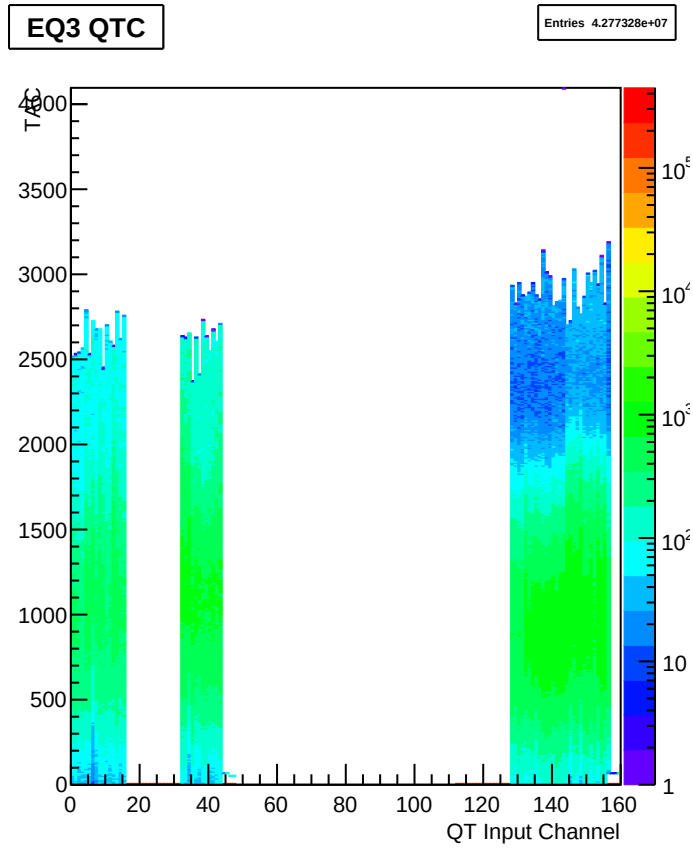

EQ2 QTC
Entries 4.277328e+07
ADC
QT Input Channel

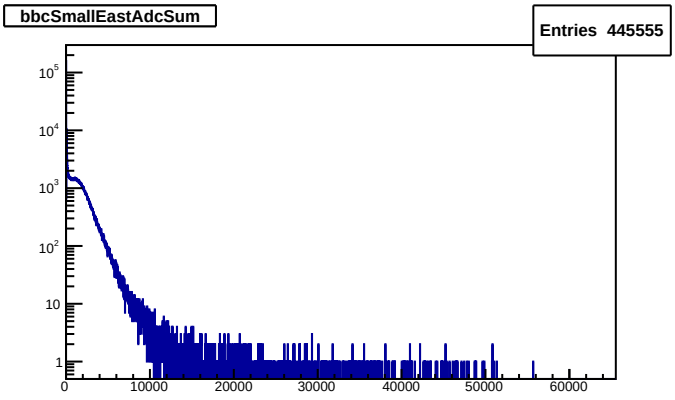

EQ2 QTC
Entries 4.277328e+07
TADC
QT Input Channel

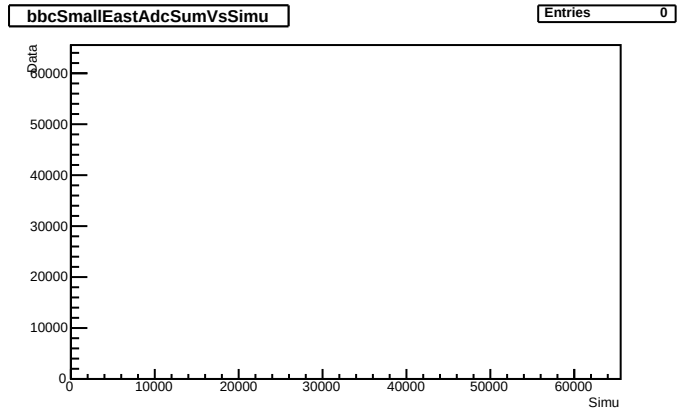

EQ3 QTB
Entries 1.140621e+08
ADC
QT Input Channel

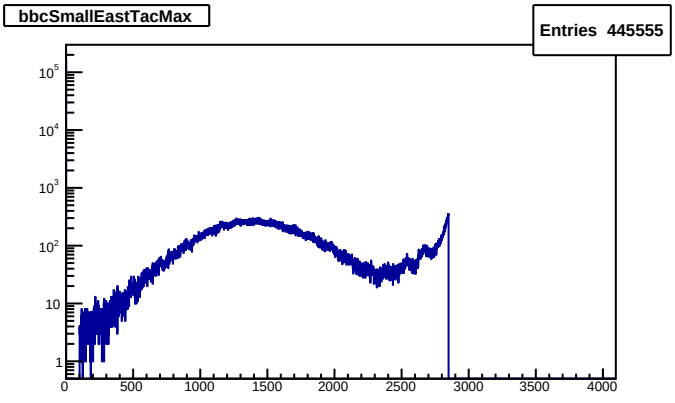

EQ3 QTB
Entries 3.43896e+07
TDC
QT Input Channel

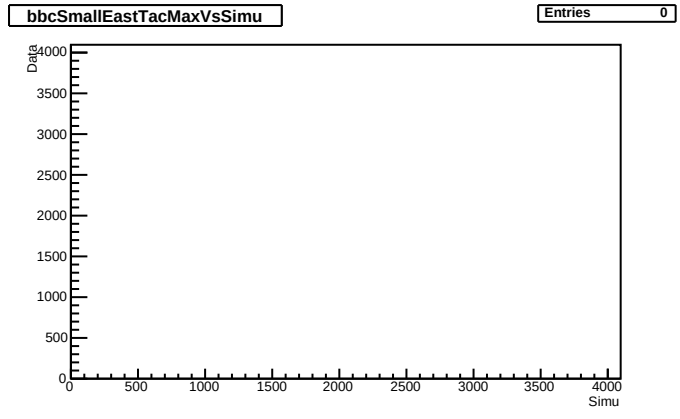

EQ3 QTC
Entries 4.277328e+07
ADC
QT Input Channel


EQ3 QTC
Entries 4.277328e+07
TADC
QT Input Channel


bbcSmallEastAdcSum
Entries 445555


bbcSmallEastAdcSumVsSimu
Entries 0
Data
Simu


bbcSmallEastTacMax
Entries 445555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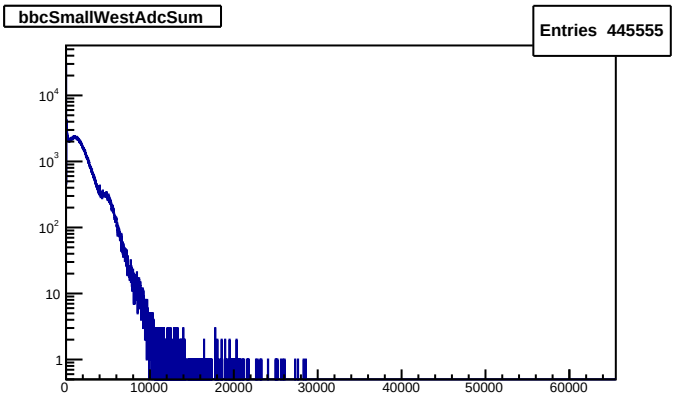
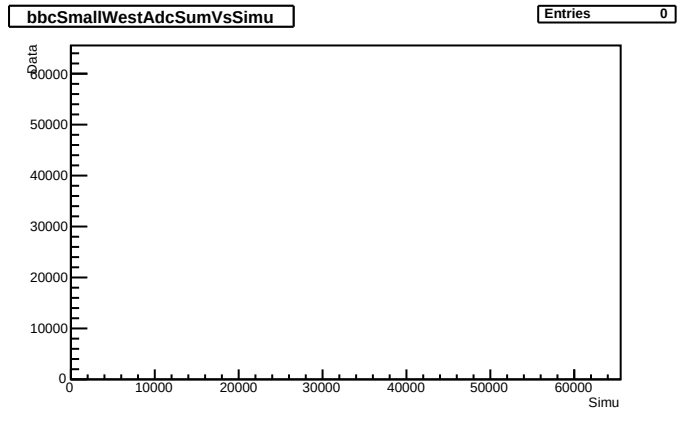

bbcSmallWestAdcSum
Entries 445555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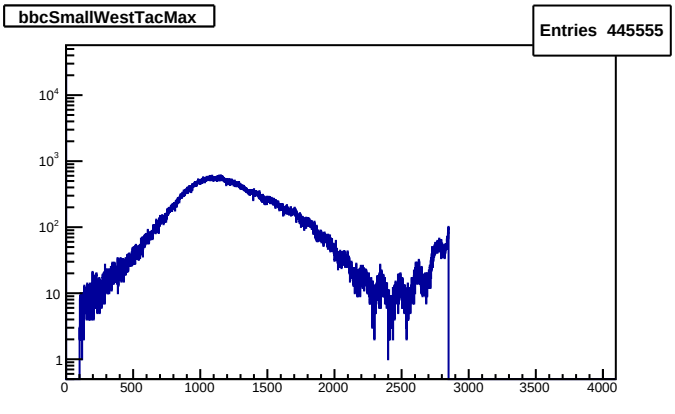
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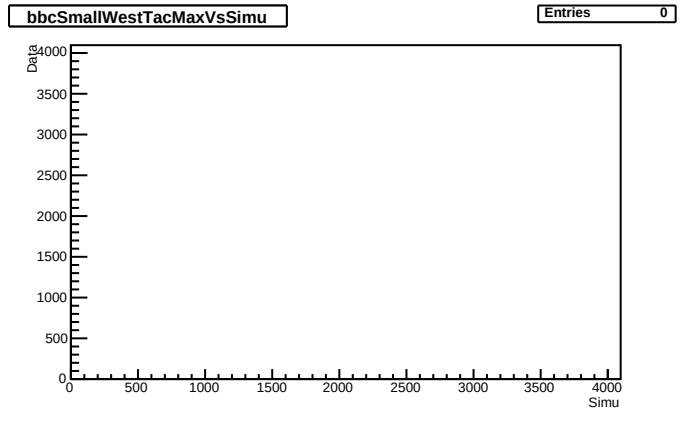
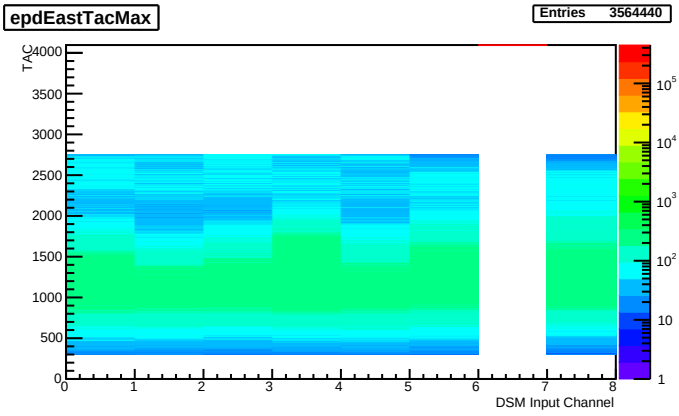
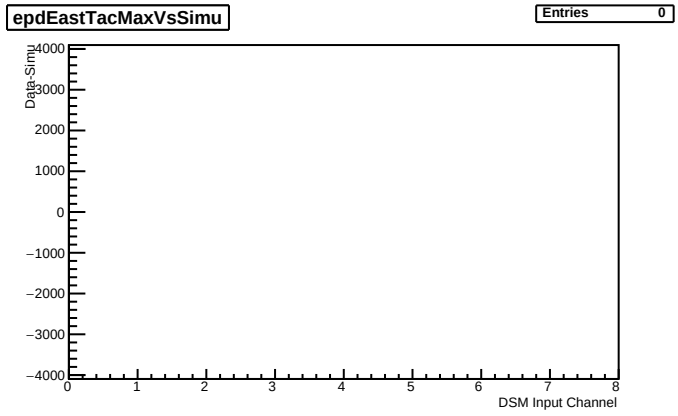
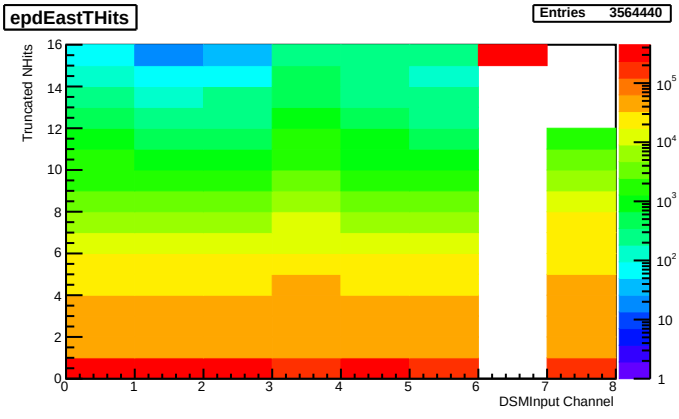

bbcSmallWestTacMaxVsSimu
Entries 0
Data
Simu


epdEastTacMax
Entries 3564440
TAC
DSM Input Channel


epdEastTacMaxVsSimu
Entries 0
Data-Simu
DSM Input Channel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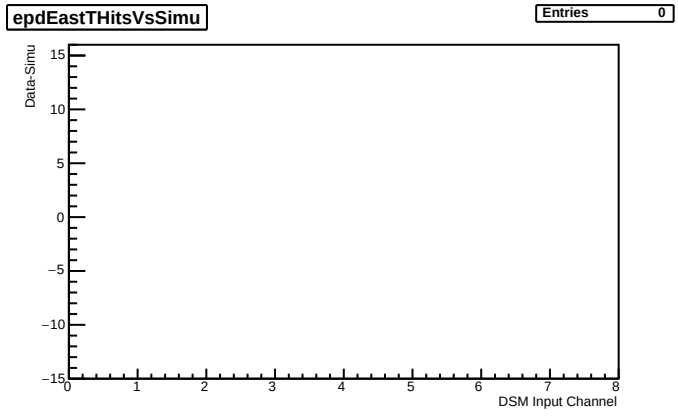
Entries 3564440
Truncated NHits
DSMInput Channel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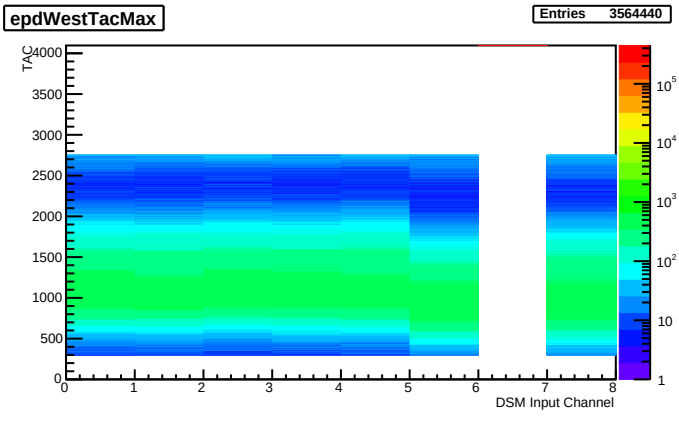
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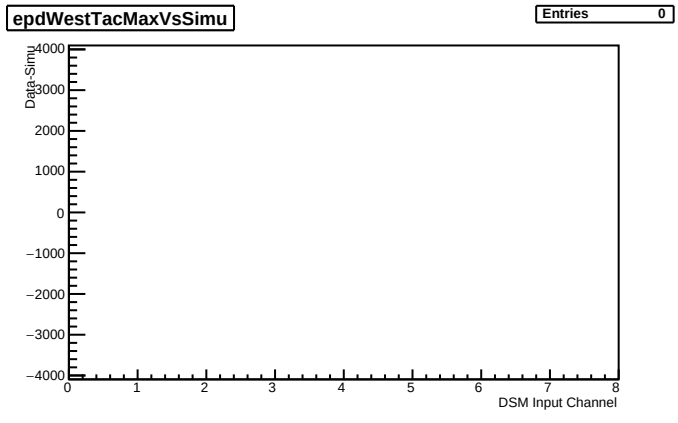
Entries 3564440
TAC
DSM Input Channel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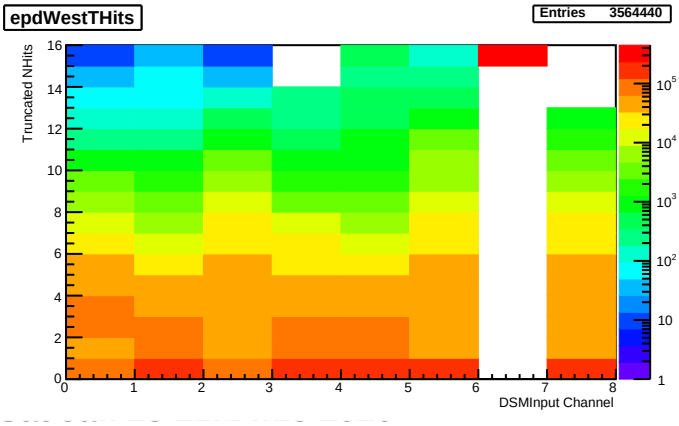
Entries 0
Data-Simu
DSM Input Channel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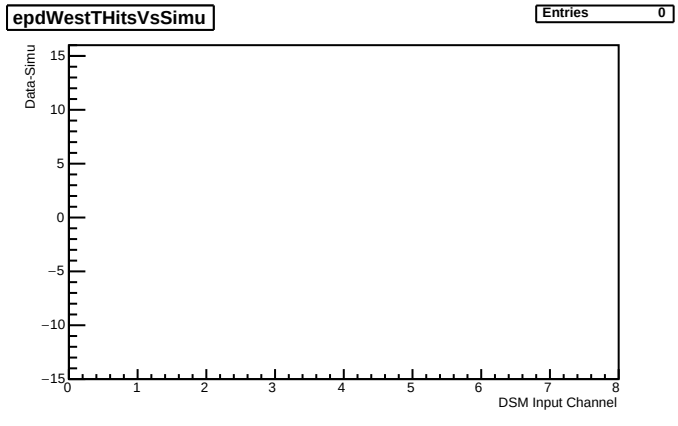
Truncated NHits
DSMInput Channel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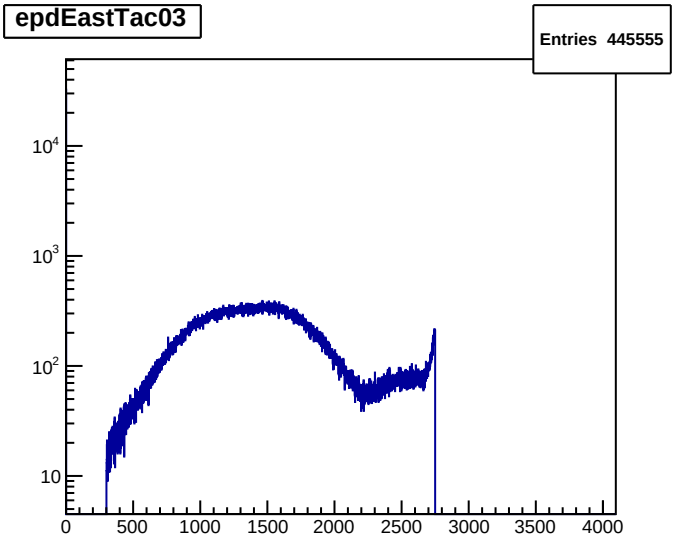
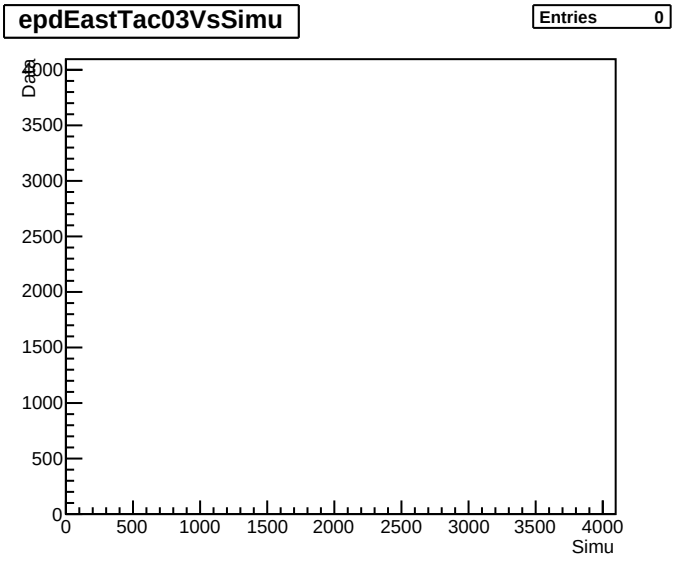
Data-Simu
DSM Input Channel


epdEastTac03
Entries 445555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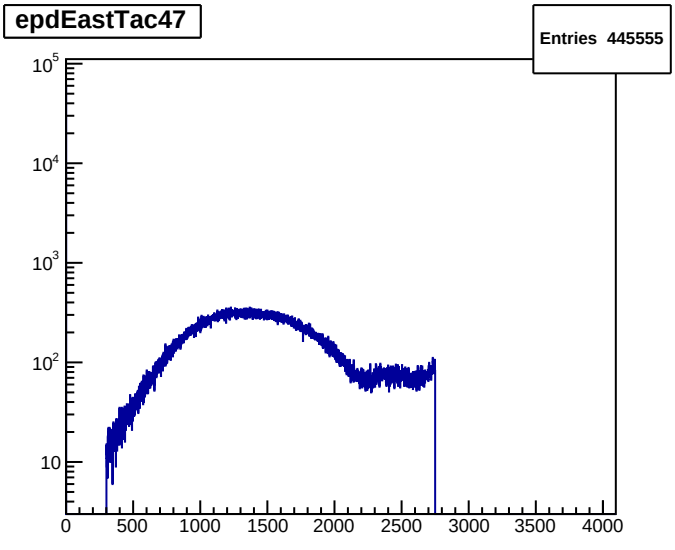
Entries 0
Data
Simu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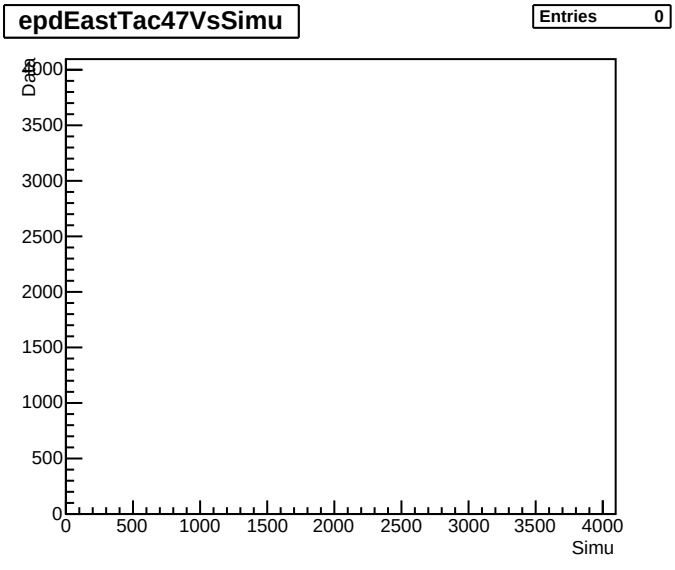
Entries 445555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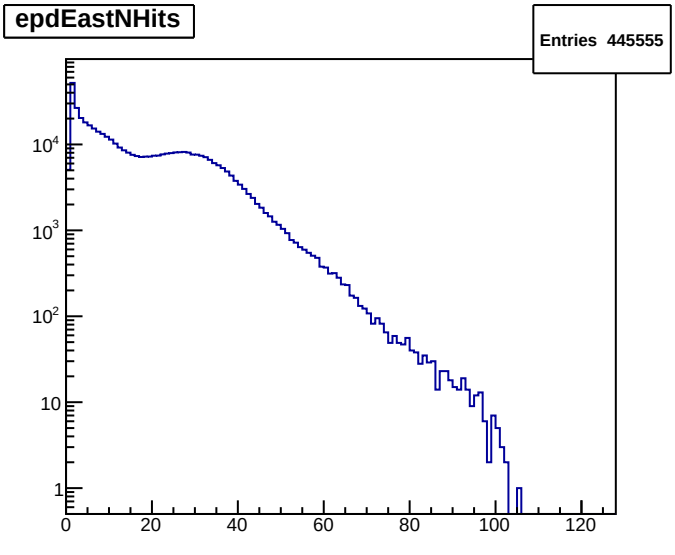
Entries 0
Data
Simu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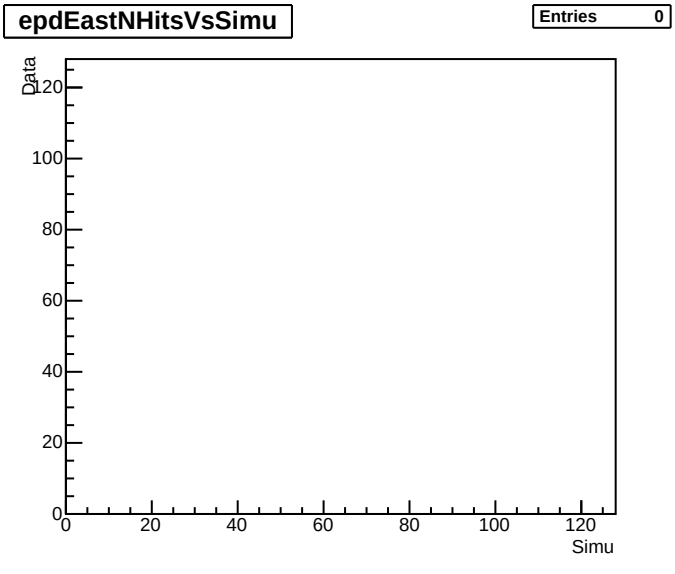
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Entries 0
Data
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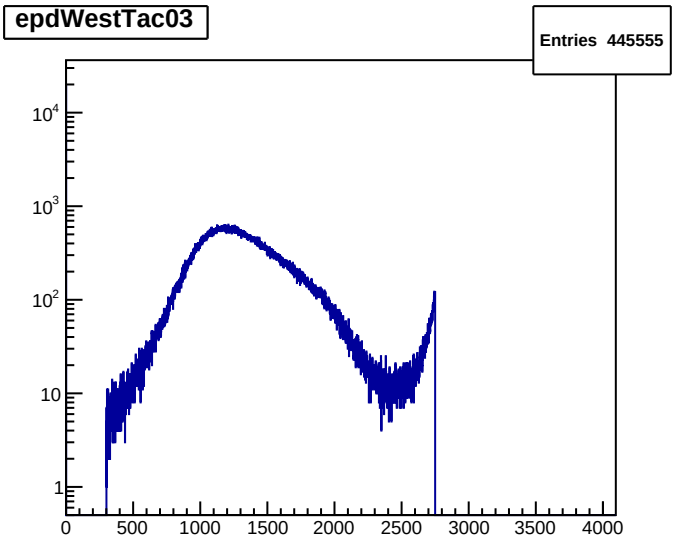
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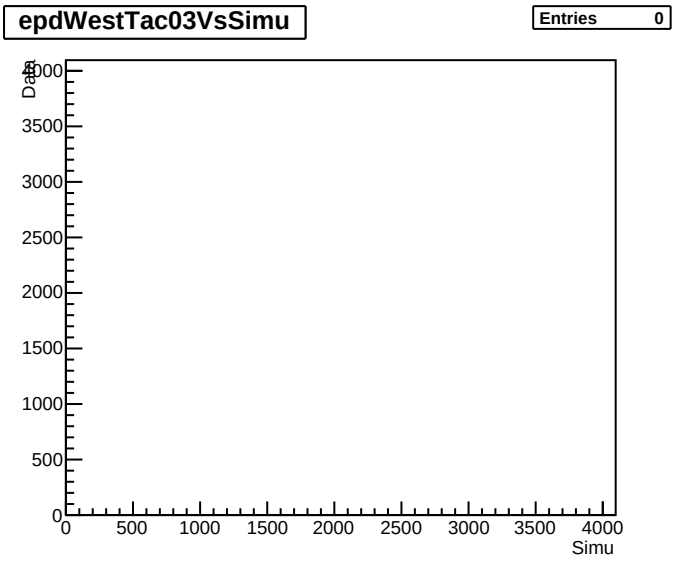
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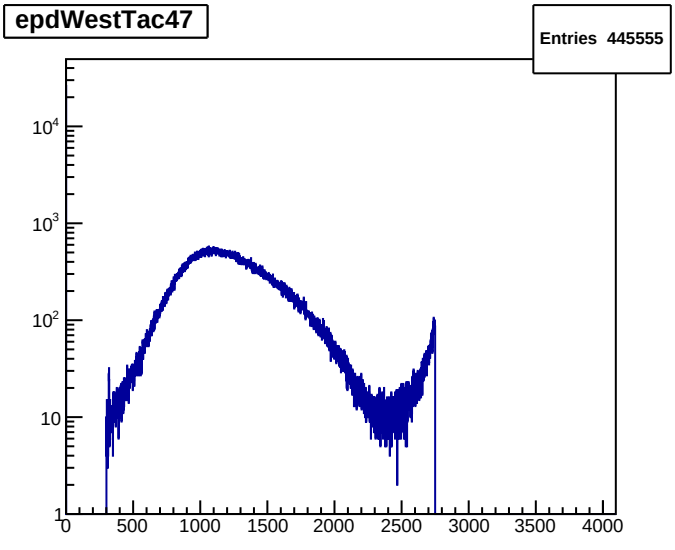
Data
Simu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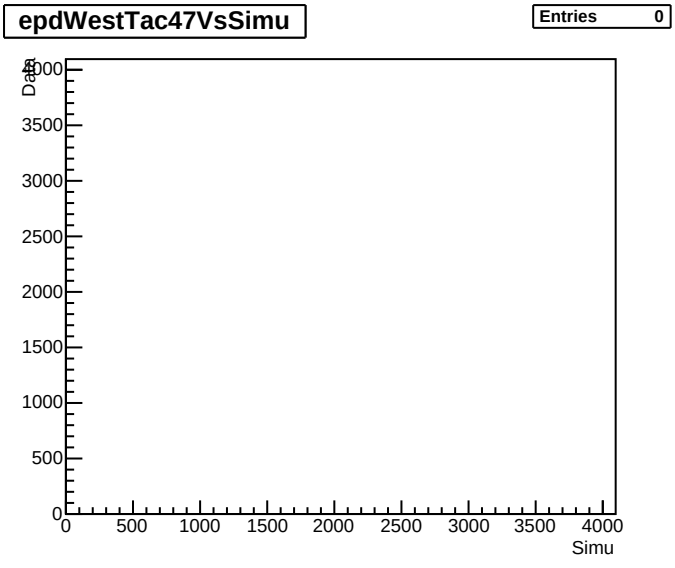
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Data
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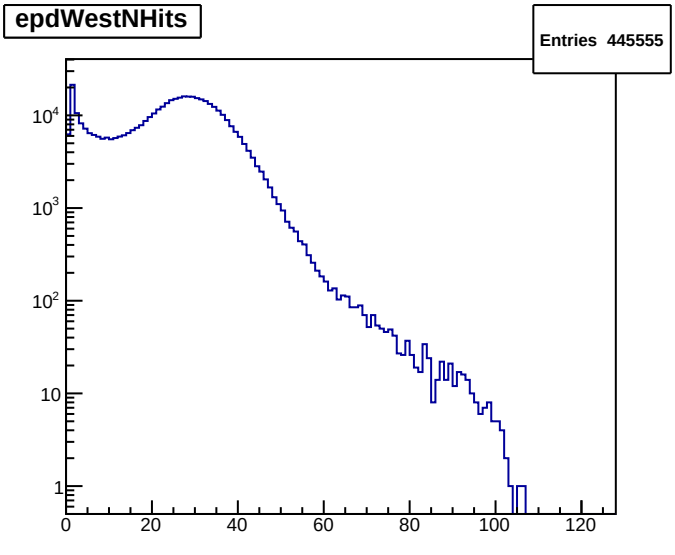
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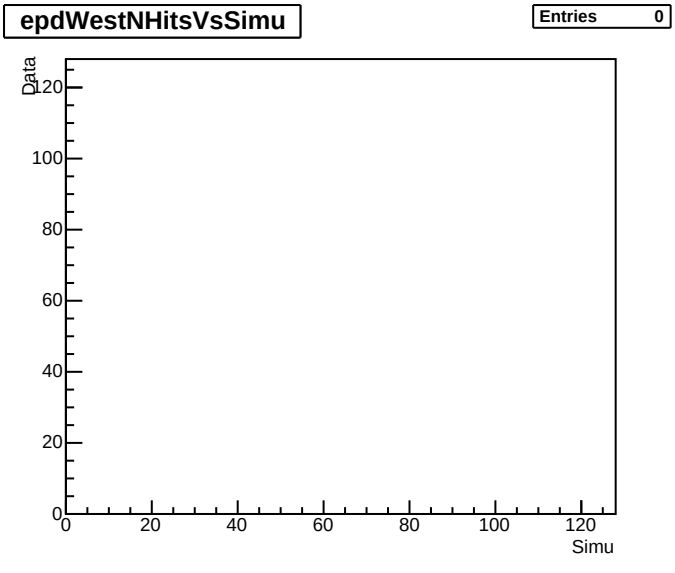
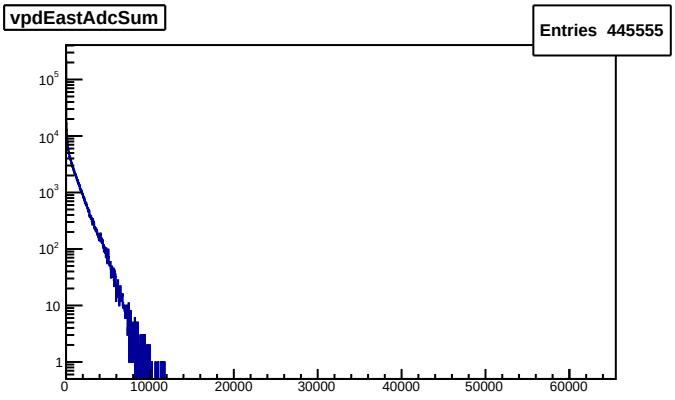
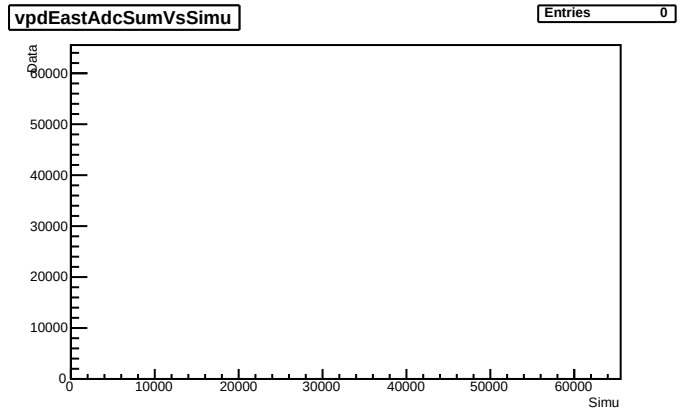
Entries 445555


epdWestNHitsVsSimu
Entries 0
Data
Simu


vpdEastAdcSum
Entries 445555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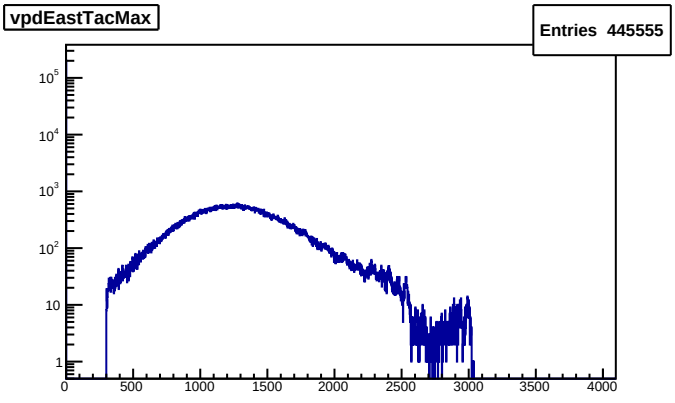
Entries 0
Data
Simu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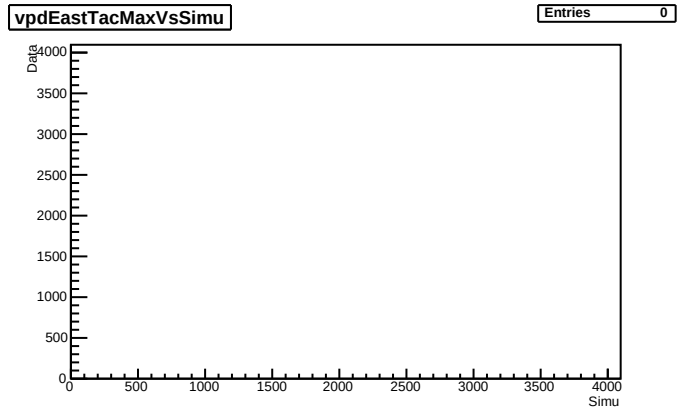
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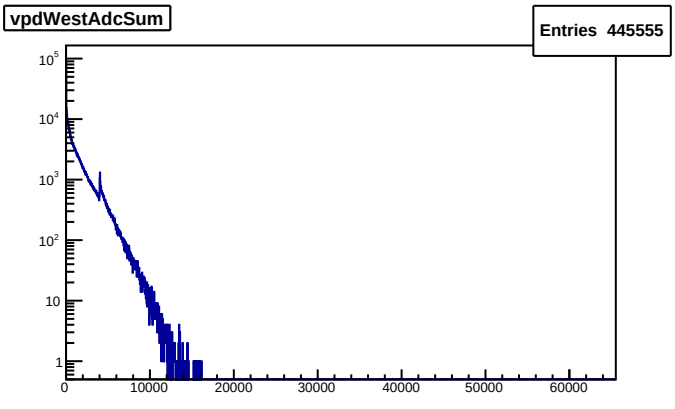
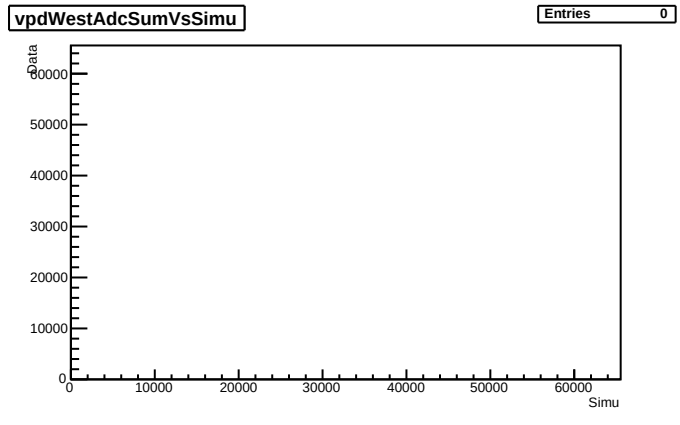
Simu


vpdWestAdcSum
Entries 445555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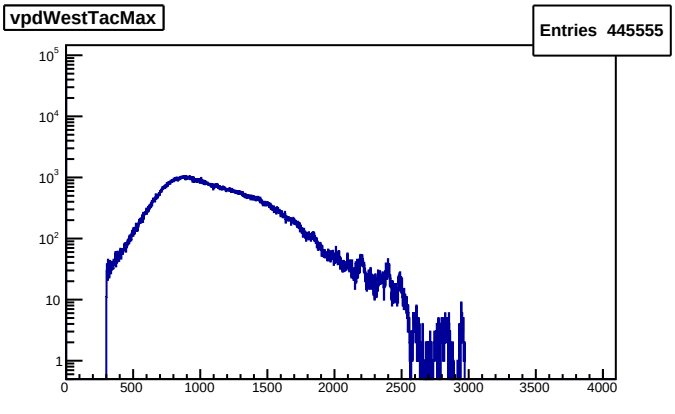
Entries 0
Data
Simu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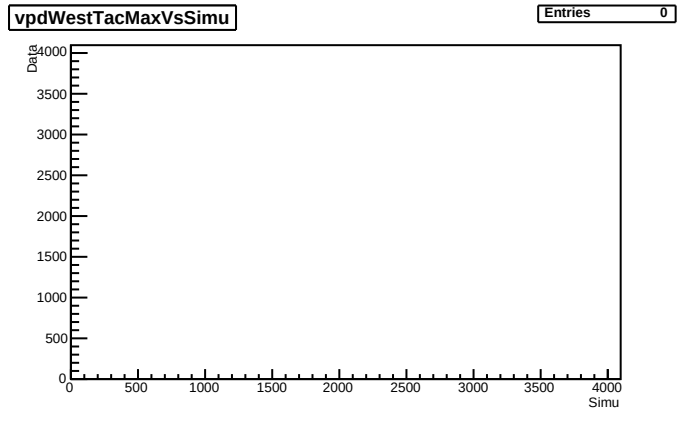
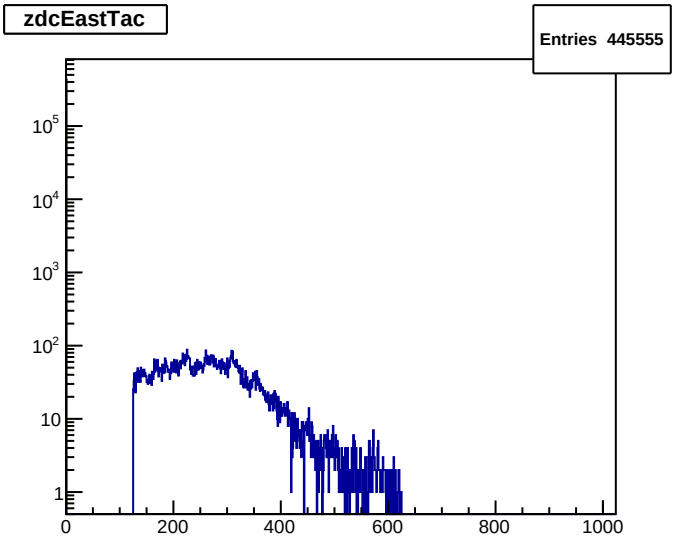
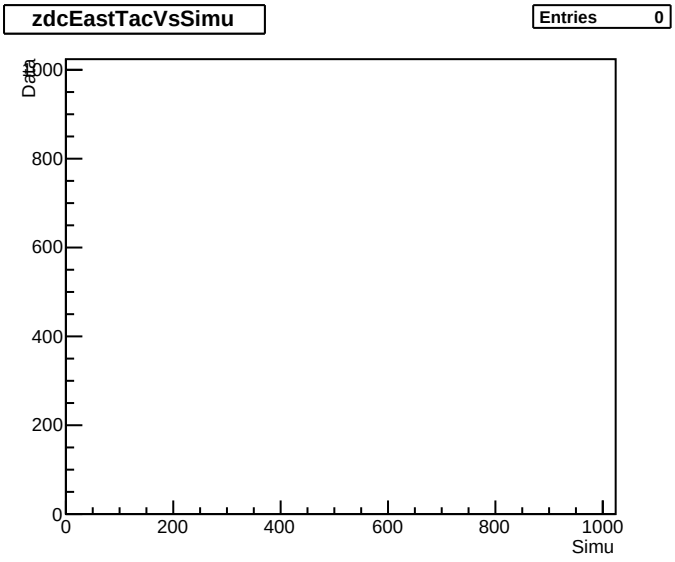
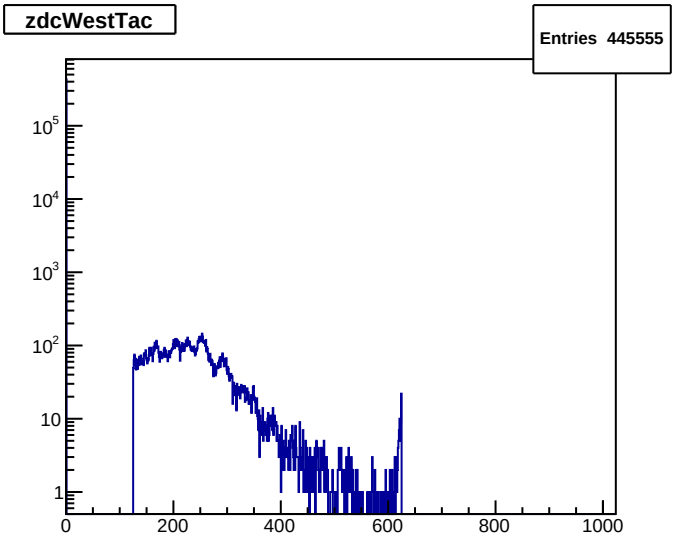
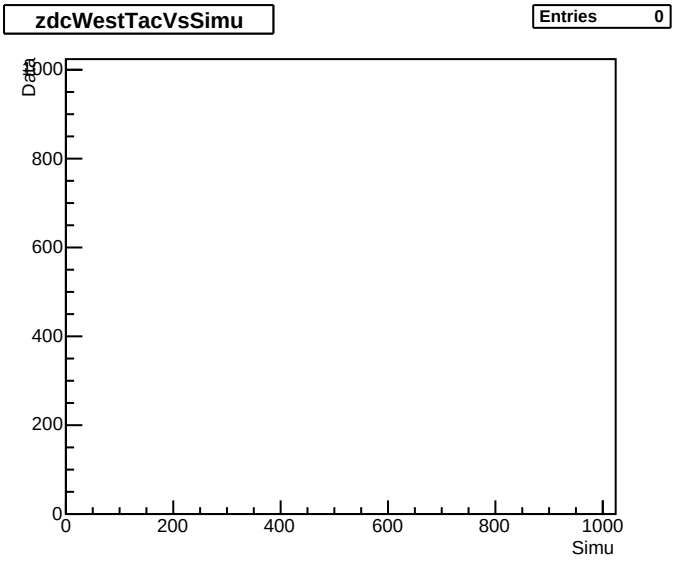
Entries 445555

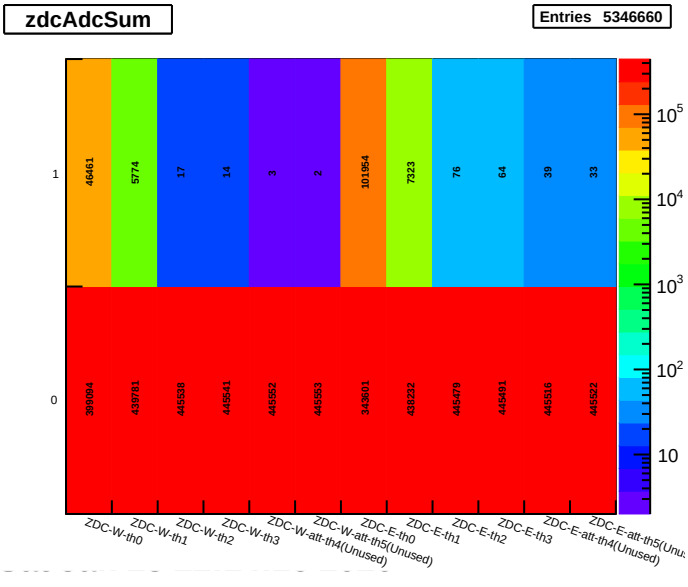

vpdWestTacMaxVsSimu
Entries 0
Data
Simu

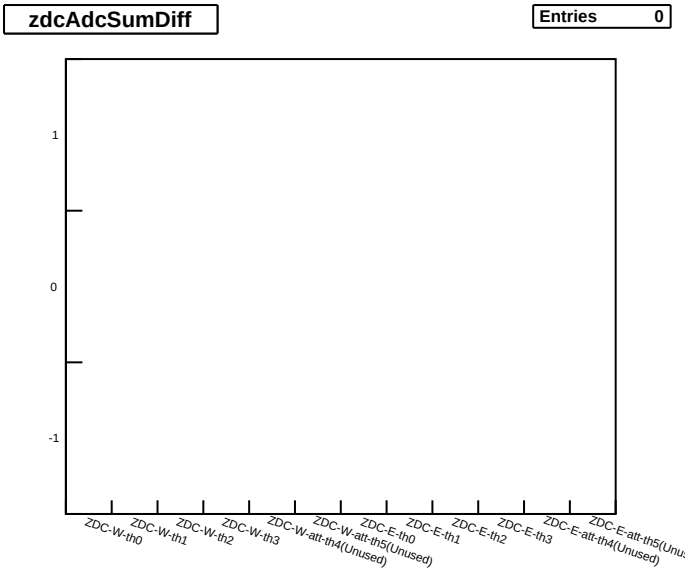

zdcEastTac
Entries 445555

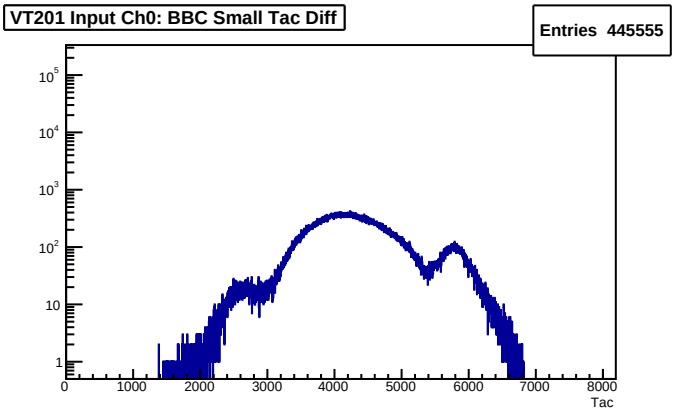

zdcEastTacVsSimu
Entries 0
Data
Simu

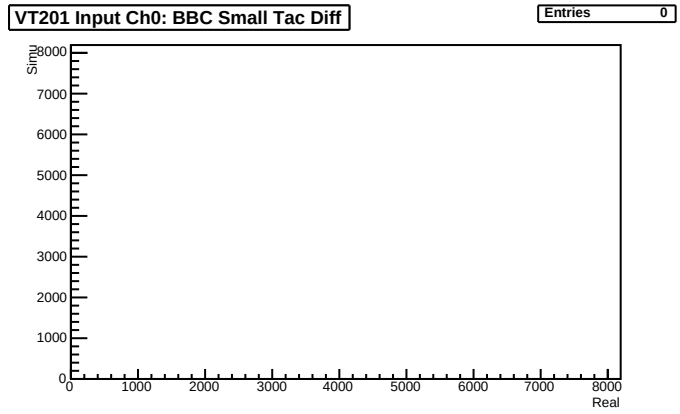

zdcWestTac
Entries 445555

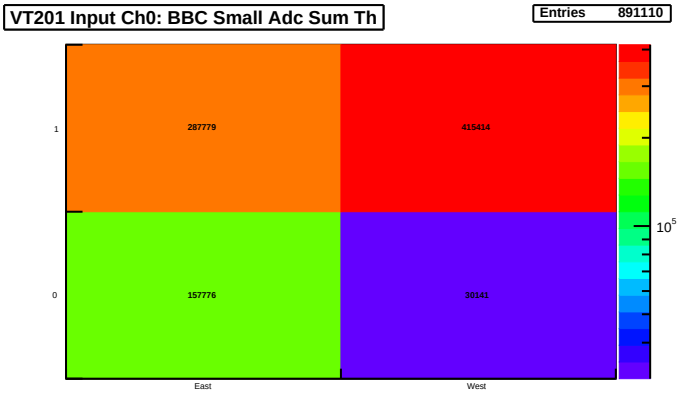

zdcWestTacVsSimu
Entries 0
Data
Simu

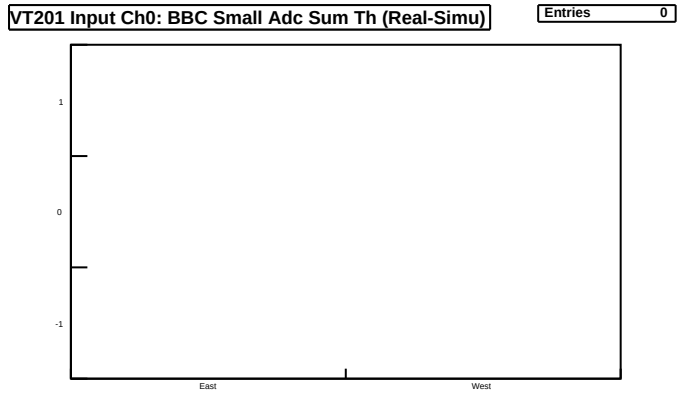

zdcAdcSum
Entries 5346660
46461
5774
17
14
3
2
101954
7323
76
64
39
33
399084
439781
445538
445541
445552
445553
343601
438232
445479
445491
445516
445522
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)

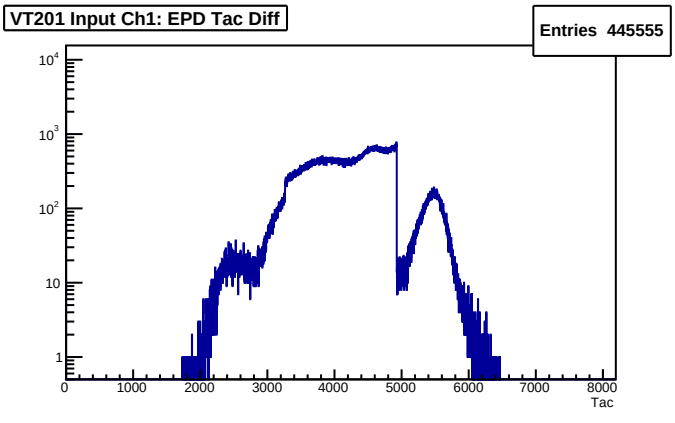

zdcAdcSumDiff
Entries 0
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)

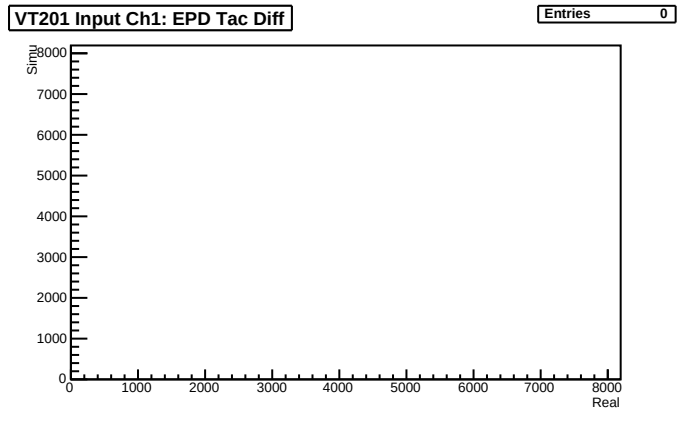

VT201 Input Ch0: BBC Small Tac Diff
Entries 445555
Tac

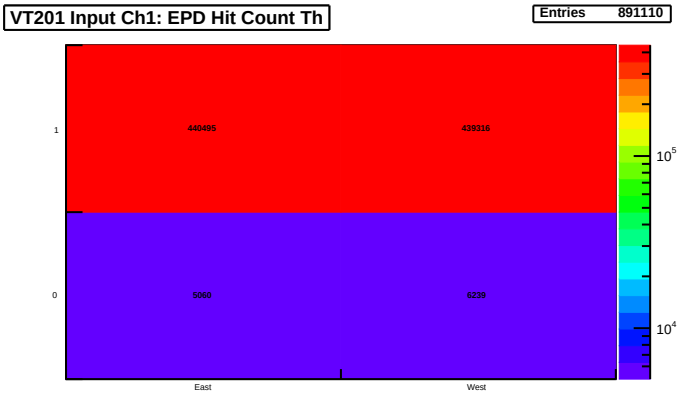

VT201 Input Ch0: BBC Small Tac Diff
Entries 0
Simu
Real

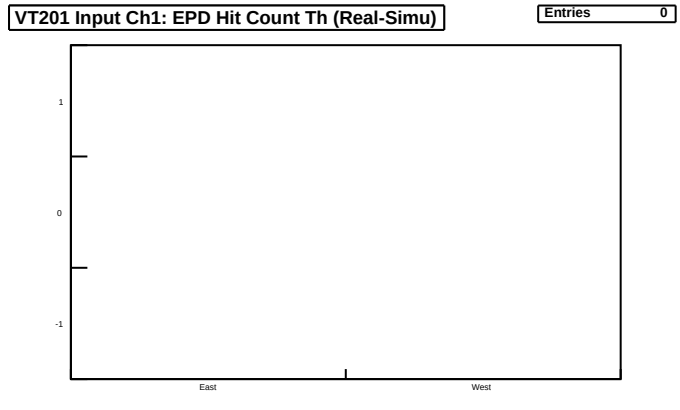

VT201 Input Ch0: BBC Small Adc Sum Th
Entries 891110
287779
415414
157776
30141
East
West

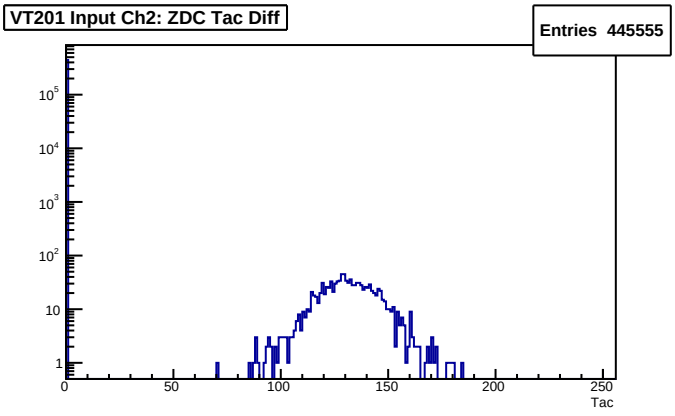

VT201 Input Ch0: BBC Small Adc Sum Th (Real-Simu)
Entries 0
East
West

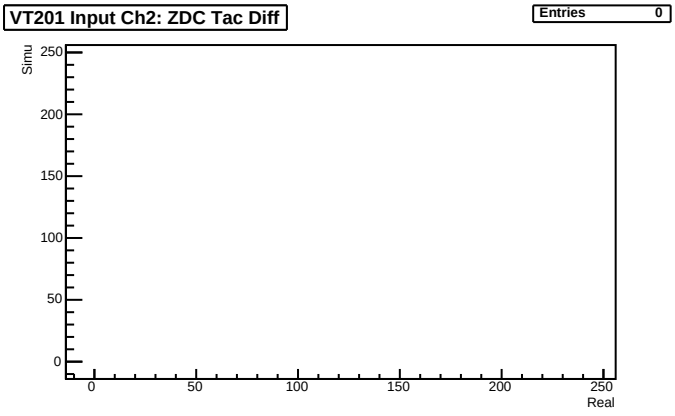

VT201 Input Ch1: EPD Tac Diff
Entries 445555
Tac

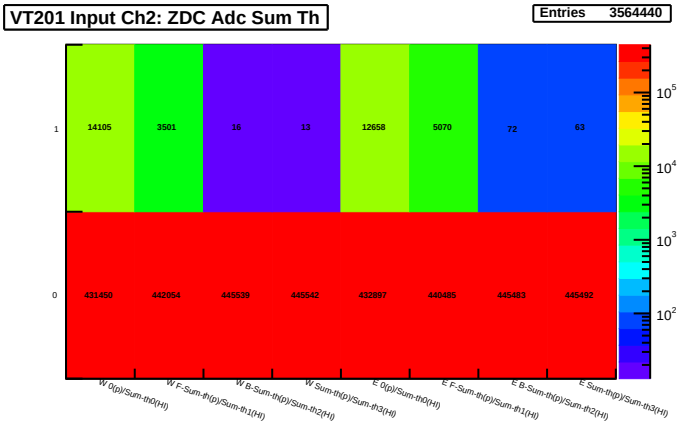

VT201 Input Ch1: EPD Tac Diff
Entries 0
Simu
Real

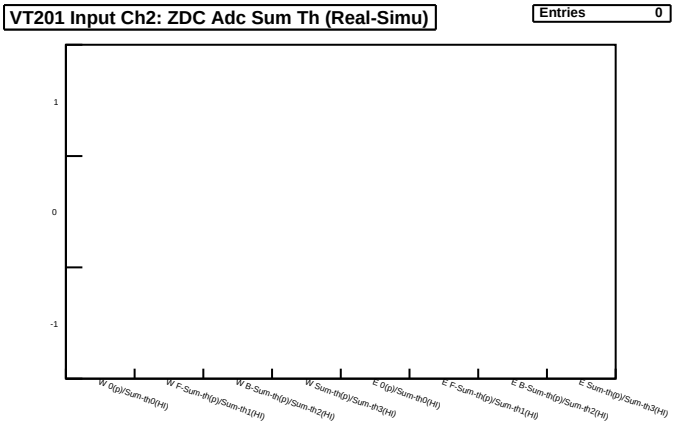

VT201 Input Ch1: EPD Hit Count Th
Entries 891110
440495
439316
5060
6239
East
West

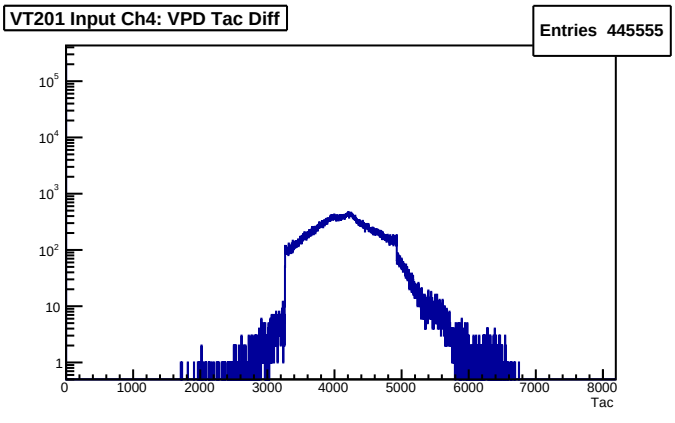

VT201 Input Ch1: EPD Hit Count Th (Real-Simu)
Entries 0
East
West

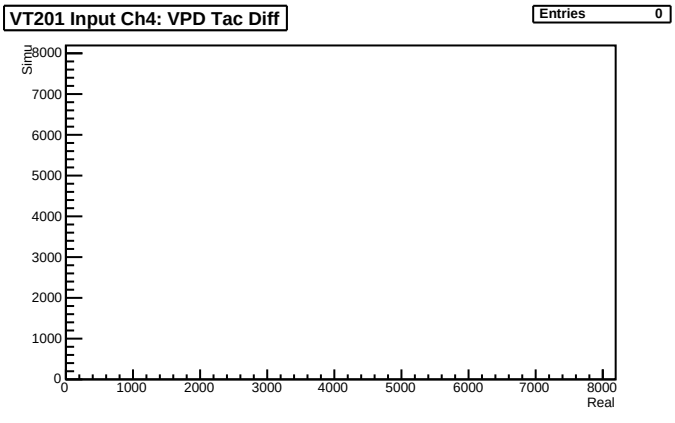

VT201 Input Ch2: ZDC Tac Diff
Entries 445555
Tac

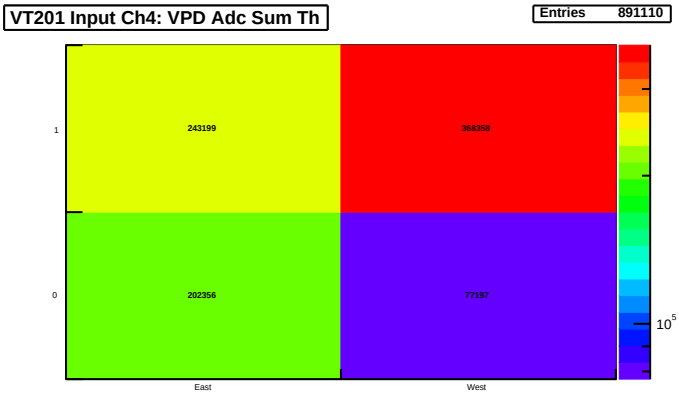

VT201 Input Ch2: ZDC Tac Diff
Entries 0
Simu
Real

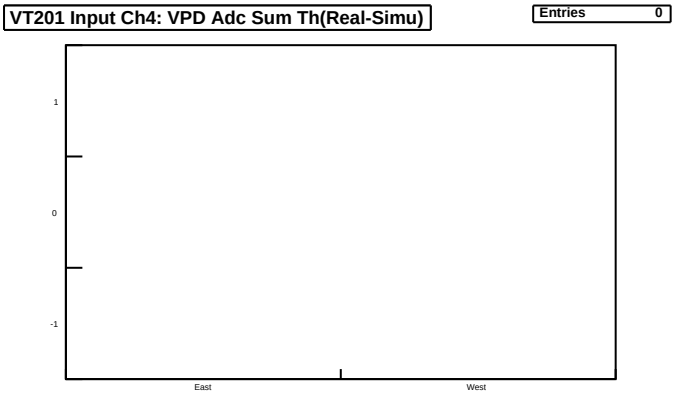

VT201 Input Ch2: ZDC Adc Sum Th
Entries 3564440
14105
3501
16
13
12658
5070
72
63
431450
442054
445539
445542
432897
440485
445483
445492
W 0(p)/Sum-th0(Hl)
W F-Sum-th(p)/Sum-th1(Hl)
W B-Sum-th(p)/Sum-th2(Hl)
W Sum-th(p)/Sum-th3(Hl)
E 0(p)/Sum-th0(Hl)
E F-Sum-th(p)/Sum-th1(Hl)
E B-Sum-th(p)/Sum-th2(Hl)
E Sum-th(p)/Sum-th3(Hl)

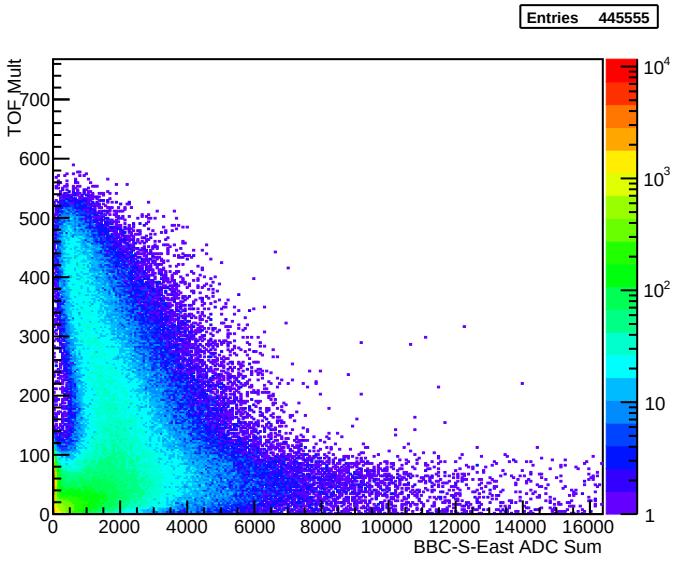

VT201 Input Ch2: ZDC Adc Sum Th (Real-Simu)
Entries 0
W 0(p)/Sum-th0(Hl)
W F-Sum-th(p)/Sum-th1(Hl)
W B-Sum-th(p)/Sum-th2(Hl)
W Sum-th(p)/Sum-th3(Hl)
E 0(p)/Sum-th0(Hl)
E F-Sum-th(p)/Sum-th1(Hl)
E B-Sum-th(p)/Sum-th2(Hl)
E Sum-th(p)/Sum-th3(Hl)

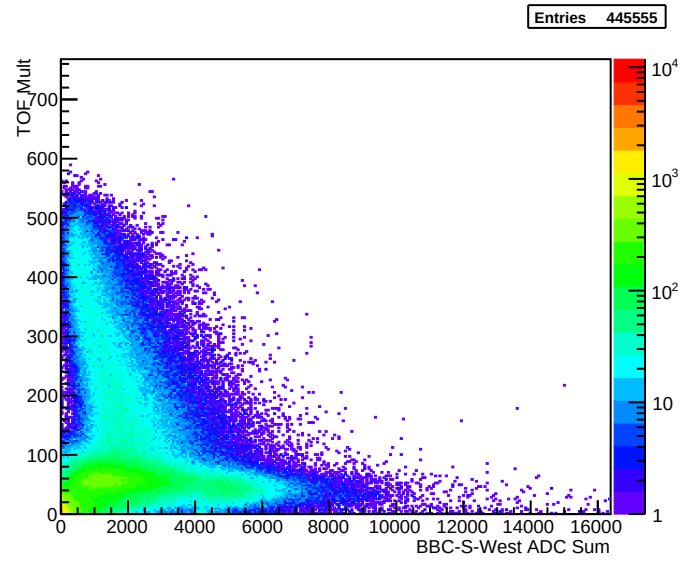

VT201 Input Ch4: VPD Tac Diff
Entries 445555
Tac

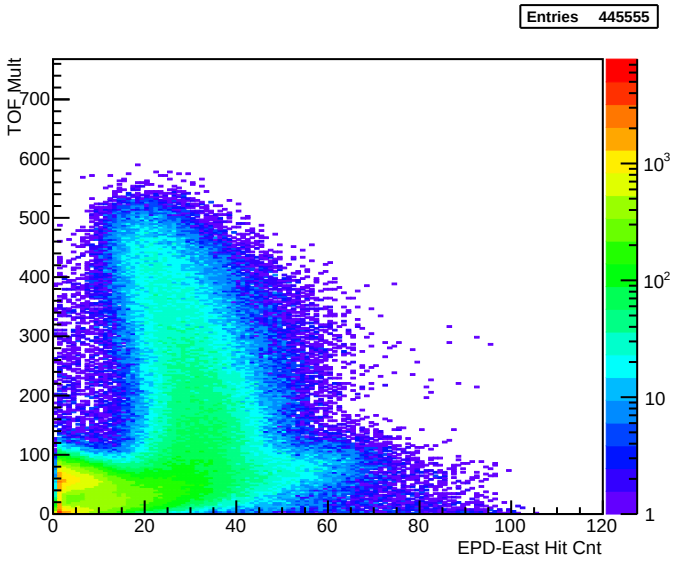

VT201 Input Ch4: VPD Tac Diff
Entries 0
Simu
Real

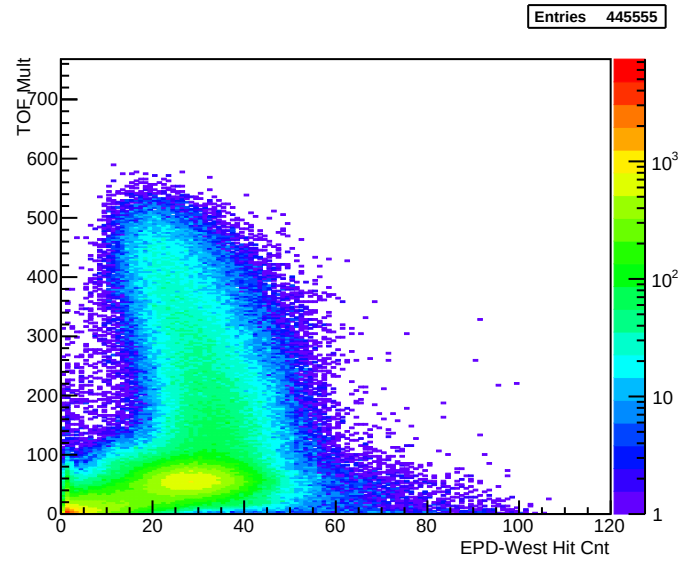

VT201 Input Ch4: VPD Adc Sum Th
Entries 891110
243199
368358
202356
77197
East
West

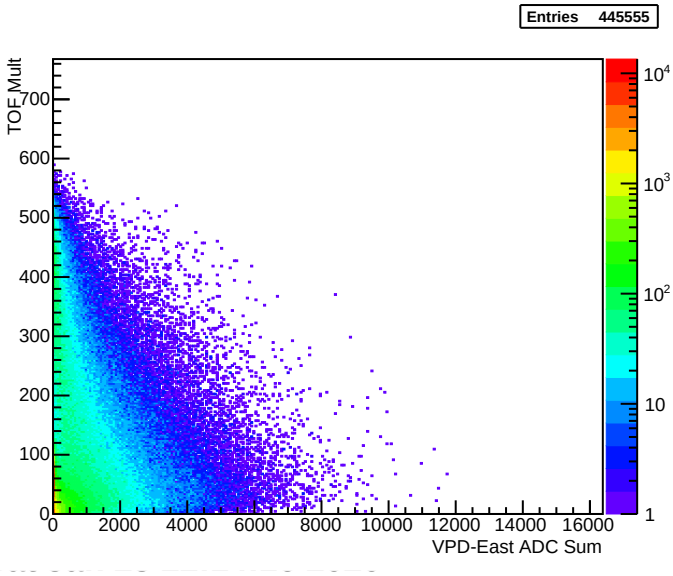

VT201 Input Ch4: VPD Adc Sum Th(Real-Simu)
Entries 0
East
West

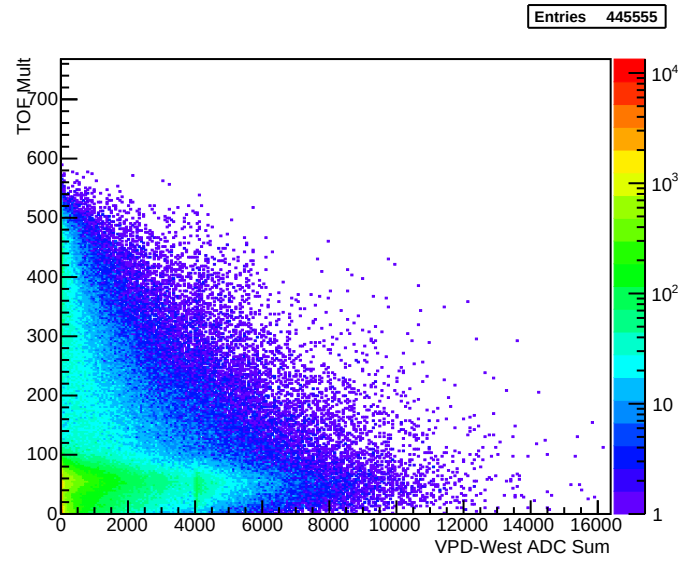

Entries 445555
TOF_Mult
BBC-S-East ADC Sum

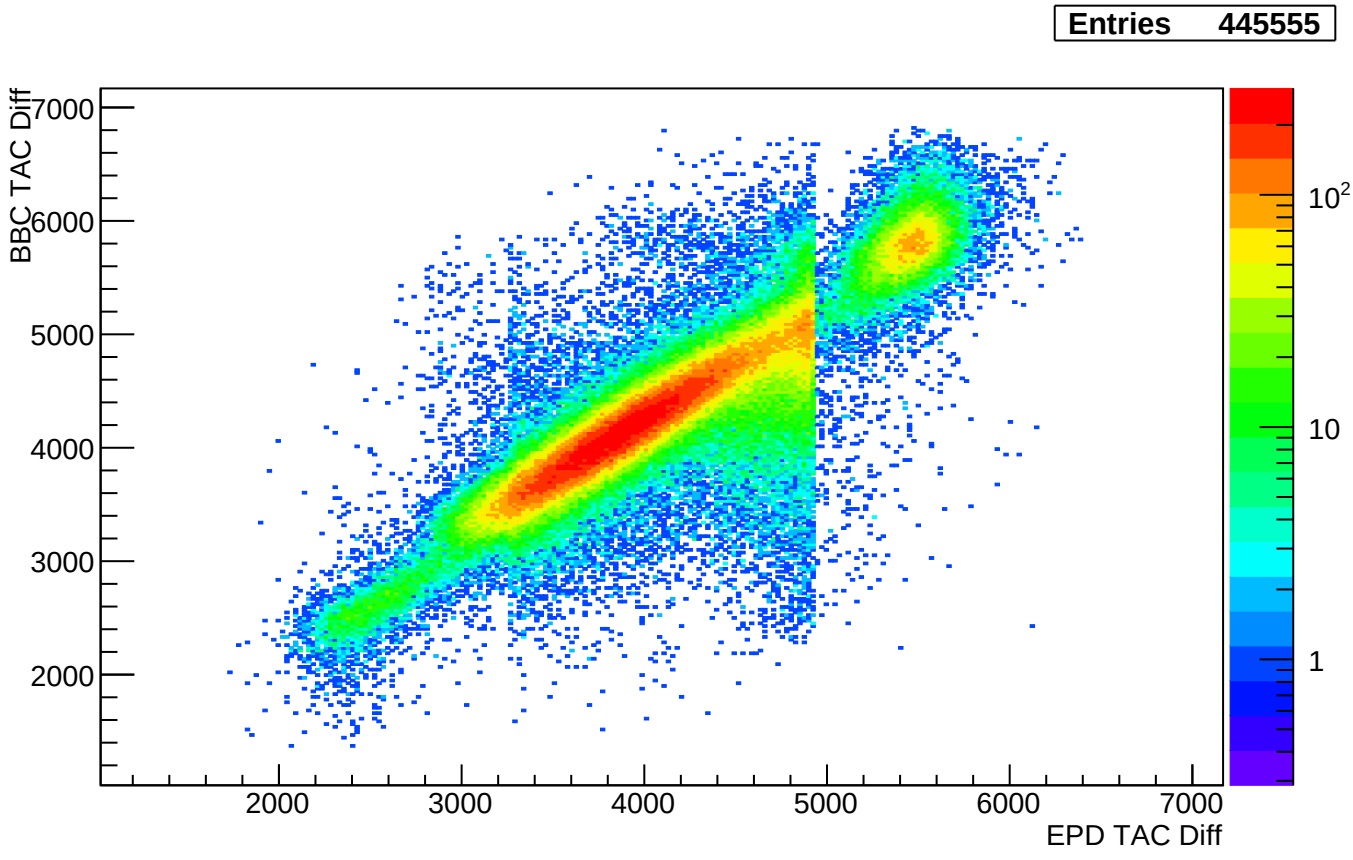

Entries 445555
TOF_Mult
BBC-S-West ADC Sum

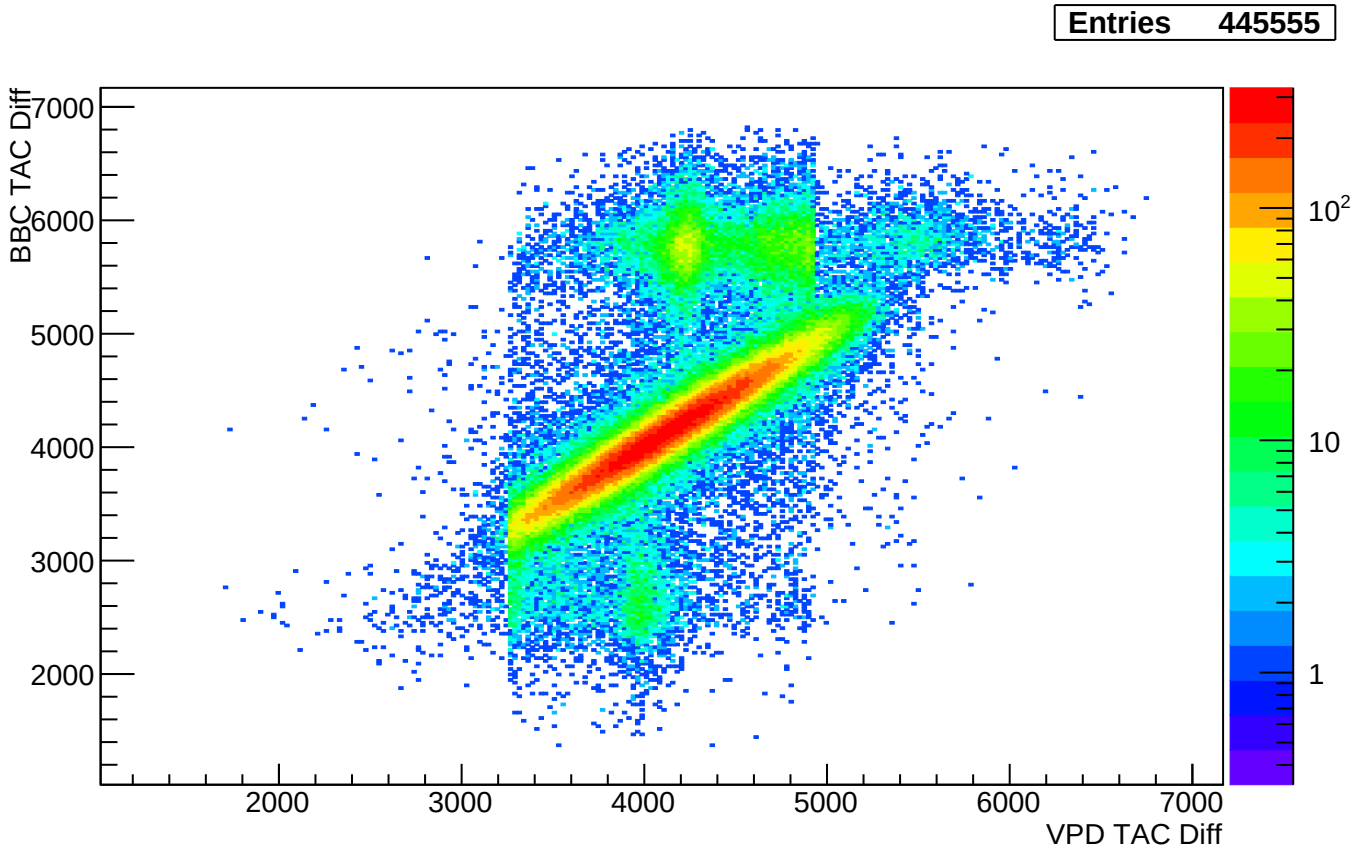

Entries 445555
TOF_Mult
EPD-East Hit Cnt

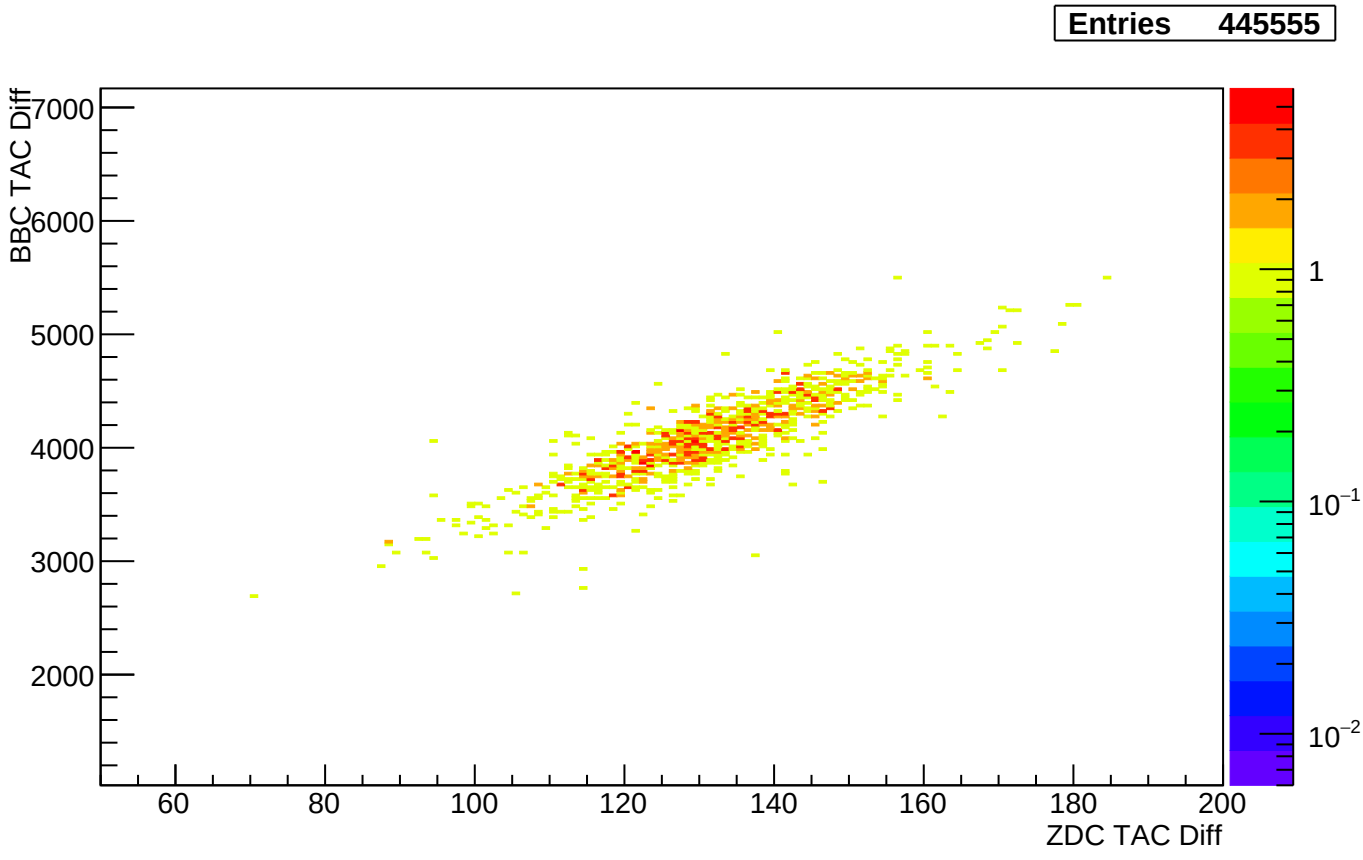

Entries 445555
TOF_Mult
EPD-West Hit Cnt

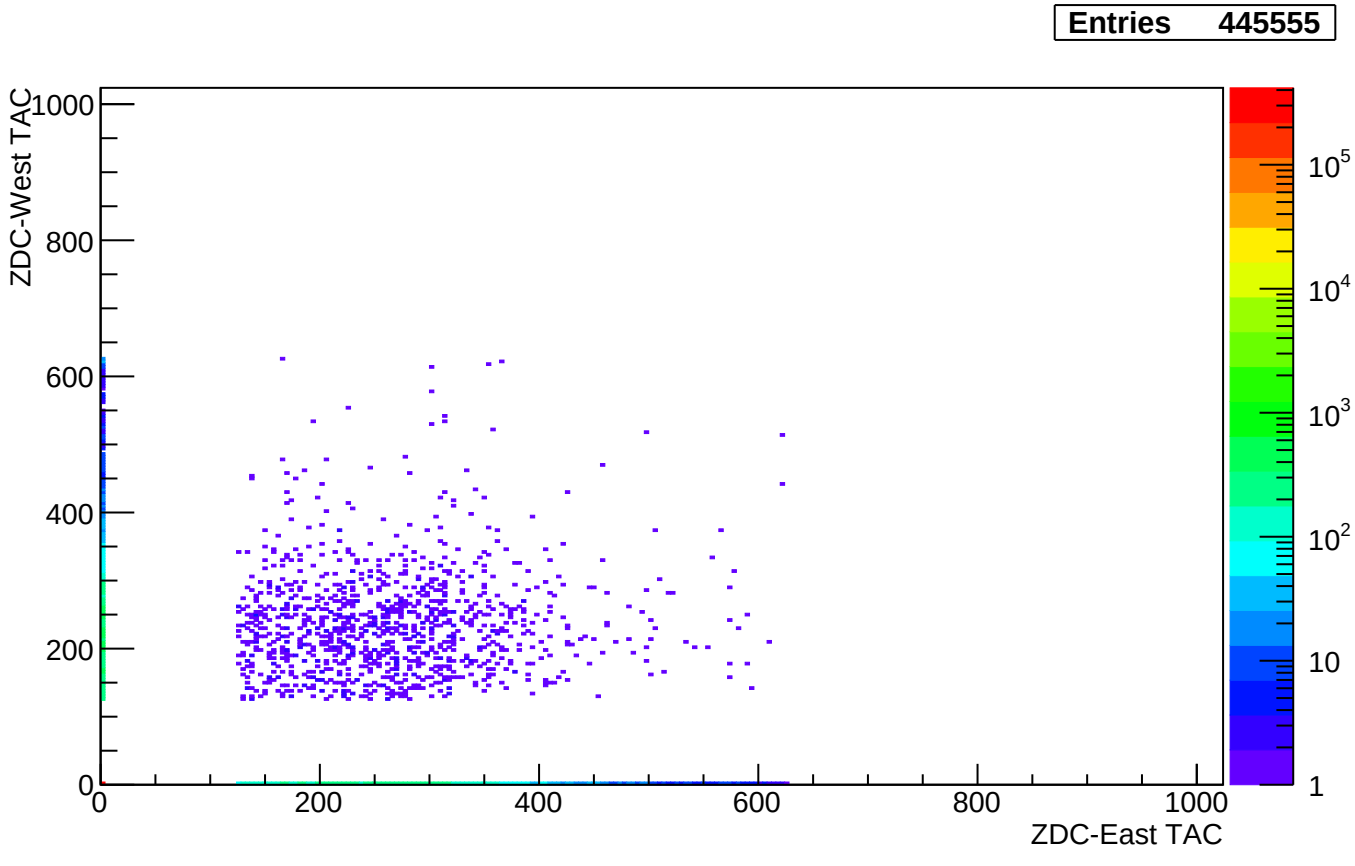

Entries 445555
TOF_Mult
VPD-East ADC Sum

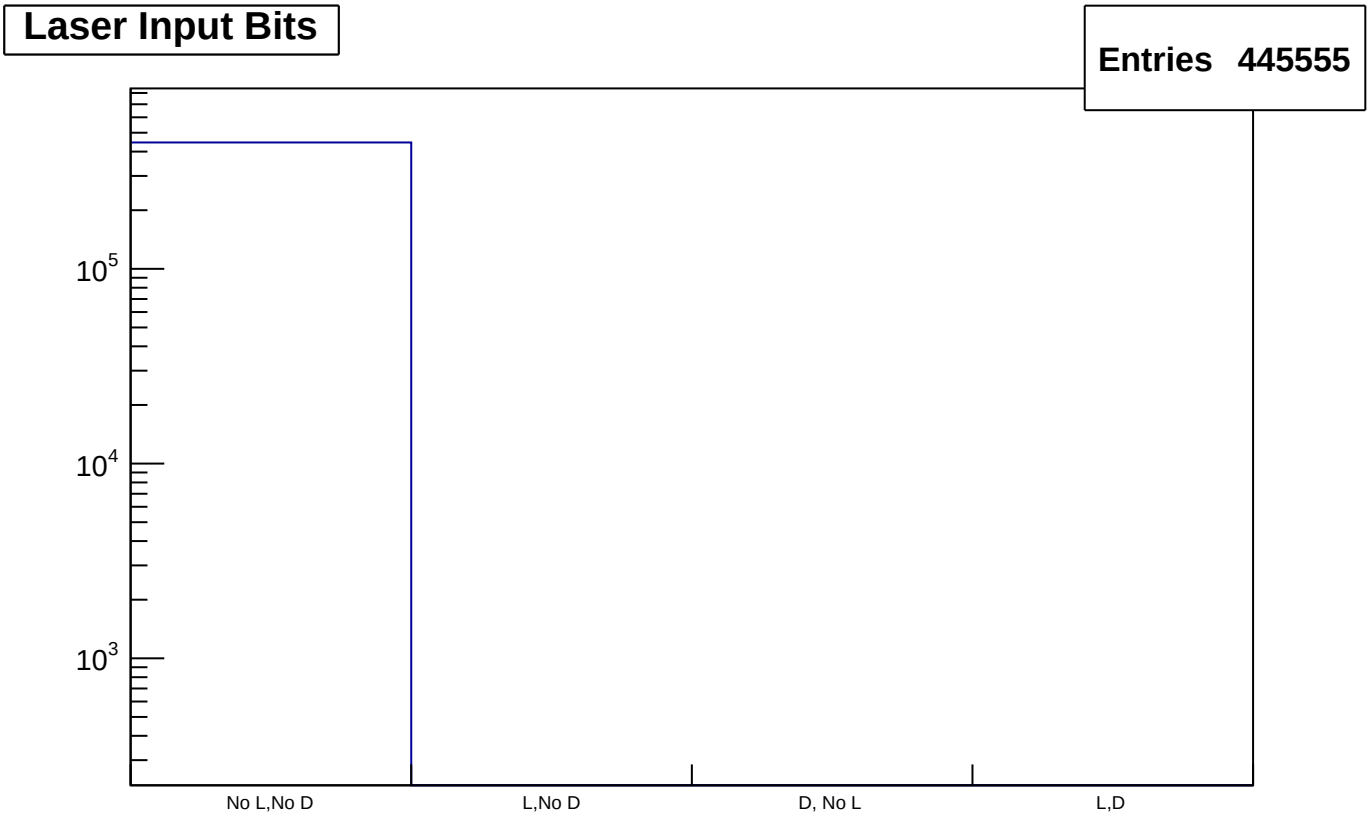

Entries 445555
TOF_Mult
VPD-West ADC Sum

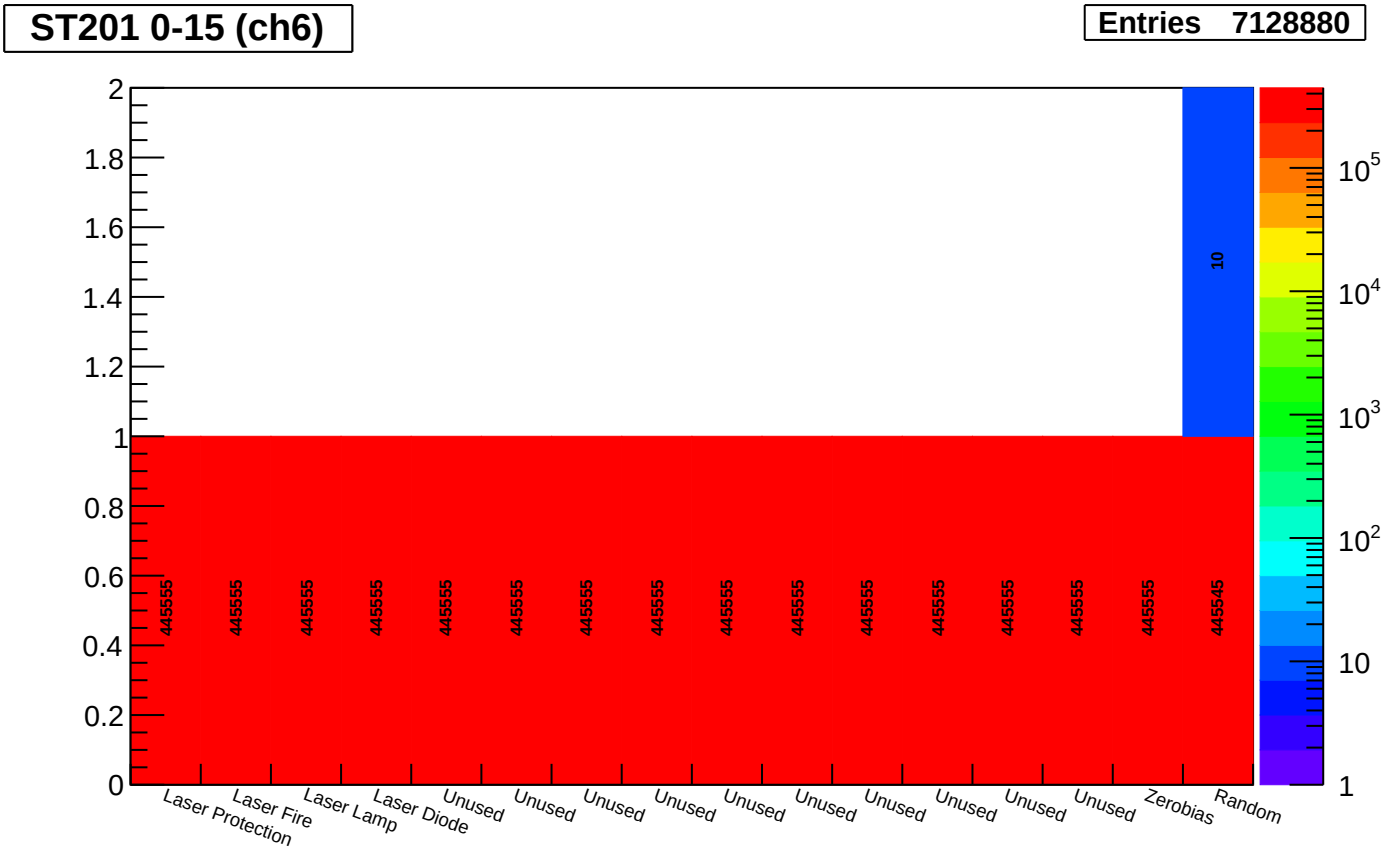

Entries 445555
BBC TAC Diff
7000
6000
5000
4000
3000
2000
2000
3000
4000
5000
6000
7000
EPD TAC Diff
10^2
10
1

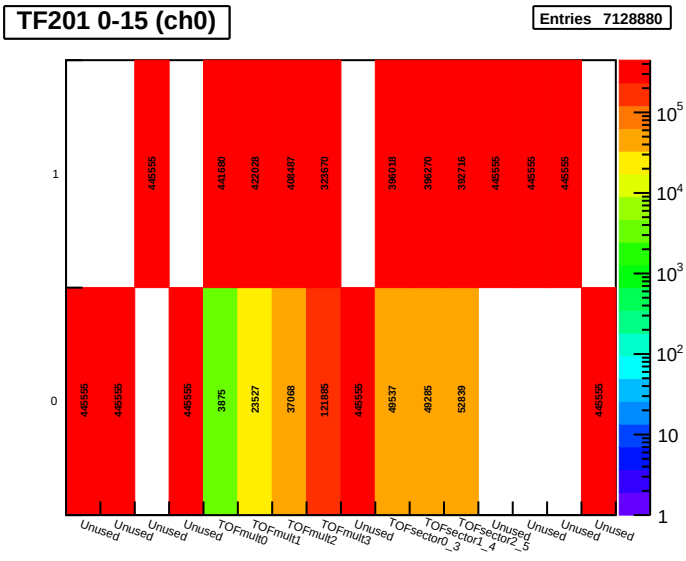

Entries 445555
BBC TAC Diff
7000
6000
5000
4000
3000
2000
2000
3000
4000
5000
6000
7000
VPD TAC Diff
10^2
10
1

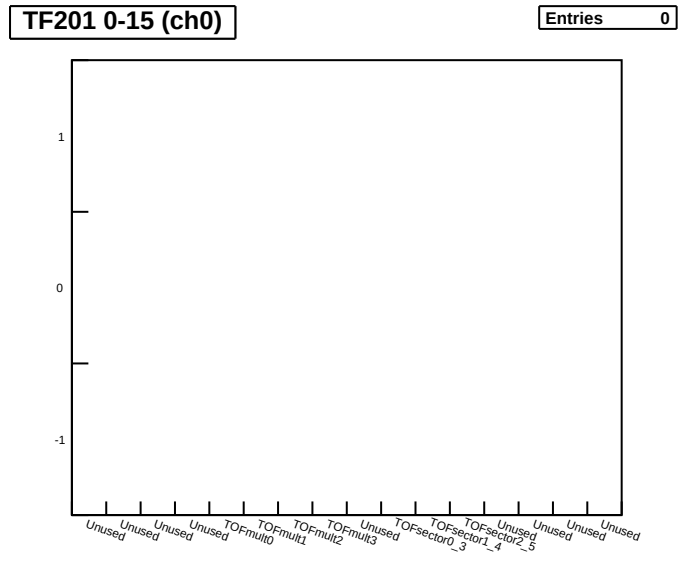

Entries 445555
BBC TAC Diff
7000
6000
5000
4000
3000
2000
60
80
100
120
140
160
180
200
ZDC TAC Diff
1
10^{-1}
10^{-2}

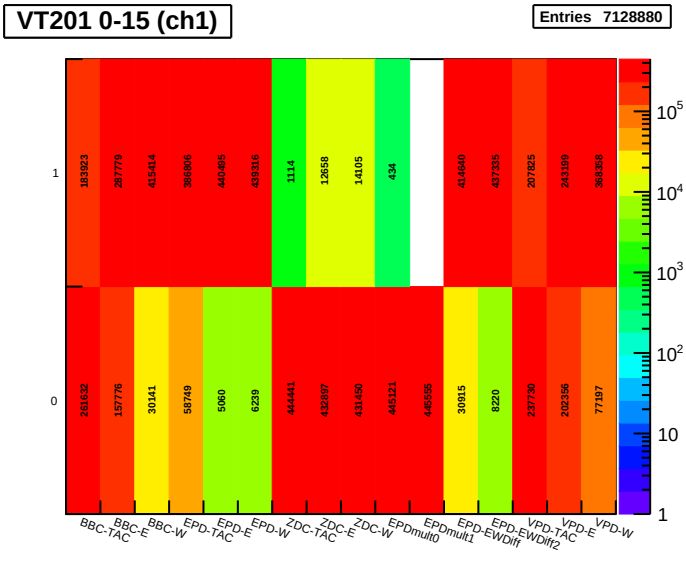

Entries 445555
ZDC-West TAC
1000
800
600
400
200
0
0
200
400
600
800
1000
ZDC-East TAC
10^5
10^4
10^3
10^2
10
1

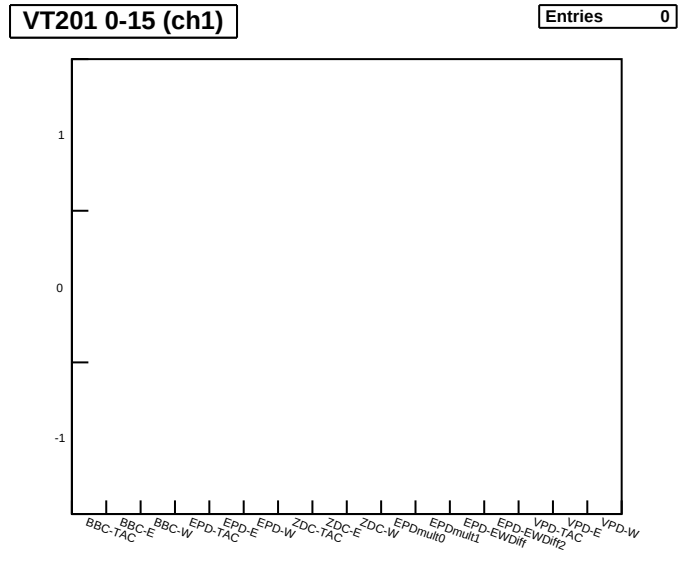

Laser Input Bits
Entries 445555
10^5
10^4
10^3
No L,No D
L,No D
D, No L
L,D

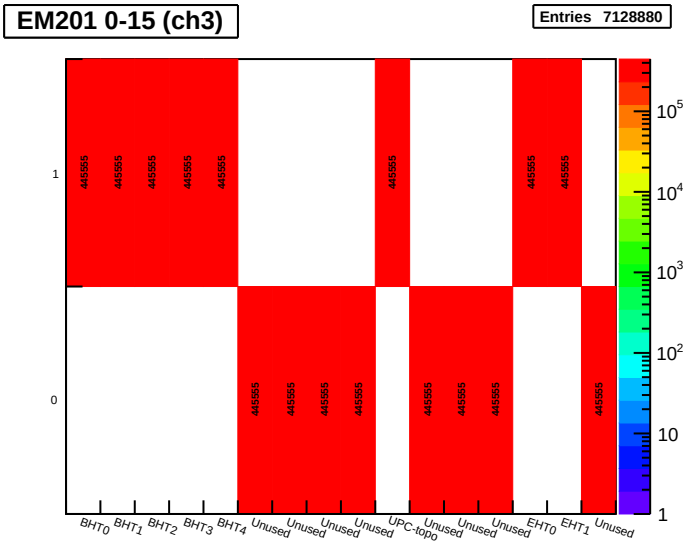

ST201 0-15 (ch6)
Entries 7128880
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
445555
445555
445555
445555
445555
445555
445555
445555
445555
445555
445555
445555
445555
445555
445555
445545
10
Laser Protection
Laser Fire
Laser Lamp
Laser Diode
Unused
Unused
Unused
Unused
Unused
Unused
Unused
Unused
Unused
Unused
Zerobias
Random
10^5
10^4
10^3
10^2
10
1

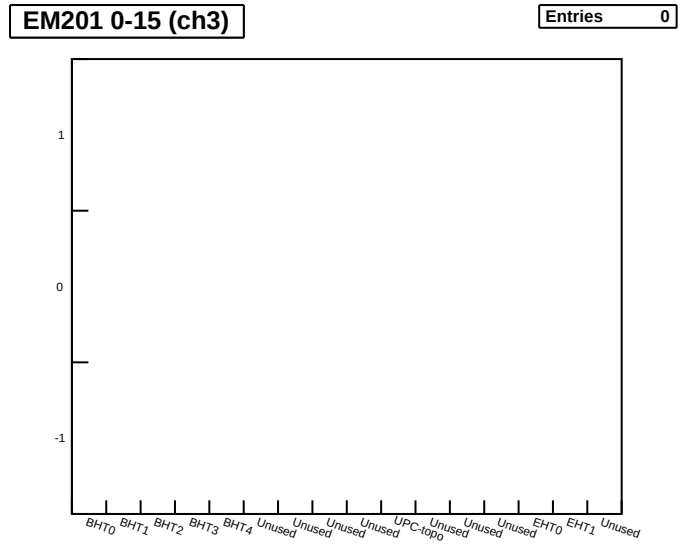

TF201 0-15 (ch0)
Entries 7128880
1
0
445555
441680
422028
408487
323670
396018
396270
392716
445555
445555
445555
445555
445555
445555
3875
23527
37068
121885
445555
49537
49285
52839
445555
Unused
TOFmult0
TOFmult1
TOFmult2
TOFmult3
TOFsector0_3
TOFsector1_4
TOFsector2_5
10^5
10^4
10^3
10^2
10
1


TF201 0-15 (ch0)
Entries 0
1
0
-1
Unused
TOFmult0
TOFmult1
TOFmult2
TOFmult3
TOFsector0_3
TOFsector1_4
TOFsector2_5


VT201 0-15 (ch1)
Entries 7128880
1
0
183923
287779
415414
386806
440485
439316
1114
12658
14105
434
414640
437335
207825
243199
368358
261632
157776
30141
58749
5060
6239
444441
432897
431450
445121
445555
30915
8220
237730
202356
77197
BBC-TAC
BBC-E
BBC-W
EPD-TAC
EPD-E
EPD-W
ZDC-TAC
ZDC-E
ZDC-W
EPDmult0
EPDmult1
EPD-EWDiff
EPD-EWDiff2
VPD-TAC
VPD-E
VPD-W
10^5
10^4
10^3
10^2
10
1


VT201 0-15 (ch1)
Entries 0
1
0
-1
BBC-TAC
BBC-E
BBC-W
EPD-TAC
EPD-E
EPD-W
ZDC-TAC
ZDC-E
ZDC-W
EPDmult0
EPDmult1
EPD-EWDiff
EPD-EWDiff2
VPD-TAC
VPD-E
VPD-W


EM201 0-15 (ch3)
Entries 7128880
1
0
445555
445555
445555
445555
445555
445555
445555
445555
445555
445555
445555
445555
445555
445555
445555
445555
445555
445555
445555
445555
445555
445555
445555
445555
BHT0
BHT1
BHT2
BHT3
BHT4
Unused
UPC-topo
EHT0
EHT1
10^5
10^4
10^3
10^2
10
1


EM201 0-15 (ch3)
Entries 0
1
0
-1
BHT0
BHT1
BHT2
BHT3
BHT4
Unused
UPC-topo
EHT0
EHT1