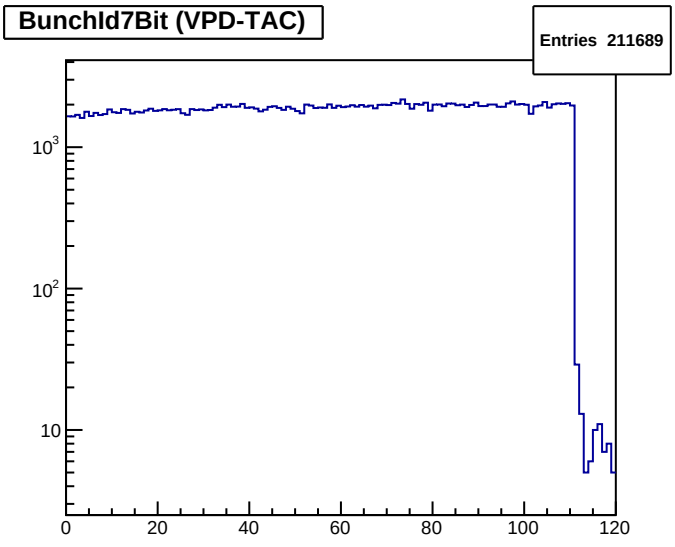
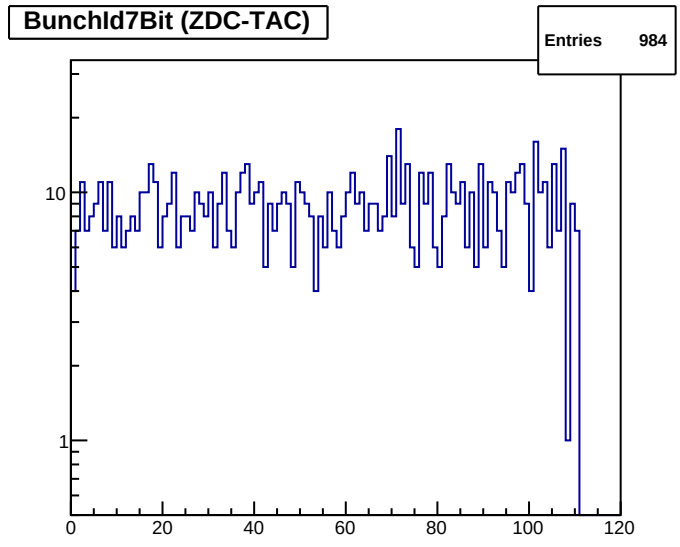
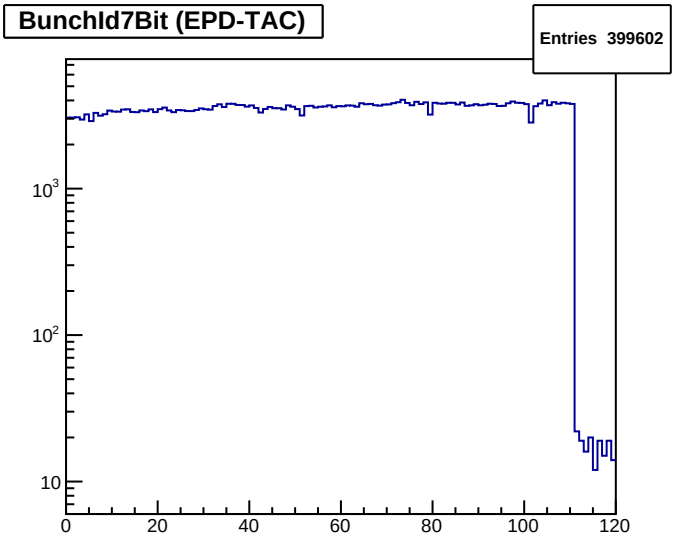
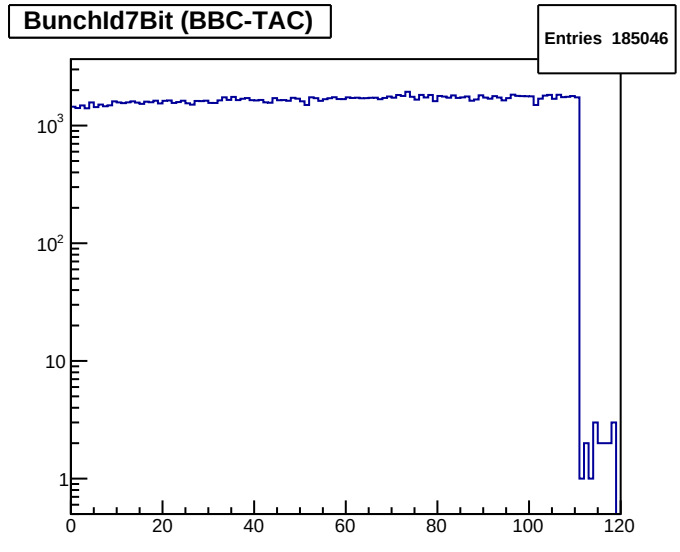
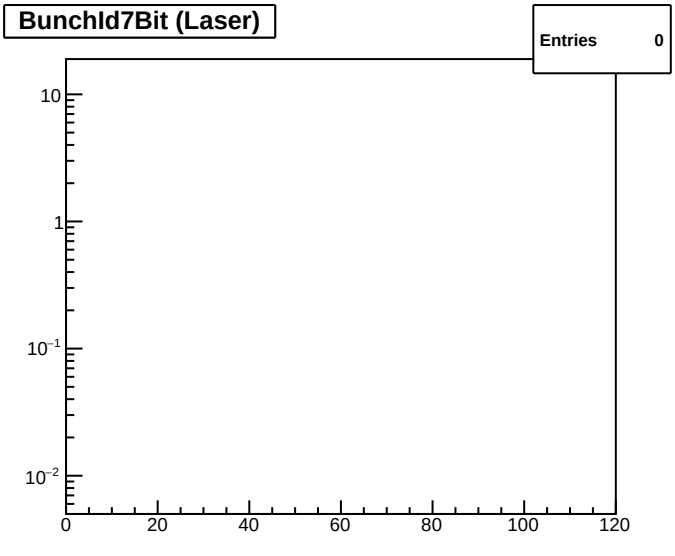
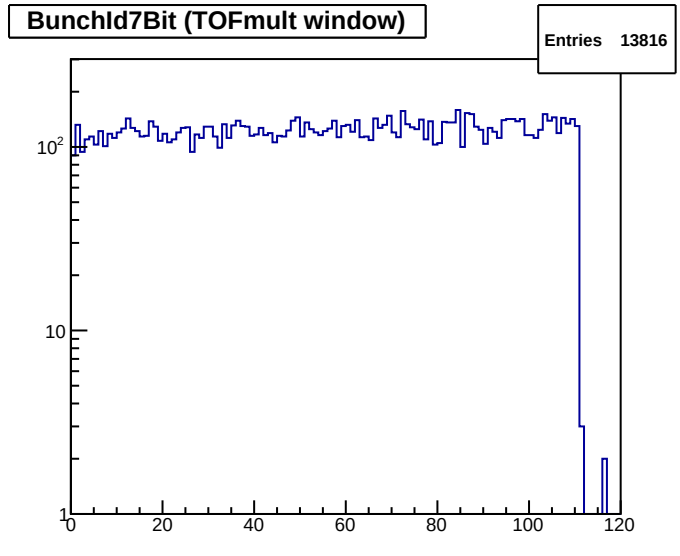

BunchId7Bit (VPD-TAC)
Entries 211689
10^3
10^2
10
0 20 40 60 80 100 120

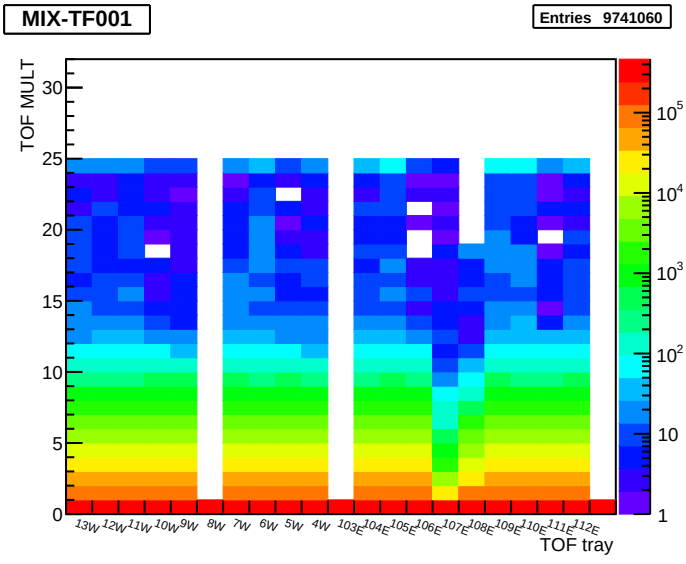

BunchId7Bit (ZDC-TAC)
Entries 984
10
1
0 20 40 60 80 100 120

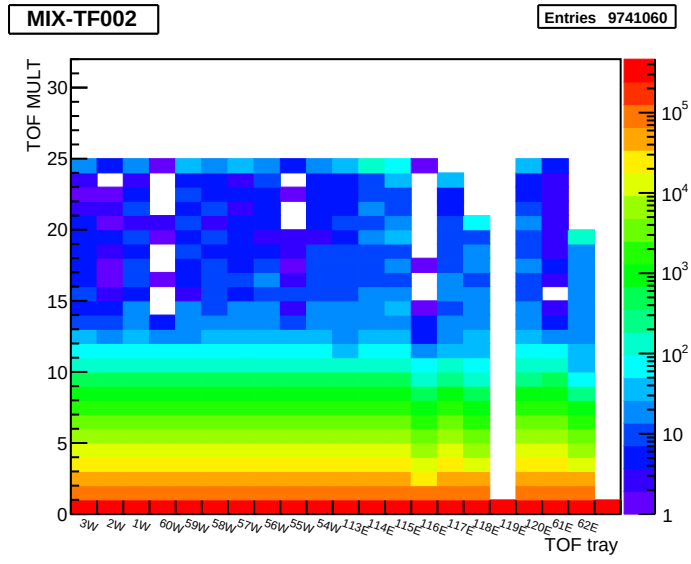

BunchId7Bit (EPD-TAC)
Entries 399602
10^3
10^2
10
0 20 40 60 80 100 120

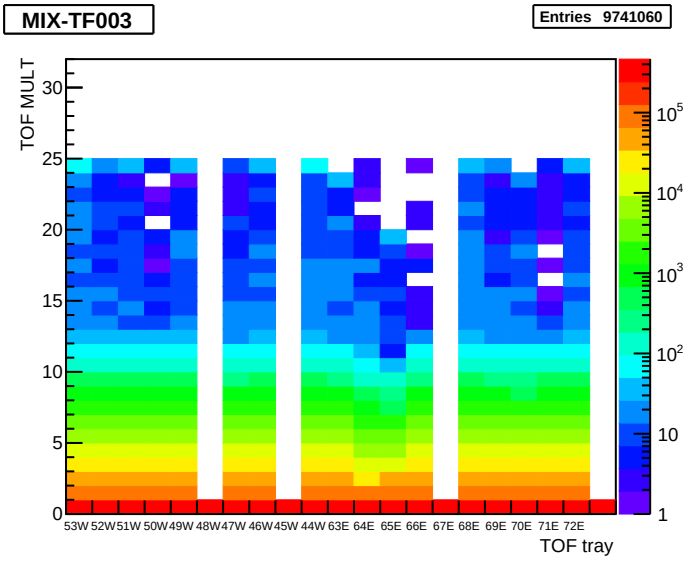

BunchId7Bit (BBC-TAC)
Entries 185046
10^3
10^2
10
1
0 20 40 60 80 100 120

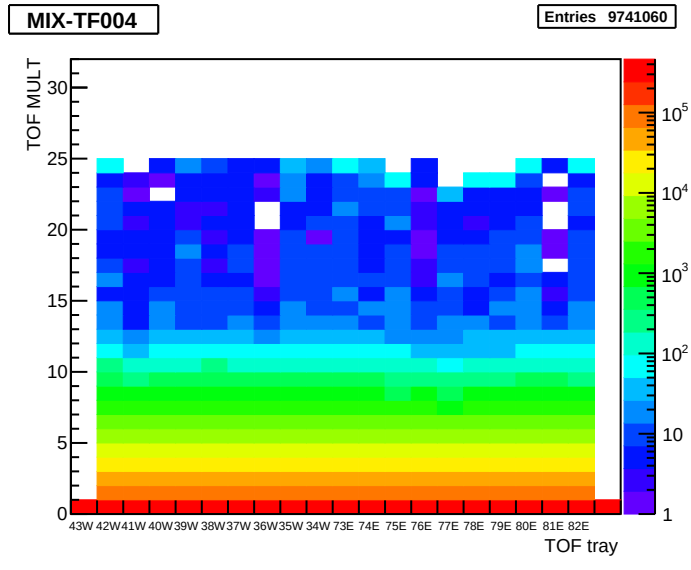

BunchId7Bit (Laser)
Entries 0
10
1
10^{-1}
10^{-2}
0 20 40 60 80 100 120

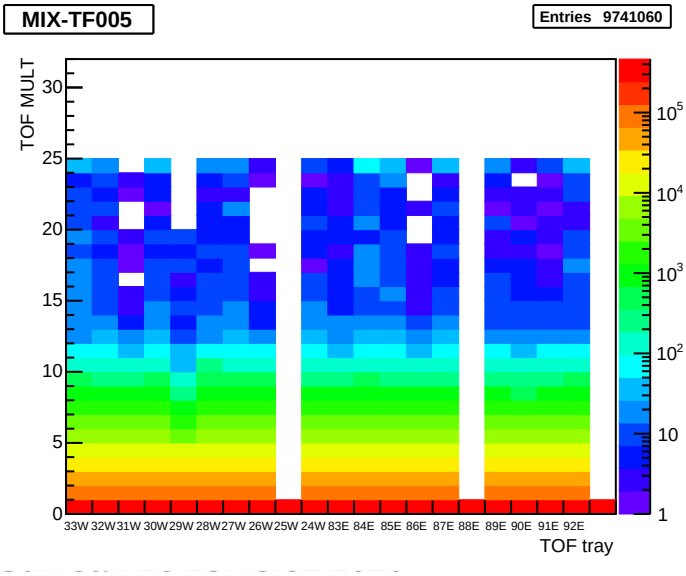

BunchId7Bit (TOFmult window)
Entries 13816
10^2
10
1
0 20 40 60 80 100 120

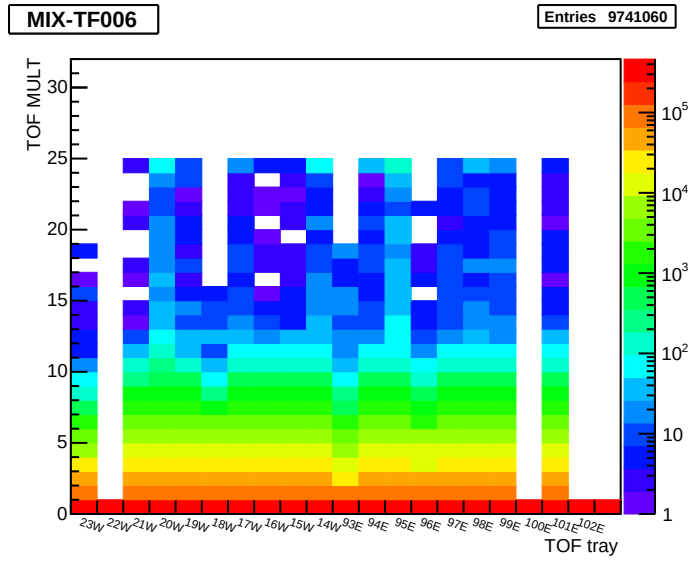

MIX-TF001
Entries 9741060
TOF MULT
TOF tray

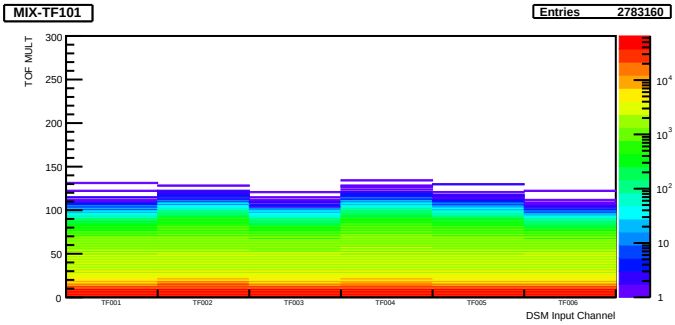

MIX-TF002
Entries 9741060
TOF MULT
TOF tray

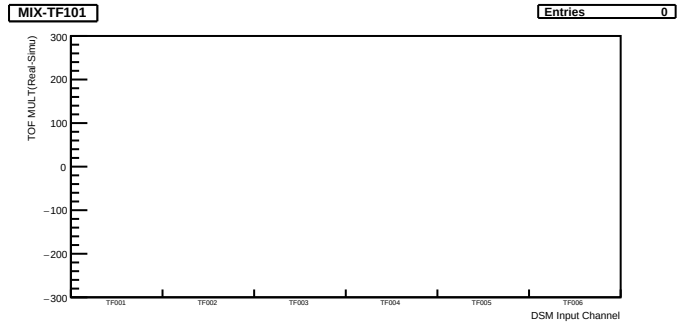

MIX-TF003
Entries 9741060
TOF MULT
TOF tray

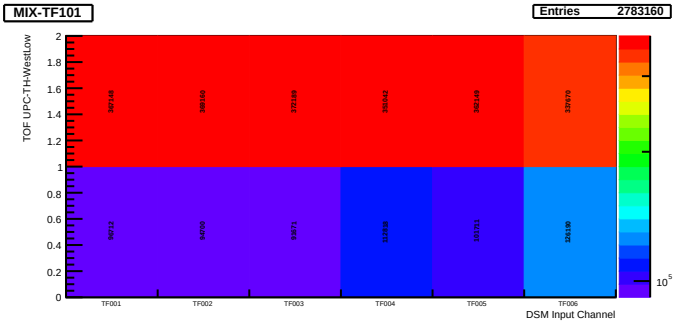

MIX-TF004
Entries 9741060
TOF MULT
TOF tray

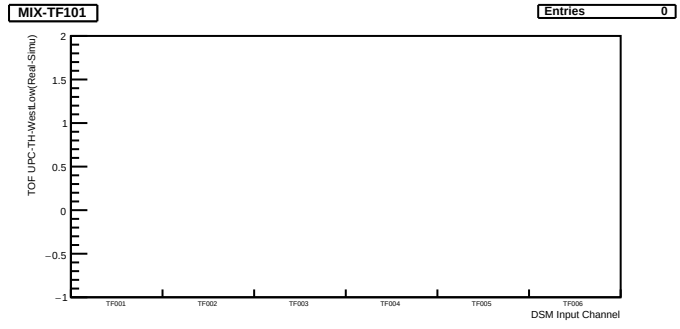

MIX-TF005
Entries 9741060
TOF MULT
TOF tray

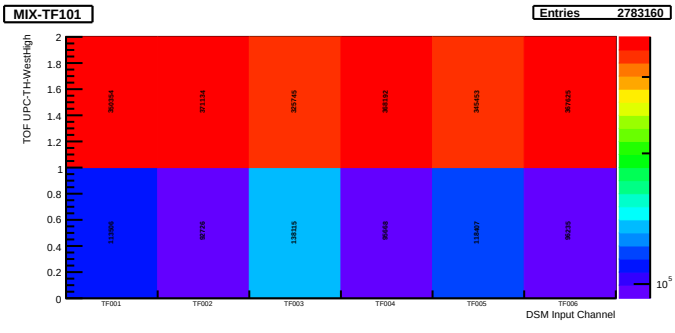

MIX-TF006
Entries 9741060
TOF MULT
TOF tray

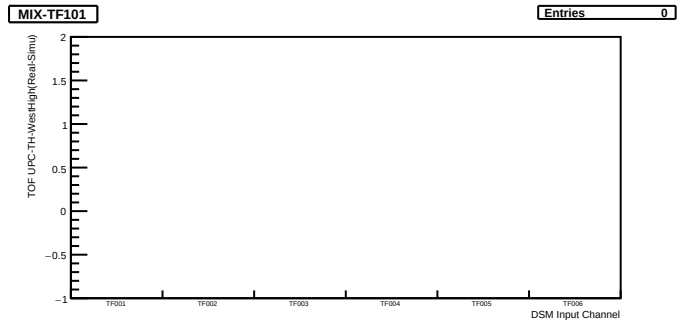

MIX-TF101
Entries 2783160
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

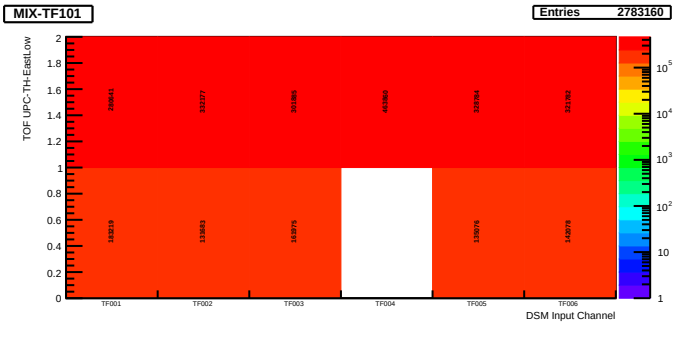

MIX-TF101
Entries 0
TOF MULT(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

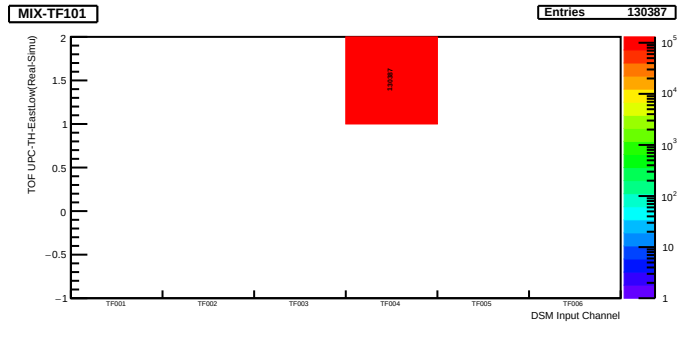

MIX-TF101
Entries 2783160
TOF UPC-TH-WestLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

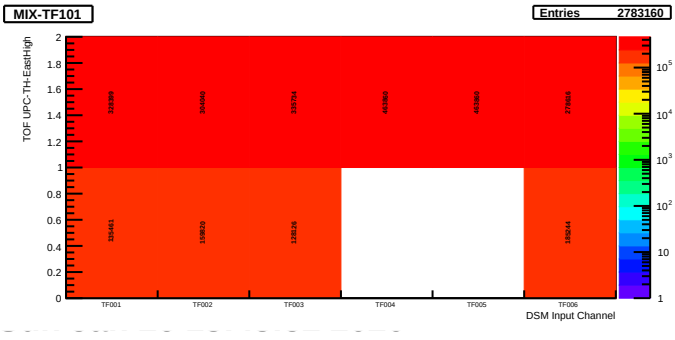

MIX-TF101
Entries 0
TOF UPC-TH-WestLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

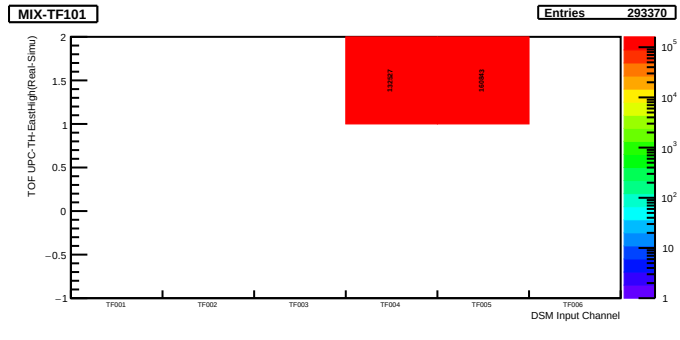

MIX-TF101
Entries 2783160
TOF UPC-TH-WestHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

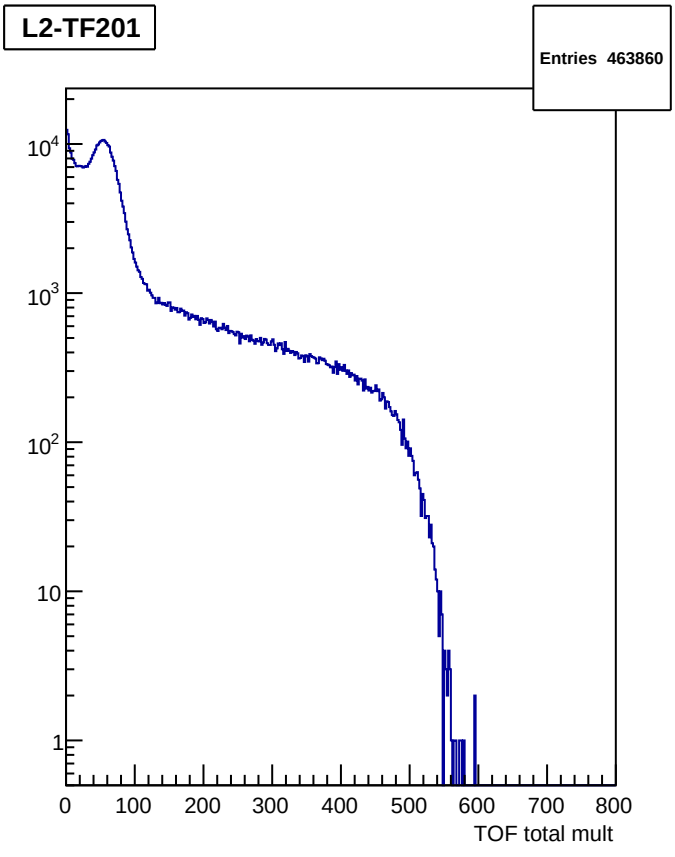

MIX-TF101
Entries 0
TOF UPC-TH-WestHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

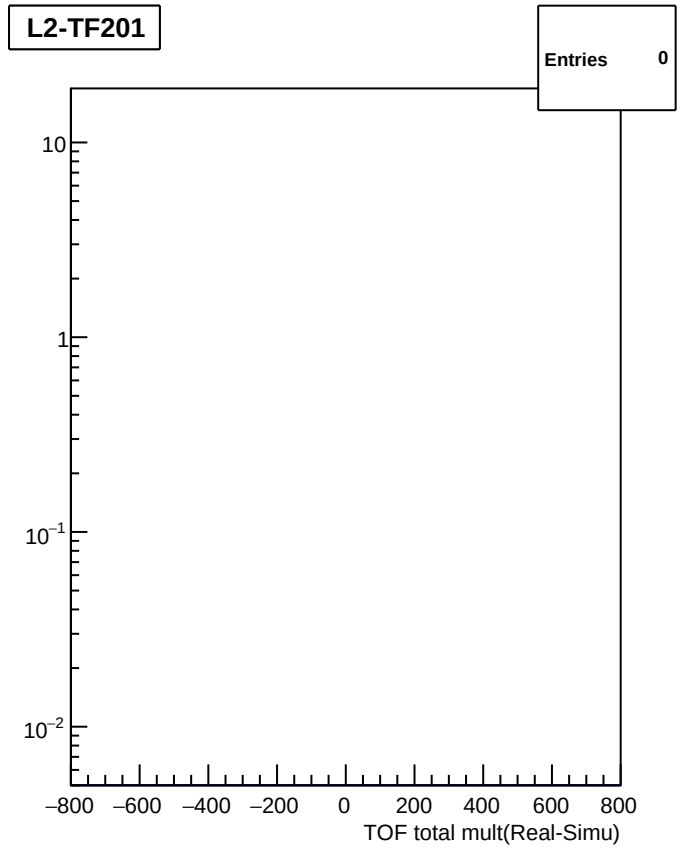

MIX-TF101
Entries 2783160
TOF UPC-TH-EastLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

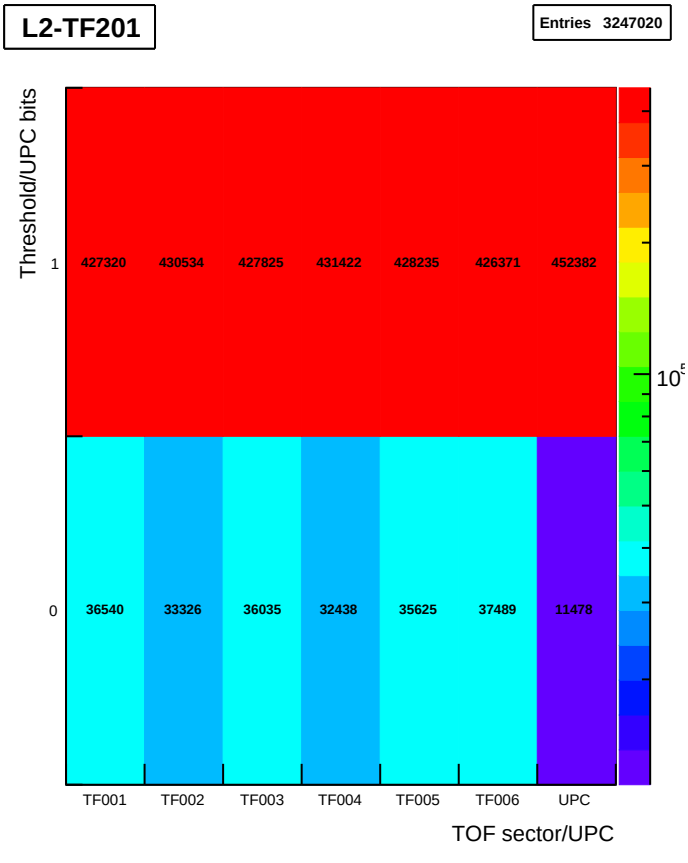

MIX-TF101
Entries 130387
TOF UPC-TH-EastLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

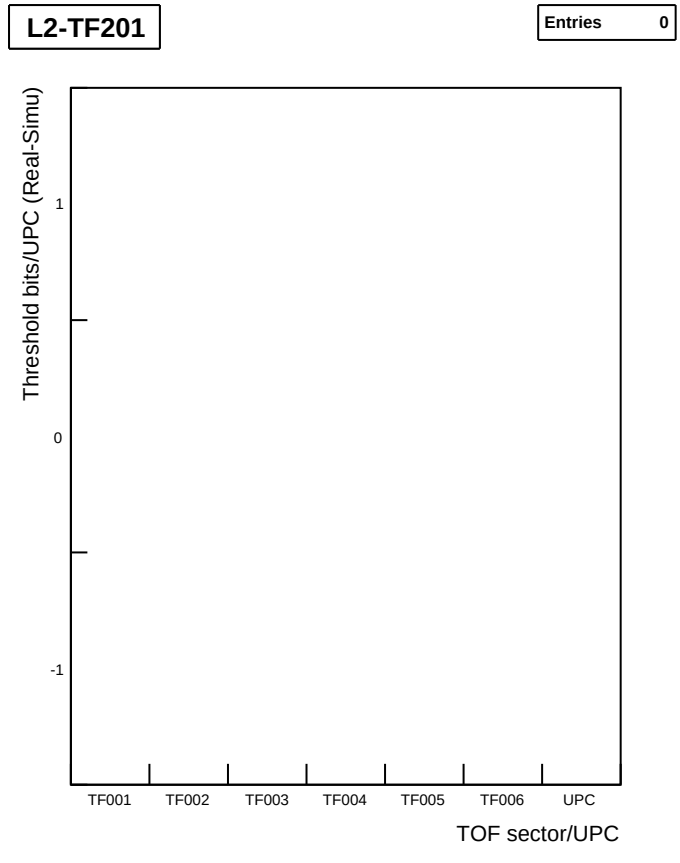

MIX-TF101
Entries 2783160
TOF UPC-TH-EastHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

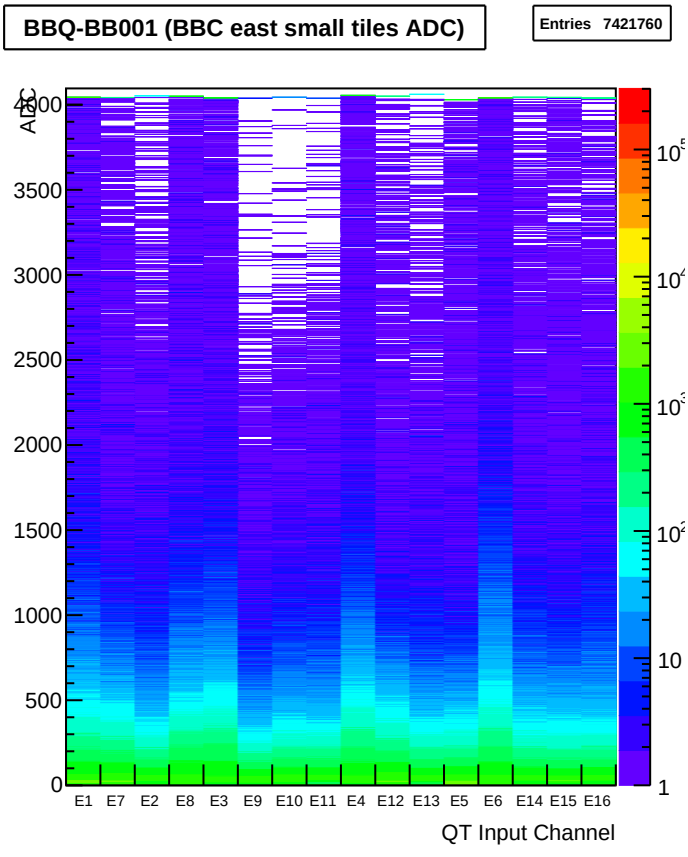

MIX-TF101
Entries 293370
TOF UPC-TH-EastHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

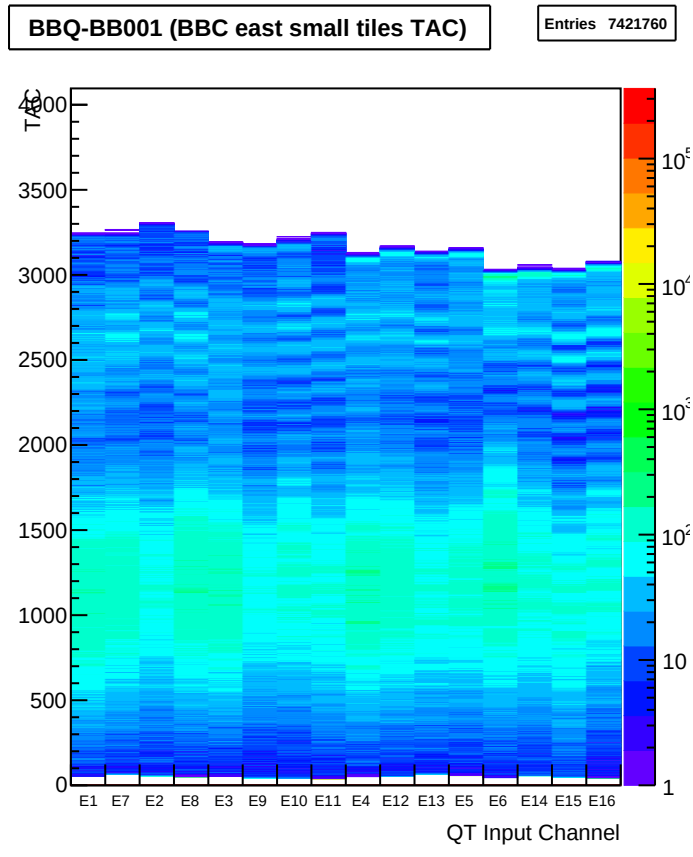

L2-TF201
Entries 463860
TOF total mult

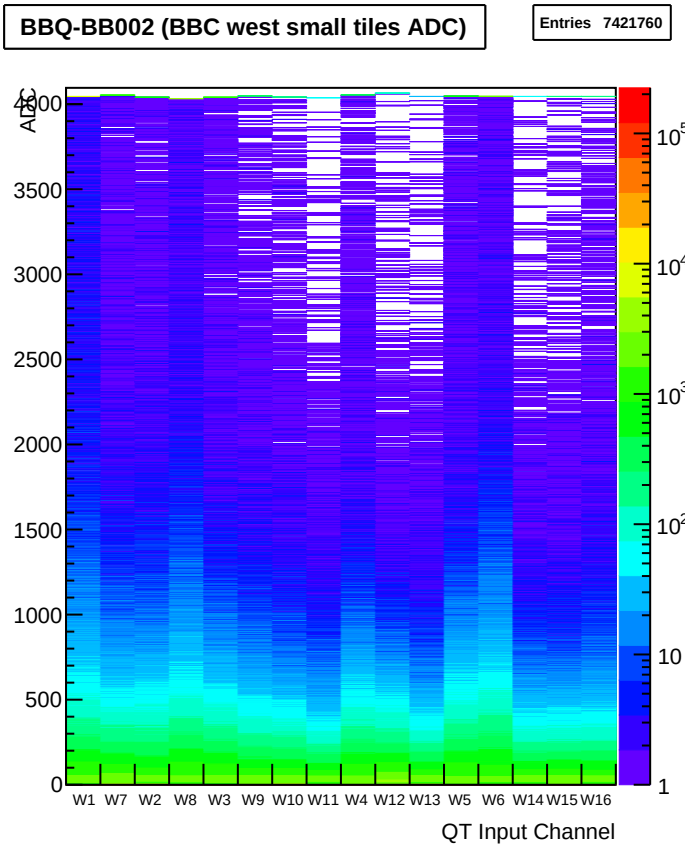

L2-TF201
Entries 0
TOF total mult(Real-Simu)

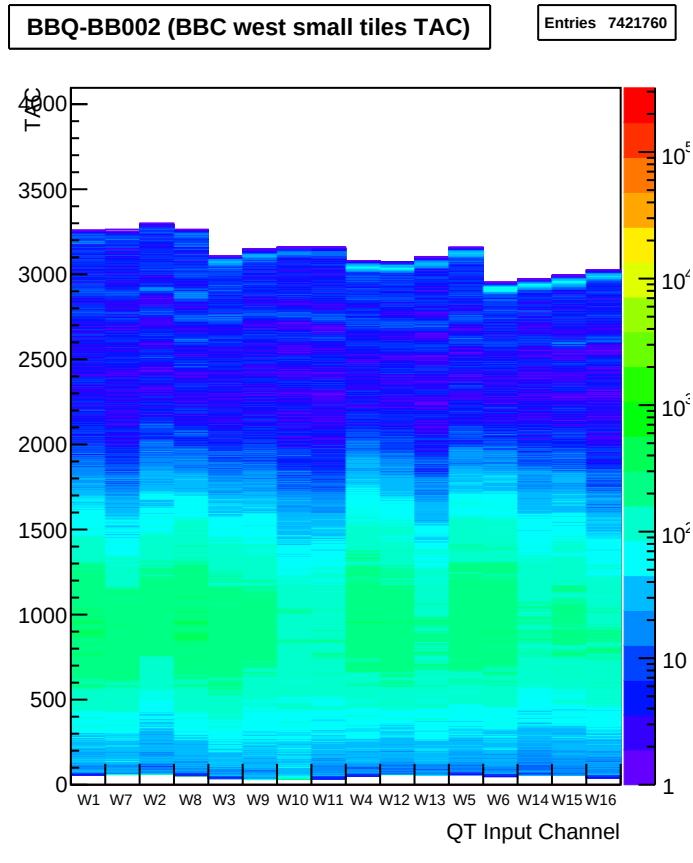

L2-TF201
Entries 3247020
Threshold/UPC bits
427320
430534
427825
431422
428235
426371
452382
36540
33326
36035
32438
35625
37489
11478
TF001
TF002
TF003
TF004
TF005
TF006
UPC
TOF sector/UPC

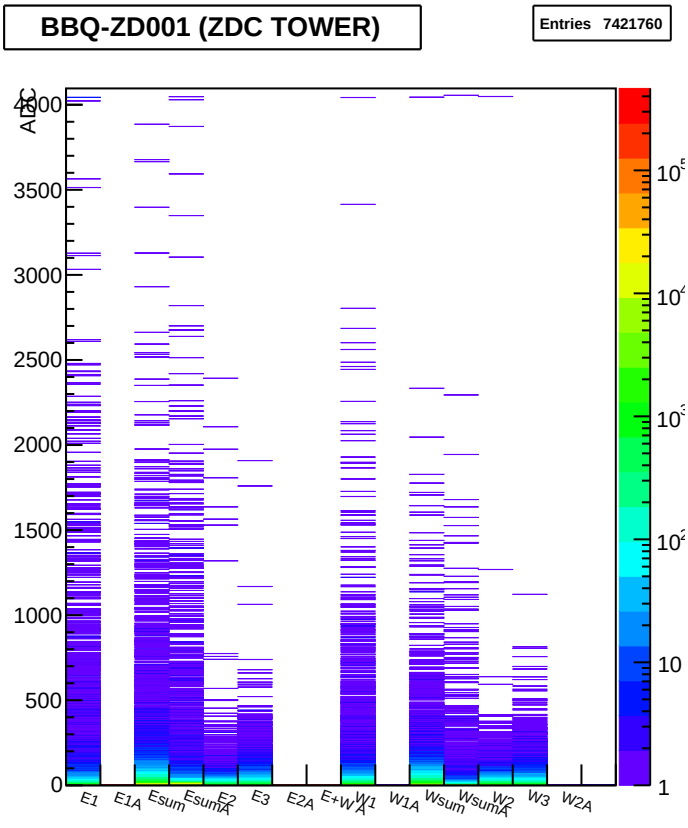

L2-TF201
Entries 0
Threshold bits/UPC (Real-Simu)
TF001
TF002
TF003
TF004
TF005
TF006
UPC
TOF sector/UPC

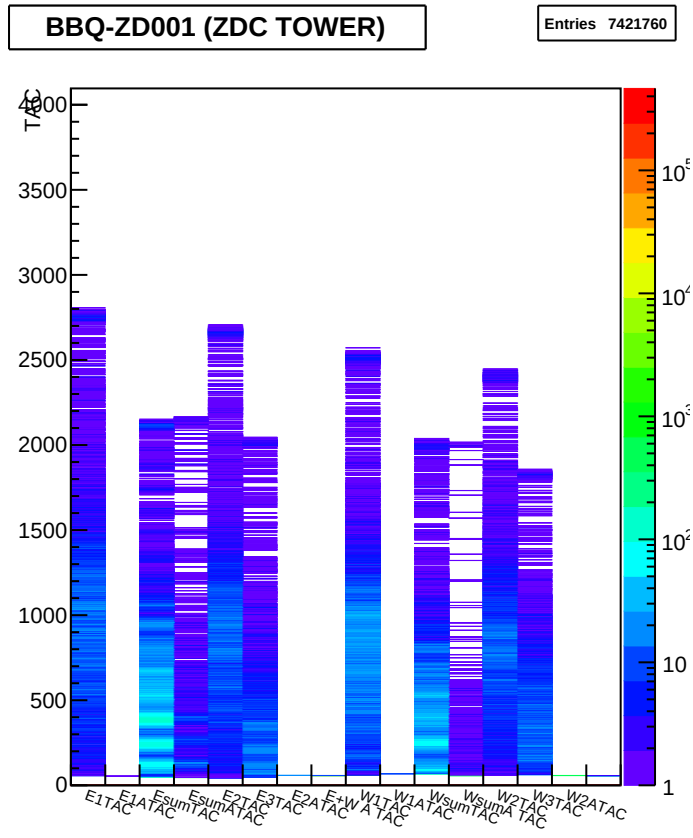

BBQ-BB001 (BBC east small tiles ADC)
Entries 7421760
ADC
QT Input Channel
E1 E7 E2 E8 E3 E9 E10 E11 E4 E12 E13 E5 E6 E14 E15 E16

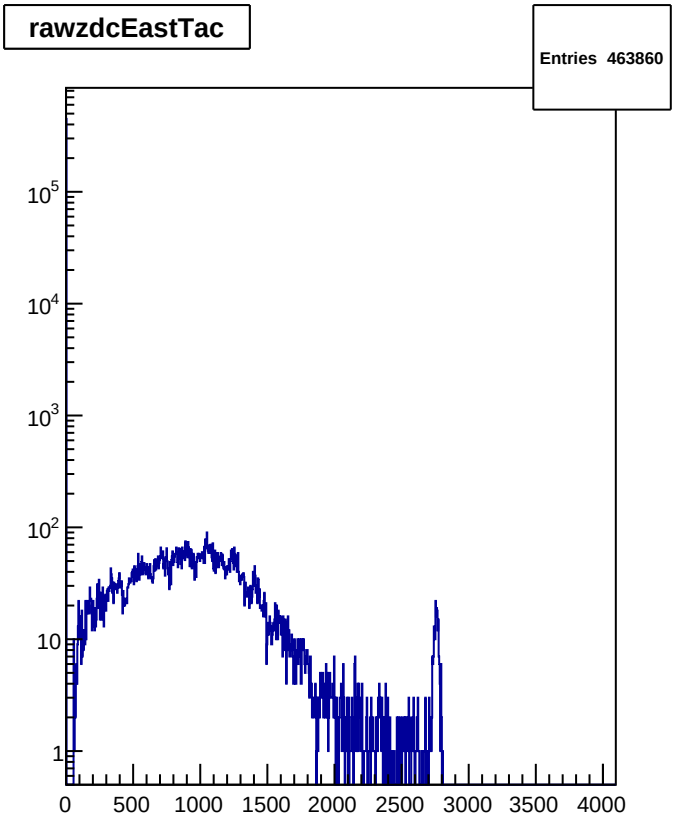

BBQ-BB001 (BBC east small tiles TAC)
Entries 7421760
TAC
QT Input Channel
E1 E7 E2 E8 E3 E9 E10 E11 E4 E12 E13 E5 E6 E14 E15 E16

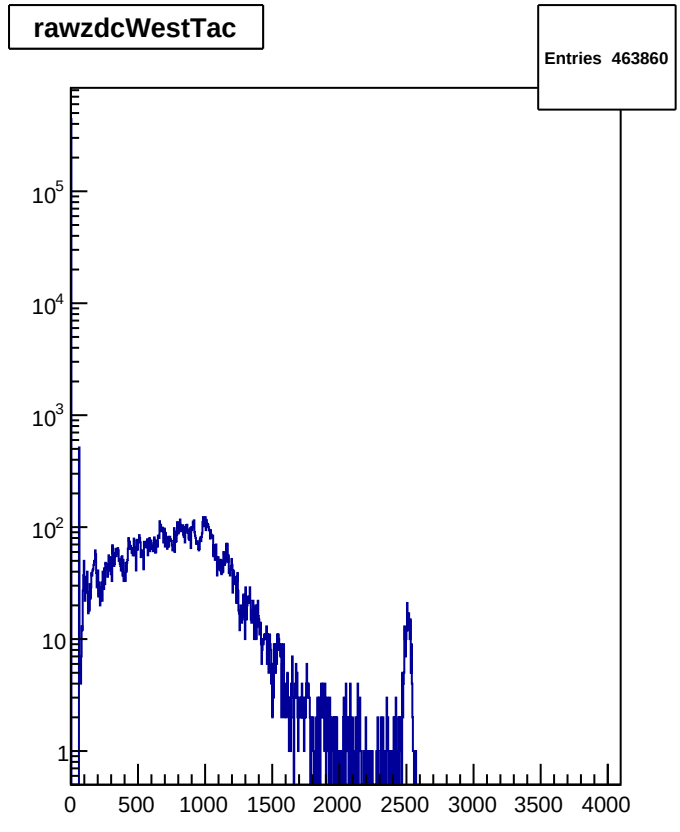

BBQ-BB002 (BBC west small tiles ADC)
Entries 7421760
ADC
QT Input Channel
W1 W7 W2 W8 W3 W9 W10 W11 W4 W12 W13 W5 W6 W14 W15 W16

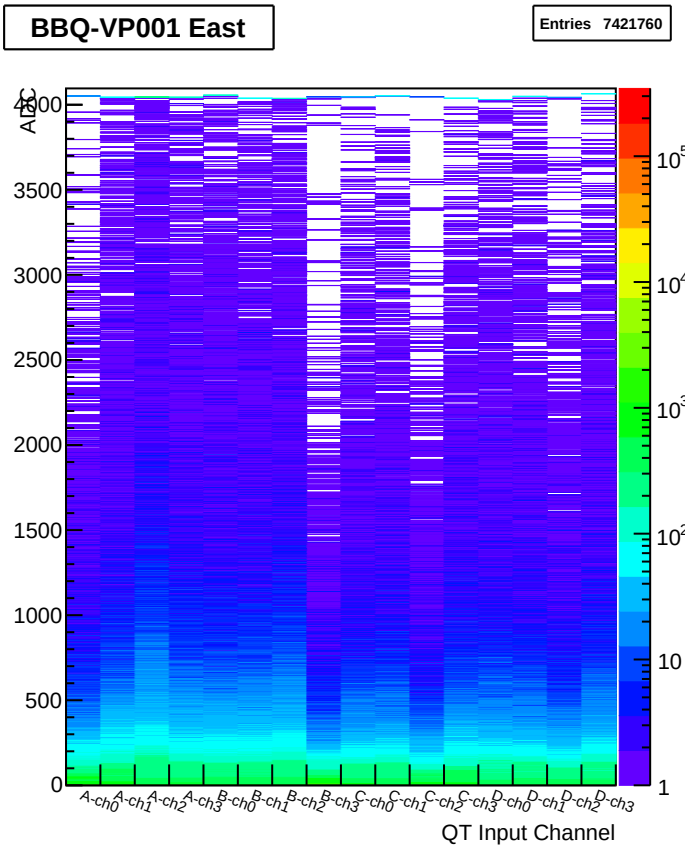

BBQ-BB002 (BBC west small tiles TAC)
Entries 7421760
TAC
QT Input Channel
W1 W7 W2 W8 W3 W9 W10 W11 W4 W12 W13 W5 W6 W14 W15 W16

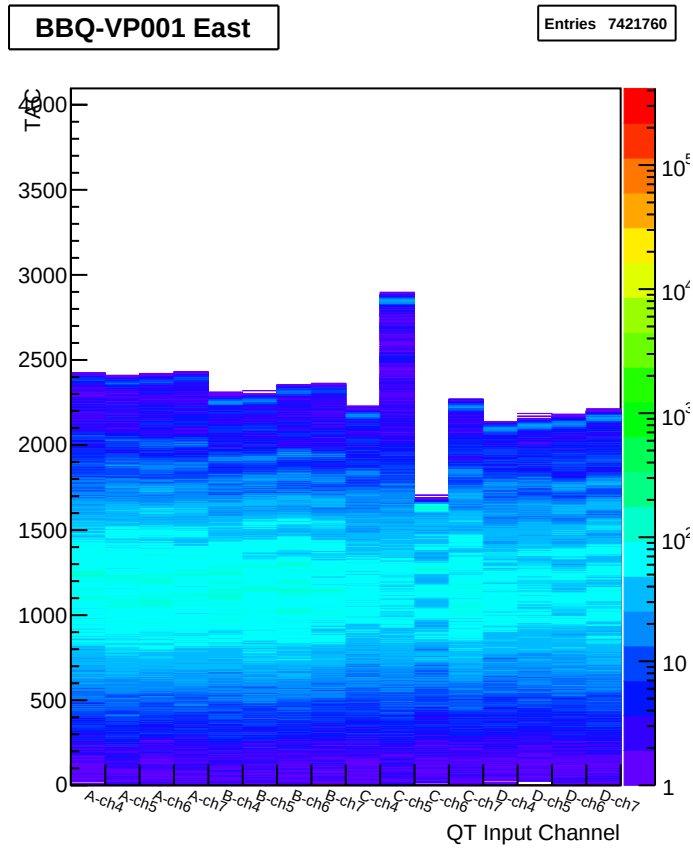

BBQ-ZD001 (ZDC TOWER)
Entries 7421760
ADC
E1
E1A
Esum
EsumA
E2
E3
E2A
E+W
W1
W1A
Wsum
WsumA
W2
W3
W2A

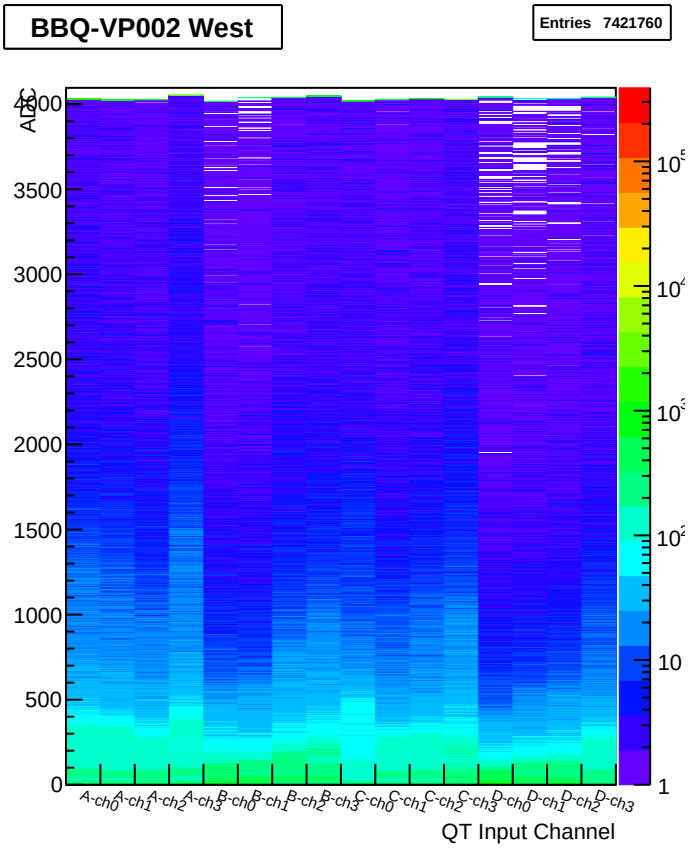

BBQ-ZD001 (ZDC TOWER)
Entries 7421760
TAC
E1TAC
E1ATAC
EsumTAC
EsumATAC
E2TAC
E3TAC
E2ATAC
E+W TAC
W1TAC
W1ATAC
WsumTAC
WsumA TAC
W2TAC
W3TAC
W2ATAC

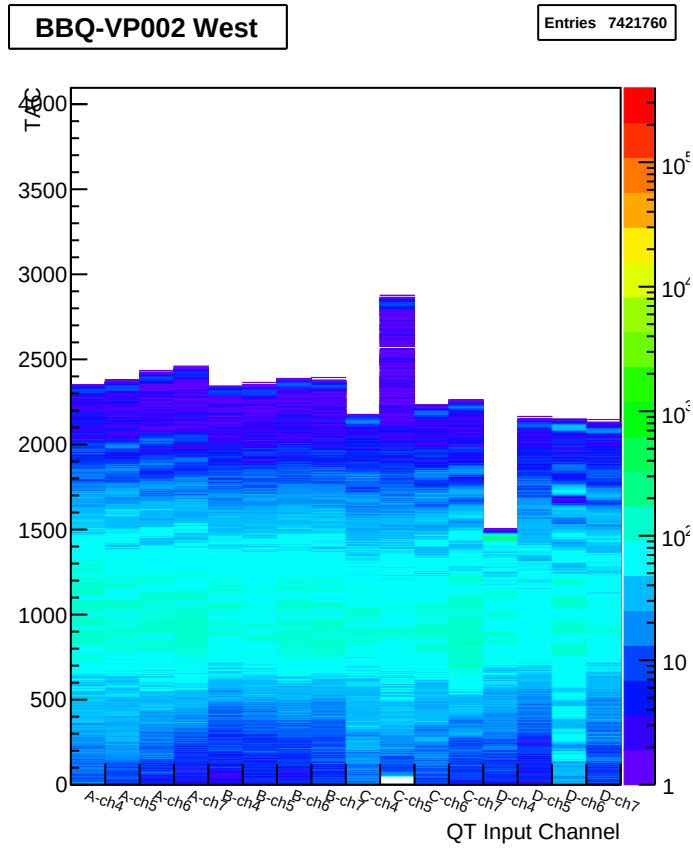

rawzdcEastTac
Entries 463860

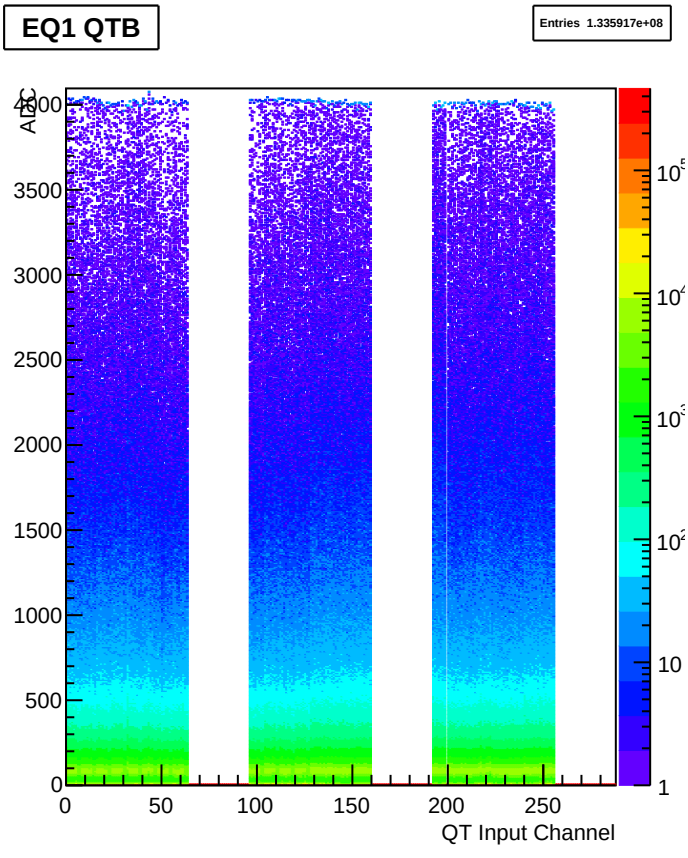

rawzdcWestTac
Entries 463860

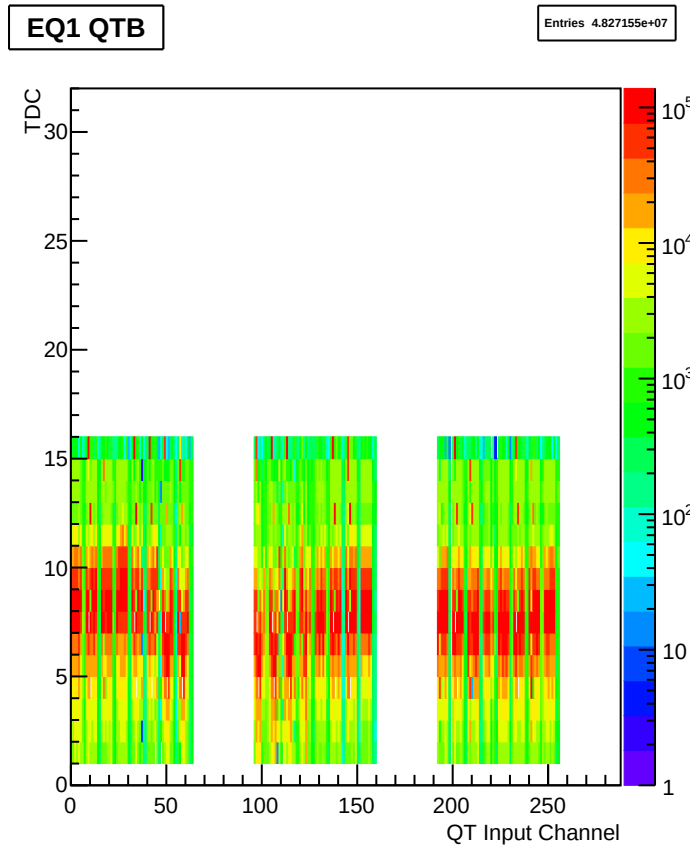

BBQ-VP001 East
Entries 7421760
ADC
QT Input Channel

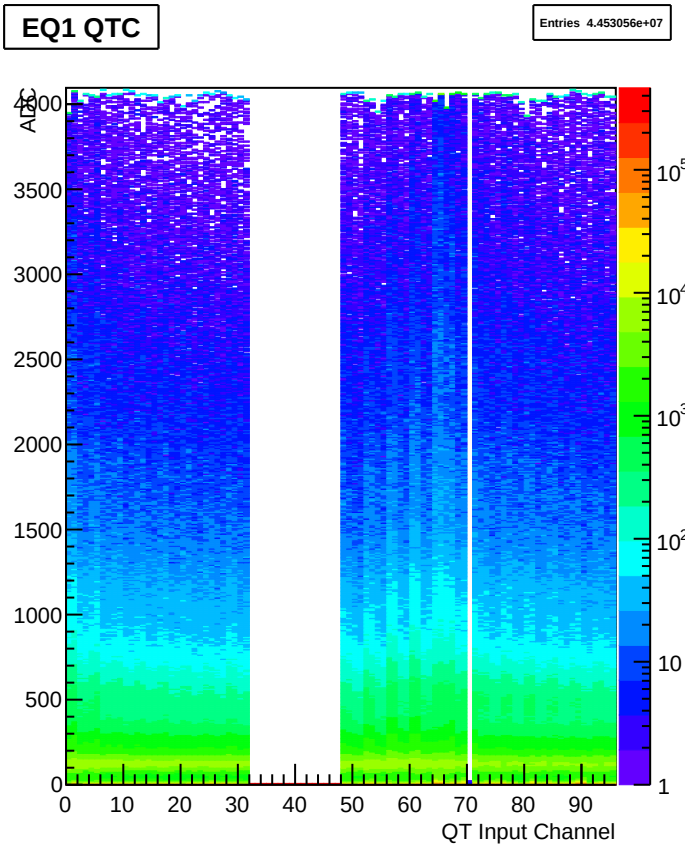

BBQ-VP001 East
Entries 7421760
ADC
QT Input Channel

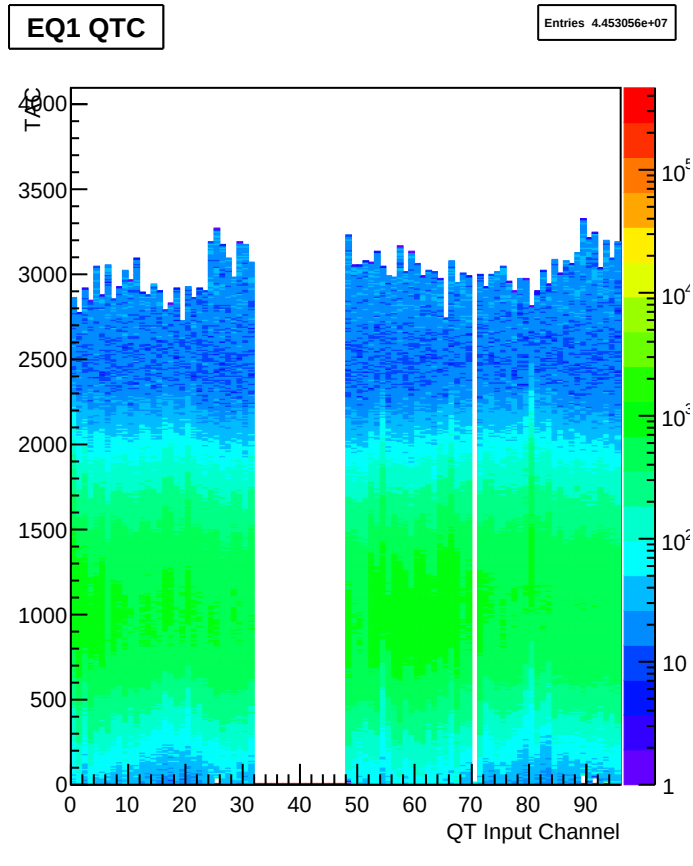

BBQ-VP002 West
Entries 7421760
ADC
QT Input Channel

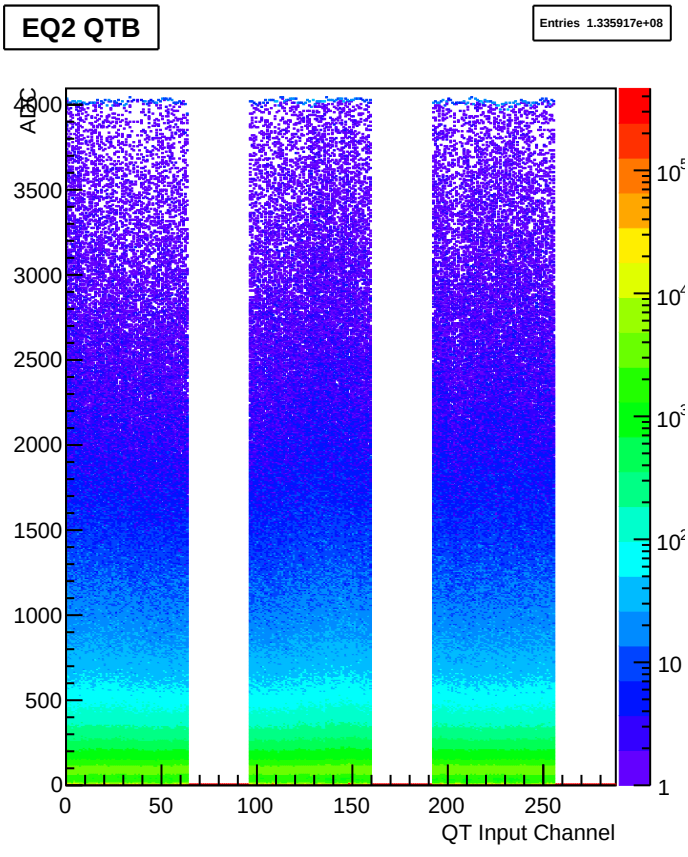

BBQ-VP002 West
Entries 7421760
ADC
QT Input Channel

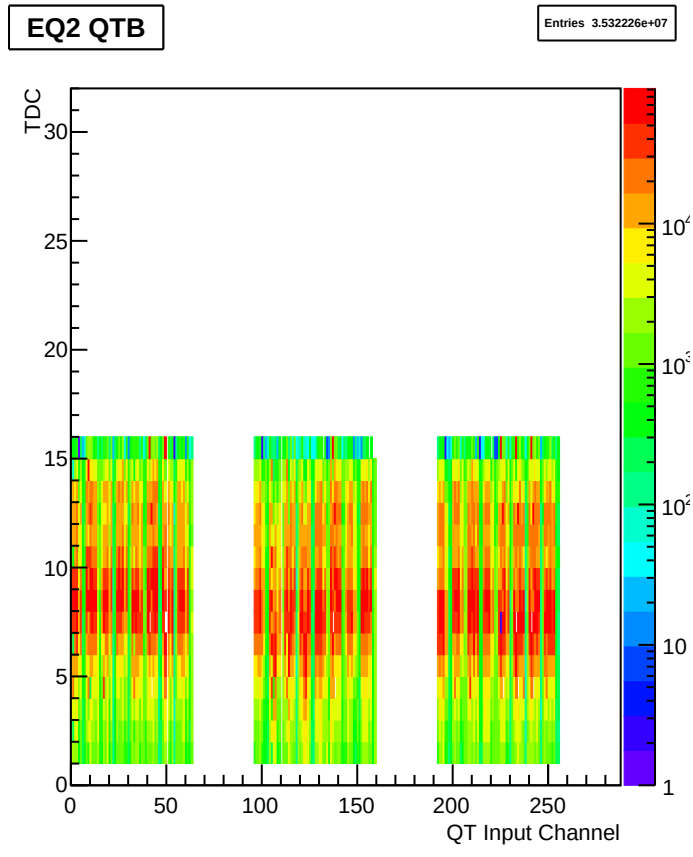

EQ1 QTB
Entries 1.335917e+08
ADC
QT Input Channel

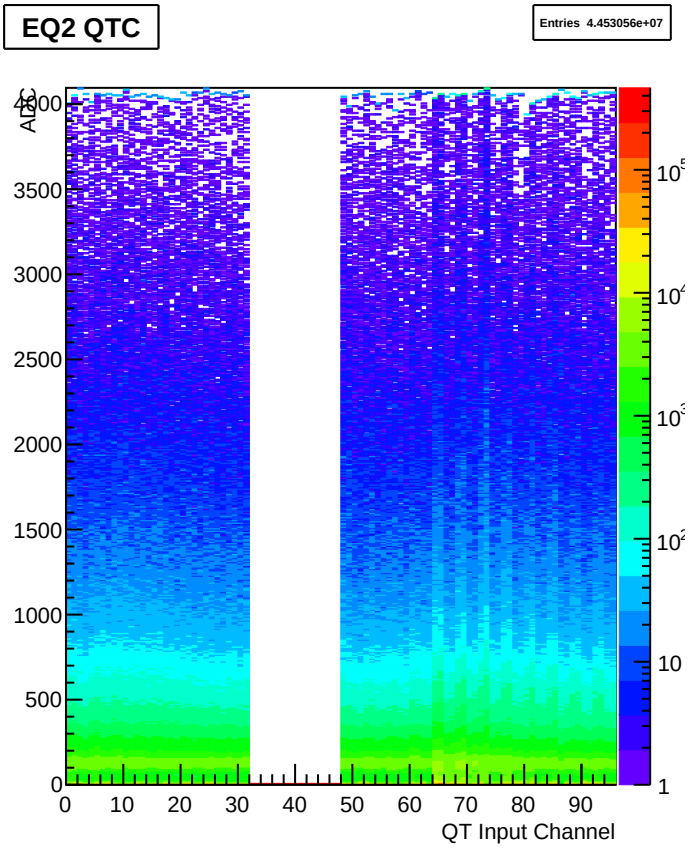

EQ1 QTB
Entries 4.827155e+07
TDC
QT Input Channel

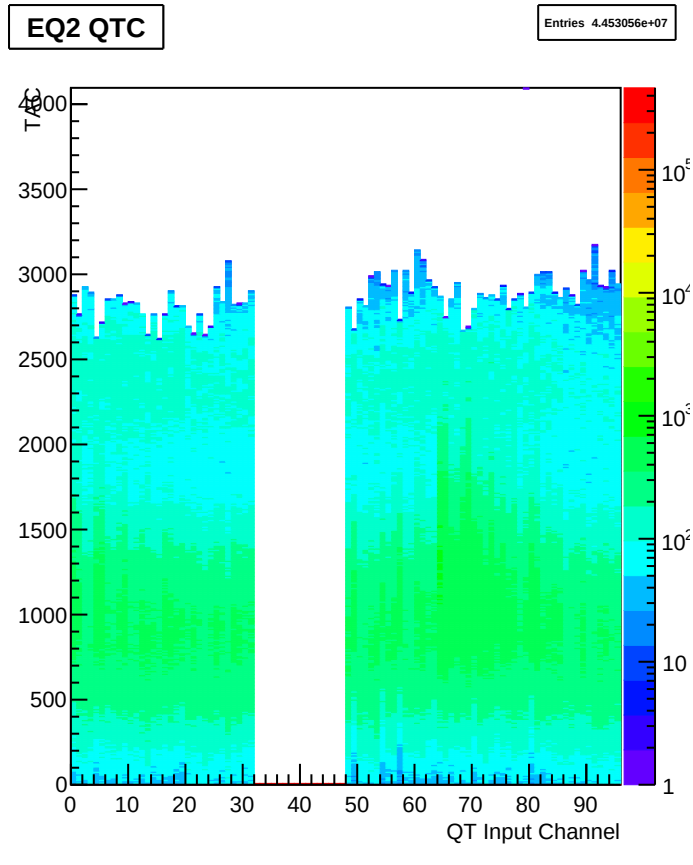

EQ1 QTC
Entries 4.453056e+07
ADC
QT Input Channel

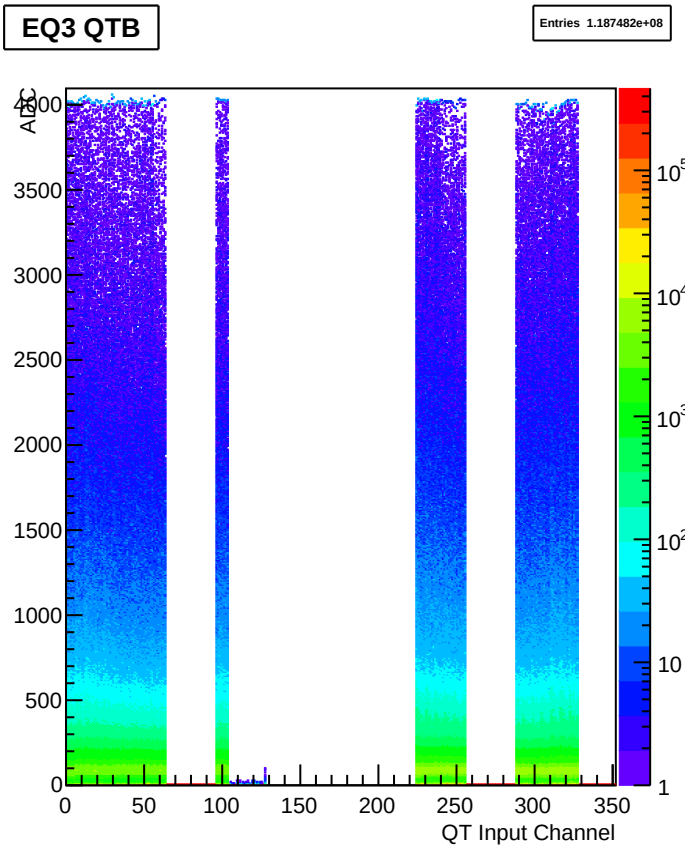

EQ1 QTC
Entries 4.453056e+07
TADC
QT Input Channel

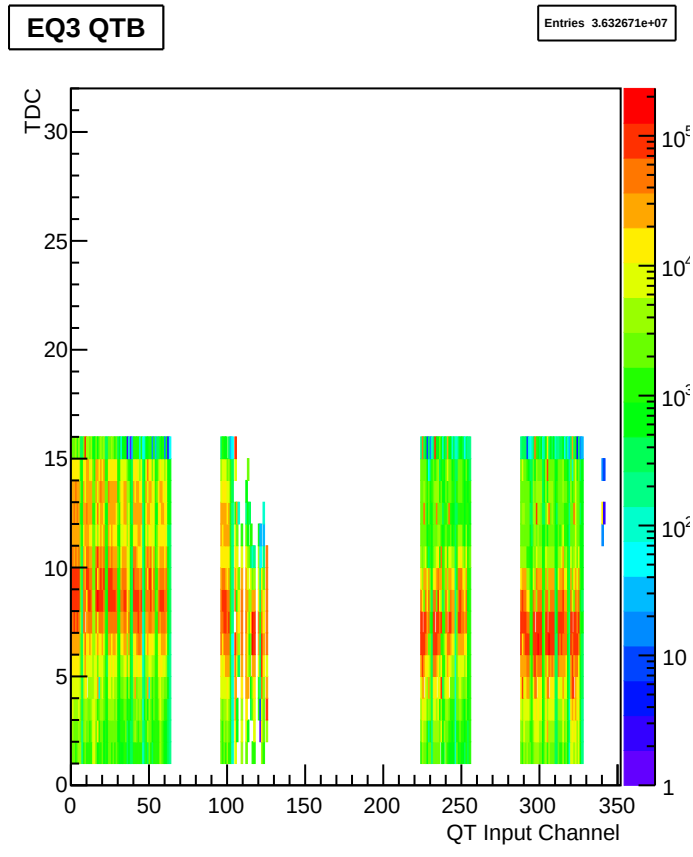

EQ2 QTB
Entries 1.335917e+08
ADC
QT Input Channel

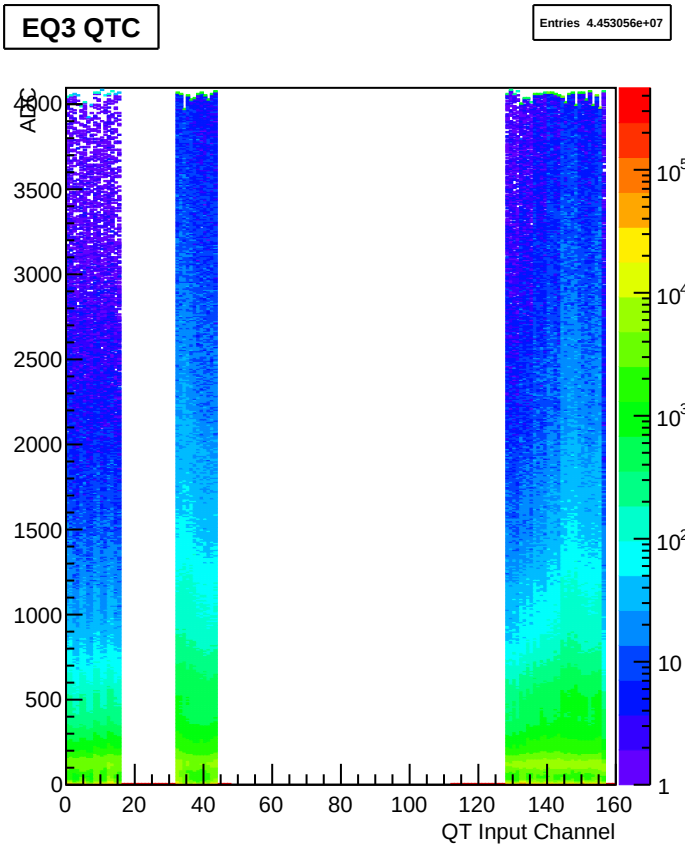

EQ2 QTB
Entries 3.532226e+07
TDC
QT Input Channel

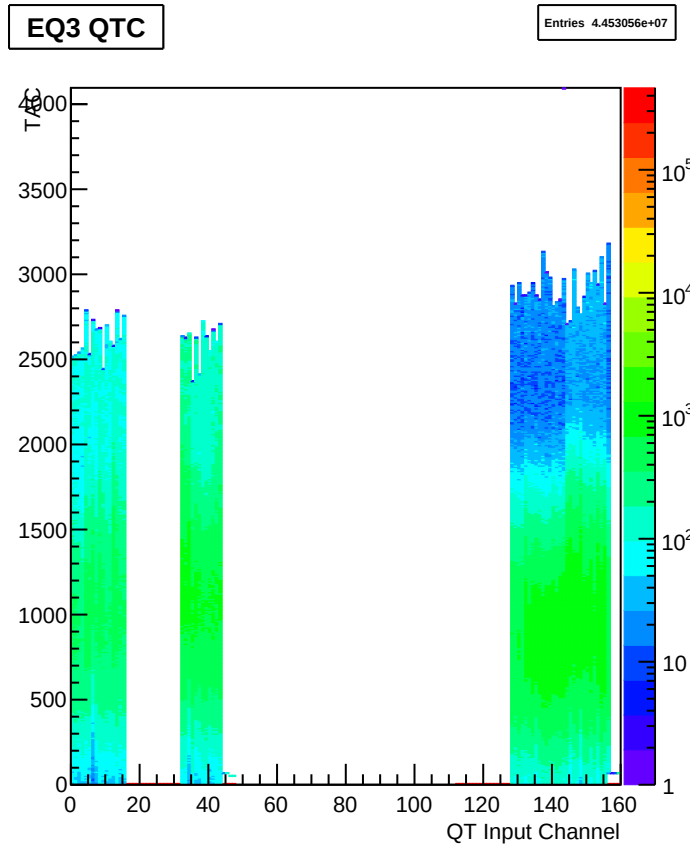

EQ2 QTC
Entries 4.453056e+07
ADC
QT Input Channel

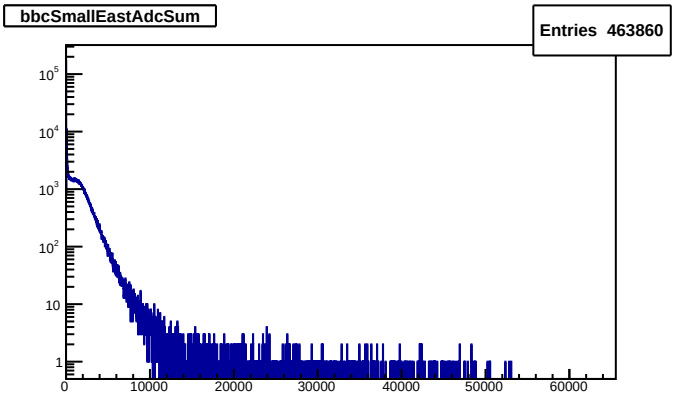

EQ2 QTC
Entries 4.453056e+07
TADC
QT Input Channel

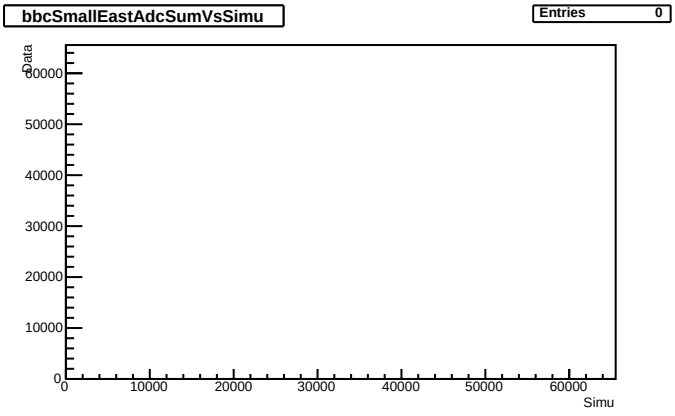

EQ3 QTB
Entries 1.187482e+08
ADC
QT Input Channel

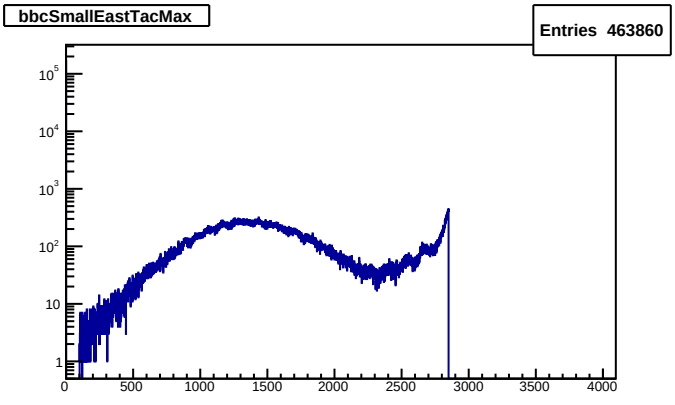

EQ3 QTB
Entries 3.632671e+07
TDC
QT Input Channel

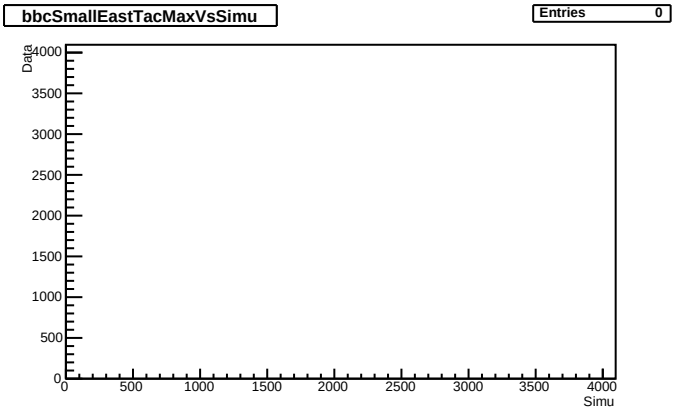

EQ3 QTC
Entries 4.453056e+07
ADC
QT Input Channel


EQ3 QTC
Entries 4.453056e+07
TADC
QT Input Channel


bbcSmallEastAdcSum
Entries 463860


bbcSmallEastAdcSumVsSimu
Entries 0
Data
Simu


bbcSmallEastTacMax
Entries 463860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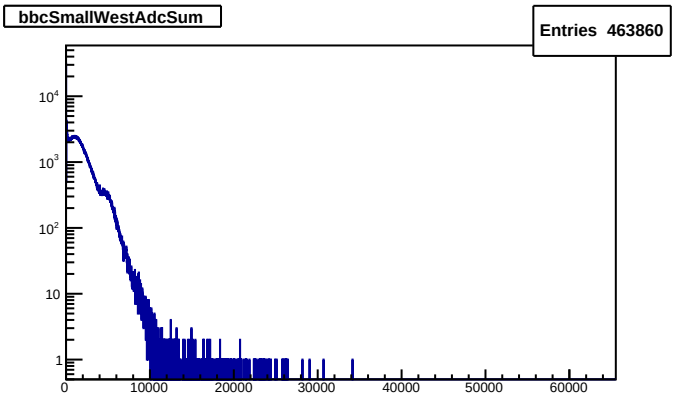
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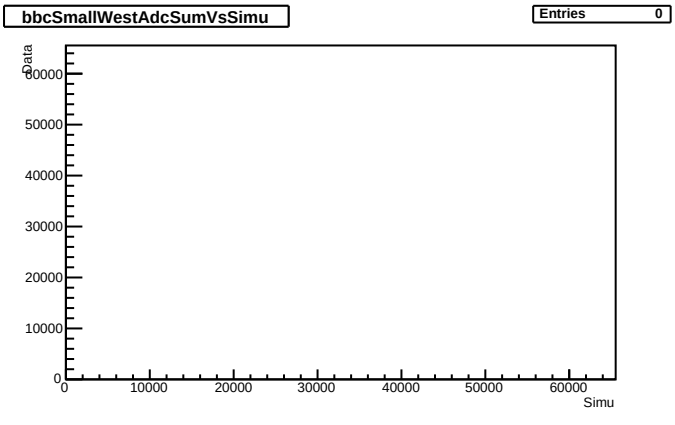
Entries 463860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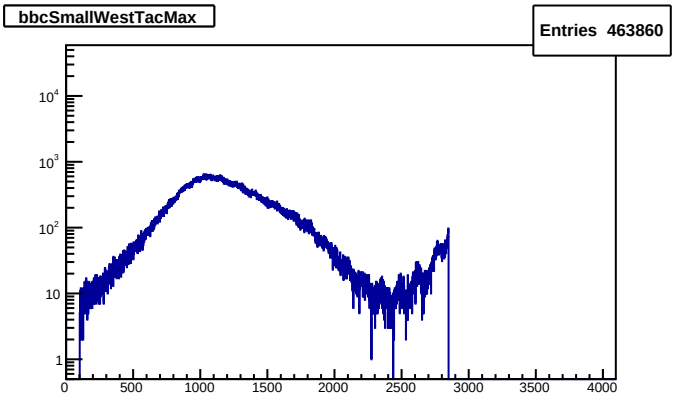
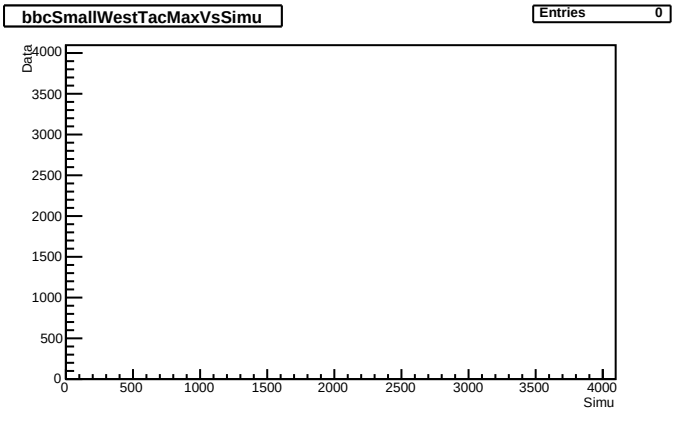
Simu


bbcSmallWestTacMax
Entries 463860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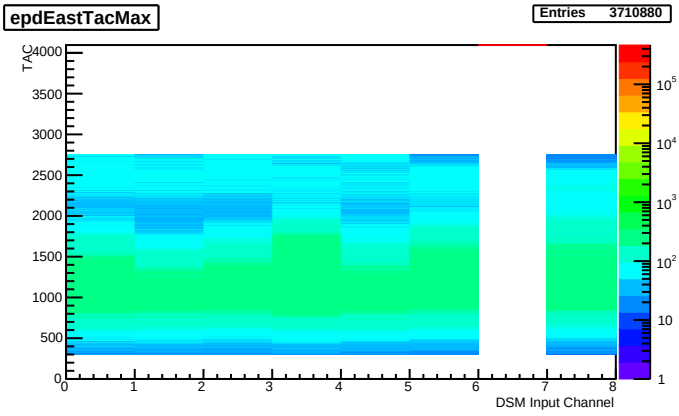
Entries 0
Data
Simu


epdEastTacMax
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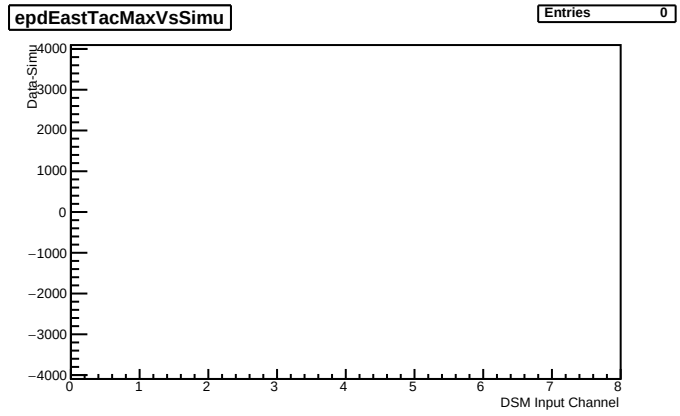
TAC
DSM Input Channel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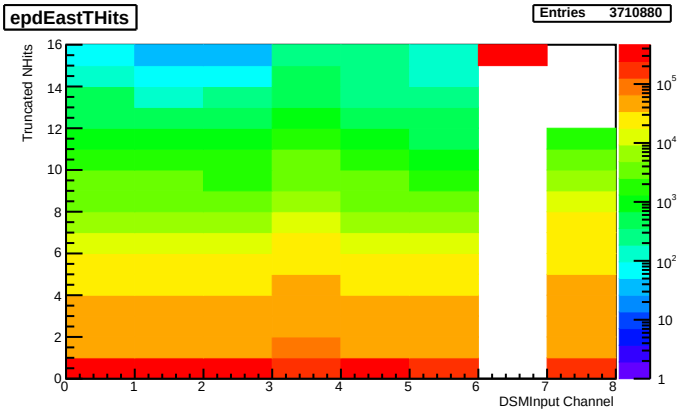
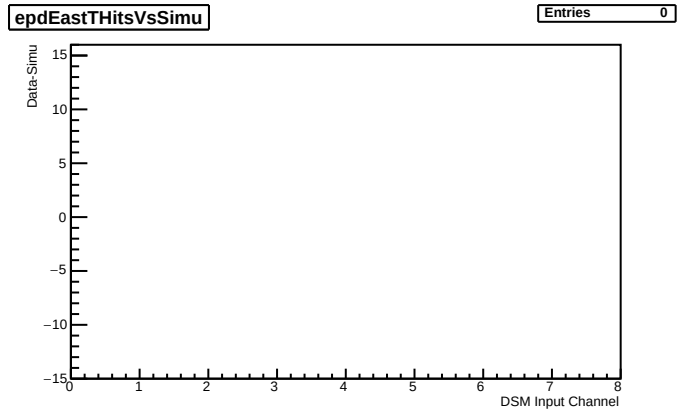
Data-Simu
DSM Input Channel


epdEastTHits
Entries 3710880
Truncated NHits
DSMInput Channel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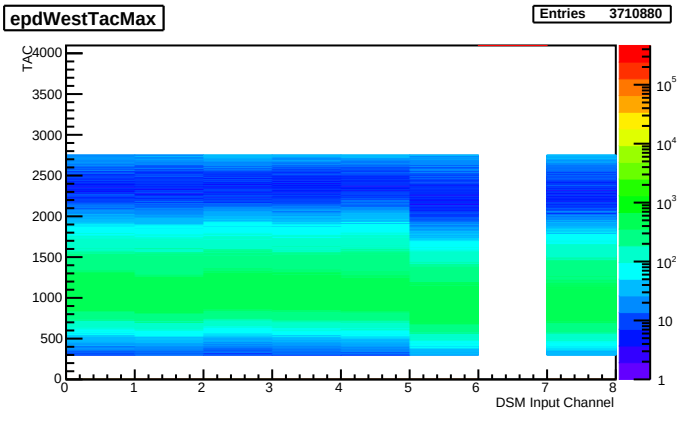
DSM Input Channel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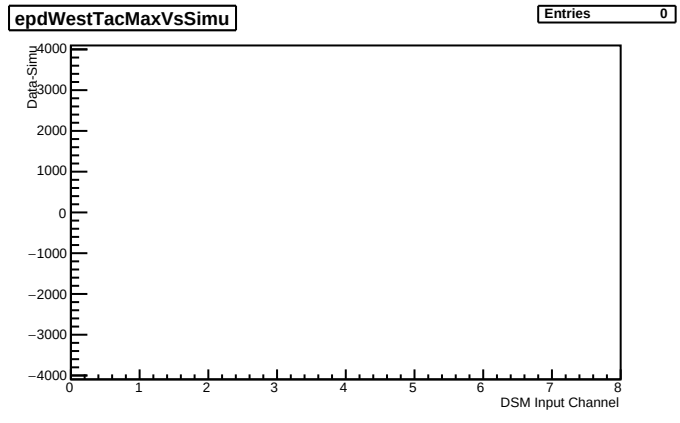
DSM Input Channel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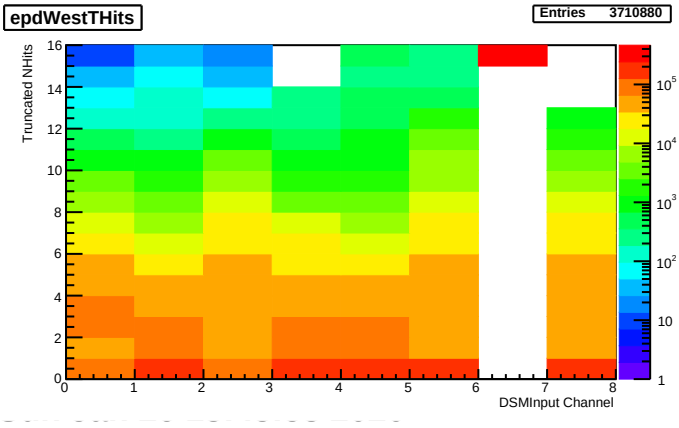
DSM Input Channel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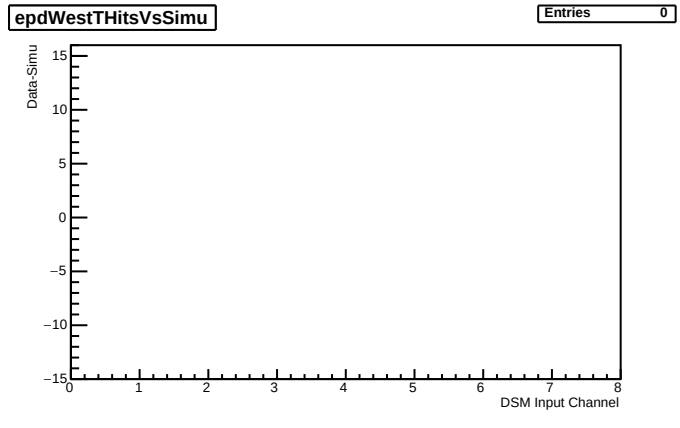
Entries 3710880
Truncated NHits
DSMInput Channel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Entries 0
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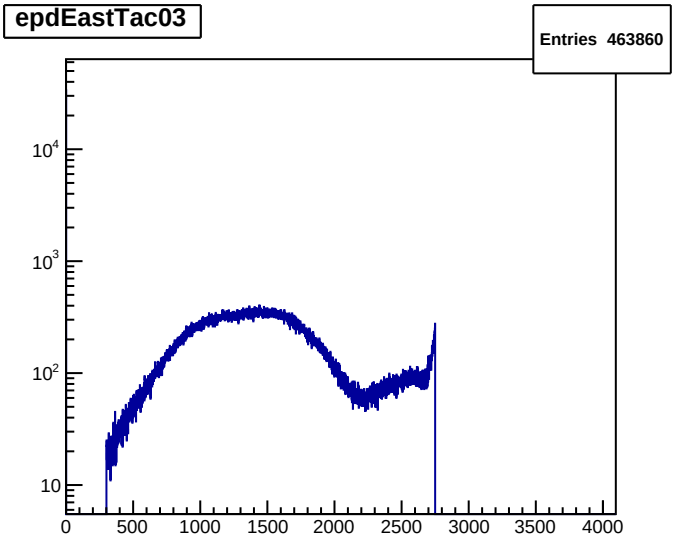
DSM Input Channel


epdEastTac03
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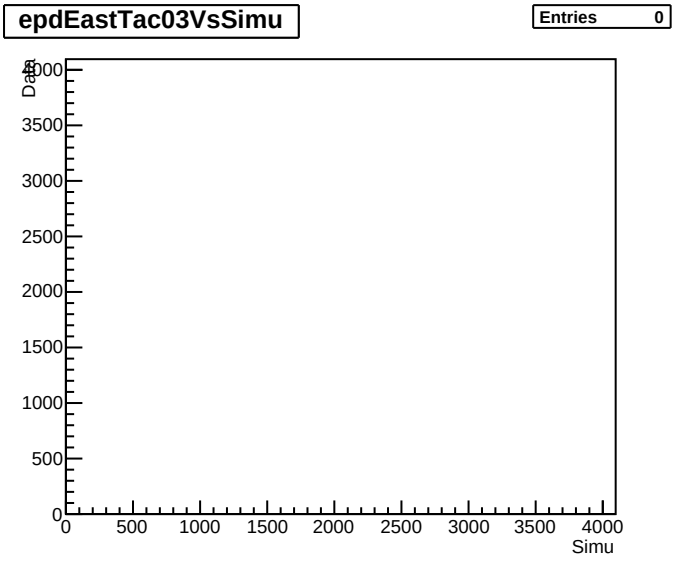
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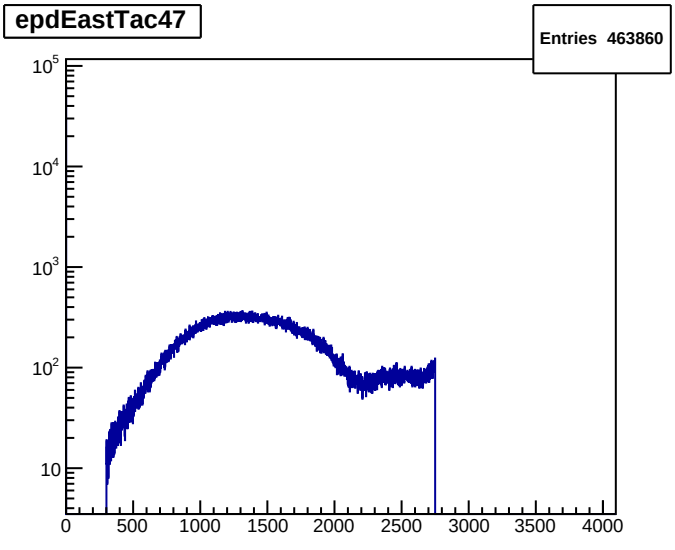
Entries 0
Data
Simu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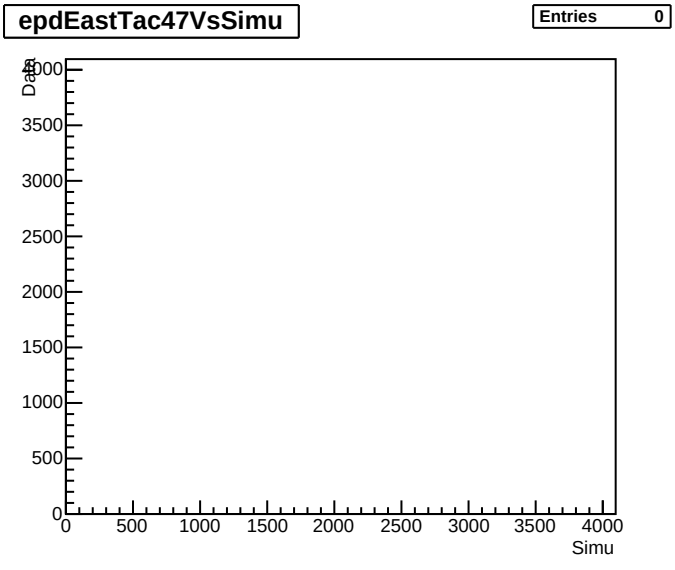
Entries 463860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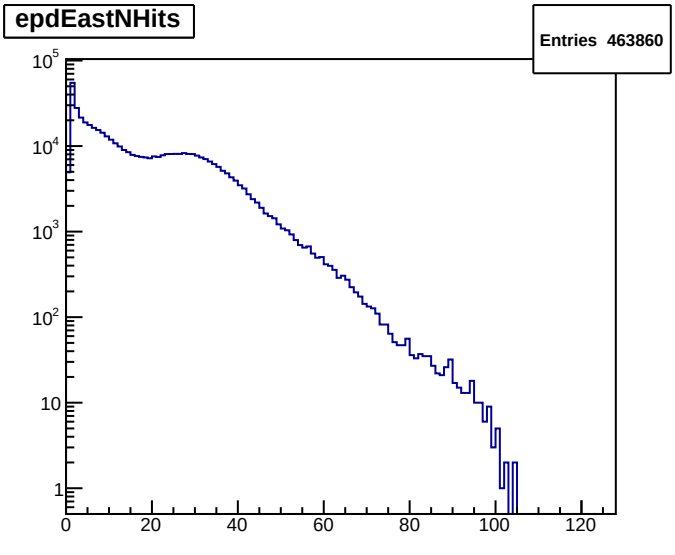
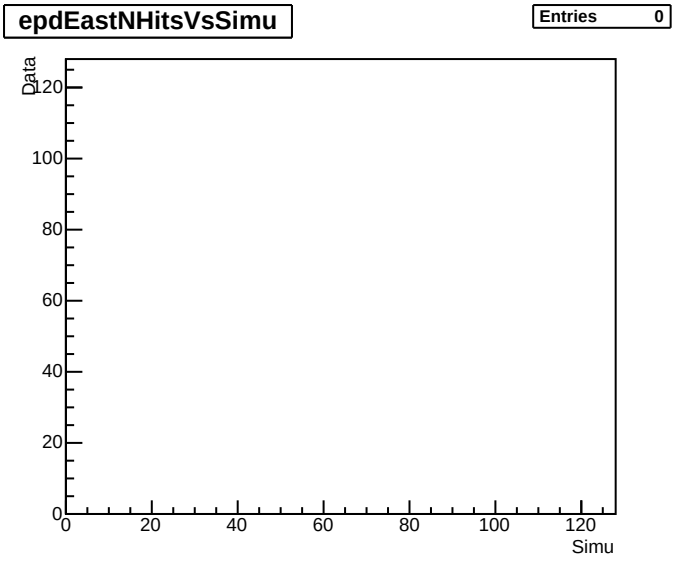
Entries 0
Data
Simu


epdEastNHits
Entries 463860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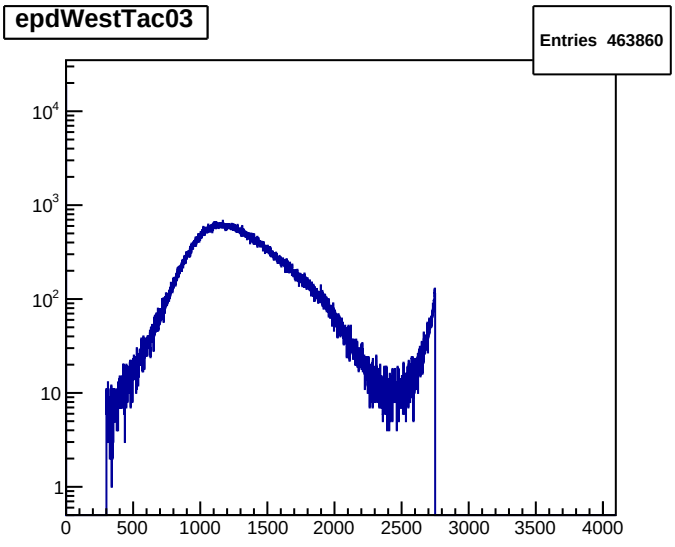
Data
Simu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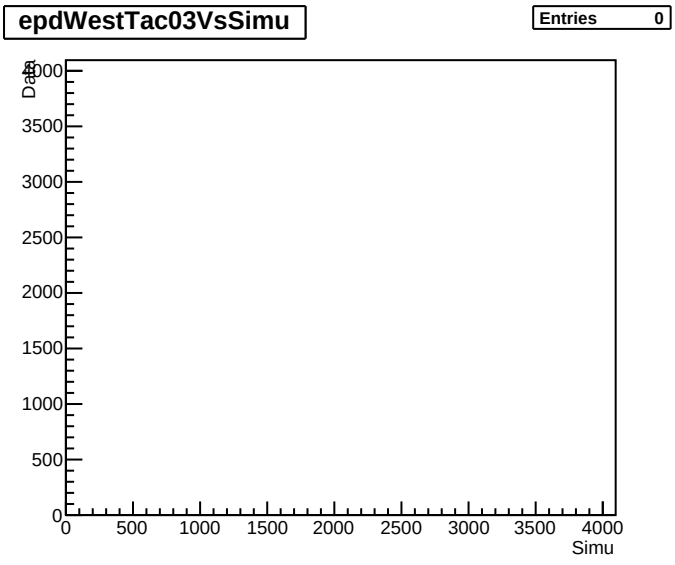
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Entries 0
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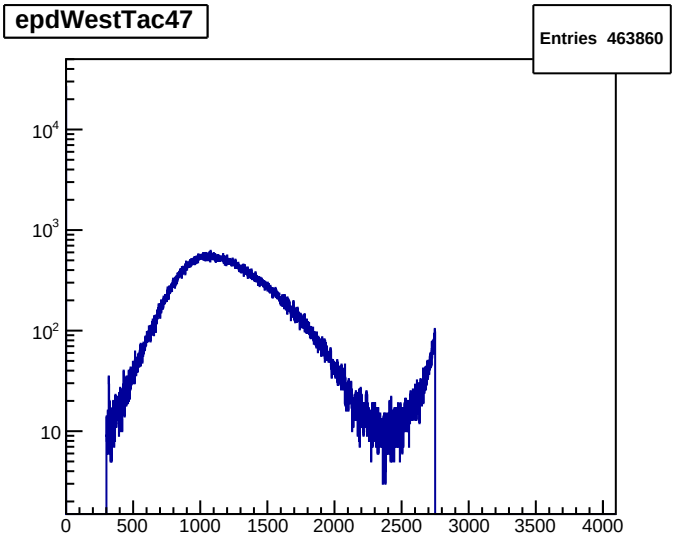
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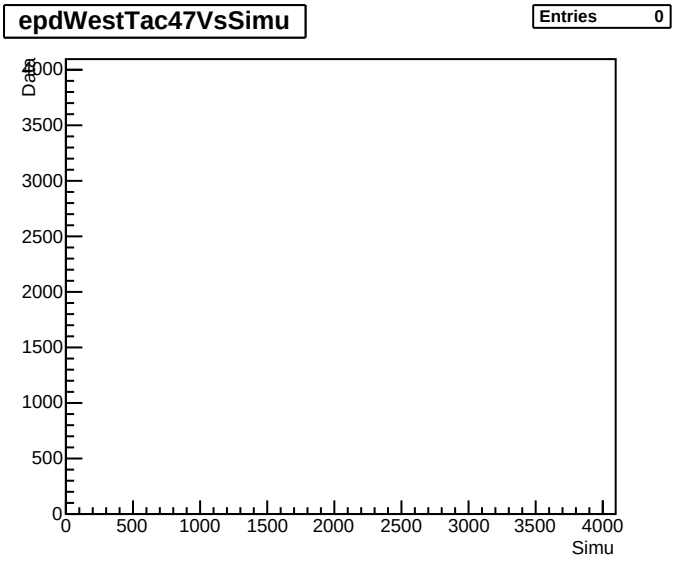
Entries 463860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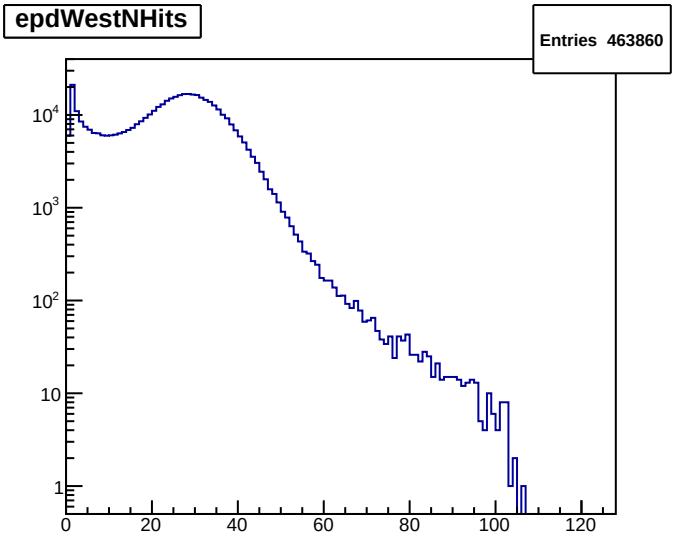
Data
Simu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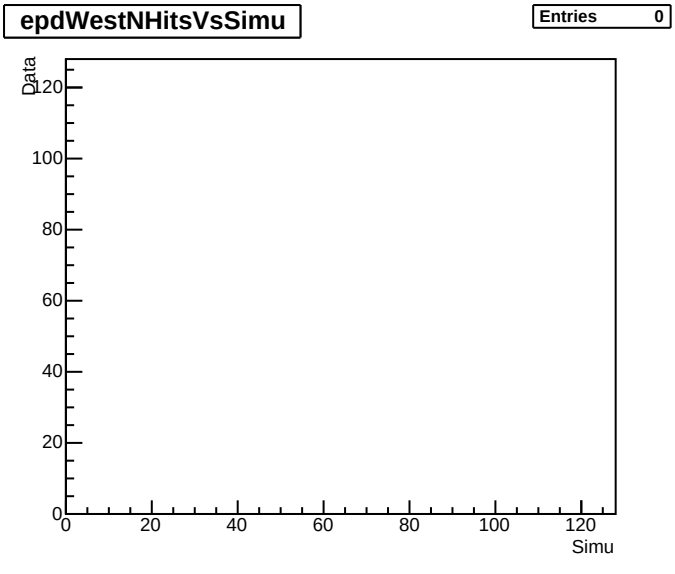
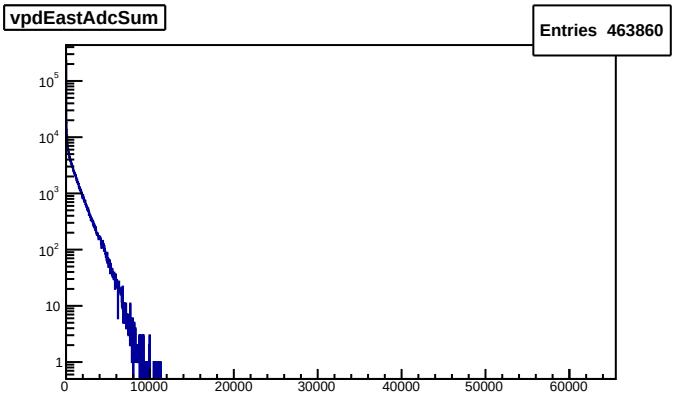
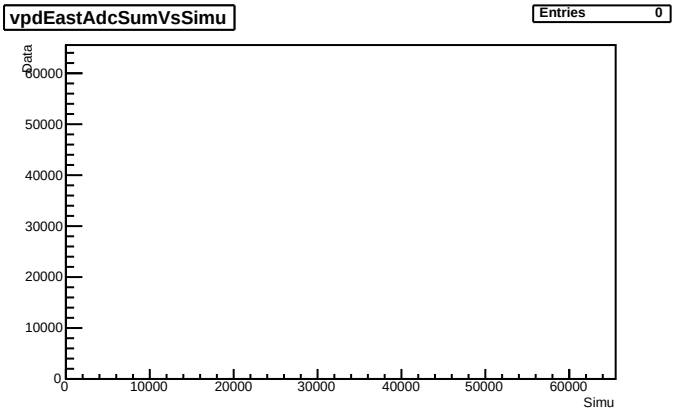
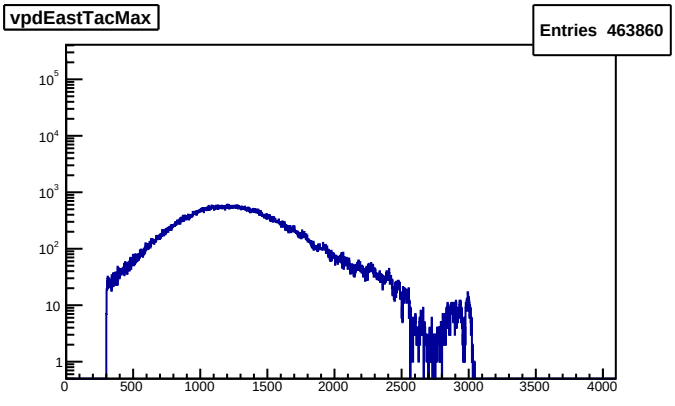
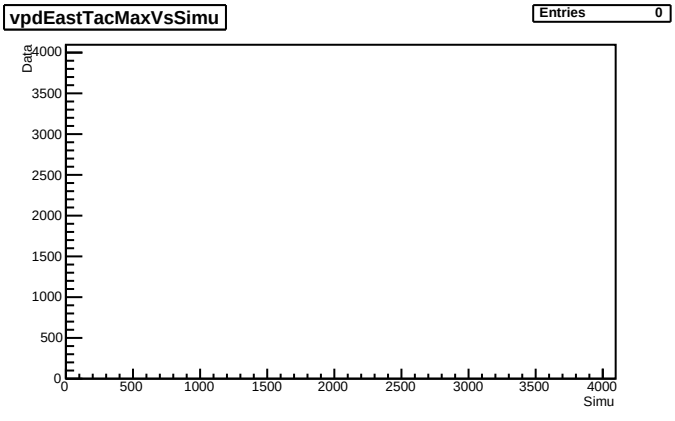
Entries 463860


epdWestNHitsVsSimu
Entries 0
Data
Simu


vpdEastAdcSum
Entries 463860


vpdEastAdcSumVsSimu
Entries 0
Data
Simu


vpdEastTacMax
Entries 463860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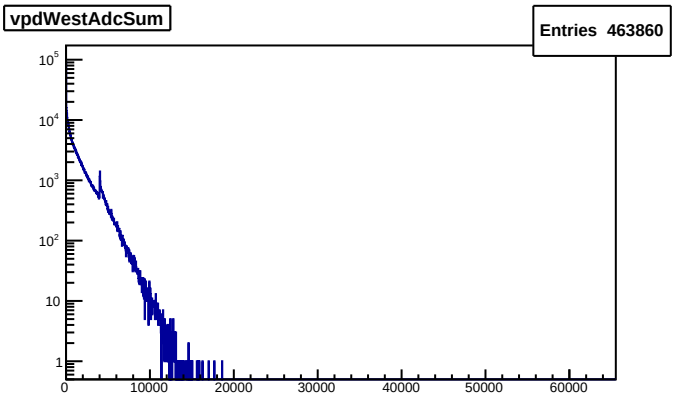
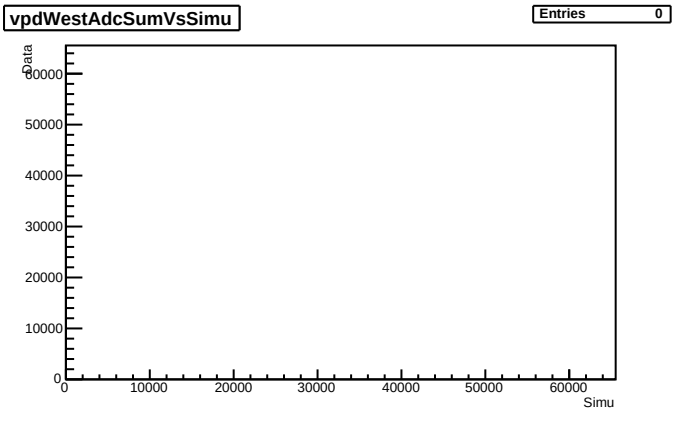
Entries 0
Data
Simu


vpdWestAdcSum
Entries 463860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Entries 0
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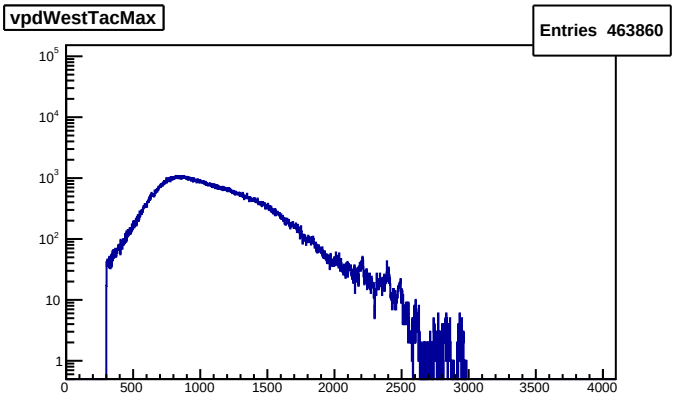
Simu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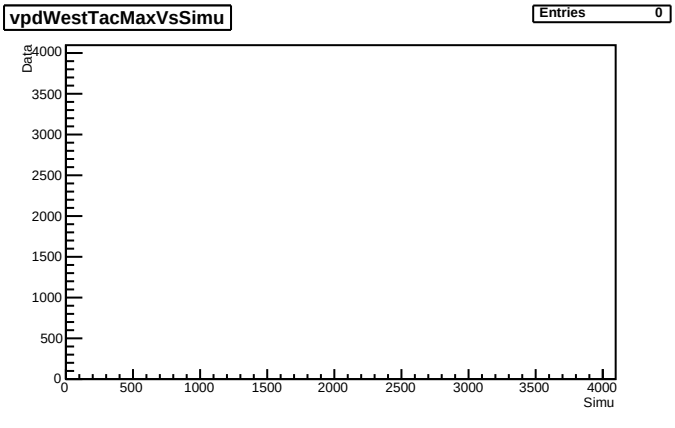
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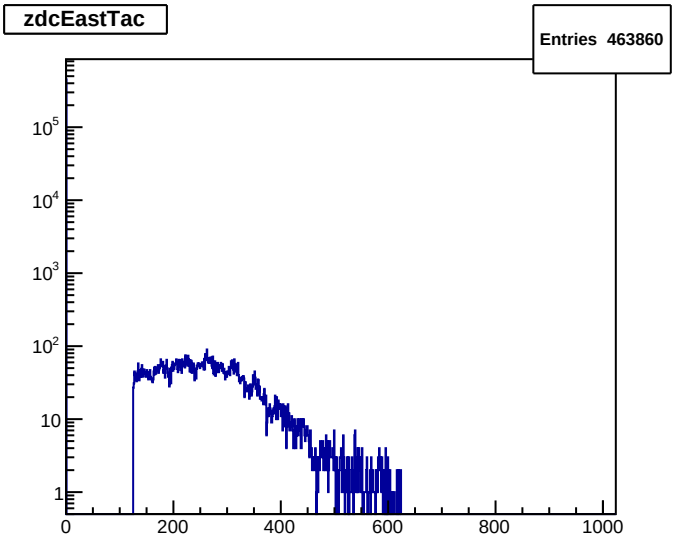
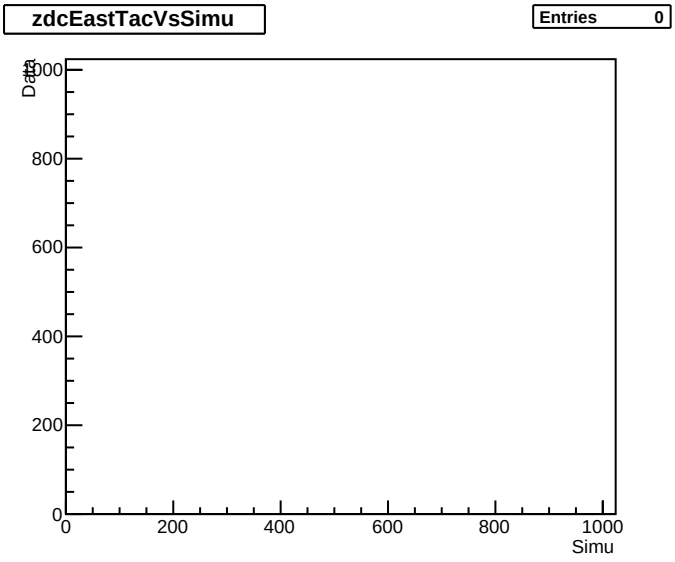
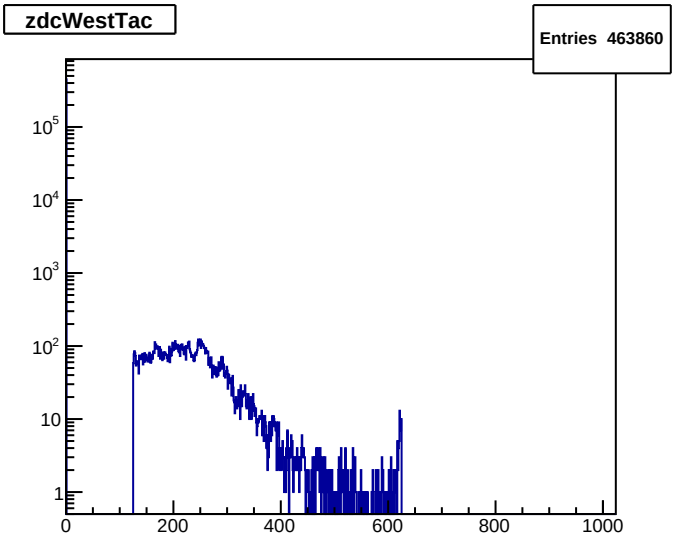
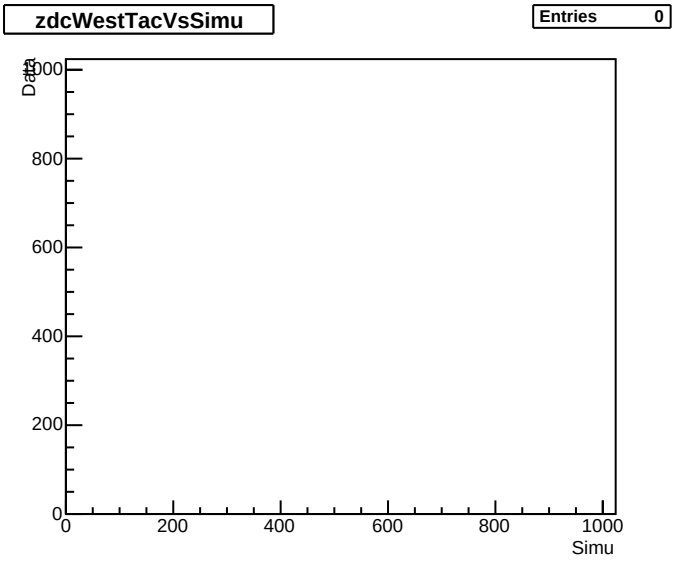
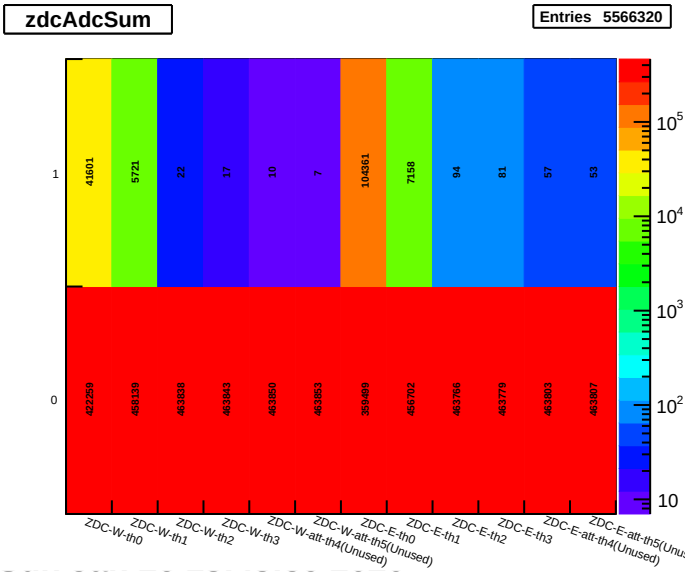
Entries 0
Data
Simu


zdcEastTac
Entries 463860
10^5
10^4
10^3
10^2
10
1
0
200
400
600
800
1000


zdcEastTacVsSimu
Entries 0
Data
1000
800
600
400
200
0
0
200
400
600
800
1000
Simu


zdcWestTac
Entries 463860
10^5
10^4
10^3
10^2
10
1
0
200
400
600
800
1000


zdcWestTacVsSimu
Entries 0
Data
1000
800
600
400
200
0
0
200
400
600
800
1000
Simu


zdcAdcSum
Entries 5566320
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41601
5721
22
17
10
7
104361
7158
94
81
57
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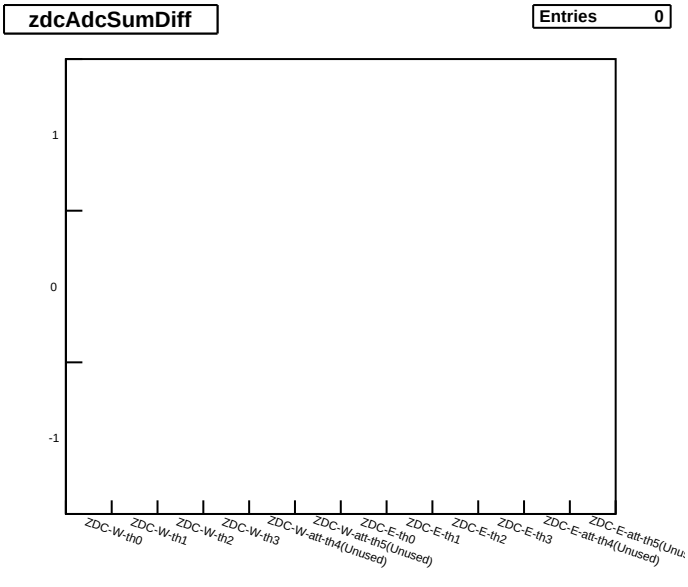
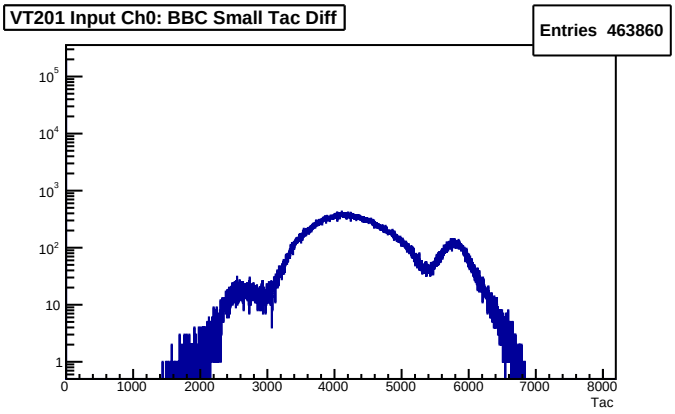
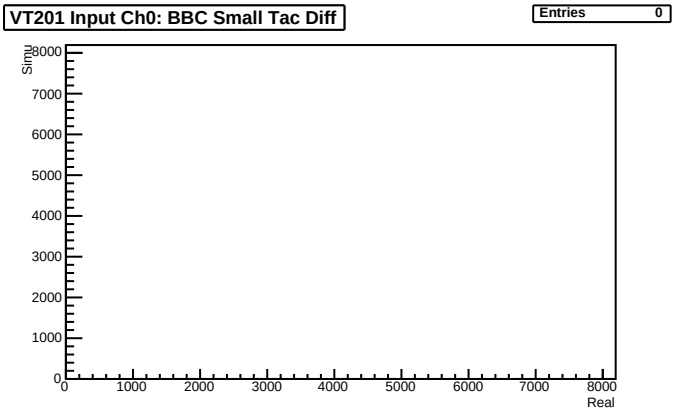
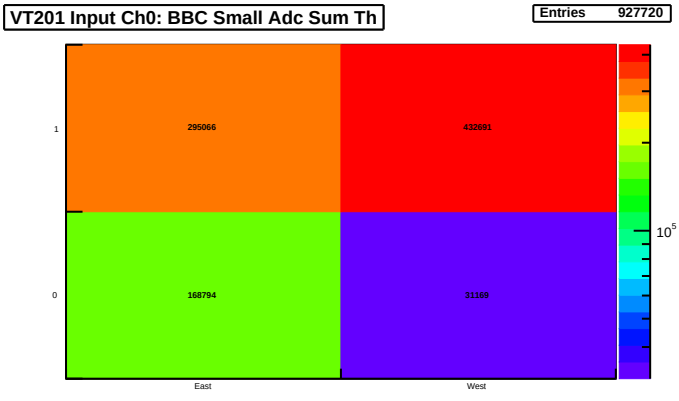
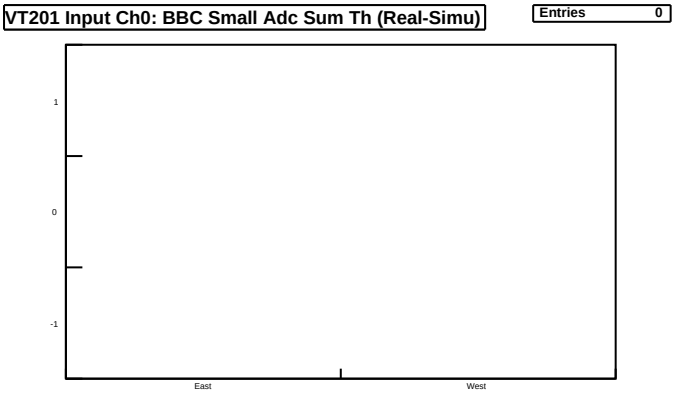
463838
463843
463850
463853
359490
456702
463766
463779
463803
463807
10^5
10^4
10^3
10^2
10
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)
ZDC-E-att-th5(Unu

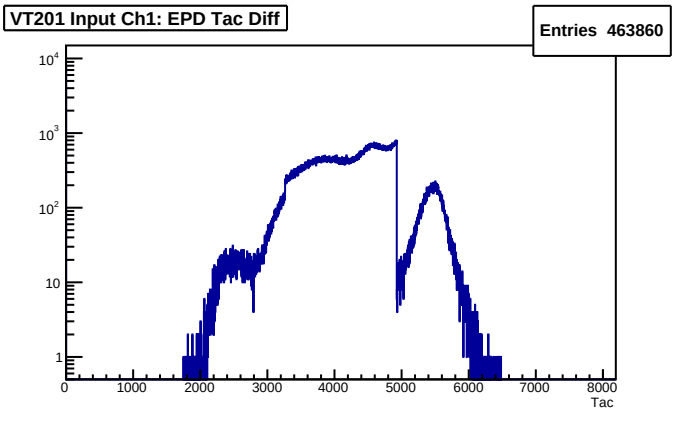

zdcAdcSumDiff
Entries 0
1
0
-1
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)
ZDC-E-att-th5(Unu

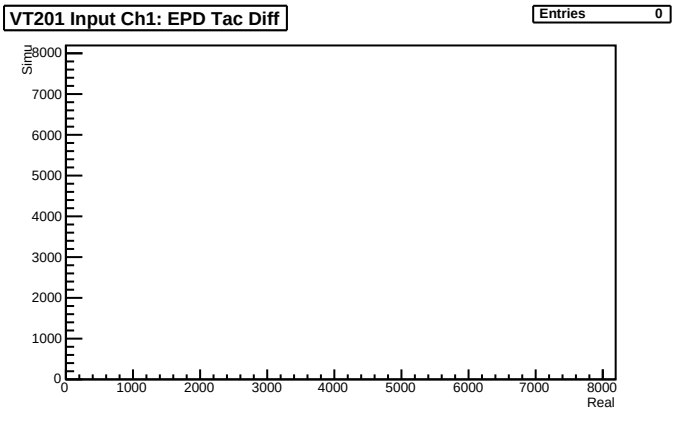

VT201 Input Ch0: BBC Small Tac Diff
Entries 463860
Tac

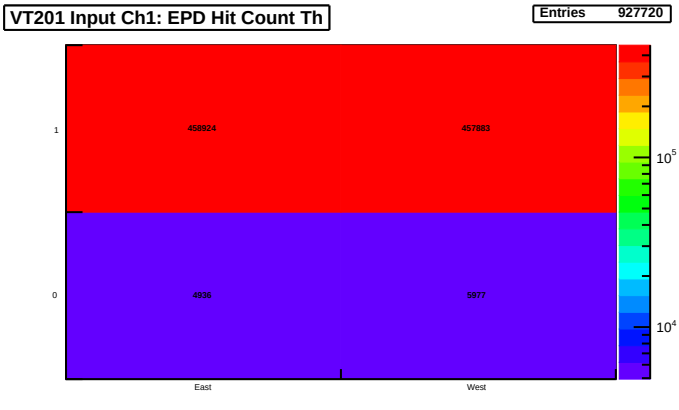

VT201 Input Ch0: BBC Small Tac Diff
Entries 0
Simu
Real

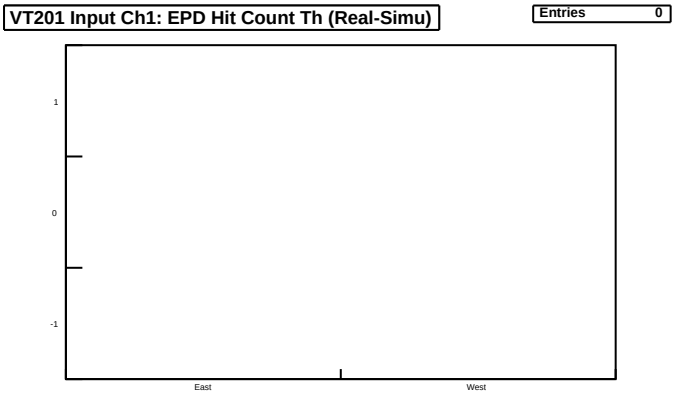

VT201 Input Ch0: BBC Small Adc Sum Th
Entries 927720
295066
432691
168794
31169
East
West

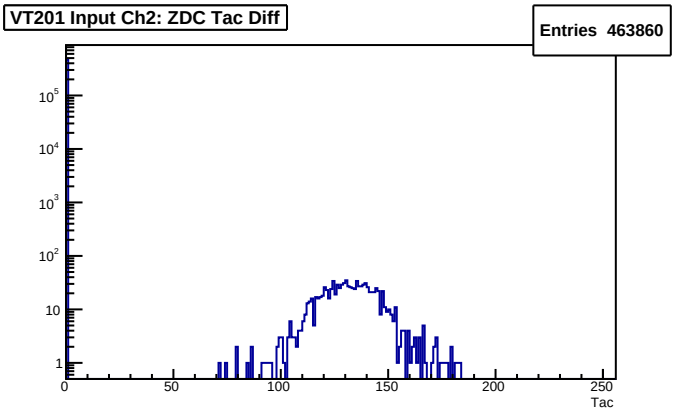

VT201 Input Ch0: BBC Small Adc Sum Th (Real-Simu)
Entries 0
East
West

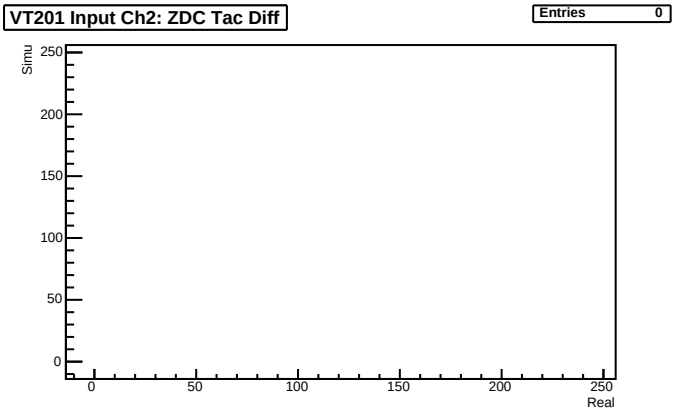

VT201 Input Ch1: EPD Tac Diff
Entries 463860
Tac

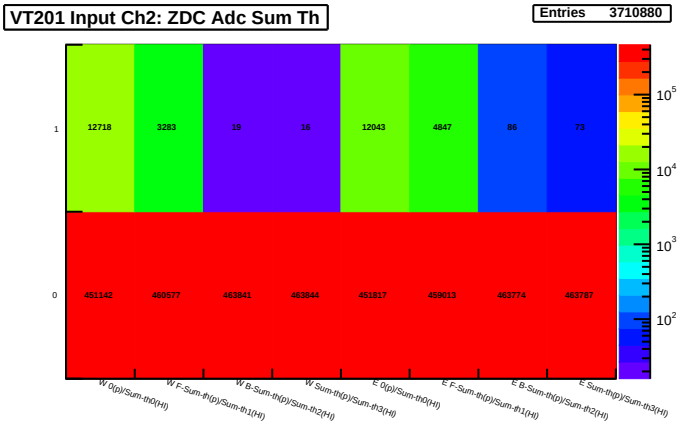

VT201 Input Ch1: EPD Tac Diff
Entries 0
Simu
Real

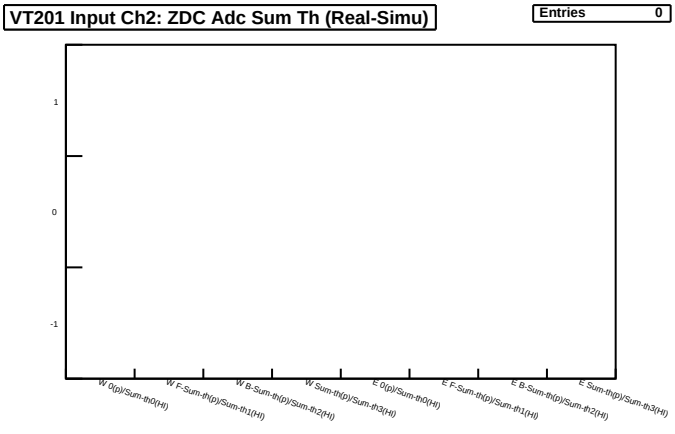

VT201 Input Ch1: EPD Hit Count Th
Entries 927720
458924
457883
4936
5977
East
West

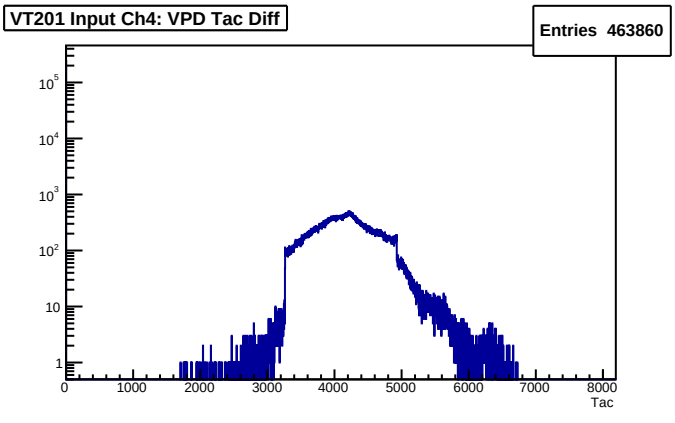

VT201 Input Ch1: EPD Hit Count Th (Real-Simu)
Entries 0
East
West

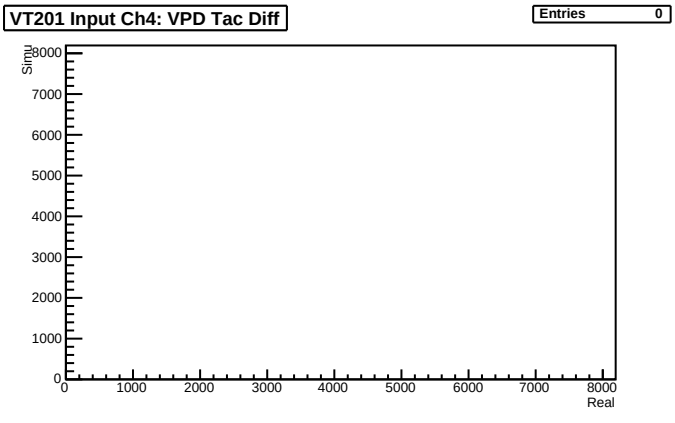

VT201 Input Ch2: ZDC Tac Diff
Entries 463860
Tac

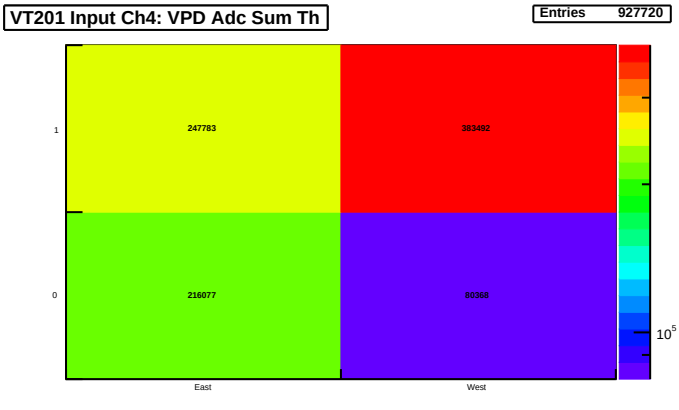

VT201 Input Ch2: ZDC Tac Diff
Entries 0
Simu
Real

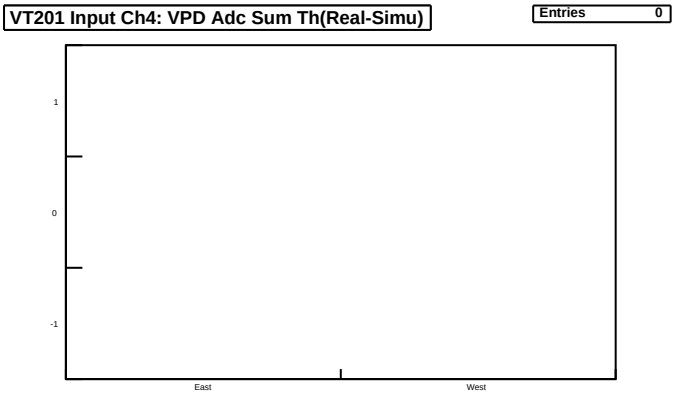

VT201 Input Ch2: ZDC Adc Sum Th
Entries 3710880
12718
3283
19
16
12043
4847
86
73
451142
460577
463841
463844
451817
459013
463774
463787
W 0(p)/Sum-th0(H)
W F-Sum-th(p)/Sum-th1(H)
W B-Sum-th(p)/Sum-th2(H)
W Sum-th(p)/Sum-th3(H)
E 0(p)/Sum-th0(H)
E F-Sum-th(p)/Sum-th1(H)
E B-Sum-th(p)/Sum-th2(H)
E Sum-th(p)/Sum-th3(H)

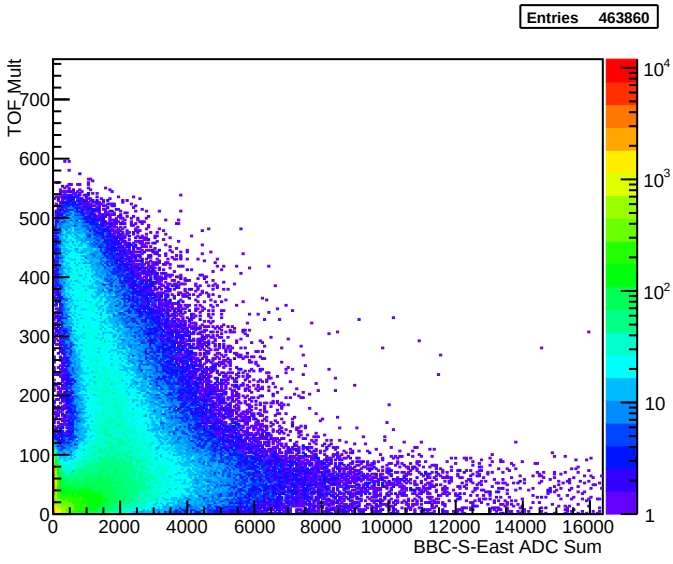

VT201 Input Ch2: ZDC Adc Sum Th (Real-Simu)
Entries 0
W 0(p)/Sum-th0(H)
W F-Sum-th(p)/Sum-th1(H)
W B-Sum-th(p)/Sum-th2(H)
W Sum-th(p)/Sum-th3(H)
E 0(p)/Sum-th0(H)
E F-Sum-th(p)/Sum-th1(H)
E B-Sum-th(p)/Sum-th2(H)
E Sum-th(p)/Sum-th3(H)

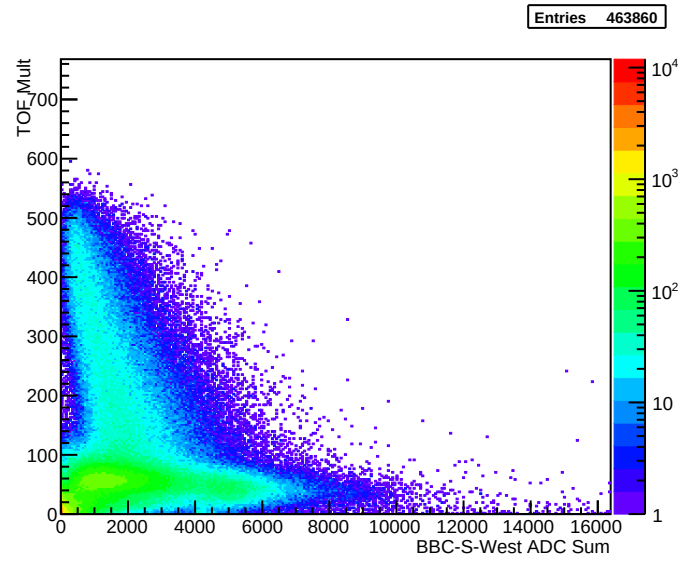

VT201 Input Ch4: VPD Tac Diff
Entries 463860
Tac

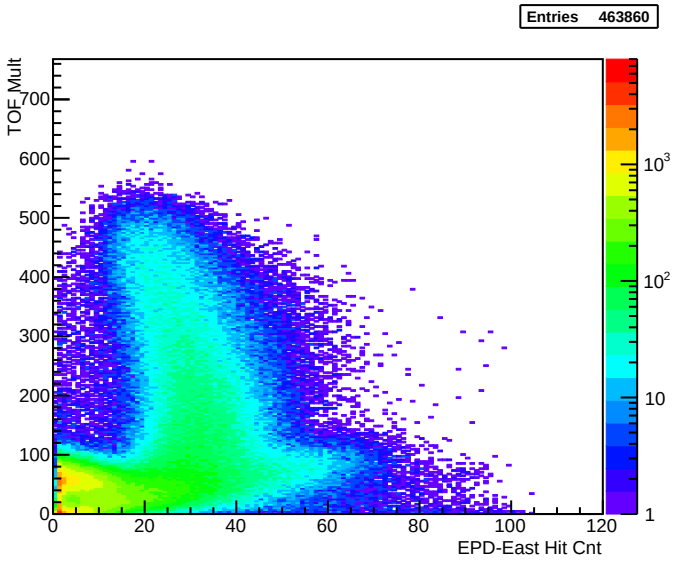

VT201 Input Ch4: VPD Tac Diff
Entries 0
Simu
Real

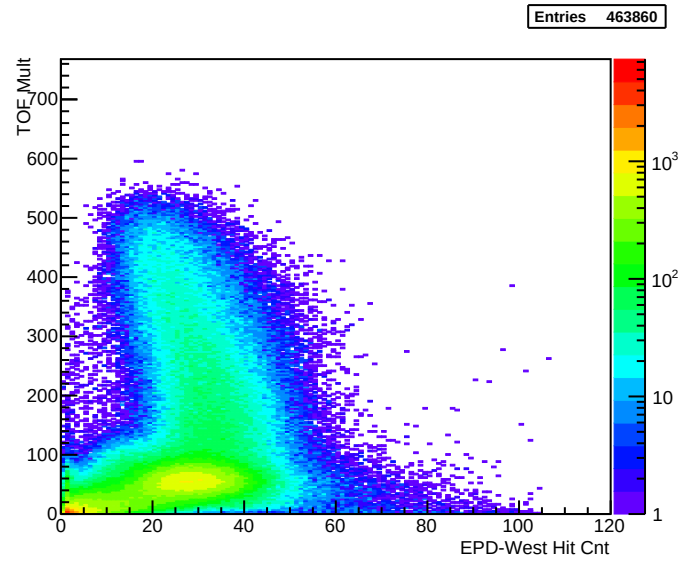

VT201 Input Ch4: VPD Adc Sum Th
Entries 927720
247783
383492
216077
80368
East
West

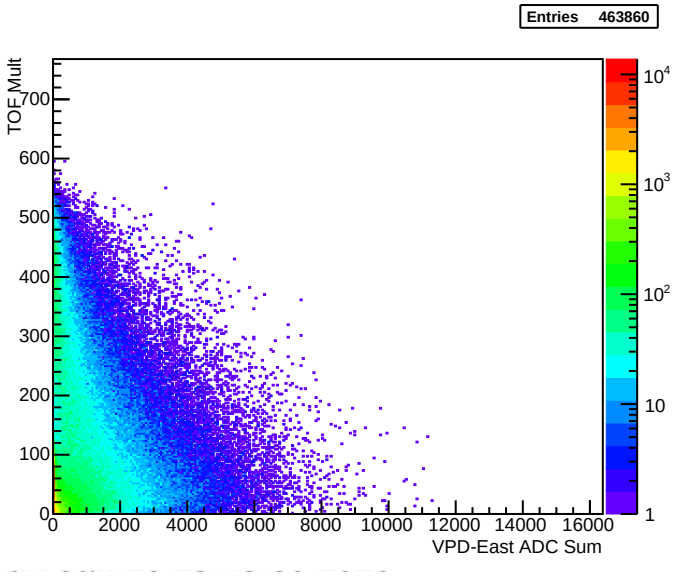

VT201 Input Ch4: VPD Adc Sum Th(Real-Simu)
Entries 0
East
West

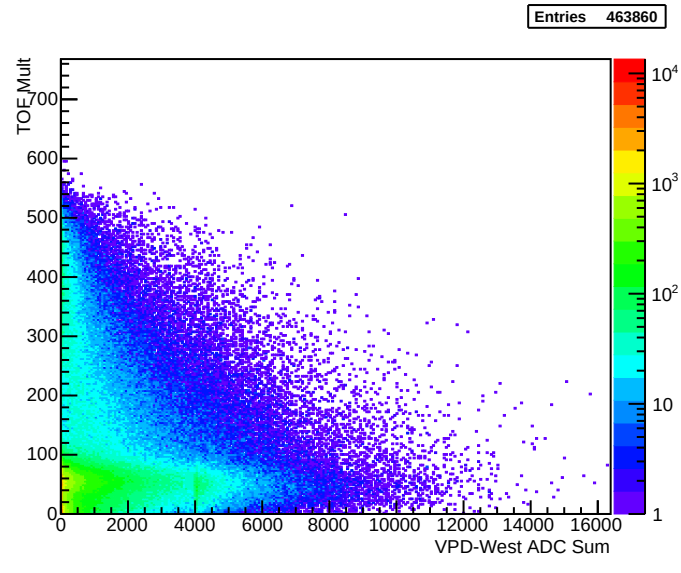

Entries 463860
TOF_Mult
BBC-S-East ADC Sum

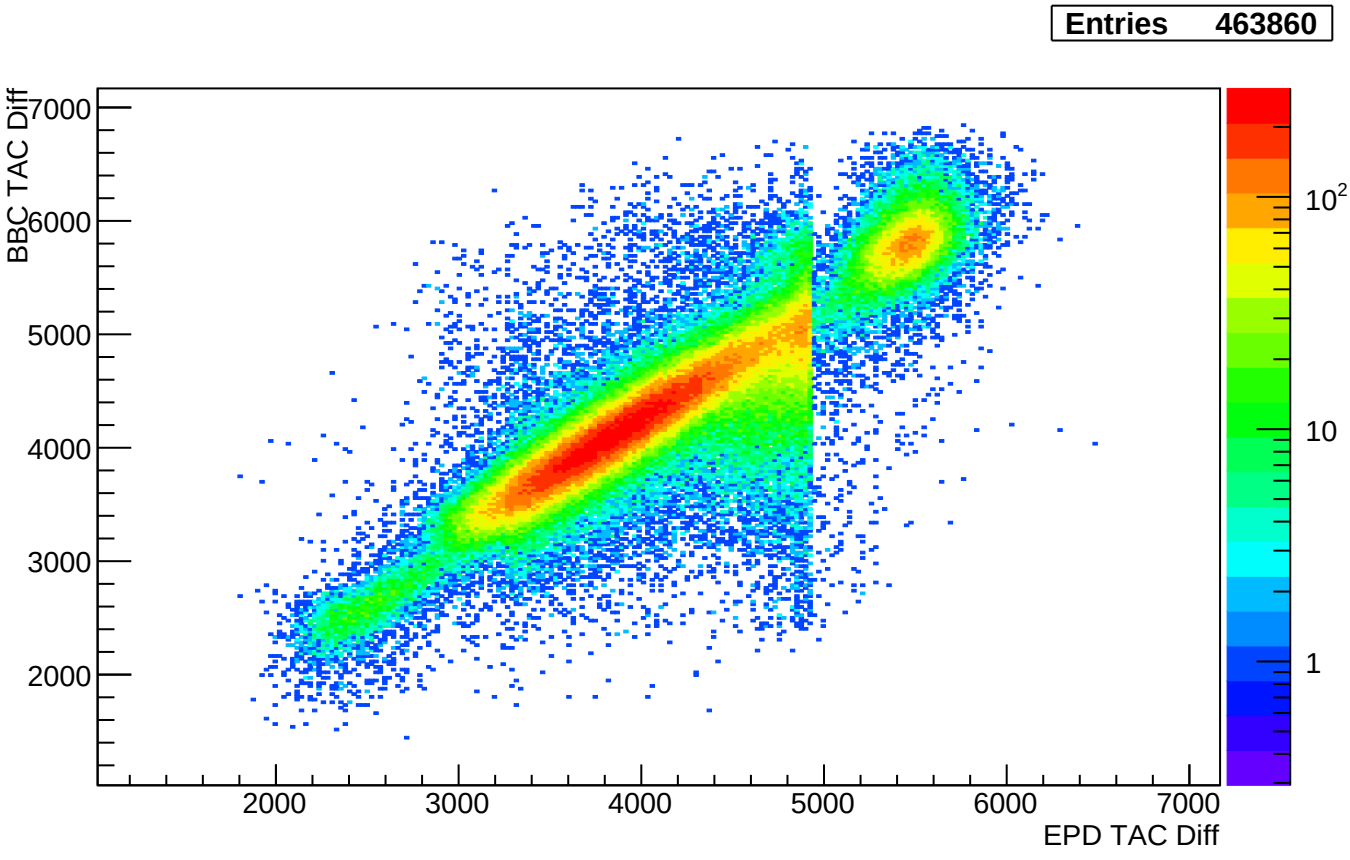

Entries 463860
TOF_Mult
BBC-S-West ADC Sum

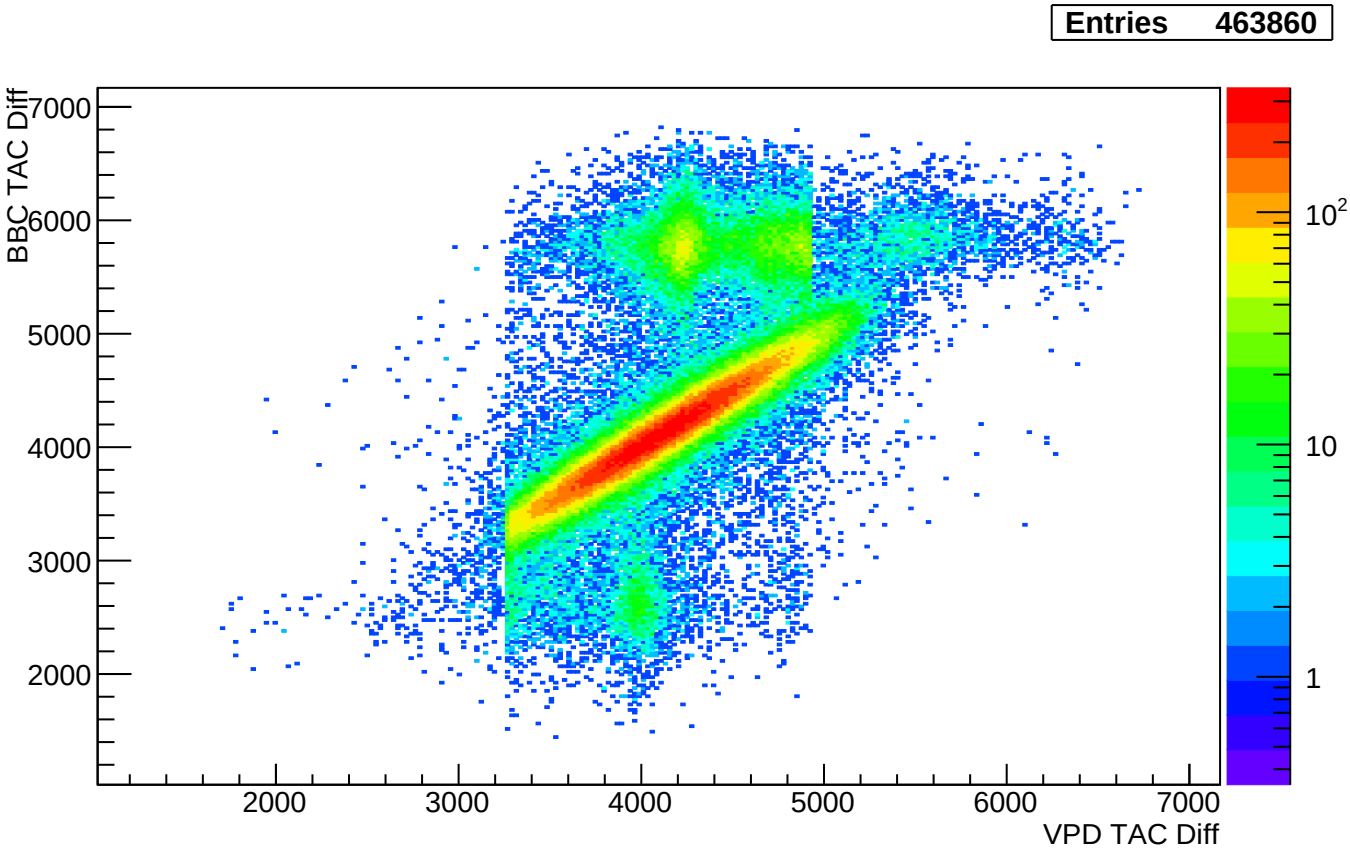

Entries 463860
TOF_Mult
EPD-East Hit Cnt

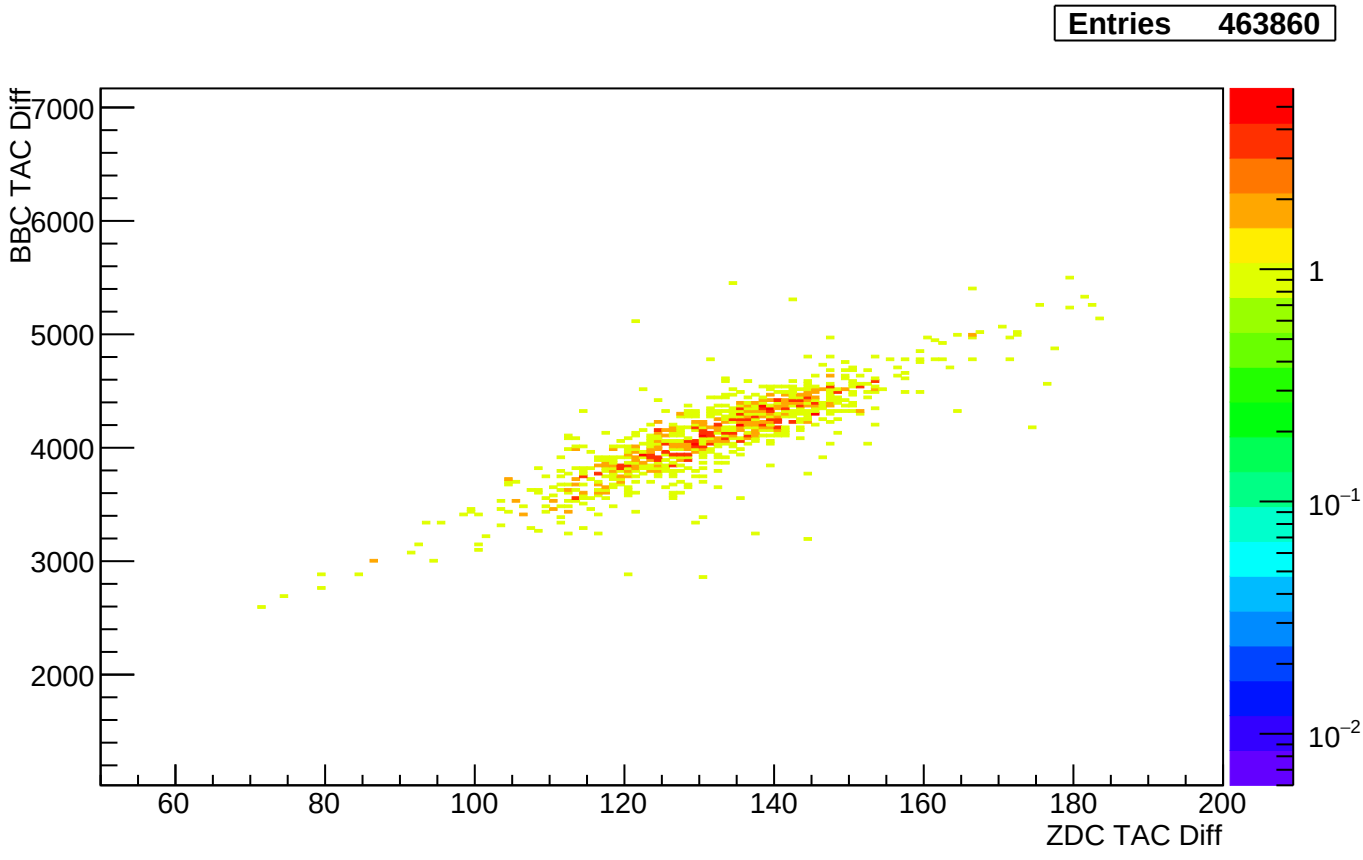

Entries 463860
TOF_Mult
EPD-West Hit Cnt

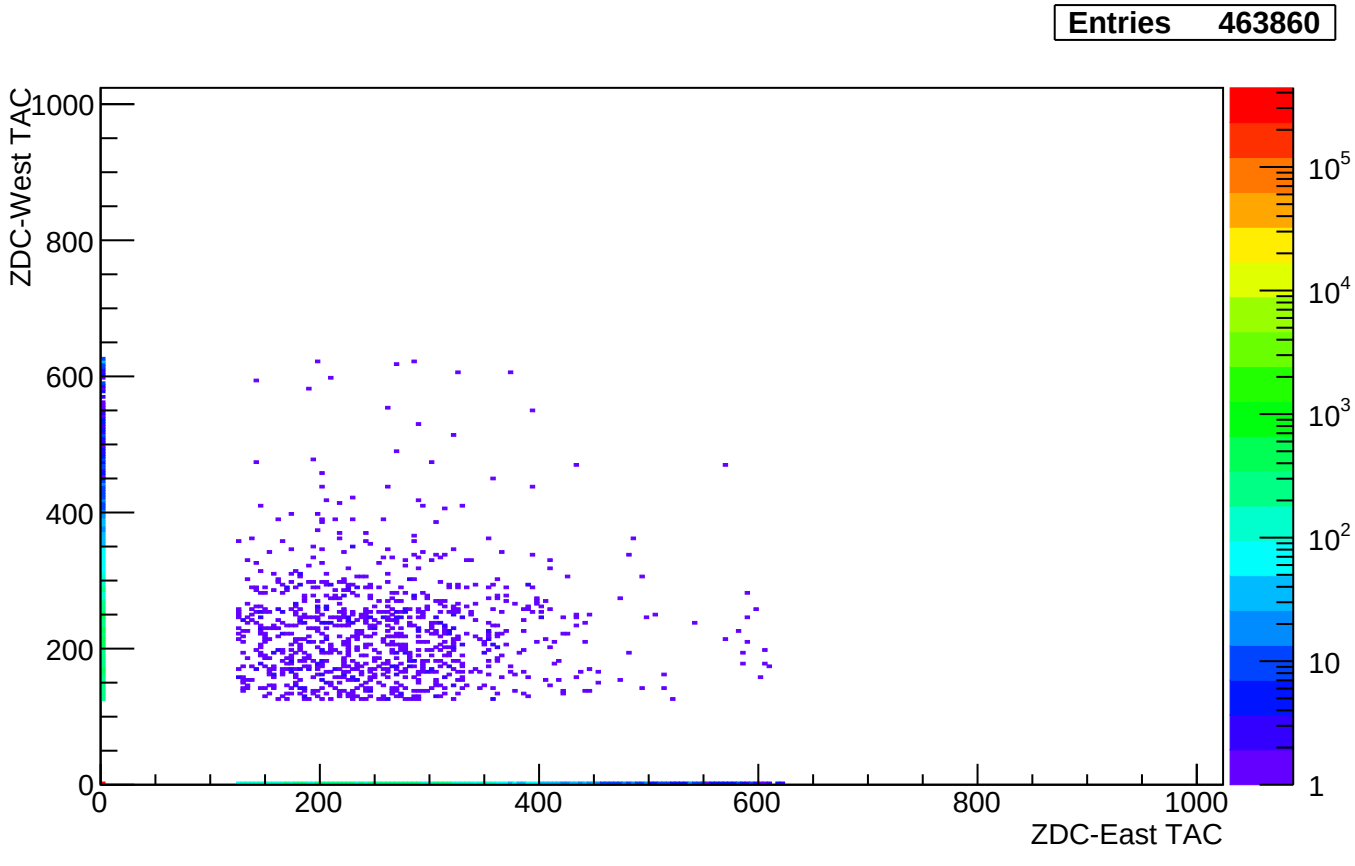

Entries 463860
TOF_Mult
VPD-East ADC Sum

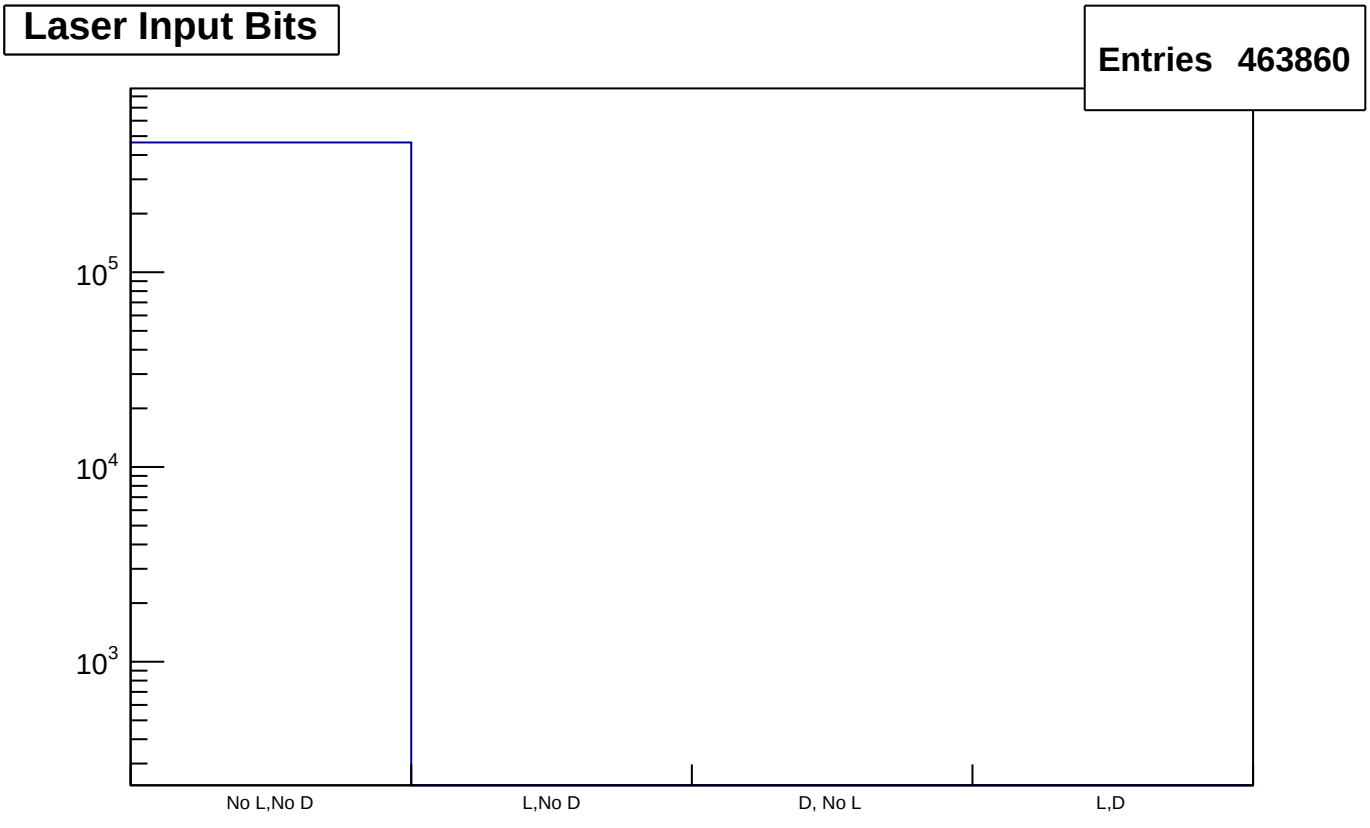

Entries 463860
TOF_Mult
VPD-West ADC Sum

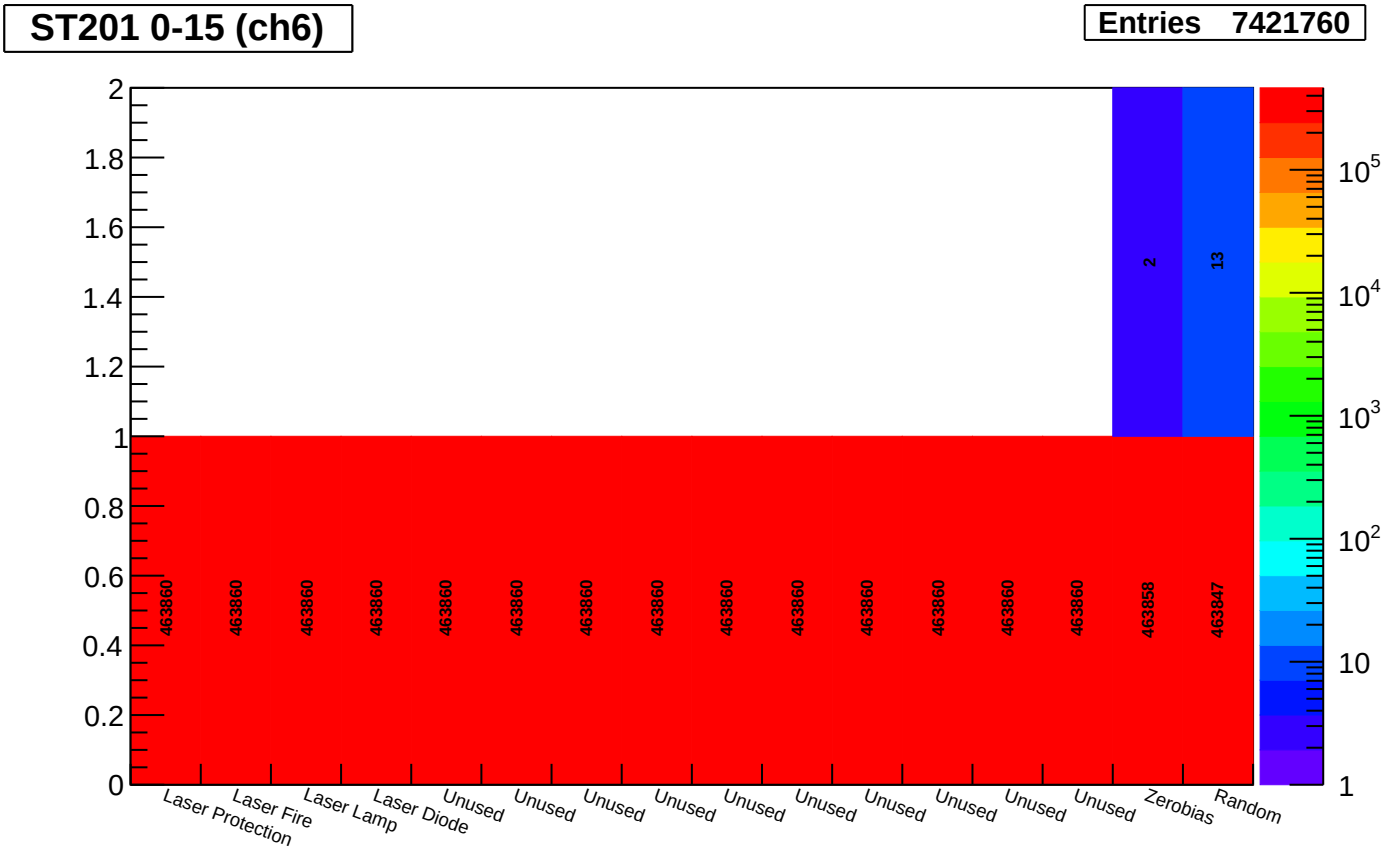

Entries 463860
BBC TAC Diff
7000
6000
5000
4000
3000
2000
2000
3000
4000
5000
6000
7000
EPD TAC Diff
10^2
10
1

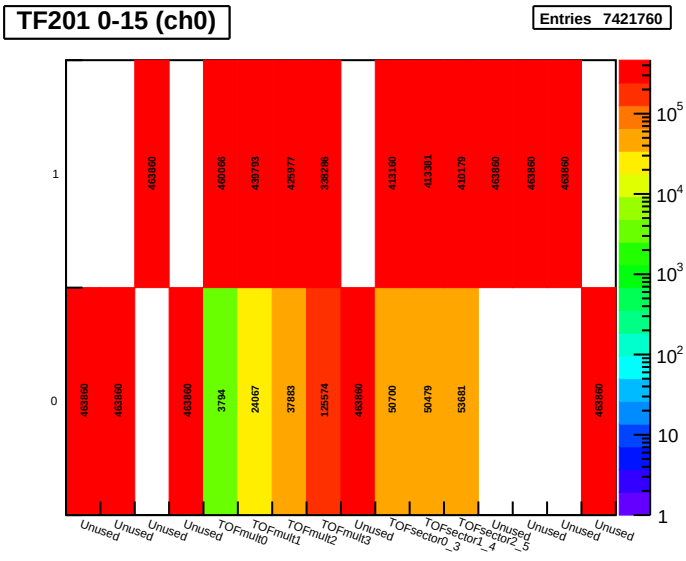

Entries 463860
BBC TAC Diff
7000
6000
5000
4000
3000
2000
2000
3000
4000
5000
6000
7000
VPD TAC Diff
10^2
10
1

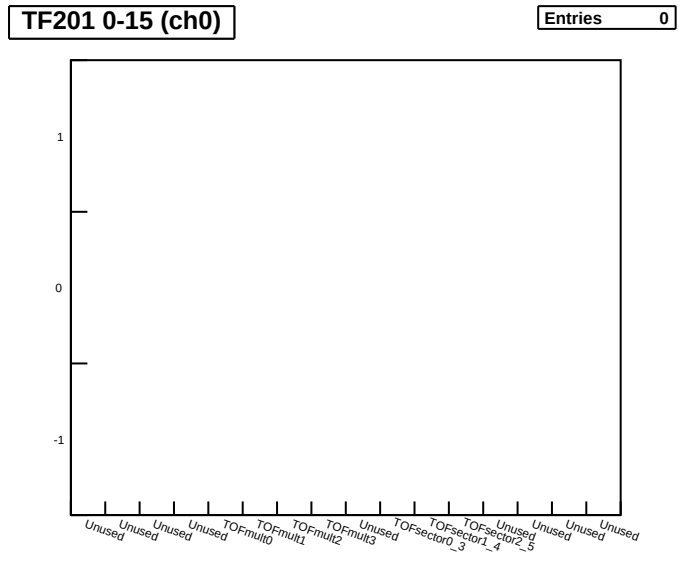

Entries 463860
BBC TAC Diff
7000
6000
5000
4000
3000
2000
60
80
100
120
140
160
180
200
ZDC TAC Diff
1
10^{-1}
10^{-2}

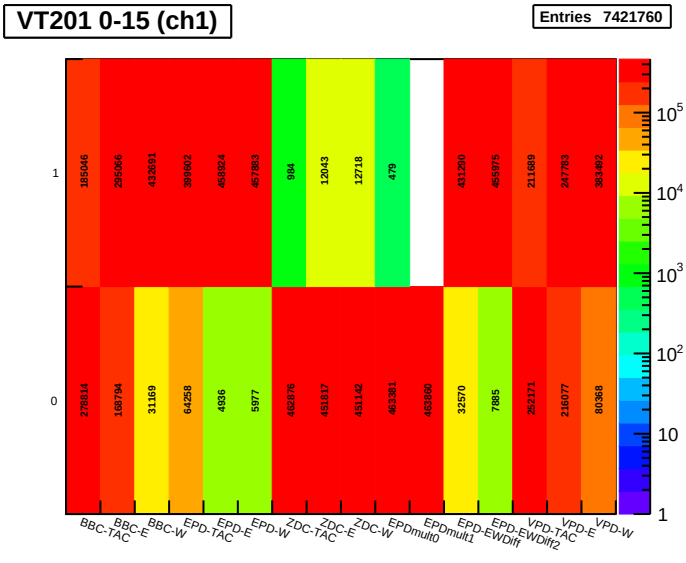

Entries 463860
ZDC-West TAC
1000
800
600
400
200
0
0
200
400
600
800
1000
ZDC-East TAC
10^5
10^4
10^3
10^2
10
1

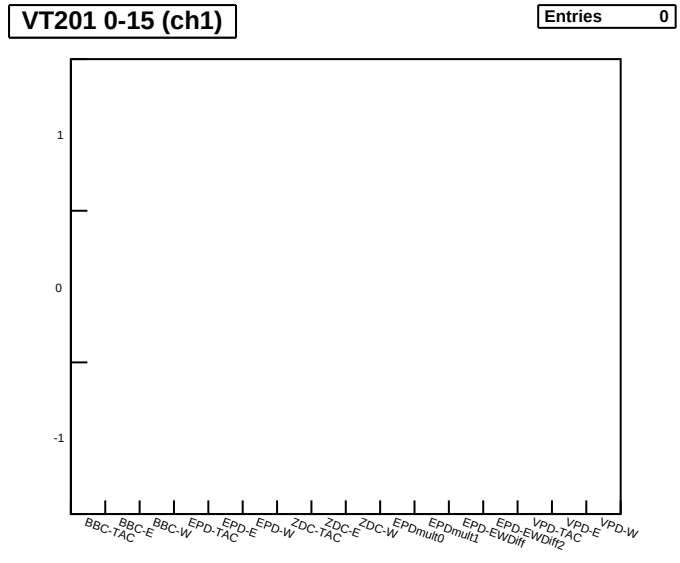

Laser Input Bits
Entries 463860
10^5
10^4
10^3
No L,No D
L,No D
D, No L
L,D

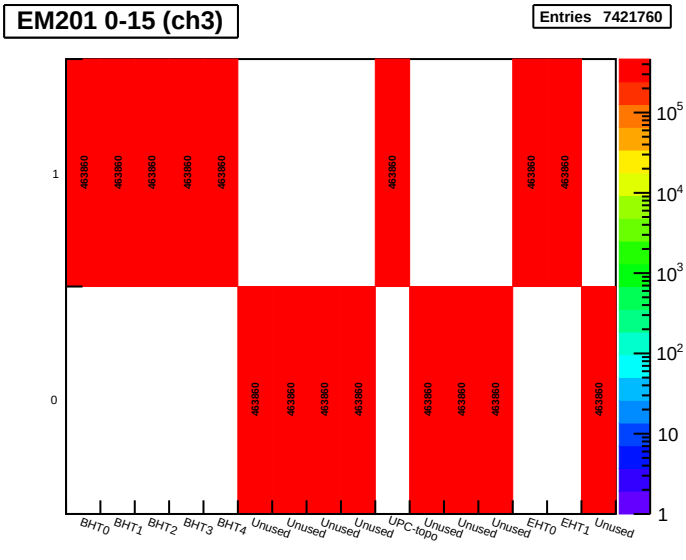

ST201 0-15 (ch6)
Entries 7421760
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
463860
463860
463860
463860
463860
463860
463860
463860
463860
463860
463860
463860
463860
463860
463858
463847
2
13
10^5
10^4
10^3
10^2
10
1
Laser Protection
Laser Fire
Laser Lamp
Laser Diode
Unused
Unused
Unused
Unused
Unused
Unused
Unused
Unused
Unused
Unused
Zerobias
Random

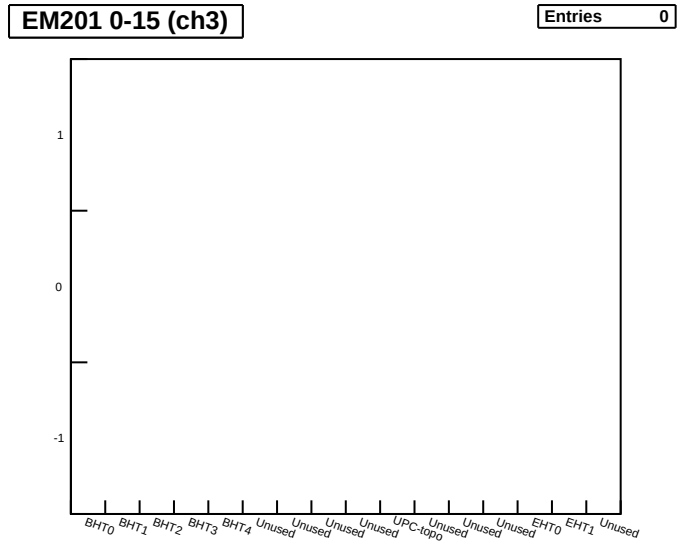

TF201 0-15 (ch0)
Entries 7421760
463860
460066
439793
425977
338286
413160
413381
410179
463860
463860
463860
463860
463860
3794
24067
37883
125574
463860
50700
50479
53681
463860
Unused
TOFmult0
TOFmult1
TOFmult2
TOFmult3
TOFsector0_3
TOFsector1_4
TOFsector2_5


TF201 0-15 (ch0)
Entries 0
Unused
TOFmult0
TOFmult1
TOFmult2
TOFmult3
TOFsector0_3
TOFsector1_4
TOFsector2_5


VT201 0-15 (ch1)
Entries 7421760
185046
295066
432691
399602
458924
457883
984
12043
12718
479
431290
455975
211689
247783
383482
278814
168794
31169
64258
4936
5977
462876
451817
451142
463381
463860
32570
7885
252171
216077
80368
BBC-TAC
BBC-E
BBC-W
EPD-TAC
EPD-E
EPD-W
ZDC-TAC
ZDC-E
ZDC-W
EPDmult0
EPDmult1
EPD-EWDiff
EPD-EWDiff2
VPD-TAC
VPD-E
VPD-W


VT201 0-15 (ch1)
Entries 0
BBC-TAC
BBC-E
BBC-W
EPD-TAC
EPD-E
EPD-W
ZDC-TAC
ZDC-E
ZDC-W
EPDmult0
EPDmult1
EPD-EWDiff
EPD-EWDiff2
VPD-TAC
VPD-E
VPD-W


EM201 0-15 (ch3)
Entries 7421760
463860
BHT0
BHT1
BHT2
BHT3
BHT4
Unused
UPC-topo
EHT0
EHT1


EM201 0-15 (ch3)
Entries 0
BHT0
BHT1
BHT2
BHT3
BHT4
Unused
UPC-topo
EHT0
EHT1